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Kim

(10) **Patent No.:** **US 7,999,908 B2**
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(54) **LIQUID CRYSTAL DISPLAY DEVICE AND METHOD OF FABRICATING THE SAME**

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

This patent is subject to a terminal disclaimer.

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Related U.S. Application Data

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(30) **Foreign Application Priority Data**

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(51) **Int. Cl.**

G02F 1/1339 (2006.01)

(52) **U.S. Cl.** 349/190; 349/153; 349/156

(58) **Field of Classification Search** 349/153-156,
349/189-190

See application file for complete search history.

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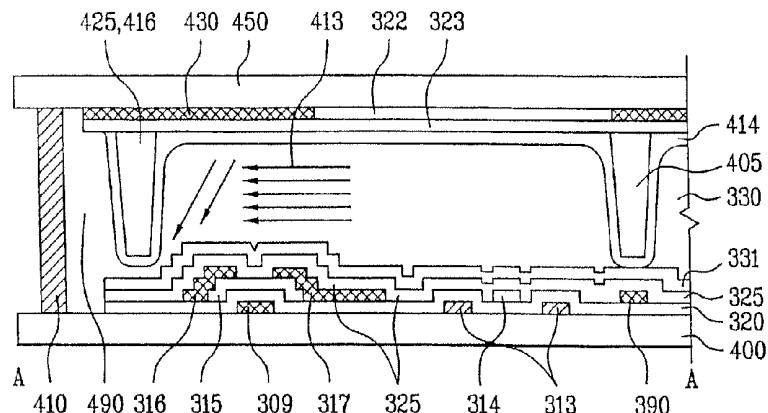
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(57) **ABSTRACT**

A liquid crystal display device and a method of fabricating the same are disclosed in the present invention. The liquid crystal display device includes first and second substrates including a plurality of liquid crystal display panel regions, each liquid crystal display panel region having an active region and a dummy region, a sealant outside the liquid crystal display panel regions, a dummy column spacer on the dummy region to control a liquid crystal flow, a buffer region between the sealant and the dummy column spacer to accommodate a liquid crystal, and a liquid crystal layer between the first and second substrates.

19 Claims, 12 Drawing Sheets



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FIG. 1
Related Art

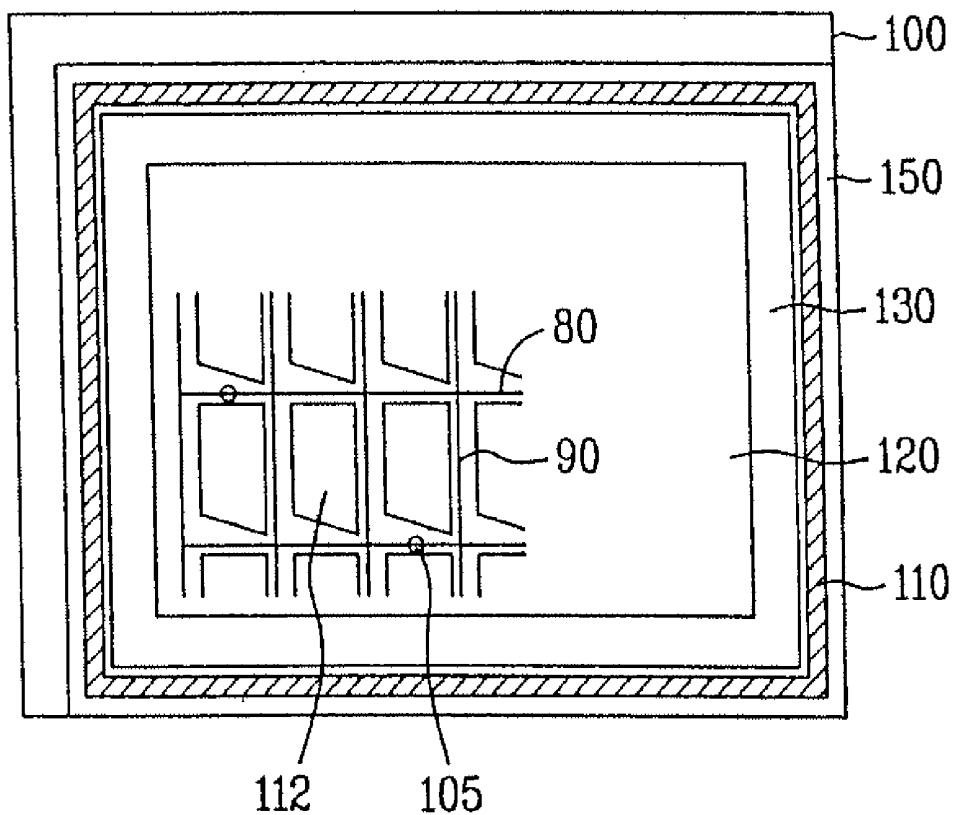


FIG. 2A
Related Art

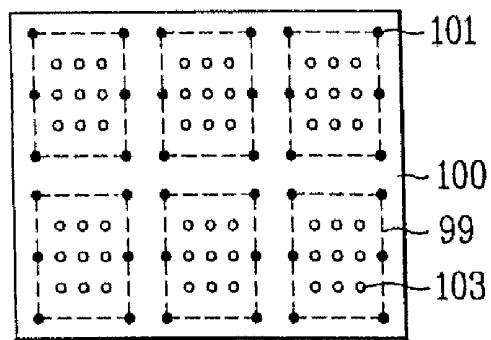


FIG. 2B
Related Art

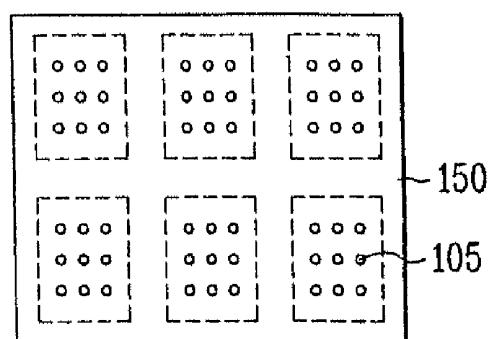


FIG. 2C
Related Art

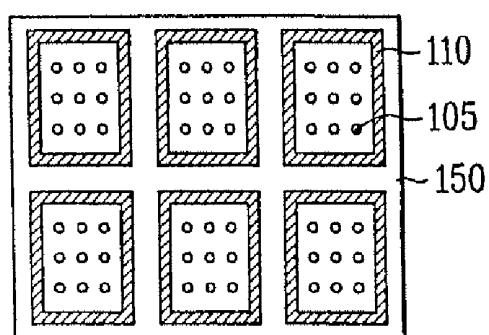


FIG. 2D
Related Art

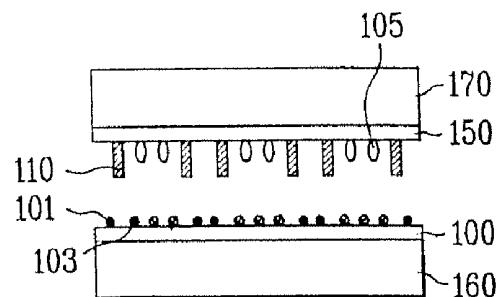


FIG. 2E
Related Art

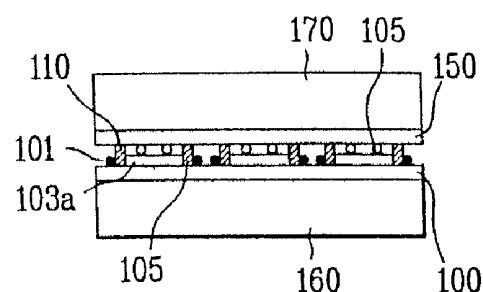


FIG. 2F
Related Art

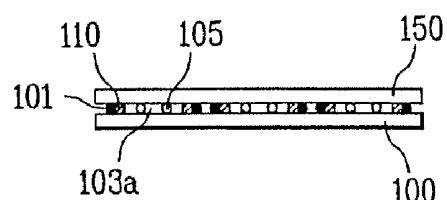


FIG. 2G
Related Art

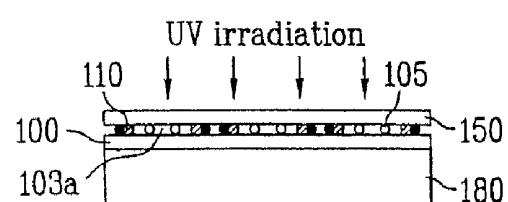


FIG. 3

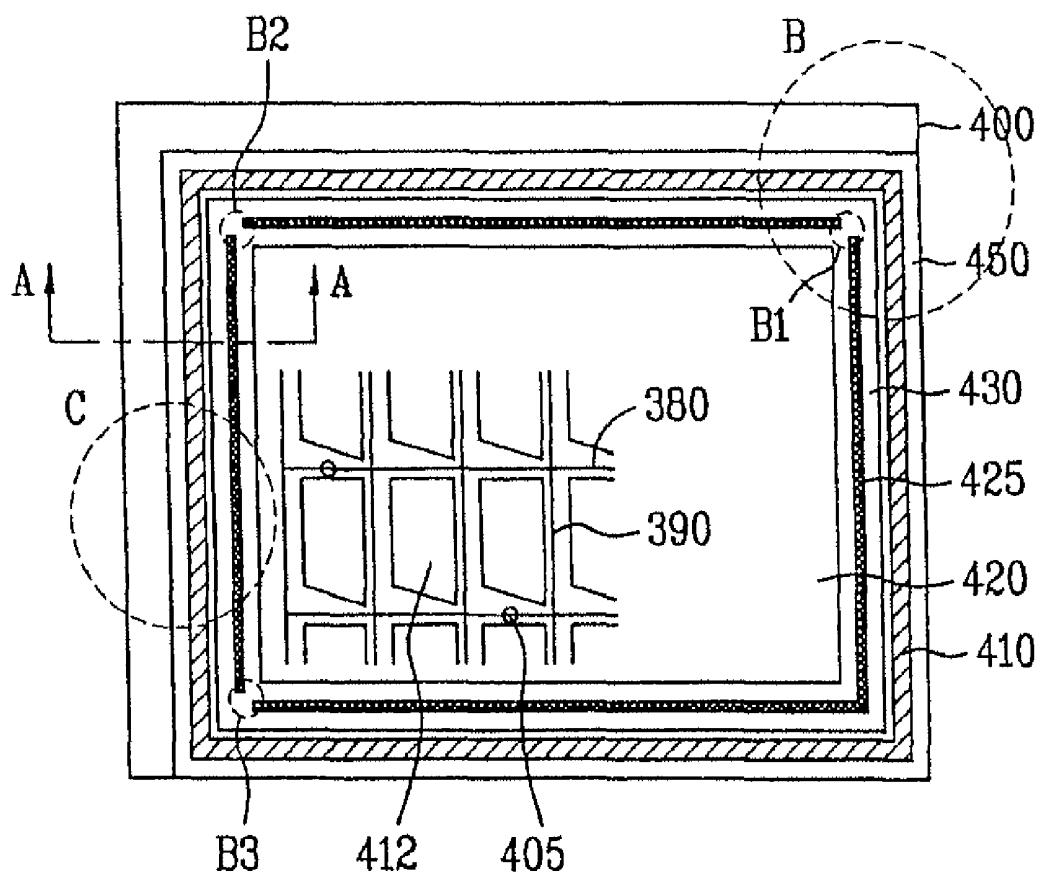


FIG. 4A

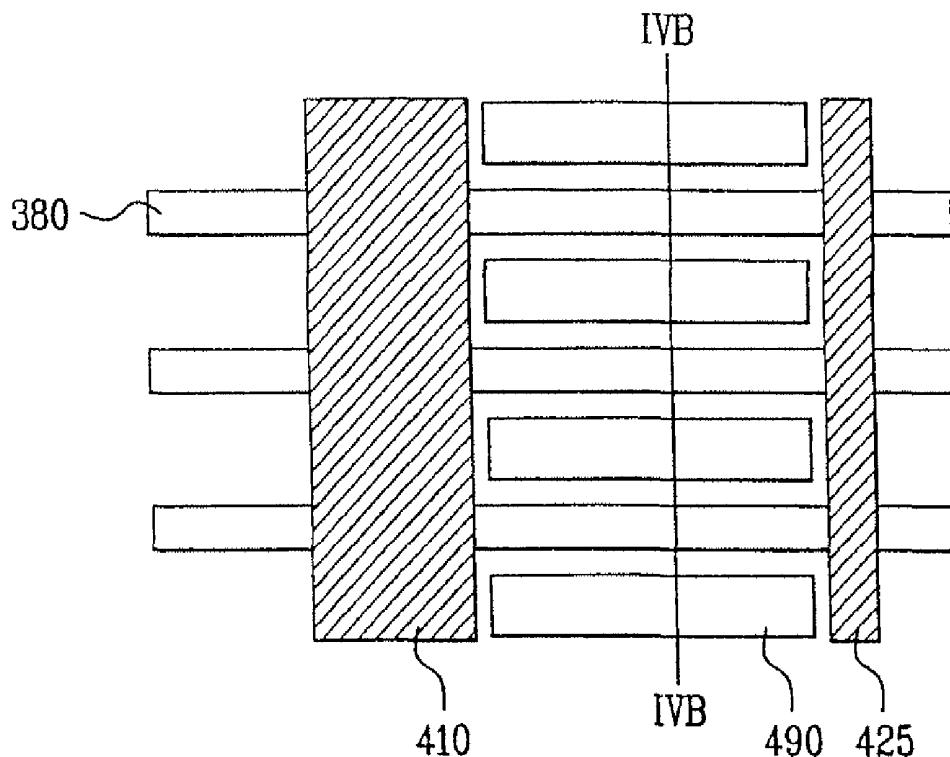


FIG. 4B

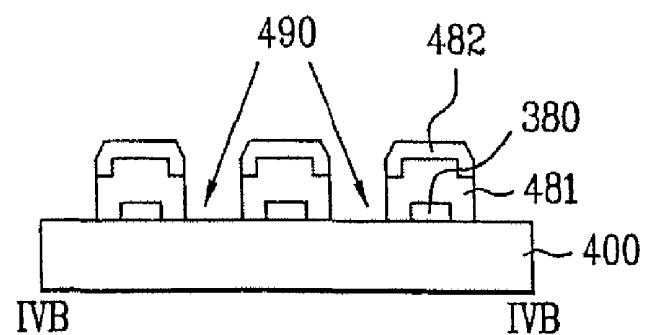


FIG. 5

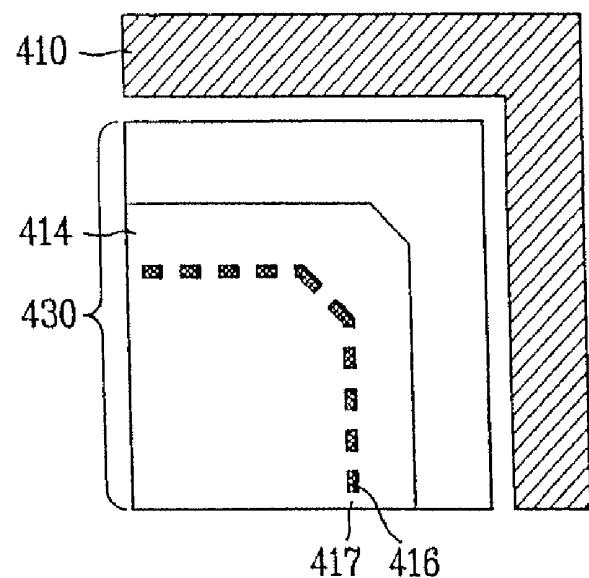


FIG. 6

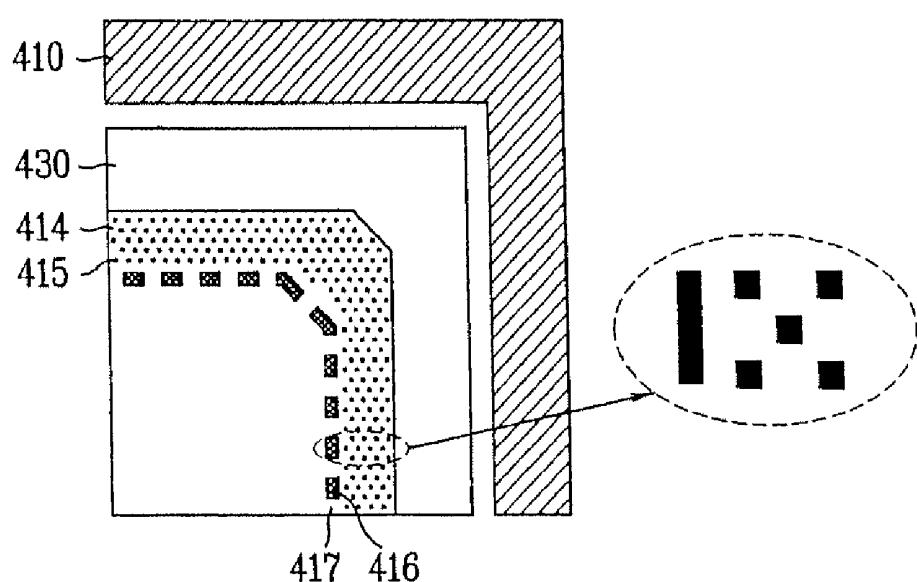


FIG. 7

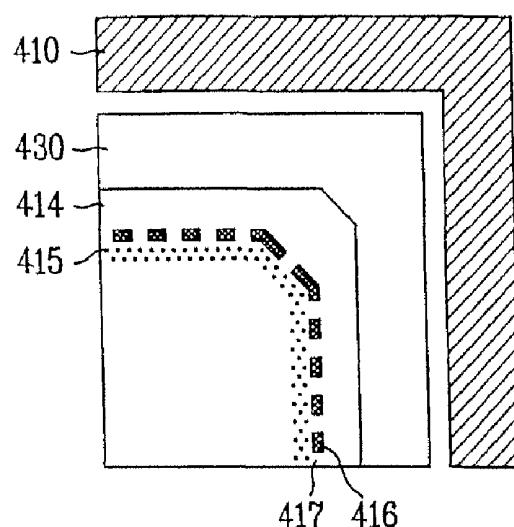


FIG. 8

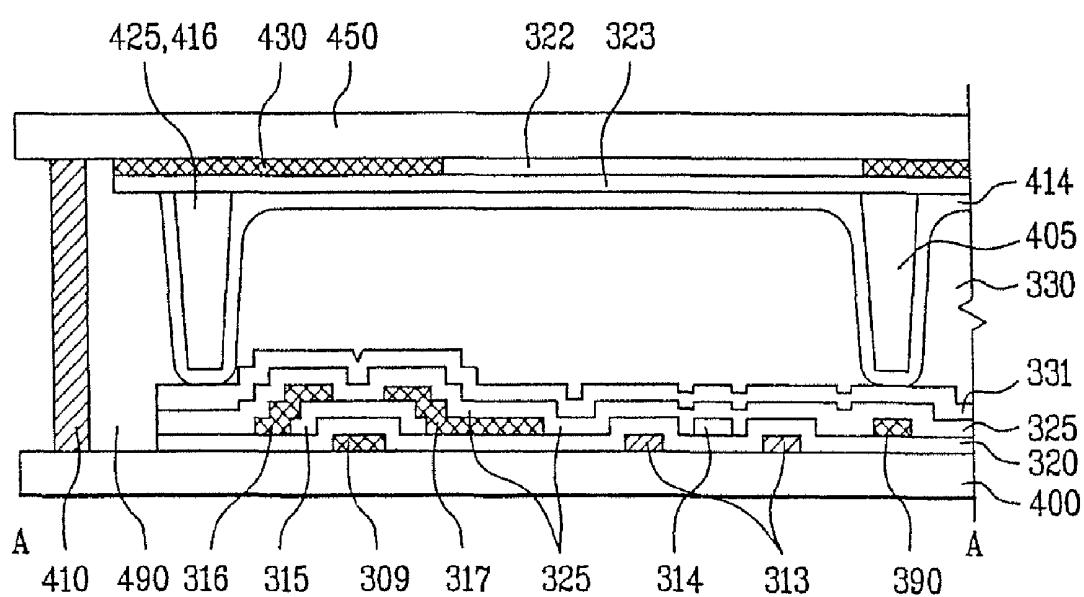


FIG. 9

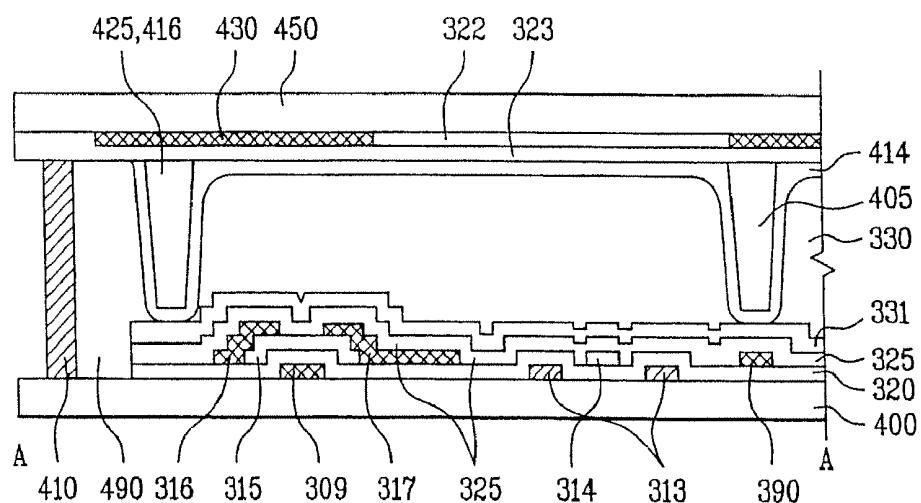


FIG. 10

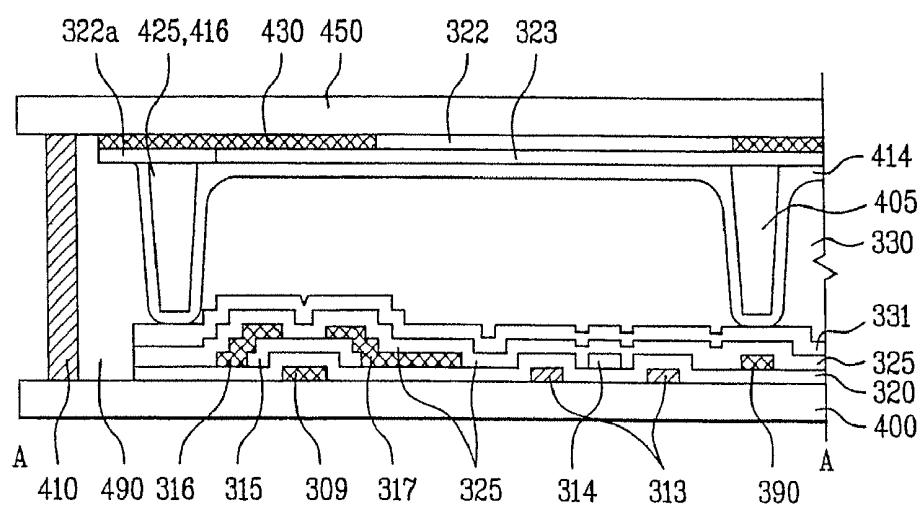


FIG. 11

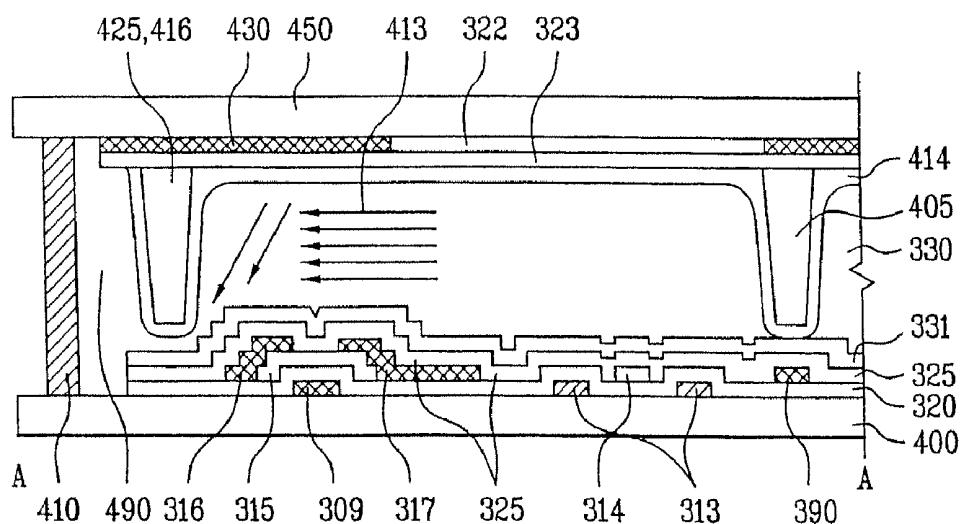


FIG. 12

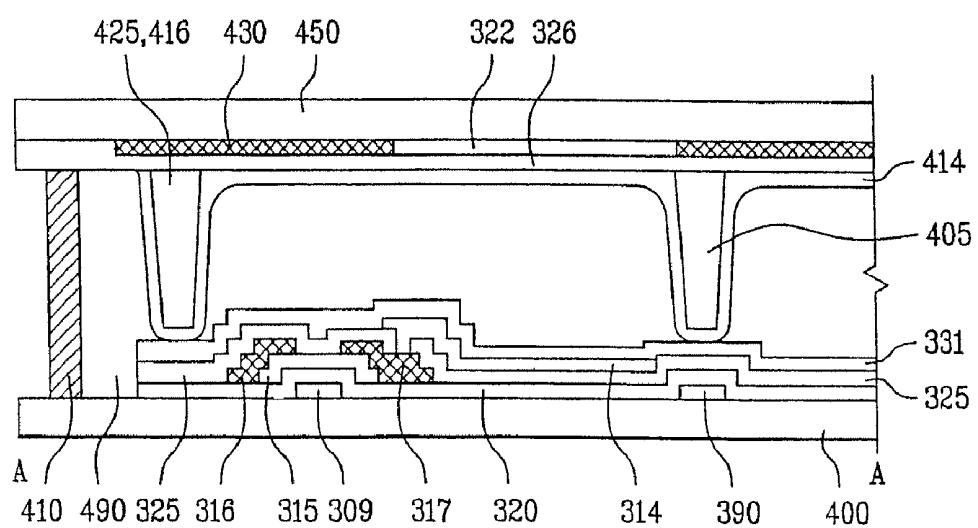


FIG. 13

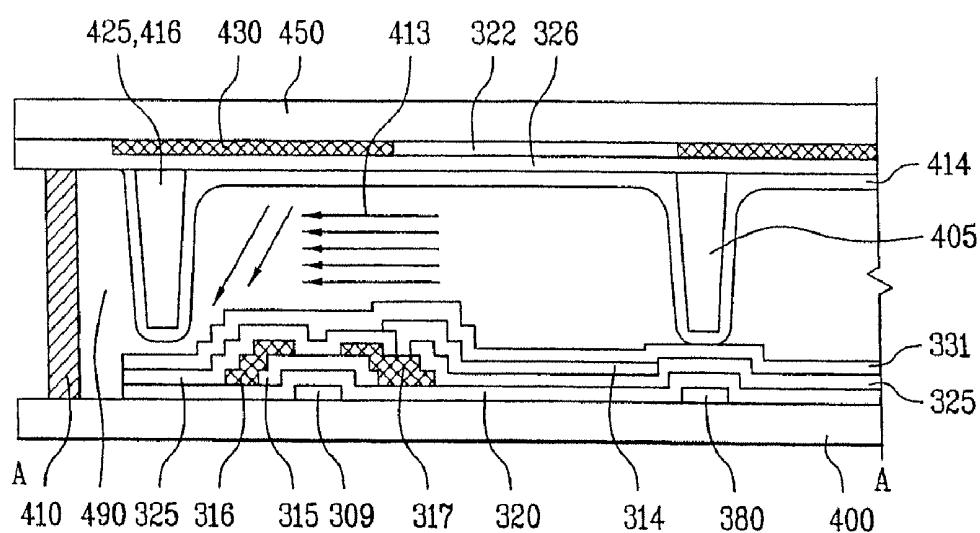


FIG. 14A

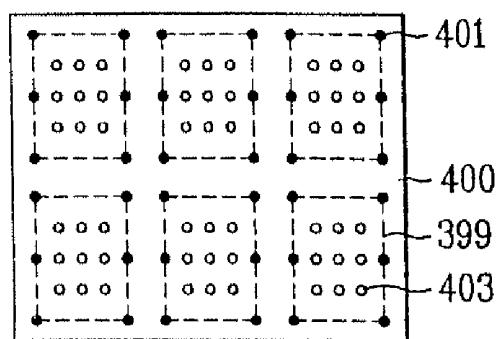


FIG. 14B

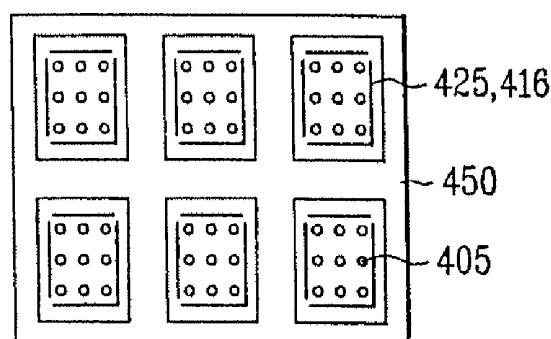


FIG. 14C

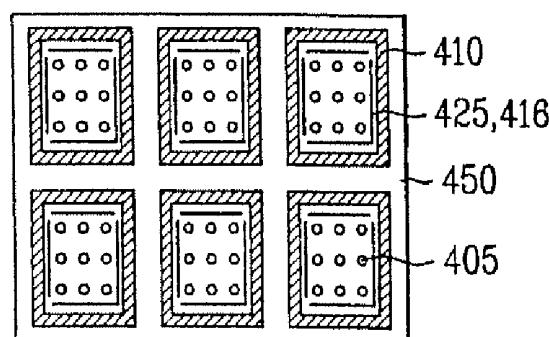


FIG. 14D

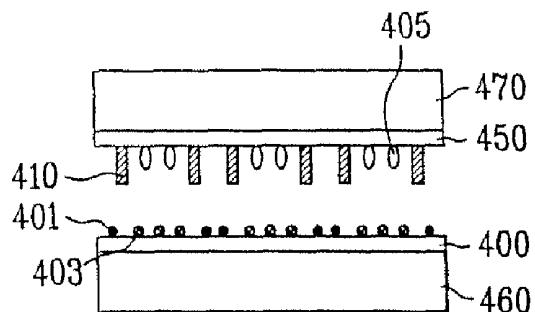


FIG. 14E

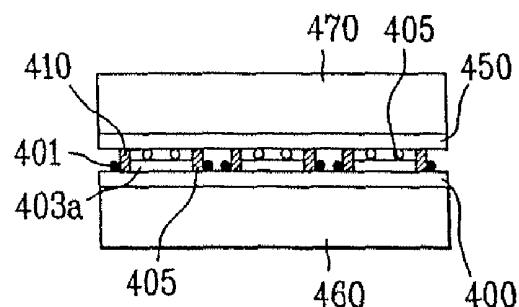


FIG. 14F

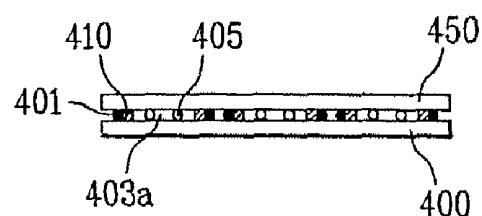
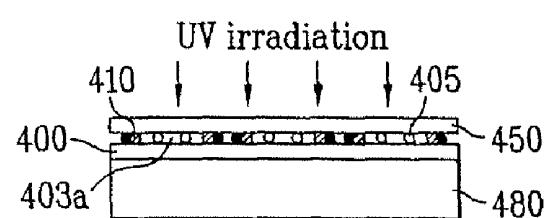


FIG. 14G



LIQUID CRYSTAL DISPLAY DEVICE AND METHOD OF FABRICATING THE SAME

This is a divisional application of U.S. patent application Ser. No. 11/050,794, filed on Feb. 7, 2005 now U.S. Pat. No. 7,692,756, which is a continuation application of U.S. patent Ser. No. 10/319,630 filed on Dec. 16, 2002 now U.S. Pat. No. 6,864,946, which is a continuation application of U.S. patent application Ser. No. 10/319,630, filed on Dec. 16, 2002, which claims the benefit of Korean Patent Application No. P2001-082739 filed on Dec. 21, 2001, all of which are hereby incorporated by reference for all purposes as if fully set forth herein.

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates to a display device, and more particularly, to a liquid crystal display device and a method of fabricating the same. Although the present invention is suitable for a wide scope of applications, it is particularly suitable for effectively controlling liquid crystal flow in fabricating a liquid crystal display.

2. Discussion of the Related Art

In general, applications of liquid crystal display devices have characteristics, such as full color realization, low voltage operation, low power consumption, thinness, lightness in weight, and high image quality. Thus, its applications have been diversified from monitors for electronic watches, calculators, notebook computers, personal computers and TV's, and monitors for aviation gauges, personal digital assistants (PDA's), and mobile stations.

A method of forming a liquid crystal layer is largely classified into two methods, such as a liquid crystal injection method and a liquid crystal dispensing method. As a panel of the liquid crystal display device becomes larger in size, the liquid crystal dispensing method is mostly used in fabricating the liquid crystal display device. This is because the liquid crystal injection method takes more time to inject the liquid crystal between first and second substrates. In the liquid crystal dispensing method, liquid crystal droplets are dispensed on one of the first and second substrates, and then the substrates are bonded to each other to form the liquid crystal layer.

When the liquid crystal is injected between the first and second substrates, a thermo-hardening sealant is usually used. For instance, epoxy resin is mixed with amine or amide in order to form a thermo-hardening sealant. The thermo-hardening sealant is hardened by applying heat to the sealant for about one or two hours at a temperature of about 100°, so that contamination of the liquid crystal may occur due to a long hardening time period. In the liquid crystal dispensing method, the first and second substrates are attached to each other with a photo-hardening sealant. In this case, the photo-hardening sealant is a mixture of a hardener and acryl resin. When UV-ray is irradiated to the sealant, the hardener becomes a radical, and then the radical acts with the acryl resin, so that the acryl resin is activated. Accordingly, the mixture is polymerized so that adhesion of the sealant is improved. After forming the liquid crystal layer between the first and second substrates, the UV-ray is irradiated to the attached substrates in order to harden the sealant between the first and second substrates for several seconds. In addition, a ball spacer is used between the first and second substrates so as to maintain a cell gap in the liquid crystal injection method.

Meanwhile, a column spacer is used between the first and second substrates so as to maintain a cell gap in the liquid crystal dispensing method.

A related art liquid crystal display device and a method of fabricating the same will be explained with reference to the accompanying drawings.

FIG. 1 is a plane view of a liquid crystal display device according to a related art liquid crystal dispensing method. FIGS. 2A to 2G are plane views illustrating process steps of fabricating a liquid crystal display device according to a related art liquid crystal dispensing method.

In FIG. 1, a plurality of gate lines 80 and a plurality of data lines 90 are formed to cross one another in an active region 120 of a thin film transistor array substrate (i.e., a first substrate) 100, thereby defining a plurality of pixel regions. A pixel electrode 112 is formed at each pixel region. A plurality of thin film transistors (not shown) are formed at each corresponding crossing point of the gate and data lines 80 and 90 so as to apply signals of the data lines to each pixel electrode 112 according to signals of the gate lines. Then, the liquid crystal (not shown) is dispensed on the first substrate 100.

Next, a color filter array substrate (i.e., a second substrate) 150 includes column spacers, a black matrix layer 130, and a photo-hardening sealant. In this case, the column spacers 105 are formed on the second substrate 150 so as to maintain a cell gap between the first and second substrates 100 and 150. The black matrix layer 130 is formed in the periphery of the active region 120. The photo-hardening sealant 110 surrounding the black matrix layer 130 is formed in the periphery of the black matrix layer 130 so as to attach the first and second substrates 100 and 150 to each other. After attaching the first and second substrates 100 and 150 to each other with the photo-hardening sealant 110, UV-ray is irradiated to the attached substrates from the top of the second substrate 150 for hardening the photo-hardening sealant 110, thereby bonding the first and second substrates to each other.

At this time, there may occur imperfect or excessive filling of the liquid crystal if the exact amount of the liquid crystal is not dispensed on the substrates. When the amount of the liquid crystal dispensed on the substrate is less than the required amount, it takes time to spread the liquid crystal from the center of the substrate to the corner regions, such as the farthest spots. As a result, a tilt angle of the liquid crystal may be changed since a contaminant is accumulated on the liquid crystal display panel. On the other hand, when too much liquid crystal is dispensed on the substrate, spots may appear on the liquid crystal display panel due to a problem in the spacer for maintaining a cell gap between the first and second substrates.

A method of fabricating a liquid crystal display device according to a related art liquid crystal dispensing method will be explained in detail.

FIGS. 2A to 2G are plane views illustrating fabricating process steps of a liquid crystal display device according to a related art liquid crystal dispensing method. In the liquid crystal display device according to the related art liquid crystal dispensing method, a plurality of liquid crystal display panel regions are formed on first and second substrates. A thin film transistor array and a color filter array are respectively formed on the first and second substrates. Then, a liquid crystal is dispensed on the substrate, and a sealant is deposited thereon. After attaching the substrates to each other, each substrate is cut into unit liquid crystal display panel regions, thereby forming the liquid crystal display panel regions on each substrate.

More specifically, in FIG. 2A, a plurality of gate lines (not shown) are formed on the first substrate 100, and a plurality of

data lines (not shown) are formed on the first substrate 100 to cross one another, thereby defining a plurality of pixel regions. A pixel electrode (not shown) is formed at each pixel region, and the thin film transistors (not shown) are formed at each crossing point of the gate and data lines for applying signals of the data lines to each pixel electrode according to signals of the gate lines. Also, a common line (not shown) is formed for applying a voltage to a common electrode. In this case, a plurality of liquid crystal display panel regions 99 are formed on the first substrate 100. Then, a plurality of silver (Ag) dots 101 are formed on the common line of each liquid crystal display panel region 99 to provide an electrical connection to the common line. The liquid crystal 103 is appropriately dispensed on each liquid crystal display panel region 99 of the first substrate 100.

As shown in FIG. 2B, a black matrix layer (not shown), a color filter layer (not shown), and a common electrode (not shown) are formed on the second substrate 150. A column spacer 105 is formed on each liquid crystal display panel region 99 of the second substrate 150 corresponding to the gate and data lines of the first substrate 100. Then, a photo-hardening sealant 110 is formed in the periphery of each liquid crystal display panel region 99 of the second substrate 150, as shown in FIG. 2C.

In FIG. 2D, the first substrate 100 is placed under the second substrate 150. The first and second substrates 100 and 150 are loaded to a bonding apparatus with a controllable vacuum chamber. The surface of the second substrate 150, on which the sealant 110 is deposited, faces down, and then is fixed to an upper stage 170 that moves along the Z-axis direction (i.e., vertical direction). Also, the first substrate 100 is fixed to a lower stage 160 that moves along the XY-axis direction (i.e., horizontal direction). After that, the second substrate 150 fixed to the upper stage 170 and the first substrate fixed to the lower stage 160 are aligned, and the chamber of the bonding apparatus is in a vacuum condition, thereby attaching the first and second substrates 100 and 150 to each other, as shown in FIG. 2E. Then, the first and second substrates are attached to each other with a first gap.

Subsequently, as shown in FIG. 2F, gas or dry air is provided to the chamber of the bonding apparatus under a vacuum condition so as to equalize the pressure of the chamber with the atmospheric pressure. Accordingly, the first and second substrates attached by the sealant 110 are maintained under vacuum, and the circumferential area of the substrates is under the atmospheric pressure, so that the first and second substrates are pressed to each other due to a pressure difference between the inside pressure of the substrates and the atmospheric pressure. Simultaneously, the liquid crystal 103 spreads out on the entire liquid crystal display panel between the first and second substrates, thereby forming a liquid crystal layer 103a. Then, the UV-ray is irradiated to the attached substrates from the top of the second substrate 150, thereby hardening the photo-hardening sealant 110.

However, the related art liquid crystal display device according to the liquid crystal dispensing method and the method of fabricating the same have the following disadvantages.

When the dispensed amount of the liquid crystal is too much on the substrate, a uniform cell gap cannot be maintained between the first and second substrates since a buffer region, in which the liquid crystal may move around, cannot be formed in the liquid crystal display device when the substrates are attached to each other. On the other hand, when the dispensed amount of the liquid crystal is not enough on the

substrate, it is difficult to regulate the liquid crystal flow in the related art liquid crystal display device since the buffer region cannot be formed therein.

SUMMARY OF THE INVENTION

Accordingly, the present invention is directed to a liquid crystal display device and a method of fabricating the same that substantially obviates one or more of problems due to limitations and disadvantages of the related art.

Another object of the present invention is to provide a liquid crystal display device and a method of fabricating the same that enables to regulate a liquid crystal flow even when the dispensed amount of the liquid crystal is too excessive or not enough on the liquid crystal panel.

Additional features and advantages of the invention will be set forth in the description which follows and in part will be apparent from the description, or may be learned by practice of the invention. The objectives and other advantages of the invention will be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

To achieve these and other advantages and in accordance with the purpose of the present invention, as embodied and broadly described, a liquid crystal display device includes first and second substrates including a plurality of liquid crystal display panel regions, each liquid crystal display panel region having an active region and a dummy region, a sealant outside the liquid crystal display panel regions, dummy column spacer on the dummy region to control a liquid crystal flow, a buffer region between the sealant and the dummy column spacer to accommodate a liquid crystal, and a liquid crystal layer between the first and second substrates.

In another aspect of the present invention, a method of fabricating a liquid crystal display device includes forming a buffer region at a dummy region of a plurality of liquid crystal display panel regions on a first substrate, each liquid crystal display panel region having active and dummy regions, forming a liquid crystal on the active region, forming a dummy column spacer for controlling a liquid crystal flow on a dummy region of a plurality of liquid crystal display panel regions on a second substrate, each liquid crystal display panel region having active and dummy regions, forming a sealant outside the liquid crystal display panel region of one of the first and second substrates, and bonding the first and second substrates.

It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE DRAWINGS

The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this application, illustrate embodiments of the invention and together with the description serve to explain the principle of the invention.

In the drawings:

FIG. 1 is a plane view of a liquid crystal display device according to a related art liquid crystal dispensing method;

FIGS. 2A to 2G are cross-sectional views illustrating process steps of fabricating a liquid crystal display device according to the related art liquid crystal dispensing method;

FIG. 3 is a plane view of a liquid crystal display device according to a liquid crystal dispensing method of the first embodiment of the present invention;

FIG. 4A is an enlarged view of "C" portion of FIG. 3 so as to explain a sealant and a dummy column spacer in a liquid crystal display device according to the first embodiment of the present invention;

FIG. 4B is a cross-sectional view of a liquid crystal display device taken along line IVB-IVB of FIG. 4A;

FIG. 5 is an enlarged view of "B" portion of FIG. 3 so as to explain a dummy column spacer in a liquid crystal display device according to a second embodiment of the present invention;

FIG. 6 is an enlarged view of "B" portion of FIG. 3 so as to explain a dummy column spacer in a liquid crystal display device according to a third embodiment of the present invention;

FIG. 7 is an enlarged view of "B" portion of FIG. 3 so as to explain a dummy column spacer in a liquid crystal display device according to a fourth embodiment of the present invention;

FIG. 8 is a cross-sectional view of an in-plane switching mode liquid crystal display device taken along line A-A of FIG. 3 according to a fifth embodiment of the present invention;

FIG. 9 is a cross-sectional view of an in-plane switching mode liquid crystal display device taken along line A-A of FIG. 3 according to a sixth embodiment of the present invention;

FIG. 10 is a cross-sectional view of an in-plane switching mode liquid crystal display device taken along line A-A of FIG. 3 according to a seventh embodiment of the present invention;

FIG. 11 is a cross-sectional view of an in-plane switching mode liquid crystal display device taken along line A-A of FIG. 3 according to a eighth embodiment of the present invention;

FIG. 12 is a cross-sectional view of a TN mode liquid crystal display device taken along line A-A of FIG. 3 according to a ninth embodiment of the present invention;

FIG. 13 is a cross-sectional view of a TN mode liquid crystal display device taken along line A-A of FIG. 3 according to a tenth embodiment of the present invention; and

FIGS. 14A to 14G are cross-sectional views illustrating process steps of fabricating a liquid crystal display device according to a liquid crystal dispensing method of the present invention.

DETAILED DESCRIPTION OF THE ILLUSTRATED EMBODIMENTS

Reference will now be made in detail to the illustrated embodiments of the present invention, examples of which are illustrated in the accompanying drawings. Wherever possible, the same reference numbers will be used throughout the drawings to refer to the same or like parts.

FIG. 3 is a plane view of a liquid crystal display device according to a liquid crystal dispensing method of a first embodiment of the present invention. FIG. 4A is an enlarged view of "C" portion of FIG. 3 so as to explain a sealant and a dummy column spacer in the liquid crystal display device according to the first embodiment of the present invention. FIG. 4B is a cross-sectional view of the liquid crystal display device taken along line IVB-IVB of FIG. 4A.

As shown in FIG. 3, a plurality of gate and data lines 380 and 390 are formed to cross one another in an active region 420 of a thin film transistor array substrate (a first substrate) 400, thereby forming a plurality of pixel regions. A pixel electrode 412 is formed at each pixel region, and a plurality of thin film transistors (not shown) are formed at each crossing

point of the gate and data lines 380 and 390 so as to apply signals of the data lines to each pixel electrode 412 according to signals of the gate lines. Then, a liquid crystal (not shown) is dispensed on the first substrate 400. The thin film transistor array substrate 400 has a plurality of liquid crystal panels regions, each having an active region and a dummy region.

Subsequently, a black matrix layer 430, column spacers, and a photo-hardening sealant 410 are formed on the surface of a color filter array substrate (a second substrate) 450, which 10 faces to the first substrate. The color filter array substrate 450 has a plurality of liquid crystal display panel regions, each having an active region and a dummy region. At this time, the black matrix layer 430 is formed outside the active region 420 on the second substrate 450 (for example, in the periphery of 15 the active layer). The column spacers 405 are formed in the active region 420 of the second substrate 450 so as to maintain a cell gap between the first and second substrates 400 and 450. The photo-hardening sealant 410 is formed outside and surrounds the black matrix layer 430 so as to attach the first and second substrates 400 and 450 to each other. Then, a dummy column spacer 425 is formed on the black matrix layer 430. In this case, the column spacers 405 and the dummy column spacer 425 are made of an organic resin material or a photo-sensitive material. The column spacers 405 are formed on the 20 second substrate 450 to correspond to the gate and data lines of the first substrate 400 except for the pixel regions to have a thickness in the range of about 5 and 30°. The photo-hardening sealant 410 may shrink during hardening, so that a mixture of a photo-hardening sealant and a thermo-hardening sealant may be used instead of using a single photo-hardening sealant. Also, the sealant 410 may be wider or narrower than 25 the black matrix layer 430 in width.

Also, the dummy column spacer 425 has openings to effectively regulate a liquid crystal flow. For example, there may be 30 openings at three corner regions of the second substrate 450 and no opening at one corner region. Each opening has a different size. Thus, the liquid crystal flows toward three corner regions B1, B2, and B3 of the second substrate 450 from the one corner region without an opening. Also, the liquid crystal flows toward the two corner regions B2 and B3 of the second substrate 450 from the corner region B1 having an opening smaller than those of the regions B2 and B3. The liquid crystal flows toward the one of the two corner regions B2 and B3 of the second substrate 450 from whichever the 35 corner region having a smaller sized opening. And finally, the liquid crystal flows out of the liquid crystal display panel at the corner region having the largest sized opening, thereby forming a uniform liquid crystal layer. Subsequently, the first and second substrates 400 and 450 are attached to each other with the photo-hardening sealant 410. Then, UV-ray is irradiated to the first and second substrate from a top of the second substrate 450 so as to harden the photo-hardening sealant 410, thereby bonding the first and second substrates 400 and 450 to each other.

As shown in FIGS. 4A and 4B, a gate insulating layer 481 and a passivation layer 482 are selectively removed between the photo-hardening sealant 410 and the dummy column spacer 425, thereby forming a buffer region 490 for filling a liquid crystal therein. In other words, portions of the gate insulating layer 481 and the passivation layer 482 are selectively removed between the gate and data lines of the first substrate 400. At this time, the buffer region 490 is used as a space to accommodate the liquid crystal when the liquid crystal is excessively dispensed on the substrate. If the amount of the liquid crystal on the substrate is not enough, the buffer region 490 acts to guide the liquid crystal. The dummy column spacer may be formed in a different way in the liquid

crystal display device having the aforementioned structure according to the first embodiment of the present invention.

FIG. 5 is an enlarged view of "B" portion of FIG. 3 so as to explain a dummy column spacer in a liquid crystal display device according to a second embodiment of the present invention. In the liquid crystal display device according to the second embodiment of the present invention, a dummy column spacer 416 for regulating a liquid crystal flow is formed on a black matrix layer 430, and an alignment layer 414 is formed on the second substrate 450 including the dummy column spacer 416. The dummy column spacer 416 is formed to have a plurality of openings 417 and is made of an organic resin material or a photosensitive material. The dummy column spacer 416 may be formed with the column spacer 405 simultaneously, or separately. Although not shown in FIG. 5, an overcoat layer may be formed on the black matrix layer 430, and the dummy column spacer 416 may be formed on the overcoat layer.

In the liquid crystal display device according to the second embodiment of the present invention, a buffer region is formed by removing portions of the gate insulating layer and/or the passivation layer between the dummy column spacer 416 and the sealant 410. When a liquid crystal is dispensed on the substrate and spreads out, it moves to the buffer region through the openings, thereby preventing generation of bubbles even when the liquid crystal is slowly filled. Also, when the liquid crystal is excessively dispensed on the substrate, it is guided to the buffer region through the openings, thereby maintaining a uniform cell gap. The dummy column spacer 416 is formed between the active region 420 and the photo-hardening sealant 410, so that it prevents external substance generated by the photo-hardening sealant 410 from penetrating into the active region 420.

A dummy column spacer and column spacers according to a third embodiment of the present invention will be explained as follows. FIG. 6 is an enlarged view of "B" portion of FIG. 3 so as to explain a dummy column spacer in a liquid crystal display device according to the third embodiment of the present invention.

Referring to FIG. 6, the dummy column spacer 416 having the plurality of openings 417 is formed on the black matrix layer 430 so as to regulate the liquid crystal flow, and dotted column spacers 415 are formed outside the dummy column space 416 toward the sealant 410. Then, the alignment layer 414 is formed on the substrate including the dummy column spacer 416 and the dotted column spacers 415. The dummy column spacer 416 and the dotted column spacers 415 are made of an organic resin material, which may be formed simultaneously or separately. The openings 417 may be formed successively or not.

As mentioned above, the buffer region is formed between the dotted column spacers 415 and the sealant 410 by removing portions of the gate insulating layer and the passivation layer. In the liquid crystal display device according to the third embodiment of the present invention, the dotted column spacer 415 are additionally formed to the dummy column spacer 416 having the openings at corner regions, thereby regulating the liquid crystal flow more effectively. Furthermore, it prevents foreign substance generated by the photo-hardening sealant 410 from penetrating into the active region 420.

A dummy column spacer and column spacers according to a fourth embodiment of the present invention will be explained as follows. FIG. 7 is an enlarged view of "B" portion of FIG. 3 so as to explain a dummy column spacer in a liquid crystal display device according to the fourth embodiment of the present invention. Referring to FIG. 7, the dummy

column space 416 having the openings 417 is formed on the black matrix layer 430 so as to regulate the liquid crystal flow, and dotted column spacers 415 are formed inside the dummy column spacer 416 toward the active region 420. Then, the alignment layer 414 is formed on the second substrate including the dummy column spacer 416 and the dotted column spacers 415. An overcoat layer (not shown) may be formed on the black matrix layer 430, and then the dummy column spacer 416 and the dotted column spacers 415 may be formed on the overcoat layer. At this time, the dummy column spacer 416 and the dotted column spacers 415 of an organic resin material may be formed simultaneously or separately. The openings 417 may be formed successively or not.

As mentioned above, the buffer region is formed between the dotted column spacers 415 and the sealant 410 by selectively removing portions of the gate insulating layer and the passivation layer. In the liquid crystal display device according to the fourth embodiment of the present invention, the dotted column spacers 415 are additionally formed to the dummy column spacer 416, thereby regulating the liquid crystal flow more effectively. In addition, it prevents foreign substances generated by the photo-hardening sealant 410 from penetrating into the active region 420.

The dummy column spacer for regulating the liquid crystal flow varies with a different structure according to the mode of the liquid crystal display device.

An in-plane switching mode liquid crystal display device according to a fifth embodiment of the present invention will be explained with reference to FIG. 8. In FIG. 8, a metal layer 303 is deposited on the first substrate 400, and a common electrode 313 and a gate line (not shown) having a gate electrode 309 are formed on the first substrate 400 by photolithography. A gate insulating layer 320 is formed on the entire surface of the first substrate 400 including the gate electrode 309 and the common electrode 313. Then, a semiconductor layer 315 is formed on the gate insulating layer 320 on the gate electrode 309.

After that, the data line 390 having source and drain electrodes 316 and 317 on both sides of the semiconductor layer 315 is formed on the gate insulating layer 320, and a pixel electrode 314 is formed in parallel to the common electrode 313 for being connected to the drain electrode in a pixel region. A passivation layer 325 is formed on the first substrate 400 including the pixel electrode 314, and the buffer region 490 is formed by selectively removing portions of the gate insulating layer 320 and the passivation layer 325 between the sealant 410 and the dummy column spacer 416/425 with a mask. Then, a first alignment layer 331 is formed on the passivation layer 325.

Next, the black matrix layer 430 and a color filter layer 322 are formed on the second substrate 450, and an overcoat layer 323 is formed on the entire surface of the second substrate 450 so as to reduce a step coverage between the black matrix layer 430 and the color filter layer 322. At this time, the overcoat layer 323 is formed of one of Cr, CrO_x, and black resin.

The column spacer 405 is formed in the active region of the second substrate 450, and the dummy column spacer 416/425 is formed in the periphery (i.e., the outside) of the active region on the overcoat layer 323. The second alignment layer 414 is formed on the column spacer 405 and the dummy column spacer 425, and then the photo-hardening sealant 410 is deposited thereon. Subsequently, the liquid crystal is dispensed on the first substrate 400 having the alignment layer, and the second substrate 450 is positioned above the first substrate 400. The first and second substrates 400 and 450 are attached to each other. At this time, the sealant may be formed on the first substrate 400.

In the in-plane switching mode liquid crystal display device according to the fifth embodiment of the present invention, a portion of the overcoat layer 323 is removed from the second substrate corresponding to the photo-hardening sealant 410. However, the overcoat layer 323 is formed on the entire surface of the second substrate including the photo-hardening sealant 410 in the in-plane switching mode liquid crystal display device according to a sixth embodiment of the present invention, as shown in FIG. 9.

In detail, the black matrix layer 430 and the color filter layer 322 are formed on the second substrate 450, and the overcoat layer 323 is formed on the entire surface of the second substrate 450 so as to reduce a step coverage between the black matrix layer 430 and the color filter layer 322. Then, the column spacer 405 and the dummy column spacer 416/425 are respectively formed in the active region and the periphery (i.e., the outside) of the active region without patterning the overcoat layer 323. The second alignment layer 414 is formed on the column spacer 405 and the dummy column spacer 416/425, and the photo-hardening sealant 410 is deposited on one of the first and second substrates 400 and 450. After dispensing the liquid crystal on the first substrate 400, the second substrate 450 is disposed on the first substrate 400.

Referring to FIG. 10, the in-plane switching mode liquid crystal display device according to a seventh embodiment of the present invention has same structure as that according to the fifth and sixth embodiments of the present invention except for that the overcoat layer 323 is patterned on the second substrate 450 and a dummy color filter layer 322a is connected to the overcoat layer 323.

Referring to FIG. 11, the in-plane switching mode liquid crystal display device according to an eighth embodiment of the present invention has same structure as that according to the fifth embodiment of the present invention except for that there is a gap between the dummy column spacer 416/425 and the first substrate 400. The liquid crystal flows into the buffer region 490 between the photo-hardening sealant 410 and the dummy column spacer 416/425 in the active region 420 since the gap is formed between the dummy column spacer and the first substrate, thereby regulating the liquid crystal flow. At this time, the buffer region 490 is formed by selectively removing portions of the gate insulating layer 320 and the passivation layer 325 between the dummy column spacer 416/425 and the photo-hardening sealant 410.

As mentioned above, in the in-plane switching mode liquid crystal display devices according to the fifth, sixth, seventh, and eighth embodiments of the present invention, the common electrode 313 may be formed on the different layer from the pixel electrode 314, or both of them may be formed on the gate insulating layer 320 or on the same layer as the source and drain electrode 316 and 317. Also, the common electrode 313 may be formed on the same layer as the gate electrode 309, and the pixel electrode 314 may be formed on the passivation layer 325. The common electrode 313 and the pixel electrode 314 may be formed on the passivation layer 325. Accordingly, the common electrode 313 and pixel electrode 314 may be disposed in various ways.

During forming the buffer region 490, both the gate insulating layer 320 and the passivation layer 325 may be removed, or only passivation layer 325 may be removed. At this time, if the pixel electrode 314 is formed on the passivation layer 325, the buffer region 490 is formed by removing the passivation layer 325 between the sealant 410 and the dummy column spacer 416/425 so as to connect the pixel electrode 314 to the drain electrode 317 of the thin film

transistor when a contact hole is formed in the passivation layer 325. The buffer region 490 may be additionally formed with a mask.

FIG. 12 is a cross-sectional view of a TN mode liquid crystal display device taken along line A-A of FIG. 3 according to a ninth embodiment of the present invention. FIG. 13 is a cross-sectional view of a TN mode liquid crystal display device taken along line A-A of FIG. 3 according to a tenth embodiment of the present invention.

The TN mode liquid crystal display device according to the ninth embodiment of the present invention will be explained with reference to FIG. 12. Referring to FIG. 12, a metal layer is deposited on the first substrate 400, and the gate line 380 having the gate electrode 309 is formed on the first substrate 400 by photolithography. Then, the gate insulating layer 320 is formed on the entire surface of the first substrate 400 including the gate electrode 309 and the gate line 380. The semiconductor layer 315 is formed on the gate insulating layer 320 above the gate electrode 309, and a data line (not shown) having the source and drain electrodes 316 and 317 on both sides of the semiconductor layer 315 is formed on the first substrate 400. The passivation layer 325 is formed on the entire surface of the first substrate 400 including the source and drain electrodes 316 and 317. After forming a contact hole on the drain electrode 317, the pixel electrode 314 is formed in the pixel region for being connected to the drain electrode 317 through the contact hole. At this time, portions of the gate insulating layer 320 and the passivation layer 325 between the sealant 410 and the dummy column spacer 416/425 are selectively removed during forming the contact hole, thereby forming the buffer region 490. The buffer region 375 may be formed by removing only the passivation layer 325. The first alignment layer 331 is formed on the entire surface of the first substrate 400 including the pixel electrode 314.

The second substrate 450 includes the black matrix layer 430, the color filter layers 322, the common electrode 326 and the photo-hardening sealant 410. The black matrix layers 430 are formed on the second substrate in order to prevent light leakage out of the liquid crystal display panel, and the color filter layers are formed between the black matrix layers 430. The common electrode 326 is formed on the entire surface of the second substrate 450 including the color filter layer 322 and the black matrix layer 430. Thereafter, the column spacer 405 is formed in the active region 420 of the second substrate 450. Simultaneously, the dummy column spacers 416/425 are formed on the common electrode 326 in the periphery (i.e., the outside) of the active region 420. The second alignment layer 414 is formed on the column spacer 405 and the dummy column spacer 425, and the photo-hardening sealant 410 is deposited in the periphery of the dummy column spacer 416/425. The liquid crystal is dispensed on the first substrate 400 having the alignment layer, and the second substrate 450 is disposed on the first substrate 400 so as to attach the first and second substrates to each other.

The embodiments of the present invention may be applied to various modes of the liquid crystal display, such as a vertical alignment (VA) mode, an optically controlled birefringence (OCB) mode, a ferroelectric liquid crystal (FLC) mode, and a reflective type mode. As shown in FIGS. 8 to 10, the overcoat layer may be formed on the color filter layer 322.

The TN mode liquid crystal display device according to the tenth embodiment of the present invention has same structure as that according to the ninth embodiment of the present invention except for that a gap is formed between the dummy column spacer 416/425 and the first substrate 400 with the predetermined gap. Accordingly, the liquid crystal flows into the buffer region 490 between the photo-hardening sealant

410 and the dummy column spacer 416/425 in the active region 420, thereby regulating the liquid crystal flow 413. At this time, the buffer region 490 is formed by selectively removing portions of the gate insulating layer 320 and the passivation layer 325 between the dummy column spacer 416/425 and the photo-hardening sealant 410.

A method of fabricating a liquid crystal display panel by using a liquid crystal dispensing method will be explained with reference to FIGS. 14A to 14G. A plurality of liquid crystal display panel regions are formed on the first and second substrates, and then a thin film transistor array and a color filter array are respectively formed on the substrates. On completing dispensing the liquid crystal and forming the sealant on the substrate, the substrate is cut into unit liquid crystal display panel regions.

Referring to FIG. 14A, a plurality of gate and data lines (not shown) are formed to cross one another on the first substrate 400, thereby defining a plurality of pixel regions. A pixel electrode (not shown) is formed at each pixel region, and a plurality of thin film transistors (not shown) are formed at each crossing point of the gate and data lines for applying signals of the data lines to each pixel electrode according to signals of the gate lines. Also, a common line pattern (not shown) is formed for applying a voltage to a common electrode. At this time, a plurality of liquid crystal display panel regions 399 are formed on the substrate. Then, a plurality of silver (Ag) dots 401 are formed on the common line pattern of each liquid crystal display panel region for being electrically connected to the common line pattern, and a liquid crystal 403 is appropriately dispensed on each liquid crystal display panel region 399.

In FIG. 14B, a black matrix layer (not shown), a color filter layer (not shown), and a common electrode (not shown) are formed on the second substrate 450. A column spacer 405 is formed on each liquid crystal display panel region 399 of the second substrate corresponding to the gate and data lines of the first substrate 400. Then, the photo-hardening sealant 410 is deposited in the periphery of each liquid crystal display panel region 399 of the second substrate 450, as shown in FIG. 14C.

As shown in FIG. 14D, the first substrate is positioned at the lower part, and the second substrate is disposed over the first substrate 400. The first and second substrates 400 and 450 are loaded to a bonding apparatus having a controllable vacuum chamber. The second substrate 450 is fixed to an upper stage 470 that moves along the Z-axis direction (i.e., vertical direction). Also, the first substrate 400 is fixed to a lower stage 460 that moves along the XY-axis direction (i.e., horizontal direction). Subsequently, as shown in FIG. 14E, the second substrate 450 fixed to the upper stage 470 and the first substrate fixed to the lower stage 460 are aligned, and the chamber of the bonding apparatus becomes in a vacuum condition, thereby attaching the first and second substrates 400 and 450 to each other. The first and second substrates are attached to each other with a first gap.

Referring to FIG. 14F, gas or dry air is provided to the chamber of the bonding apparatus under a vacuum condition so as to make a pressure of the chamber to the atmospheric pressure. Accordingly, the first and second substrates 400 and 450 attached by the sealant 410 are maintained under a vacuum condition, and the circumferential region of the substrates become under the atmospheric pressure, so that the first and second substrates are pressed to each other due to a pressure difference between the inside pressure of the substrates 400 and 450 and the atmospheric pressure. Simultaneously, the liquid crystal 403 spreads out on the entire liquid crystal display panel between the first and second substrates

400 and 450, thereby forming a liquid crystal layer 403a. After that, the UV-ray is irradiated to only photo-hardening sealant from the top of the second substrate 450 so as to harden the photo-hardening sealant 410, as shown in FIG. 14G.

In the liquid crystal display device and method of fabricating the same according to the embodiments of the present invention, it is possible to form the liquid crystal, the sealant, the column spacer, the and dummy column spacer on any one of the first and second substrates. In this case, the substrate having the liquid crystal is disposed at the lower stage of the bonding apparatus, and the other substrate is disposed at the upper stage of the bonding apparatus.

As mentioned above, the liquid crystal display device and method of fabricating the same has the following advantages.

The buffer region is formed by removing portions of the gate insulating layer and the passivation layer between the dummy column spacer and the sealant, so as to regulate the liquid crystal flow. If the liquid crystal is excessively dispensed on the substrate, the liquid crystal flows into the buffer region, thereby accommodating the liquid crystal on the liquid crystal display panel appropriately. Meanwhile, if the amount of the liquid crystal on the substrate is not enough, the liquid crystal moves from other regions to the buffer region, thereby filling the liquid crystal on the liquid crystal display panel appropriately.

It will be apparent to those skilled in the art that various modifications and variations can be made in the liquid crystal display device and the method of fabricating the same of the present invention without departing from the spirit or scope of the inventions. Thus, it is intended that the present invention covers the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

What is claimed is:

1. A method of fabricating a liquid crystal display device, comprising:
 - forming a buffer region at a dummy region of a plurality of liquid crystal display panel regions on a first substrate, each liquid crystal display panel region having active and dummy regions;
 - forming a liquid crystal on the active region;
 - forming a dummy column spacer controlling a liquid crystal flow on the dummy region of a plurality of liquid crystal display panel regions on a second substrate, each liquid crystal display panel region having active and dummy regions;
 - forming a sealant outside the liquid crystal display panel region of one of the first and second substrates; and bonding the first and second substrates.
2. The method of claim 1, wherein the sealant is one of a photo-hardening sealant and a mixture of a photo-hardening sealant and a thermo-hardening sealant.
3. The method of claim 1, further comprising forming a column spacer on the active region of the second substrate.
4. The method of claim 3, wherein the column spacer and the dummy column spacer are formed of the same material.
5. The method of claim 4, wherein the column spacer and the dummy column spacer are formed at the same time.
6. The method of claim 1, wherein the dummy column spacer surrounds the active region.
7. The method of claim 6, wherein the dummy column spacer has at least one opening at corner regions.
8. The method of claim 6; wherein the dummy column spacer is formed to have a shape matching the shape of the liquid crystal display device.

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9. The method of claim 6, wherein the dummy column spacer has at least one opening—at corner regions.

10. The method of claim 9, wherein the at least one opening has a different size.

11. The method of claim 9, wherein the at least one opening is formed at a different time.

12. The method of claim 1, further comprising forming a plurality of dotted column spacers in a periphery of the dummy column spacer.

13. The method of claim 12, wherein the dotted column spacers are formed between the active region and the dummy column spacer.

14. The method of claim 12, wherein the dotted column spacers are formed between the sealant and the dummy column spacer.

15. The method of claim 1, wherein the buffer region is formed by selectively removing portions of a passivation layer or passivation and gate insulating layers.

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16. The method of claim 15, wherein the selectively removed portions of the passivation layer or the passivation and gate insulating layers are located between the gate lines or the data lines.

17. The method of claim 1, wherein the dummy column spacer is separated from the first substrate by a gap when the first and second substrates are bonded to each other.

18. The method of claim 1, further comprising a black matrix layer and an overcoat layer on the second substrate.

19. The method of claim 1, wherein the bonding the first and second substrates includes:

loading the first and second substrates to a bonding apparatus;

forming a vacuum condition in the bonding apparatus;

aligning the first and second substrates;

attaching the first and second substrates to each other by the sealant; and

pressing the first substrate to the second substrate by maintaining the bonding apparatus as the vacuum state.

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专利名称(译)	液晶显示装置及其制造方法		
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摘要(译)

在本发明中公开了一种液晶显示装置及其制造方法。液晶显示装置包括第一和第二基板，第一和第二基板包括多个液晶显示板区域，每个液晶显示板区域具有有源区域和虚设区域，液晶显示板区域外部的密封剂，虚设柱状间隔物用于控制液晶流动的虚设区域，密封剂和虚设柱状间隔物之间的缓冲区域以容纳液晶，以及在第一和第二基板之间的液晶层。

