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(54) **LIQUID CRYSTAL DISPLAY DEVICE AND METHOD OF DRIVING THE SAME**

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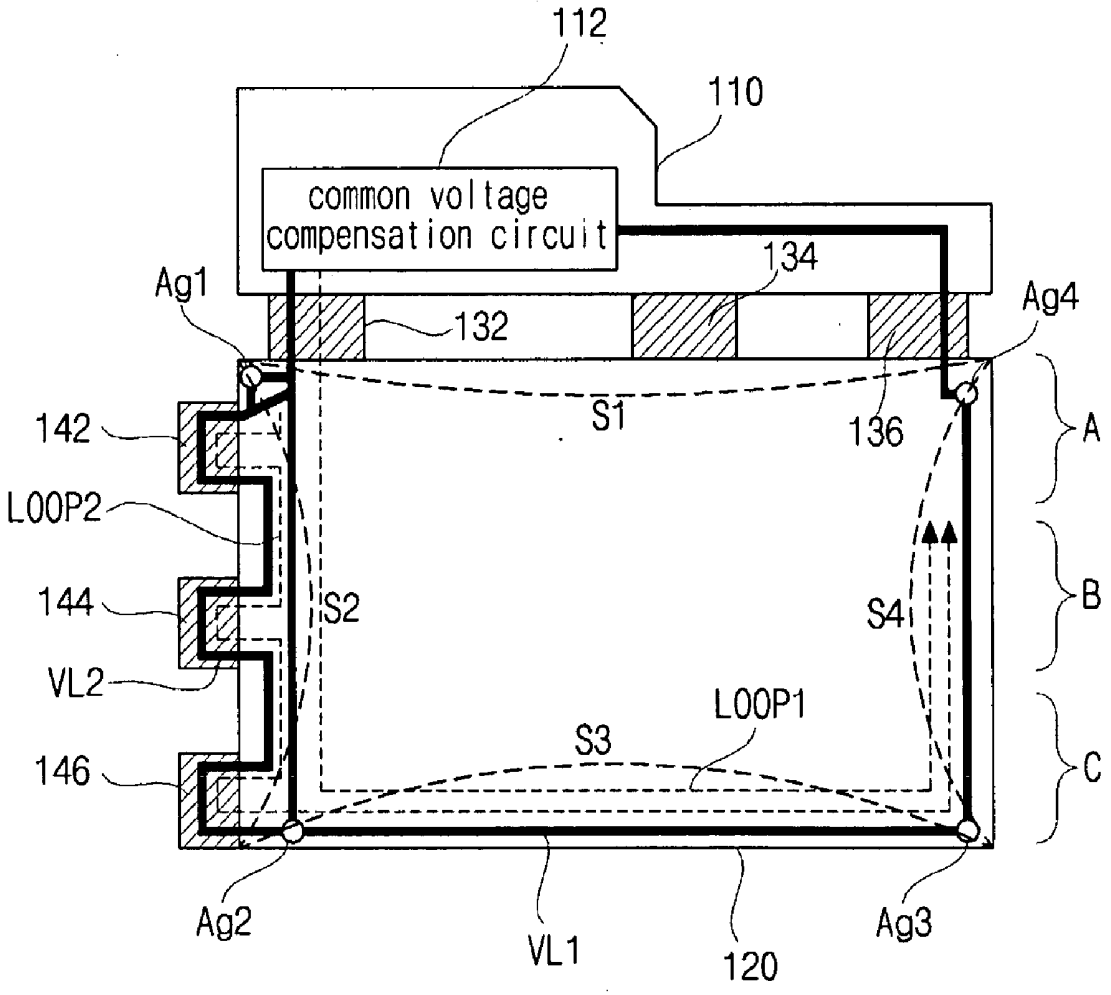
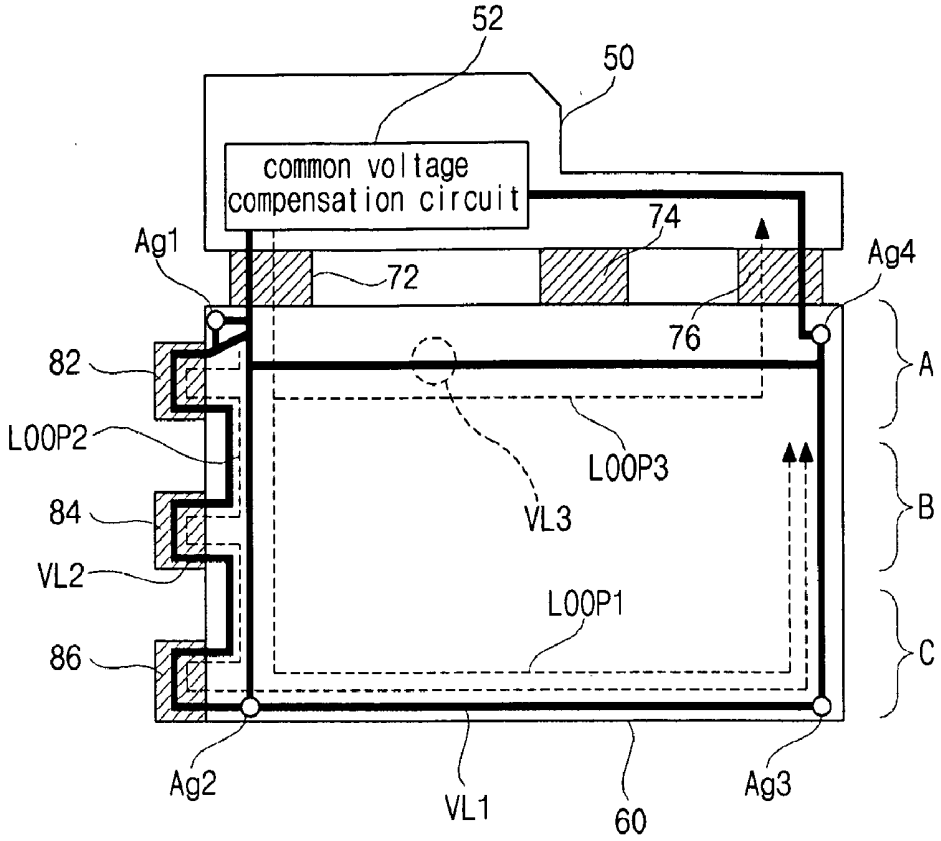
(57) **ABSTRACT**

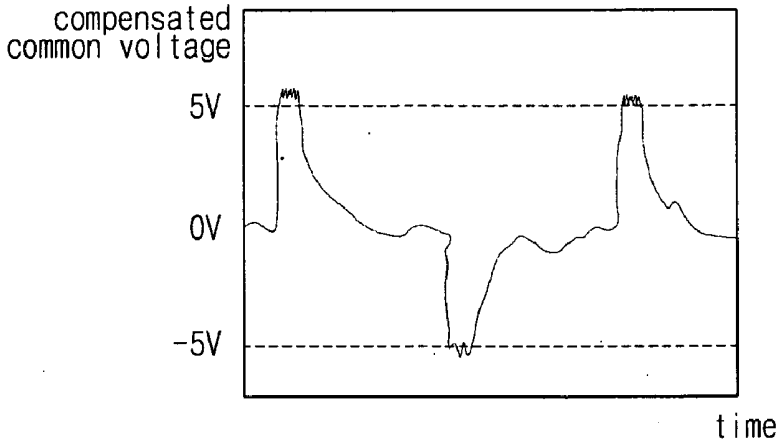
A liquid crystal display device includes: a first substrate having a display area and a non-display area surrounding the display area, the first substrate including first, second, third and fourth sides; a first common line formed in the non-display area corresponding to the second, third and fourth sides of the first substrate; a second substrate facing and spaced apart from the first substrate; a common electrode formed on an entire surface of the second substrate; and a liquid crystal layer between the first and second substrates.





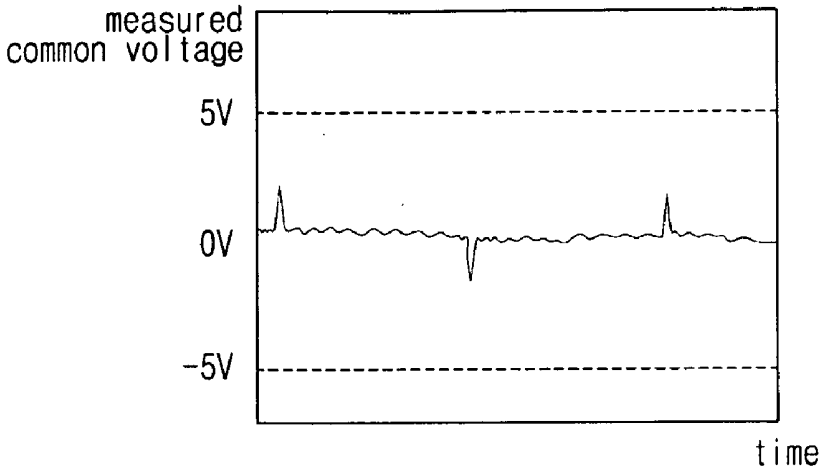
(related art)

FIG. 1



(related art)

FIG. 2A



(related art)

FIG.2B

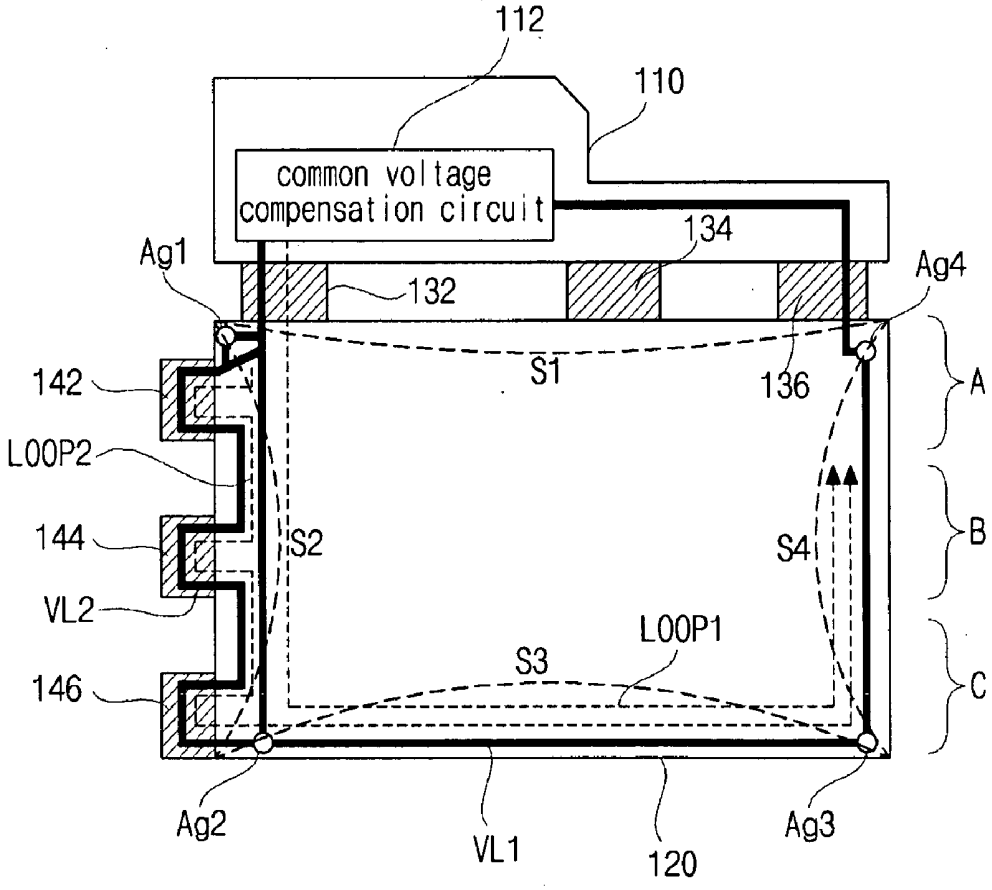


FIG. 3

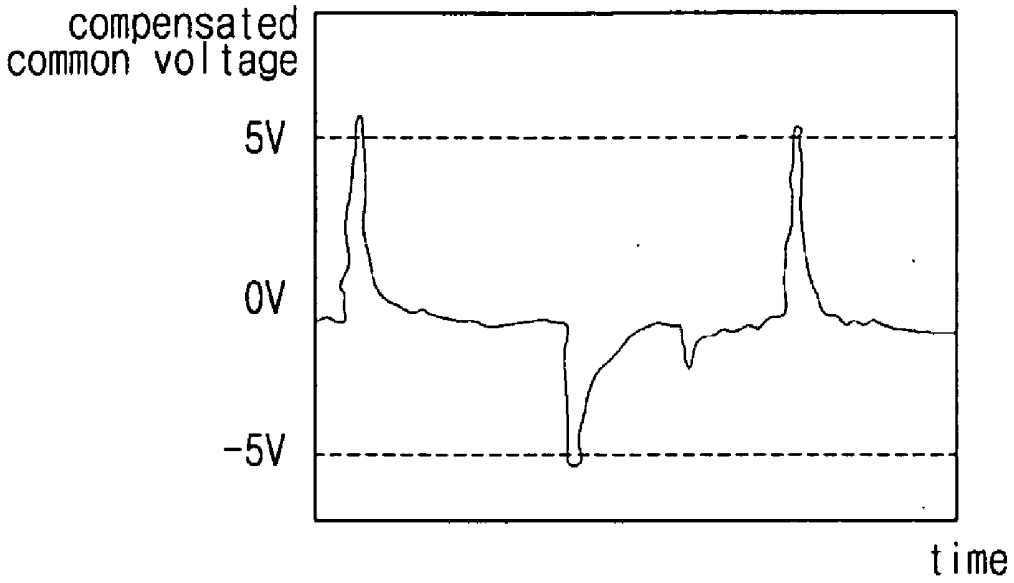

compensated
common voltage
5V
0V
-5V
time

FIG. 4A

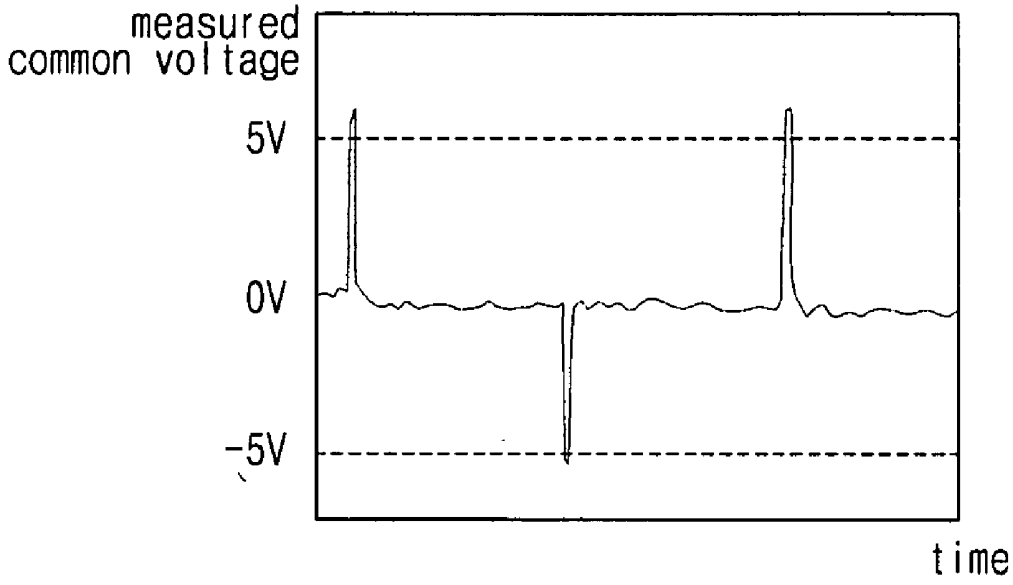

measured
common voltage
5V
0V
-5V
time

FIG. 4B

LIQUID CRYSTAL DISPLAY DEVICE AND METHOD OF DRIVING THE SAME

[0001] This application claims the benefit of Korean Patent Application No. 2007-0119283, filed on Nov. 21, 2007, which is hereby incorporated by reference in its entirety.

TECHNICAL FIELD

[0002] The present application relates to a liquid crystal display device, and more particularly, to a liquid crystal display device having a common line and a common voltage compensation circuit unit and a method of driving the liquid crystal display device.

BACKGROUND

[0003] Among various display devices, a liquid crystal display (LCD) device has been widely used in notebook computers, office automation apparatus, and audio/video apparatus because of their superior operational characteristics such as light weight and low power consumption. Specifically, an active matrix LCD (AM-LCD) device that employs switching elements and pixel electrodes arranged in a matrix structure is the subject of significant research and development because of its high resolution and superior suitability for displaying moving images.

[0004] For the purpose of displaying images in a liquid crystal panel of an LCD device, a common voltage is supplied to each pixel so that liquid crystal molecules rotate according to a voltage difference between a data signal supplied from a data driving unit and the common voltage.

[0005] FIG. **1** is a view showing a liquid crystal display device according to the related art. In FIG. **1**, a liquid crystal display device includes a liquid crystal panel **60**, a driving printed circuit board (PCB) unit **50**, a plurality of gate driving units **82**, **84** and **86** and a plurality of data driving units **72**, **74** and **76**. The driving PCB unit **50** includes a plurality of driving circuits such as a timing controller, a power supply and a gamma reference voltage generator and a common voltage compensation circuit unit **52** for supplying and compensating a common voltage. The common voltage compensation circuit **52** receives the common voltage passing through the liquid crystal panel **60** and compares the common voltage with a reference voltage or an initial common voltage. Further, the common voltage compensation circuit **52** generates a compensated common voltage on the basis of a difference between the common voltage and the reference voltage and supplies the compensated common voltage to the liquid crystal panel **60** again.

[0006] The liquid crystal panel **60** includes first and second substrates (not shown) and a liquid crystal layer (not shown) between the first and second substrates. A display area and a non-display area surrounding the display area are defined in liquid crystal panel **60** and the display area includes a plurality of pixel regions. A plurality of conductive dots, e.g., first to fourth conductive dots Ag**1** to Ag**4** including silver (Ag) are formed at edge portions of the non-display area of the first substrate. The common voltage supplied by the common voltage compensation circuit **52** is transmitted to a common electrode on the second substrate through the first to fourth conductive dots Ag**1** to Ag**4**.

[0007] The plurality of data driving units may include first, second and third data driving units **72**, **74** and **76** and the plurality of gate driving units may include first, second and third gate driving units **82**, **84** and **86**. Each of the first, second and third data driving units **72**, **74** and **76** includes a data driving integrated circuit (IC) and is connected to the driving PCB unit **50** and one side of the liquid crystal panel **60**. In addition, each of the first, second and third gate driving units **82**, **84** and **86** includes a gate driving IC and is connected to the other side of the liquid crystal panel **60**. For example, each of the first, second and third data driving units **72**, **74** and **76** and the first, second and third gate driving units **82**, **84** and **86** may include one of tape carrier package (TCP) and flexible printed circuit (FPC).

[0008] The common voltage outputted from the common voltage compensation circuit **52** is supplied to the liquid crystal panel **60** through the first data driving unit **72**, and is transmitted to the first to fourth conductive dots Ag**1** to Ag**4** though first, second and third common lines VL**1**, VL**2** and VL**3**. The first common line VL**1** is formed in the non-display area corresponding to three sides of the liquid crystal panel **60**. In addition, the second common line VL**2** is formed in the first, second and third gate driving units **82**, **84** and **86**, and the third common line VL**3** is formed in the non-display area corresponding to one side of the liquid crystal panel **60** adjacent to the first, second and third data driving units **72**, **74** and **76**. The common voltage transmitted to the first to fourth conductive dots Ag**1** to Ag**4** is applied to a common electrode of the second substrate. The common voltage as a feedback voltage is inputted to the common voltage compensation circuit **52** through the third data driving unit **76**, and the common voltage compensation circuit **52** generates the compensated common voltage using the common voltage through the liquid crystal panel **60**. The common voltage compensation circuit **52** supplies the compensated common voltage to the liquid crystal panel **60** again.

[0009] The first common line VL**1** forms a first loop LOOP**1** as a path for the common voltage. In addition, portions of the first common line VL**1** and the second common line VL**2** forms a second loop LOOP**2** as a path for the common voltage, and the third common line VL**3** between the first and fourth conductive dots Ag**1** and Ag**4** forms a third loop LOOP**3** as a path for the common voltage. Accordingly, the common voltage is transmitted to the first to fourth conductive dots Ag**1** to Ag**4** through the first, second and third loops LOOP**1**, LOOP**2** and LOOP**3** of the first substrate and is applied to the common electrode of the second substrate.

[0010] However, since the first, second and third common lines VL**1**, VL**2** and VL**3** have a different length and a different arrangement, the first, second and third loops LOOP**1**, LOOP**2** and LOOP**3** have a different resistance and a different capacitance. For example, the common voltage through the third loop LOOP**3** having a minimum length may have a minimum voltage drop. As a result, most of a current for the common voltage flows through the third loop LOOP**3** and the compensated common voltage generated by the common voltage compensation circuit **52** reflects only the voltage drop in third loop LOOP**3** and in a portion of the common electrode between the first and fourth conductive dots Ag**1** and Ag**4**. Accordingly, the common voltage compensation circuit **52** compensates the voltage drop through an upper portion A of the liquid crystal panel **60** and does not compensate the voltage drop through middle and lower portions B and C of the liquid crystal panel **60**. The voltage drop in the common

voltage at the middle and lower portions B and C causes deterioration of display quality such as a greenish phenomenon.

[0011] In addition, the compensated common voltage is not completely applied to the entire portion of the common electrode due to the voltage drop. FIGS. **2**A and **2**B are views showing a compensated common voltage and a measured common voltage, respectively, of a liquid crystal display device according to the related art. In FIG. **2**A, the compensated voltage generated by the common voltage compensation circuit **52** (of FIG. **1**) changes from about −6V to about +6V according to a time. Although the compensated common voltage is supplied to the liquid crystal panel through the first data driving unit **72** (of FIG. **1**), most of the current for the compensated common voltage flows through the third loop LOOP**3** (of FIG. **1**) from the first conductive dot Ag**1** to the fourth conductive dot Ag**4**. As a result, the compensated common voltage is not properly applied to the middle and lower portions B and C of the common electrode due to the voltage drop in the first and second loops LOOP**1** and LOOP**2**. Specifically, as shown in FIG. **2**B, the measured common voltage at the lower portion C changes from about −2V to about +2V even when the compensated voltage of FIG. **2**A is applied. As a result, the common voltage is not completely compensated at the entire portion of the common electrode, and deterioration such as a greenish phenomenon is caused.

SUMMARY

[0012] Accordingly, the present invention is directed to a liquid crystal display device and a method of driving the liquid crystal display device that substantially obviates one or more of the problems due to limitations and disadvantages of the related art. An object of the present invention is to provide a liquid crystal display device where a common voltage is compensated by uniformly reflecting a voltage drop in common lines and a method of driving the liquid crystal display device.

[0013] A liquid crystal display device includes: a first substrate having a display area and a non-display area surrounding the display area, the first substrate including first, second, third and fourth sides; a first common line formed in the non-display area corresponding to the second, third and fourth sides of the first substrate; a second substrate facing and spaced apart from the first substrate; a common electrode formed on an entire surface of the second substrate; and a liquid crystal layer between the first and second substrates.

[0014] In another aspect, a method of driving a liquid crystal display device including a first substrate, a second substrate and a liquid crystal layer between the first and second substrates includes: supplying a common voltage to a first common line on a first substrate, the first substrate including first, second, third and fourth sides and the first common line corresponding to the second, third and fourth sides; generating a compensated common voltage using the common voltage through the first common line; and supplying the compensated common voltage to the first common line.

BRIEF DESCRIPTION OF THE DRAWINGS

[0015] The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate embodiments of the invention.

[0016] FIG. **1** is a view showing a liquid crystal display device according to the related art;

[0017] FIGS. **2**A and **2**B are views showing a compensated common voltage and a measured common voltage, respectively, of a liquid crystal display device according to the related art;

[0018] FIG. **3** is a view showing a liquid crystal display device according to an embodiment of the present invention; and

[0019] FIGS. **4**A and **4**B are views showing a compensated common voltage and a measured common voltage, respectively, of a liquid crystal display device according to an embodiment of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0020] Reference will now be made in detail to embodiments which are illustrated in the accompanying drawings. Wherever possible, similar reference numbers will be used to refer to the same or similar parts.

[0021] FIG. **3** is a view showing a liquid crystal display device according to an embodiment of the present invention. In FIG. **3**, a liquid crystal display (LCD) device includes a liquid crystal panel **120**, a driving printed circuit board (PCB) unit **110**, a plurality of gate driving units **132**, **134** and **136** and a plurality of data driving units **142**, **144** and **146**. The driving PCB unit **110** includes a plurality of driving circuits such as a timing controller, a power supply and a gamma reference voltage generator and a common voltage compensation circuit unit **112** for supplying and compensating a common voltage. The common voltage compensation circuit **112** receives the common voltage passing through the liquid crystal panel **120** and compares the common voltage with a reference voltage or an initial common voltage. Further, the common voltage compensation circuit **112** generates a compensated common voltage on the basis of a difference between the common voltage and the reference voltage and supplies the compensated common voltage to the liquid crystal panel **120** again.

[0022] The liquid crystal panel **120** includes first and second substrates (not shown) facing and spaced apart from each other and a liquid crystal layer (not shown) between the first and second substrates. A display area and a non-display area surrounding the display area are defined in liquid crystal panel **120** and the display area includes a plurality of pixel regions each having a thin film transistor (not shown) and a pixel electrode (not shown) connected to the thin film transistor. Each pixel region is defined by a gate line (not shown) and a data line (not shown) crossing each other and connected to the thin film transistor. In addition, the liquid crystal panel **120** includes first, second, third and fourth sides S**1**, S**2**, S**3** and S**4**. A plurality of conductive dots, e.g., first to fourth conductive dots Ag**1** to Ag**4** including silver (Ag) are formed in the non-display area at corner portions of the first substrate. Since the plurality of conductive dots connects a first common line VL**1** of the first substrate and a common electrode of the second substrate, the common voltage supplied by the common voltage compensation circuit **112** is transmitted to the common electrode on the second substrate through the first to fourth conductive dots Ag**1** to Ag**4**. The common electrode may be formed on an entire surface of the second substrate and may include a transparent conductive material such as indium-tin-oxide (ITO) and indium-zinc-oxide (IZO) in a twisted nematic (TN) mode LCD device.

[0023] The plurality of data driving units may include first, second and third data driving units **132**, **134** and **136**, and the plurality of gate driving units may include first, second and third gate driving units **142**, **144** and **146**. Each of the first, second and third data driving units **132**, **134** and **136** includes a data driving integrated circuit (IC) and is connected to the driving PCB unit **110** and the first side S**1** of the liquid crystal panel **120**. In addition, each of the first, second and third gate driving units **142**, **144** and **146** includes a gate driving IC and is connected to the second side S**2** adjacent to the first side S**1** of the liquid crystal panel **120**. For example, each of the first, second and third data driving units **132**, **134** and **136** and the first, second and third gate driving units **142**, **144** and **146** may include one of tape carrier package (TCP) and flexible printed circuit (FPC).

[0024] The common voltage outputted from the common voltage compensation circuit **112** is supplied to the liquid crystal panel **120** through the first data driving unit **132**, and is transmitted to the first to fourth conductive dots Ag**1** to Ag**4** though first and second common lines VL**1** and VL**2**. The first common line VL**1** is formed in the non-display area corresponding to the second, third and fourth sides S**2**, S**3** and S**4** of the liquid crystal panel **120** to have a U shape. In addition, the second common line VL**2** is formed in the first, second and third gate driving units **142**, **144** and **146** and the second side S**2** of the first substrate to have a zigzag shape. The non-display area corresponding to the first side S**1** of the liquid crystal panel **120** adjacent to the common voltage compensation circuit **112** does not include a common line. Accordingly, the first common line VL**1** is exclusively formed in the non-display area corresponding to three sides except one side of the liquid crystal panel **120** adjacent to the common voltage compensation circuit **112**.

[0025] The common voltage transmitted to the first to fourth conductive dots Ag**1** to Ag**4** is applied to the common electrode of the second substrate. The common voltage as a feedback voltage is inputted to the common voltage compensation circuit **112** through the third data driving unit **136**, and the common voltage compensation circuit **112** generates the compensated common voltage using the common voltage through the liquid crystal panel **120**. The common voltage compensation circuit **112** supplies the compensated common voltage to the liquid crystal panel **120** again.

[0026] The first common line VL**1** forms a first loop LOOP**1** as a path for the common voltage. In addition, portions of the first common line VL**1** corresponding to the third and fourth sides S**3** and S**4** and the second common line VL**2** form a second loop LOOP**2** as a path for the common voltage. The common voltage is transmitted to the first to fourth conductive dots Ag**1** to Ag**4** through the first and second loops LOOP**1** and LOOP**2** of the first substrate and is applied to the common electrode of the second substrate. Since the first and second loops LOOP**1** and LOOP**2** have the substantially same resistance and capacitance as each other, a current for the common voltage flows through the first loop LOOP**1** corresponding to the second, third and fourth sides S**2**, S**3** and S**4** and through the second loop LOOP**2** corresponding to the first, second and third gate driving units **142**, **144** and **146** and third and fourth sides S**3** and S**4**. As a result, the compensated common voltage generated by the common voltage compensation circuit **112** reflects the voltage drop in first and second loops LOOP**1** and LOOP**2** and in the common electrode through the first and fourth conductive dots Ag**1** and Ag**4** and through the second and third conductive dots Ag**2** and Ag**3**. Therefore, the common voltage compensation circuit **112** compensates the voltage drop through an entire portion including upper, middle and lower portions A, B and C of the liquid crystal panel **120**, and deterioration in display quality such as a greenish phenomenon is prevented at the middle and lower portions B and C of the liquid crystal panel **120**.

[0027] In the LCD device according to an embodiment of the present invention, since the liquid crystal panel **120** does not include a loop directly connecting the first and fourth conductive dots Ag**1** and Ag**4** at the first side S**1**, the common voltage is uniformly applied to the first to fourth conductive dots Ag**1** to Ag**4** and the entire portion of the common electrode through the first and second loops LOOP**1** and LOOP**2**. As a result, the common voltage inputted to the common voltage compensation circuit **112** reflects a voltage drop in the first and second common lines VL**1** and VL**2** and the entire portion of the common electrode. Since the common voltage compensation circuit **112** generates the compensated common voltage on the basis of the common voltage, the compensated common voltage reflects the voltage drop in the whole portion of the liquid crystal panel **120** and deterioration such as a greenish phenomenon is prevented.

[0028] FIGS. **4**A and **4**B are views showing a compensated common voltage and a measured common voltage, respectively, of a liquid crystal display device according to an embodiment of the present invention.

[0029] In FIG. **4**A, the compensated voltage generated by the common voltage compensation circuit **112** (of FIG. **3**) changes from about −6V to about +6V according to a time. The compensated common voltage is supplied to the liquid crystal panel through the first data driving unit **142** (of FIG. **3**), the current for the compensated common voltage flows through the first and second loops LOOP**1** and LOOP**2** (of FIG. **3**) and the common electrode from the first and second conductive dots Ag**1** and Ag**2** to the third and fourth conductive dots Ag**3** and Ag**4**. As a result, the compensated common voltage is uniformly applied to the entire portion including upper, middle and lower portions A, B and C of the common electrode with the substantially same voltage drop in the first and second loops LOOP**1** and LOOP**2**. Accordingly, as shown in FIG. **4**B, the measured common voltage at the lower portion C changes from about −6V to about +6V when the compensated voltage of FIG. **4**A is applied. As a result, the common voltage is compensated at the entire portion of the common electrode, and deterioration such as a greenish phenomenon is prevented.

[0030] It will be apparent to those skilled in the art that various modifications and variations can be made in a liquid crystal display device and a method of driving the liquid crystal display device of the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention cover the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

What is claimed is:

1. A liquid crystal display device, comprising:

a first substrate having a display area and a non-display area surrounding the display area, the first substrate including first, second, third and fourth sides;

a first common line formed in the non-display area corresponding to the second, third and fourth sides of the first substrate;

a second substrate facing and spaced apart from the first substrate;

a common electrode formed on an entire surface of the second substrate; and

a liquid crystal layer between the first and second substrates.

2. The device according to claim **1**, further comprising:

a plurality of gate driving units connected to the second side of the first substrate;

a plurality of data driving units connected to the first side of the first substrate; and

a driving printed circuit board (PCB) unit connected to the plurality of data driving units, the driving PCB unit including a common voltage compensation unit supplying a common voltage to the common line.

3. The device according to claim **2**, wherein each of the plurality of gate driving units includes a data driving integrated circuit (IC) and each of the plurality of data driving units includes a gate driving IC.

4. The device according to claim **2**, further comprising a second common line formed in the plurality of gate driving units and the second side of the first substrate to have a zigzag shape.

5. The device according to claim **2**, wherein the common voltage compensation circuit generates a compensated common voltage using the common voltage through the first common line.

6. The device according to claim **1**, further comprising a plurality of conductive dots in the non-display area of the first substrate, the plurality of conductive dots connecting the first common line and the common electrode.

7. The device according to claim **6**, wherein the plurality of conductive dots include silver (Ag).

8. The device according to claim **6**, wherein the plurality of conductive dots include first, second, third and fourth conductive dots at corner portions of the first substrate.

9. A method of driving a liquid crystal display device including a first substrate, a second substrate and a liquid crystal layer between the first and second substrates, comprising:

supplying a common voltage to a first common line on a first substrate, the first substrate including first, second, third and fourth sides and the first common line corresponding to the second, third and fourth sides;

generating a compensated common voltage using the common voltage through the first common line; and

supplying the compensated common voltage to the first common line.

10. The method according to claim **9**, wherein the compensated common voltage is generated by a common voltage compensation circuit adjacent to the first side of the first substrate.

11. The method according to claim **9**, further comprising applying the common voltage to a common electrode on an entire surface of a second substrate facing and spaced apart from the first substrate.

12. The device according to claim **9**, further comprising supplying the common voltage to a second common line in a plurality of gate driving units connected to the second side of the first substrate and in the second side of the first substrate to have a zigzag shape.

* * * * *

专利名称(译)	液晶显示装置及其驱动方法		
公开(公告)号	US20090128536A1	公开(公告)日	2009-05-21
申请号	US12/292136	申请日	2008-11-12
[标]申请(专利权)人(译)	乐金显示有限公司		
申请(专利权)人(译)	LG DISPLAY CO. , LTD.		
当前申请(专利权)人(译)	LG DISPLAY CO. , LTD.		
[标]发明人	SEO BYEONG RYEOL		
发明人	SEO, BYEONG-RYEOL		
IPC分类号	G09G3/36 G06F3/038 G09G5/00		
CPC分类号	G09G3/3655 G09G2300/04 G09G2320/0233 G09G2320/0223 G09G2320/02		
优先权	1020070119283 2007-11-21 KR		
其他公开文献	US9035931		
外部链接	Espacenet USPTO		

摘要(译)

一种液晶显示装置，包括：第一基板，具有显示区域和围绕显示区域的非显示区域，第一基板包括第一，第二，第三和第四侧面;形成在非显示区域中的第一公共线，对应于第一基板的第二，第三和第四侧;第二基板面向第一基板并与第一基板间隔开;公共电极形成在第二基板的整个表面上;以及第一和第二基板之间的液晶层。