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(54) **LIQUID CRYSTAL DISPLAY DEVICE AND MANUFACTURING METHOD THEREFOR**

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(75) **Inventors:** Akio Nakayama, Kikuchi-gun (JP);  
Shingo Nagano, Kikuchi-gun (JP);  
Masaya Mizunuma, Kikuchi-gun (JP);  
Kazuhiro Kobayashi, Kikuchi-gun (JP);  
Hitoshi Koyama, Kikuchi-gun (JP)

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Correspondence Address:  
**OBLON SPIVAK MCCLELLAND MAIER & NEUSTADT PC**  
**FOURTH FLOOR**  
**1755 JEFFERSON DAVIS HIGHWAY**  
**ARLINGTON, VA 22202 (US)**

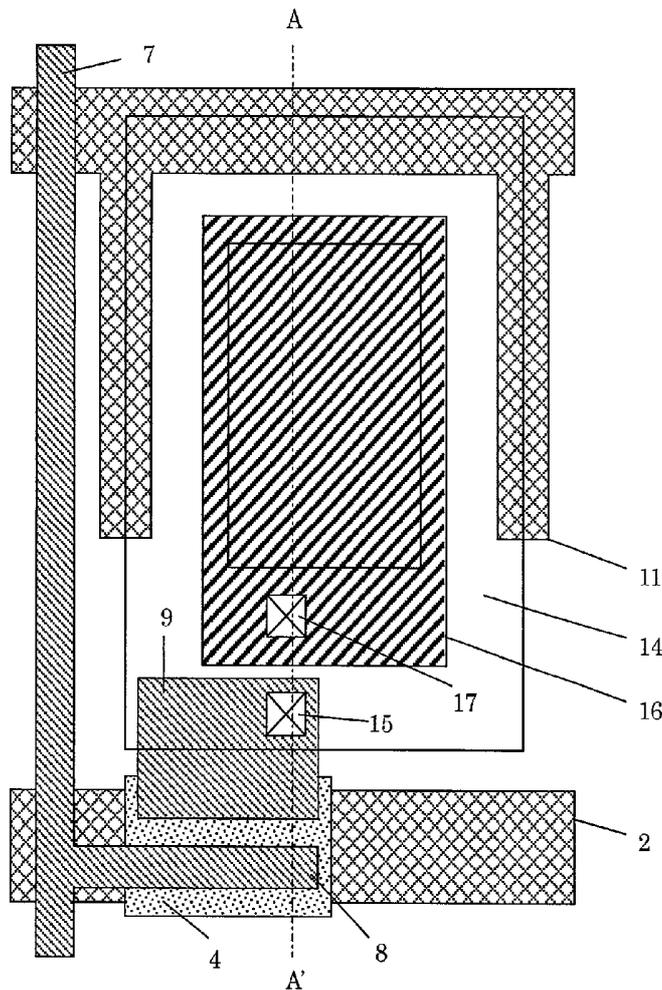
(57) **ABSTRACT**

A liquid crystal display device having transistors disposed at the intersections of gate lines and source lines, pixel electrodes connected with the transistors, opposite electrodes opposite to these pixel electrodes, and liquid crystal held between said opposite electrodes and said pixel electrodes is disclosed. The pixel electrodes comprise a first pixel electrode and a second pixel electrode. The second pixel electrode are disposed in a layer above an insulating layer which is itself disposed in a layer above the first pixel electrode. The second pixel electrode has a region that does not overlap with the first pixel electrode. The second pixel electrode are electrically connected with the first pixel electrode.

(73) **Assignee:** **ADVANCED DISPLAY INC.**, Kikuchi-gun (JP)

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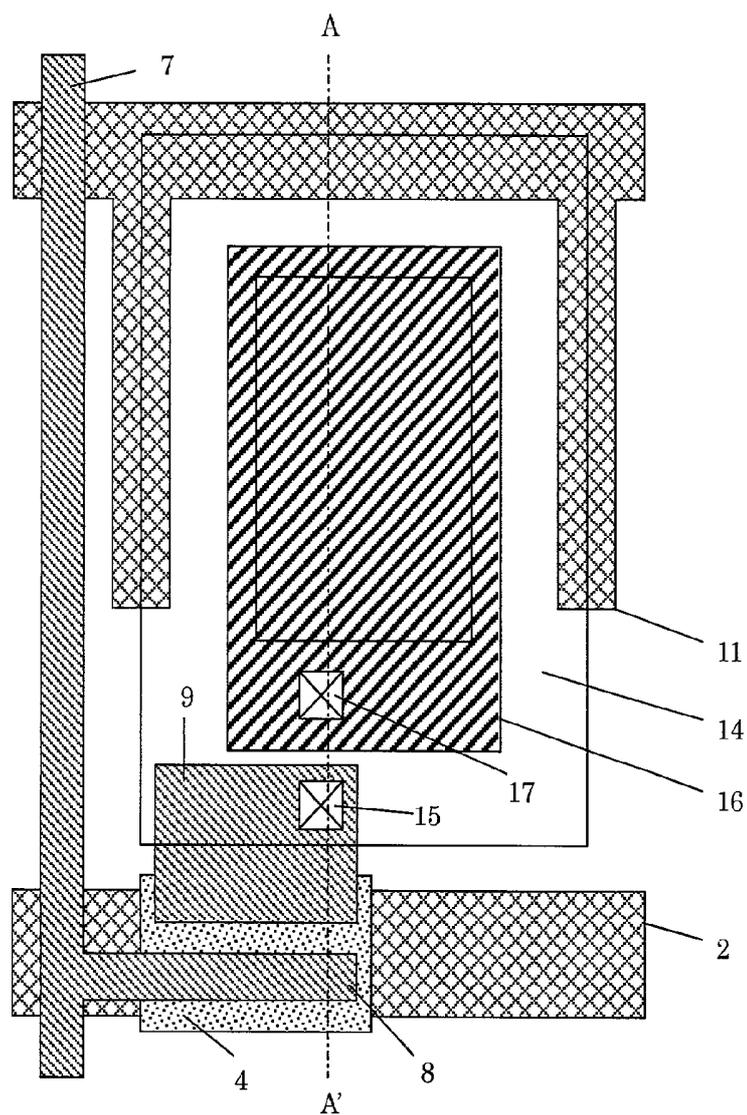


FIG. 1

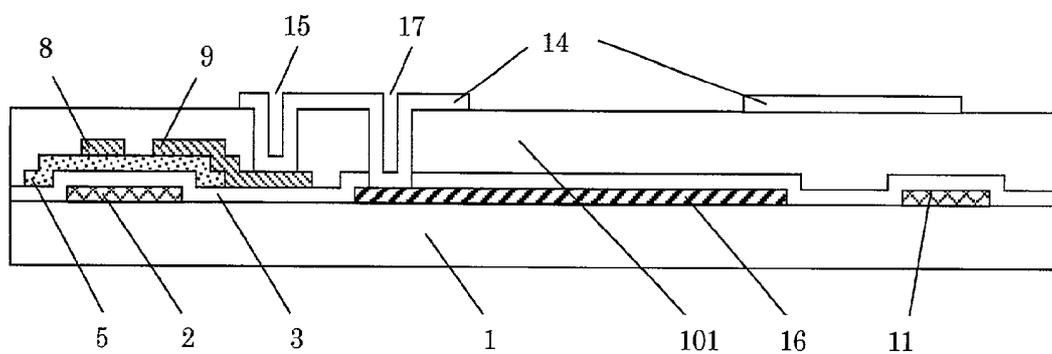


FIG. 2

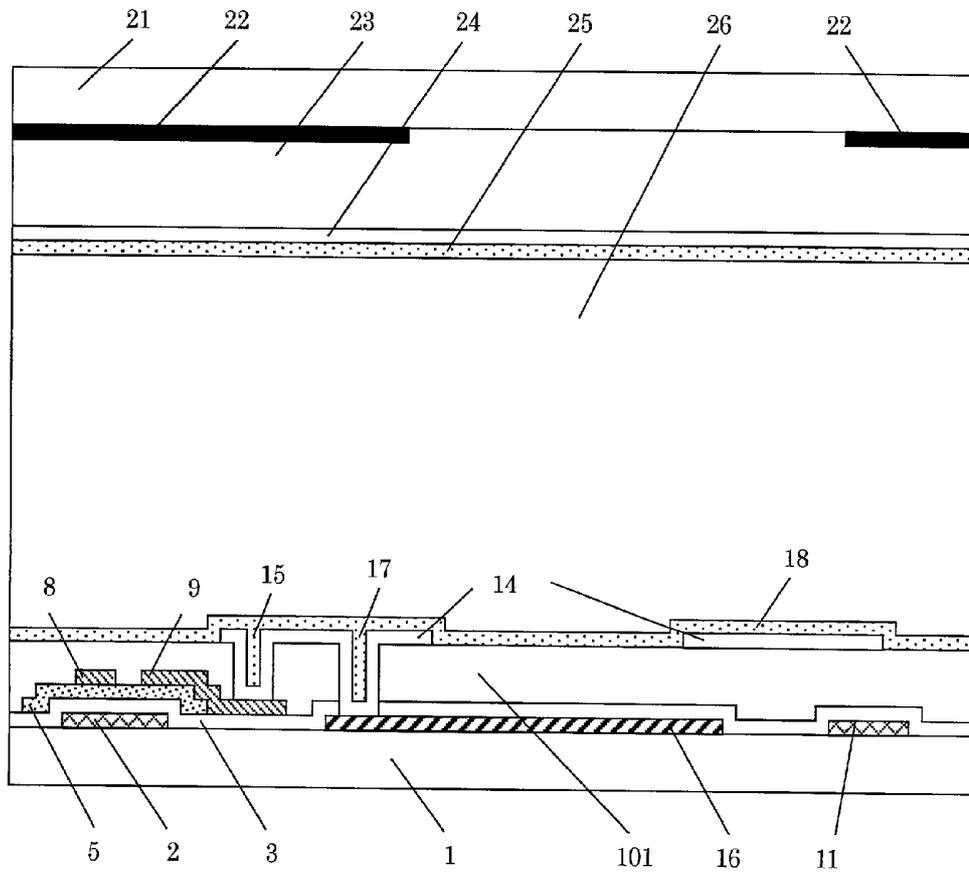


FIG. 3

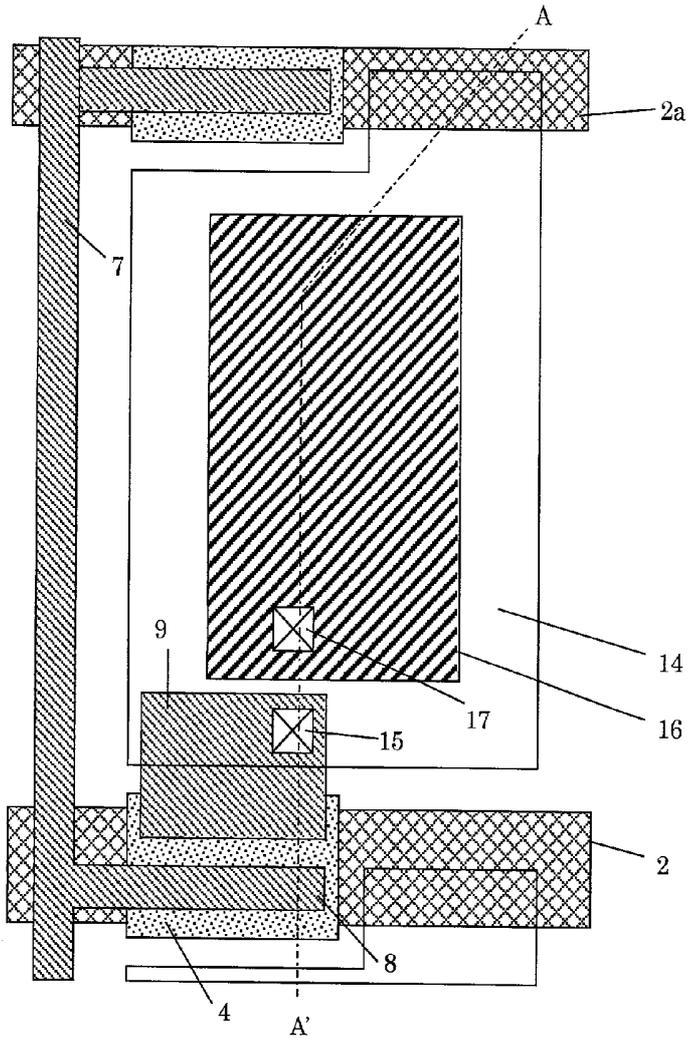


FIG. 4

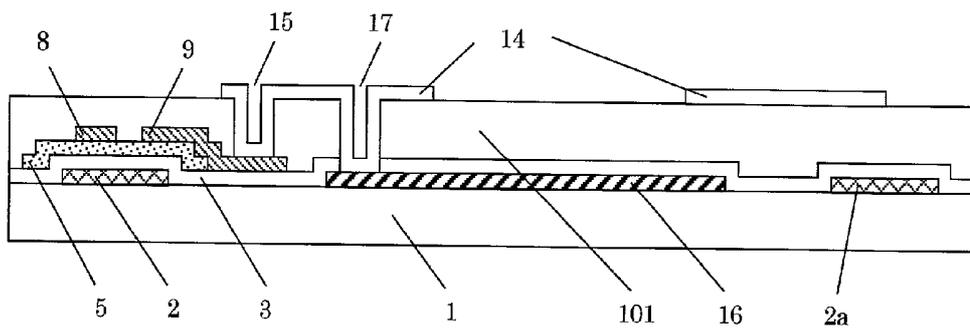


FIG. 5

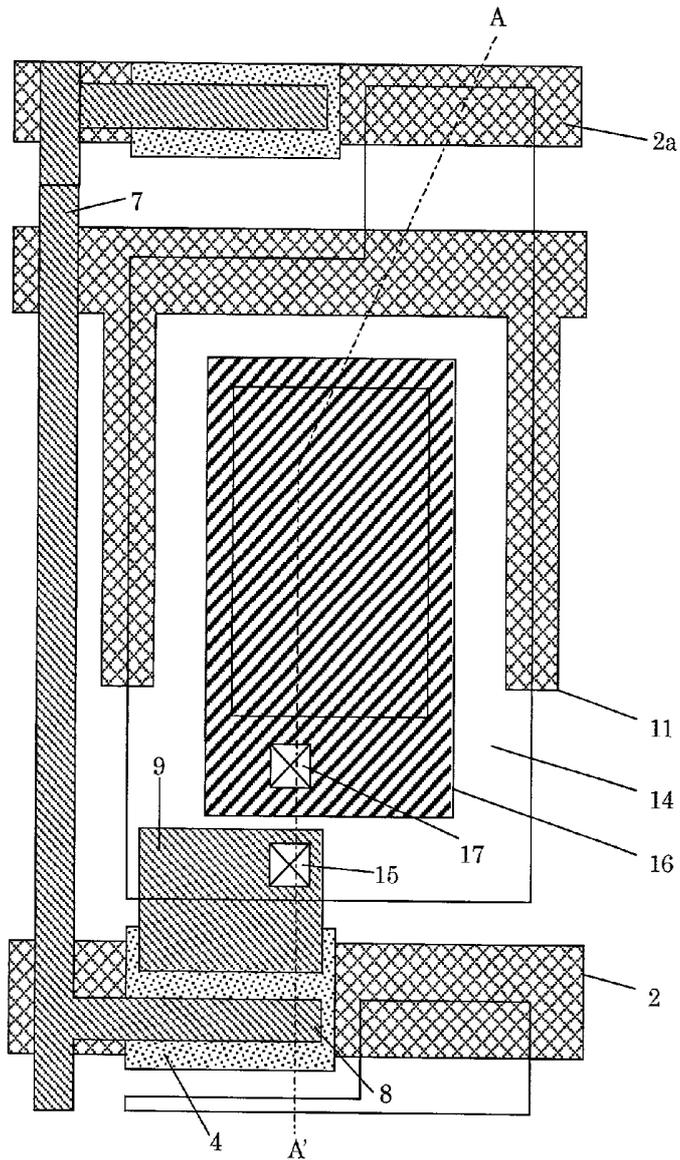


FIG. 6

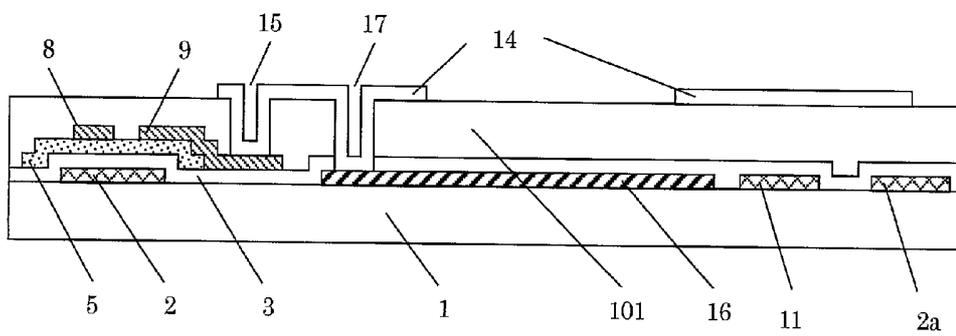


FIG. 7

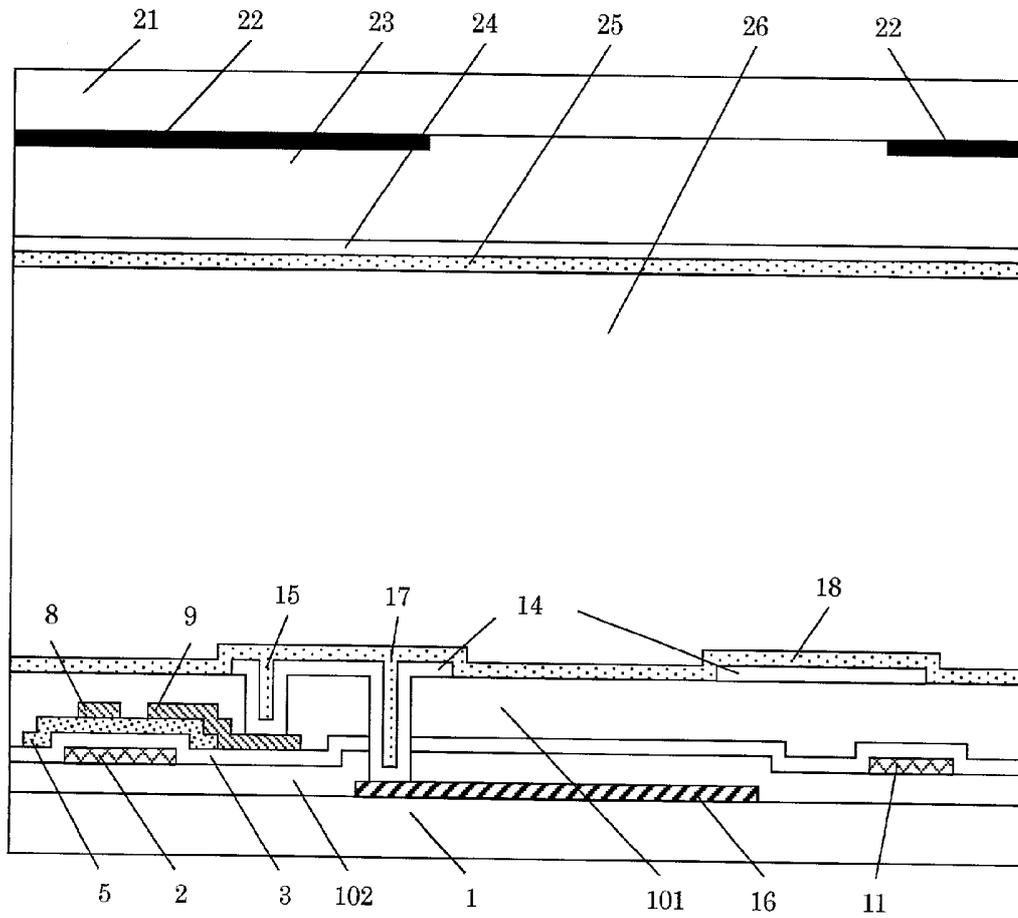


FIG.8

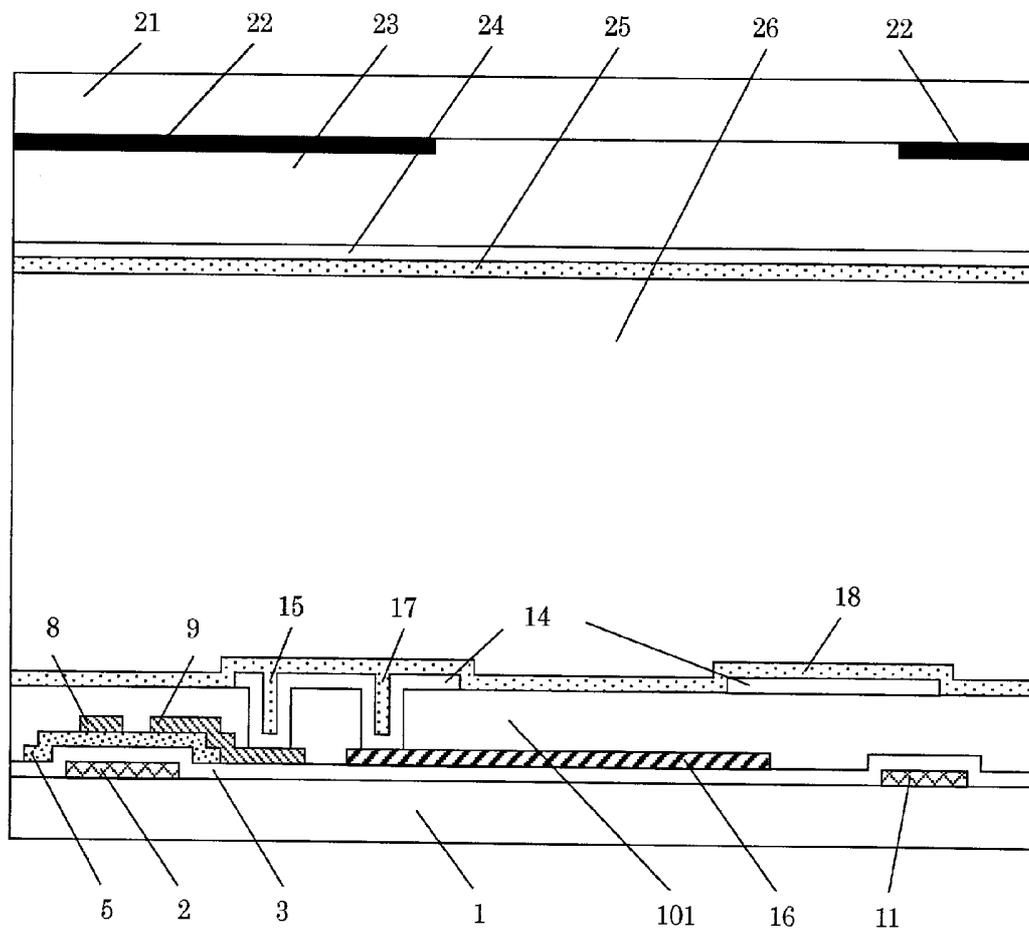


FIG.9

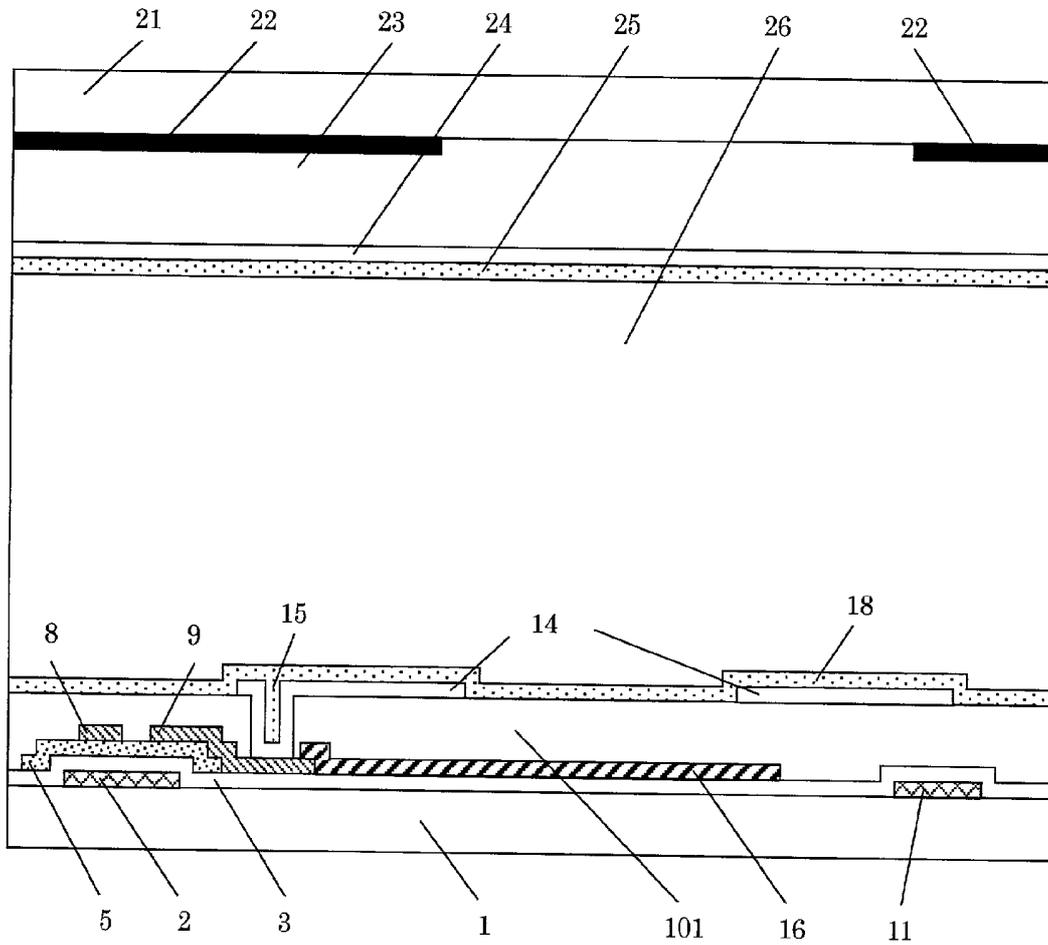


FIG.10

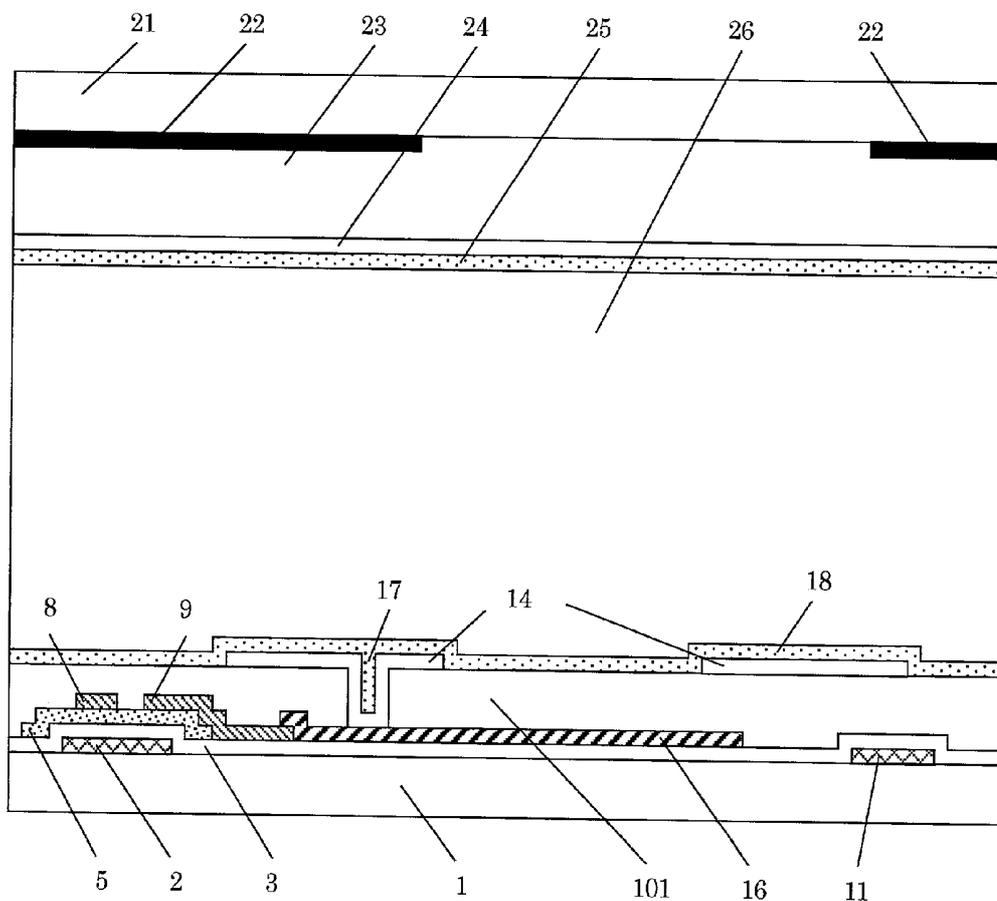


FIG.11

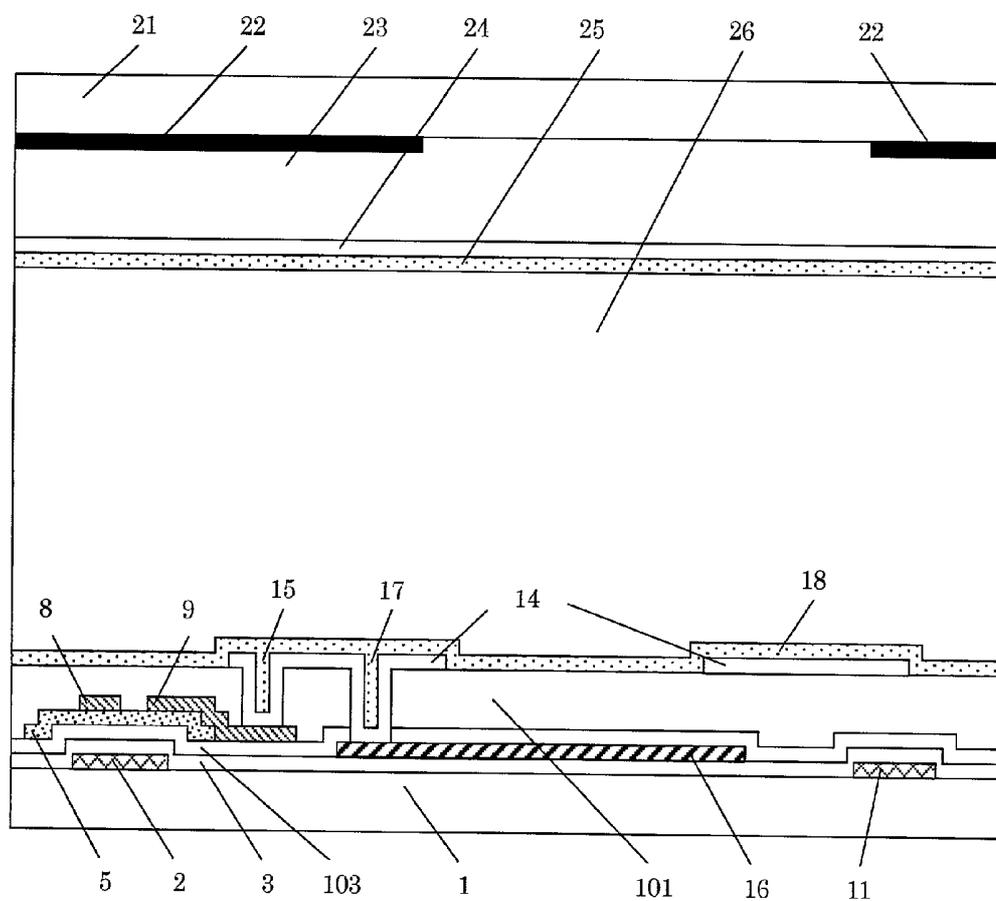
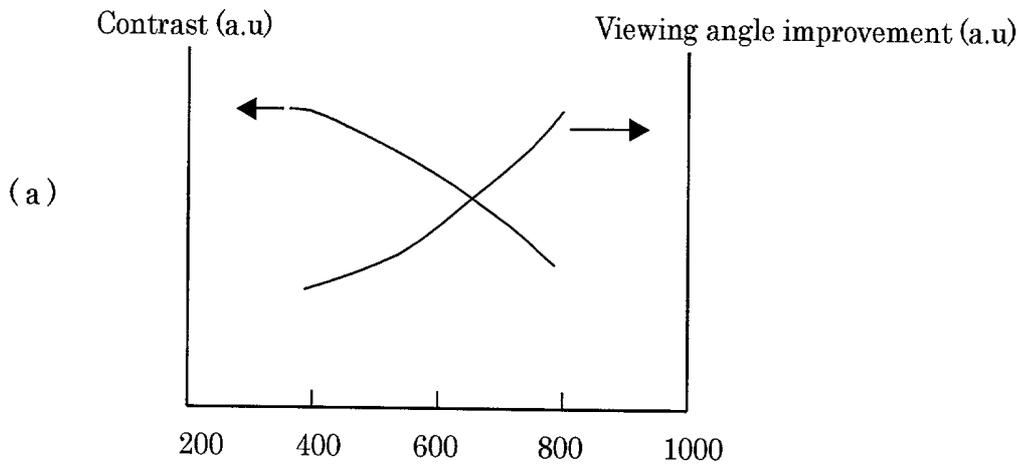
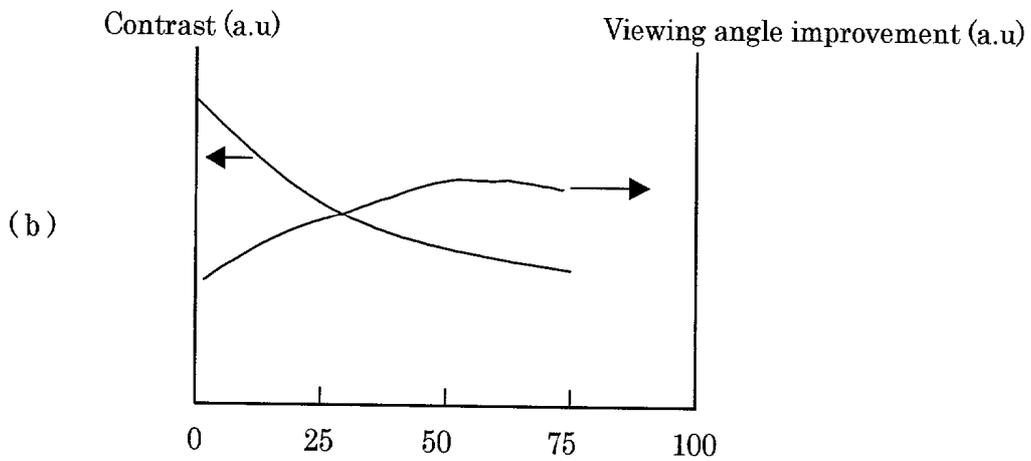


FIG.12



Thickness of insulating film established between the first layer and the second layer of pixel electrode [nm]



Area of region with the first layer pixel electrode / Area of region with opening portion of the pixel × 100(%)

FIG.13

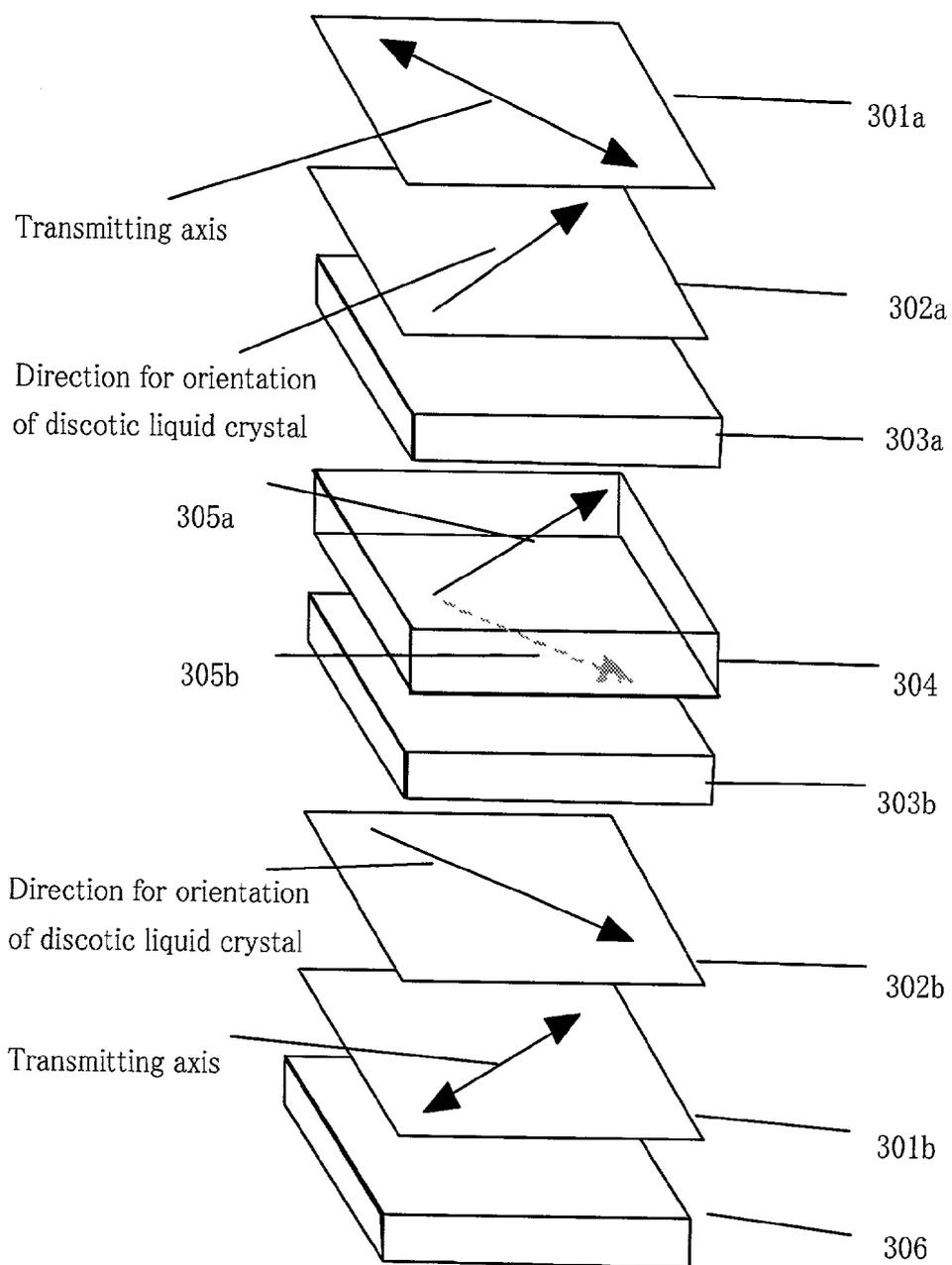


FIG.14

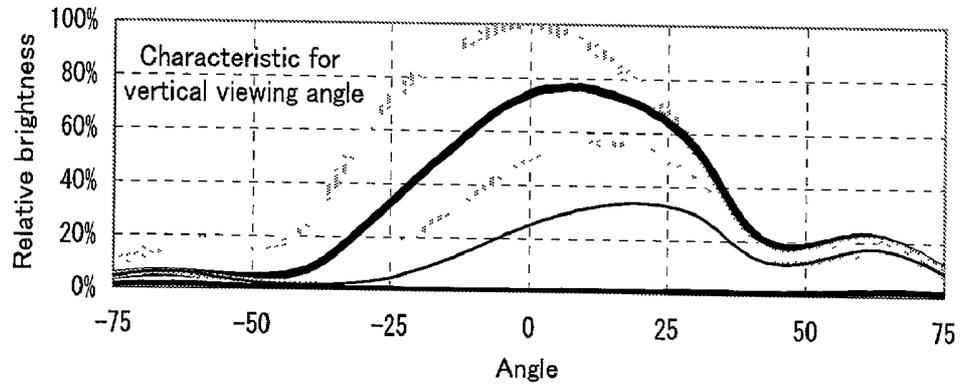


FIG.15

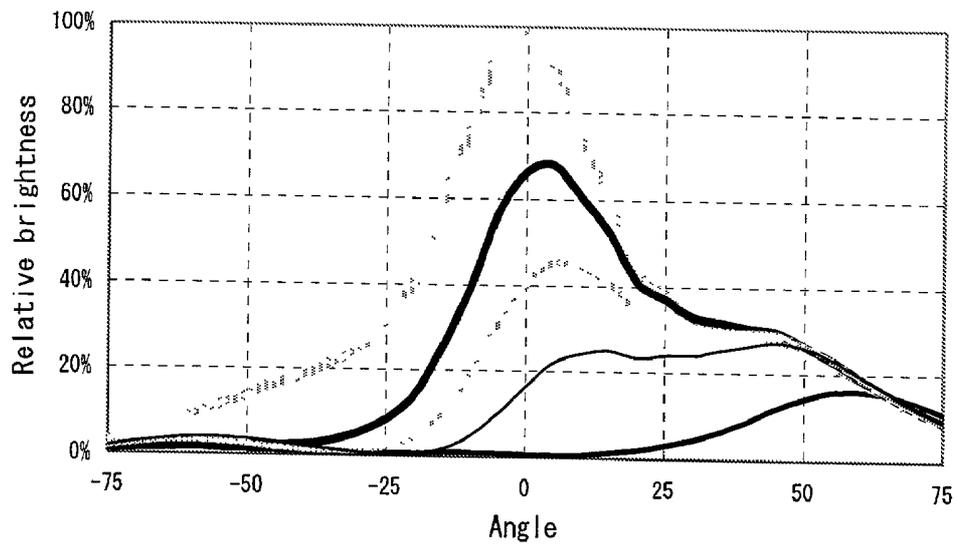


FIG.22

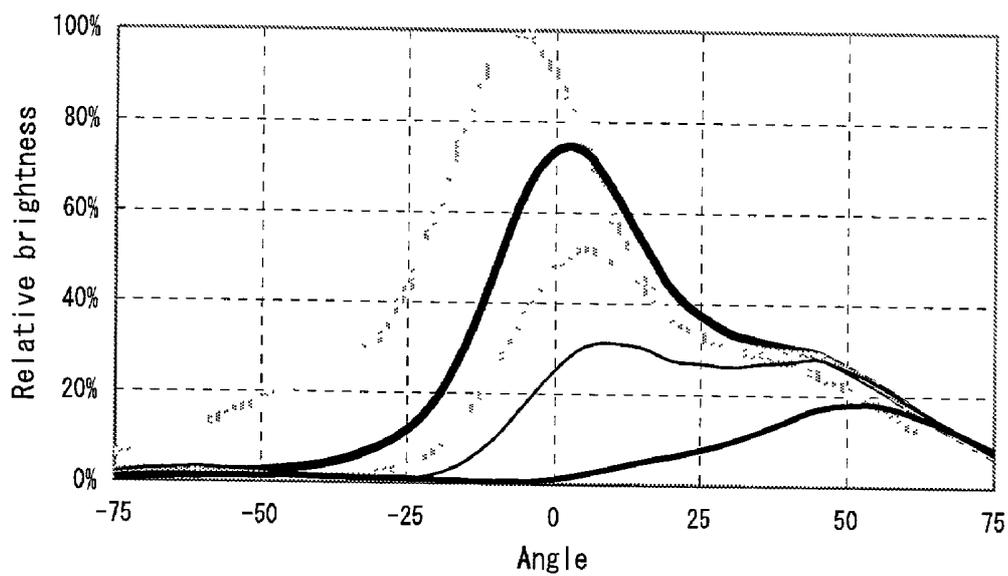


FIG. 16

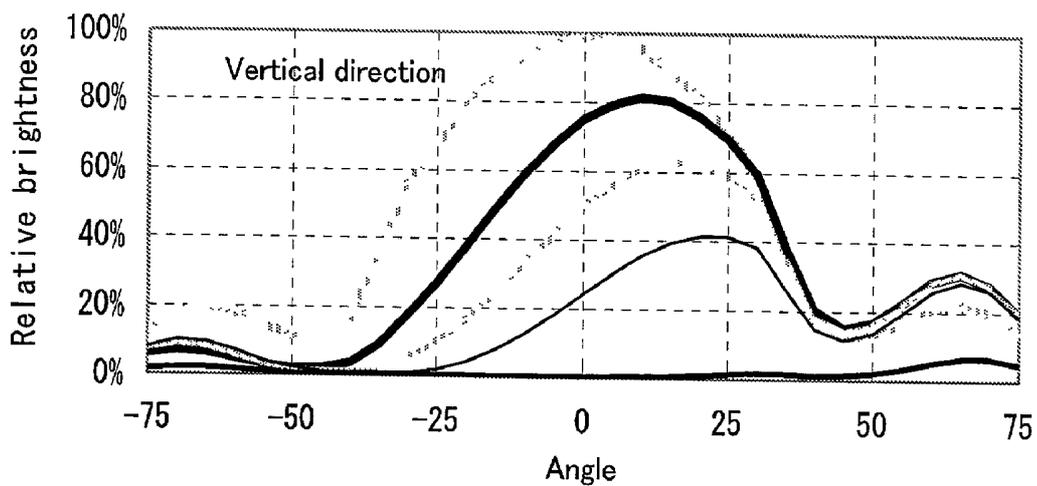


FIG. 23

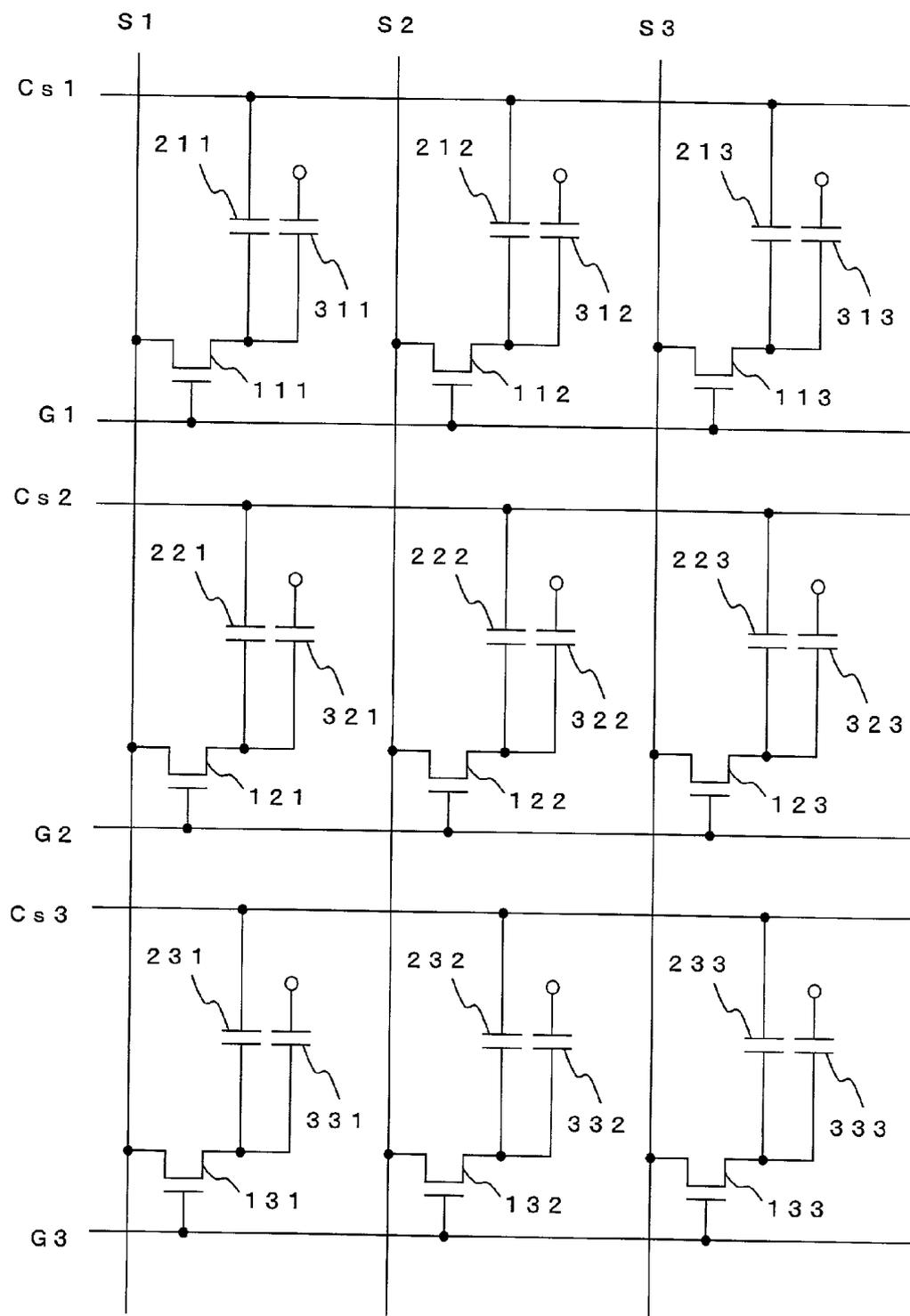


FIG.17

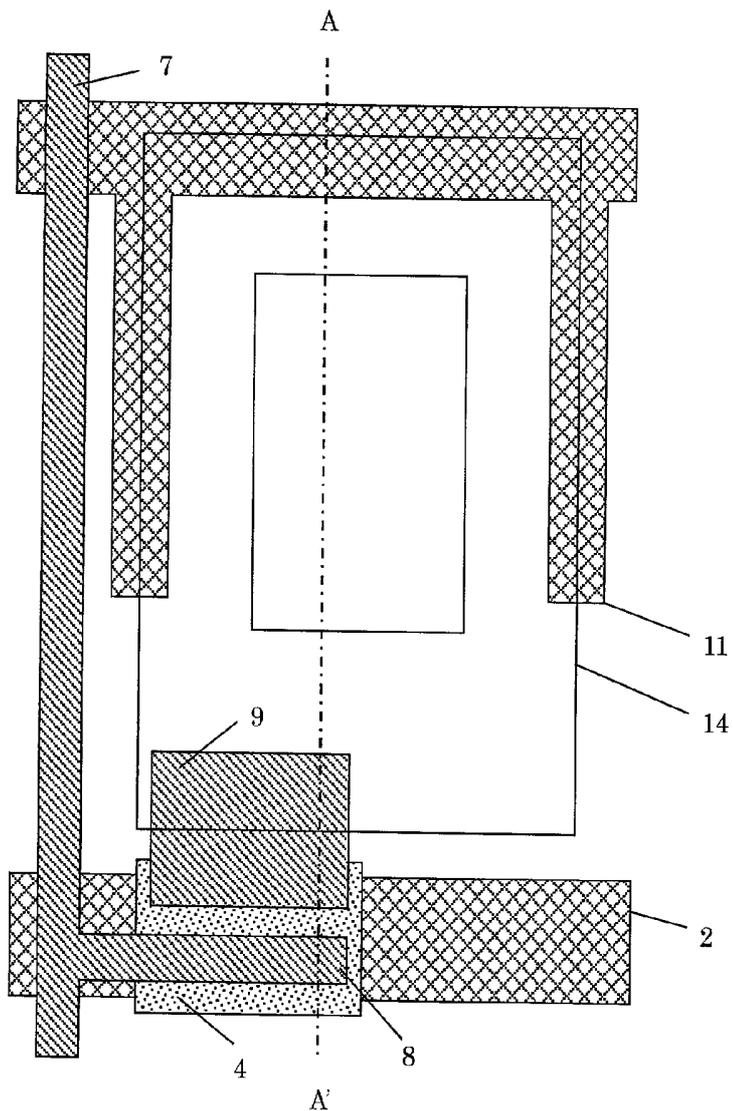


FIG. 18

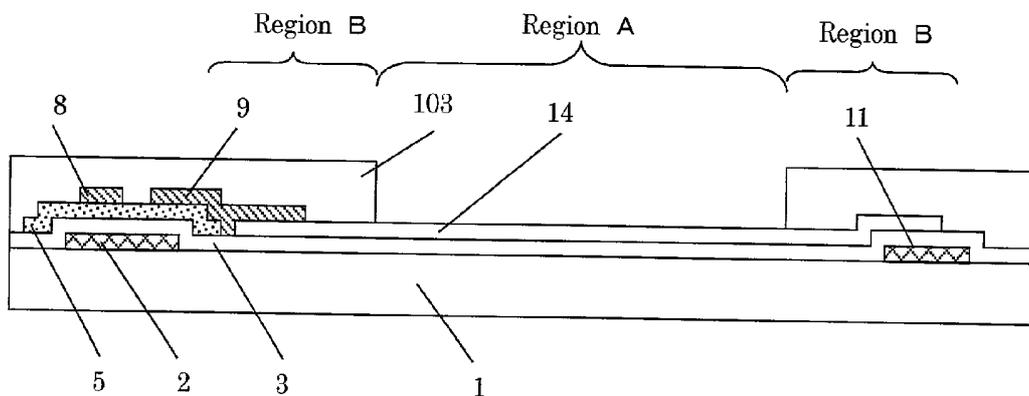


FIG. 19

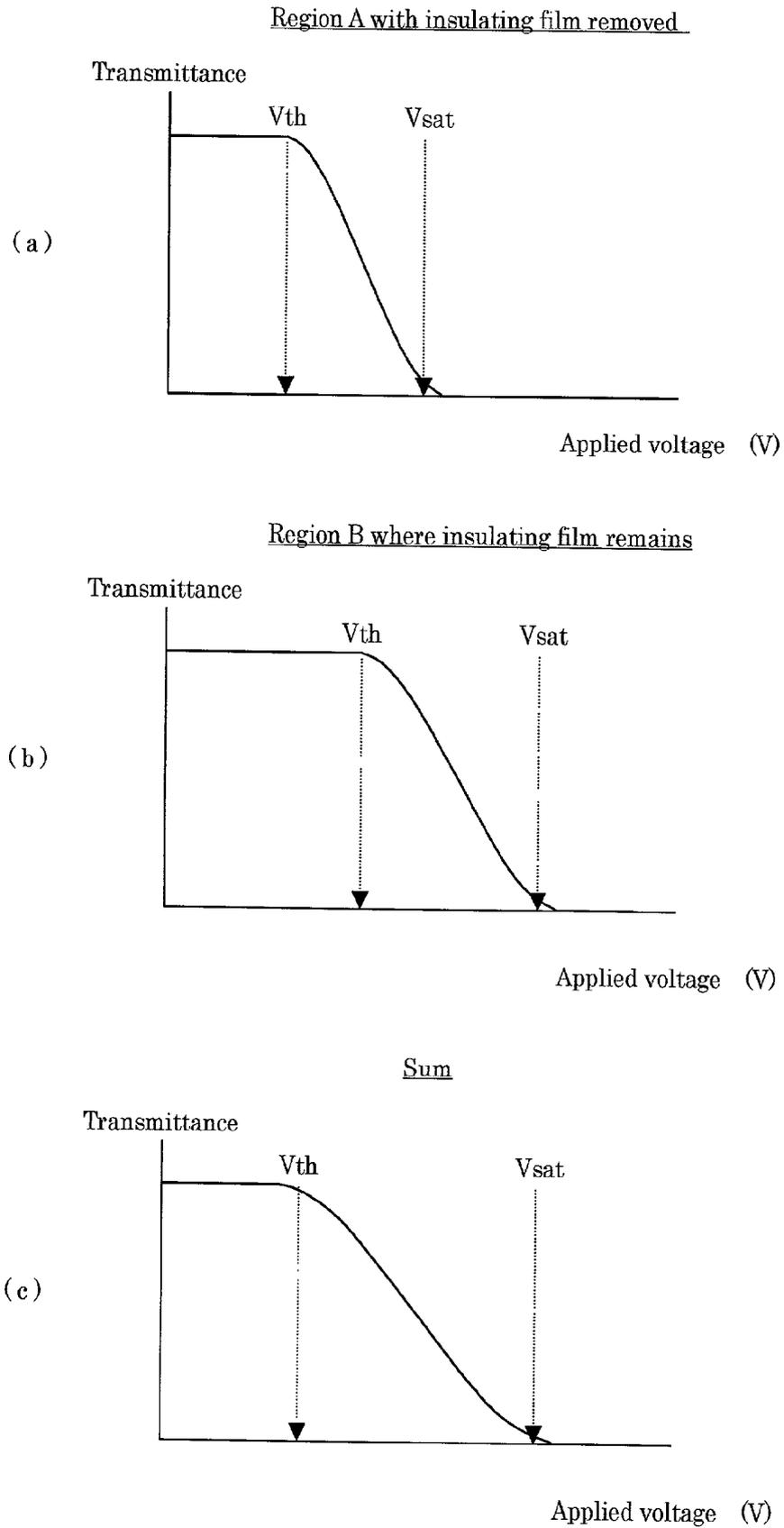


FIG.20

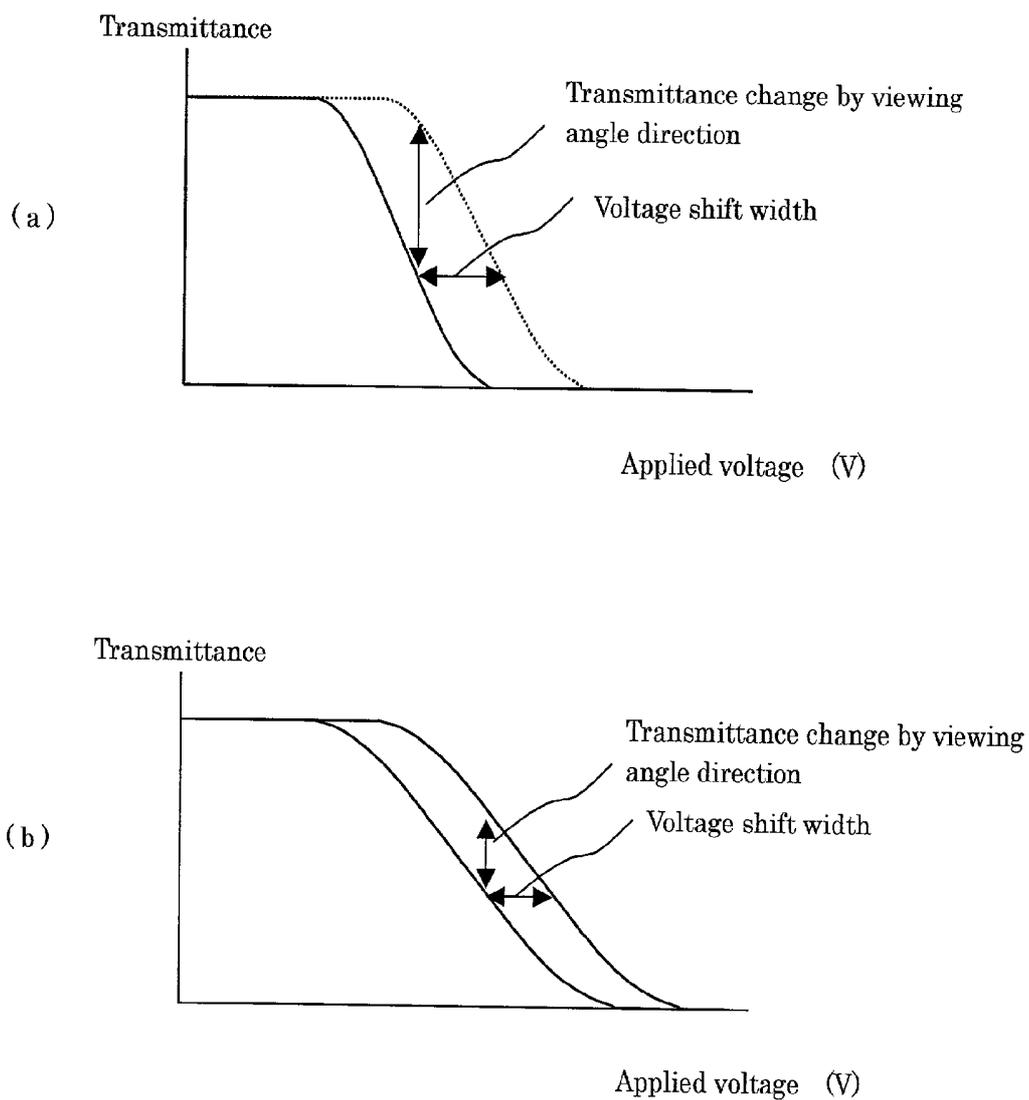


FIG.21

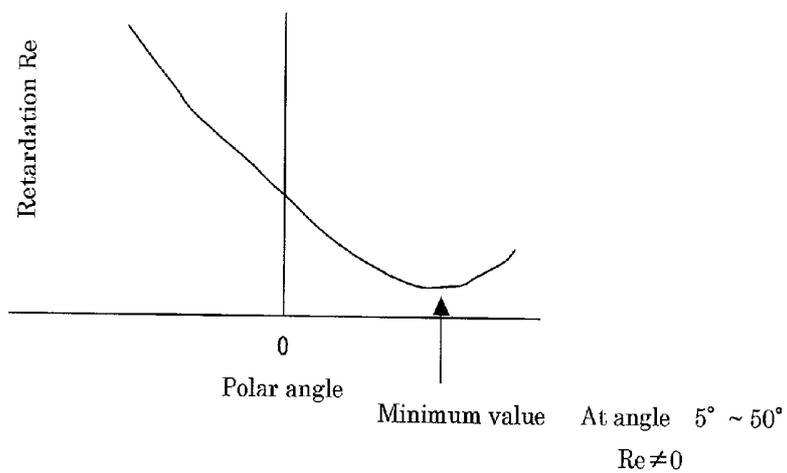


FIG.24

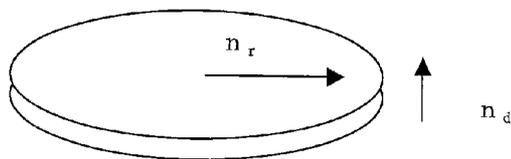


FIG.25

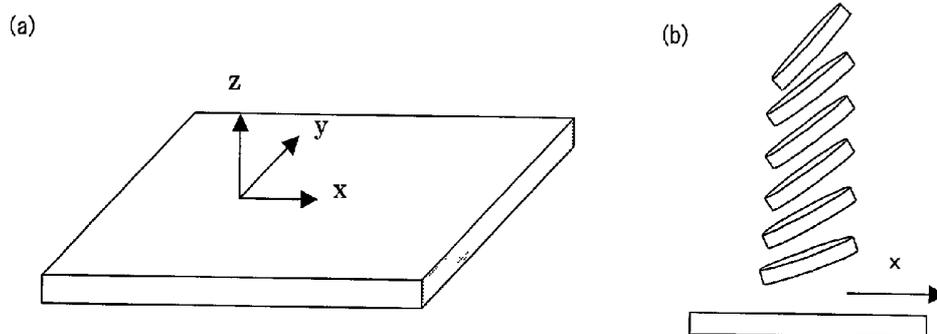


FIG.26

## LIQUID CRYSTAL DISPLAY DEVICE AND MANUFACTURING METHOD THEREFOR

### BACKGROUND OF THE INVENTION

[0001] 1. Field of the Invention

[0002] The present invention relates to a matrix-type liquid crystal display device, and is more particularly associated with technology relating to the viewing angle characteristics of this liquid crystal display device.

[0003] 2. Description of the Related Art

[0004] A matrix-type display device such as a liquid crystal display device usually comprises two substrates: a thin film transistor array substrate (hereinafter "TFT array substrate") whereon thin film transistors (hereinafter "TFTs") and so forth are established, and an opposite substrate whereon a color filter, black matrix, and so forth are established, with a display material such as liquid crystal held between those substrates. Image display is realized by selectively applying voltage to this display material.

[0005] FIG. 17 shows an equivalent circuit for a TFT array substrate. As shown in FIG. 17, the pixels are arranged in a matrix. In FIG. 17, G1, G2, and G3 are scanning signal lines (hereinafter "gate lines"). S1, S2, and S3 are picture signal lines (hereinafter "source lines") Cs1, Cs2, and Cs3 are storage capacitive electrode lines (hereinafter "Cs lines") for stabilizing the potential of the pixel electrodes during the holding period. 111 through 133 are TFTs. These TFTs function as switching elements and control the charging and discharging of charge to the pixel electrodes. 211 through 233 are storage capacitors (hereinafter "Cs capacitors") and are created by forming an insulating film between the pixel electrodes and Cs lines. The pixel electrodes are formed with transparent electrodes such as ITO (Indium tin oxide) 311 through 333 are liquid crystal capacitors C<sub>lc</sub> formed between the opposite substrate and liquid crystal.

[0006] Turning the TFT ON and OFF is controlled by applying voltage pulses to the gate lines connected to the gate electrodes of the TFTs. The pixel electrodes are connected with the source lines through the TFTs. The amount of charge in the pixel electrodes varies according to the signal level in the source lines and the potential of the pixel electrodes is established. The displacement amount of the liquid crystal changes according to the voltage across the pixel electrodes and opposite electrodes and changes the quantity of light transmitted from the back surface. Consequently, the optical signal change is controlled and the image is displayed by controlling the signal level of the source lines.

[0007] In order to improve image quality, it is necessary to reduce as much as possible the variation of pixel potential due to the change in signal level of the gate lines and so forth. For this reason, the Cs capacitors 211 through 233 are established on the pixel electrodes and provide a large total capacitance for the pixel. The Cs capacitors are formed by establishing an insulating film between the Cs lines, at the same potential as the opposite electrodes, and the pixel electrodes.

[0008] Next, FIG. 18 shows the pixel layout in a conventional TFT array substrate. FIG. 19 shows a cross sectional view of the pixel in FIG. 18 at the line A-A'.

[0009] In FIG. 18, 2 shows a gate line, 4 shows a semiconductor thin film, 7 shows a source line, 8 shows a source electrode, 9 shows a drain electrode, 11 shows a Cs line, and 14 shows a pixel electrode. In FIG. 19, 1 shows the glass substrate, 2 shows the TFT gate line, 3 shows the gate insulating film, 4 shows the semiconductor thin film, 8 shows the source electrode, 9 shows the drain electrode, 11 shows the Cs line, 14 shows the pixel electrode, and 103 shows the insulating film.

[0010] The manufacturing method for a conventional TFT array substrate is explained using FIG. 19. First, a metal film to become the gate electrode is formed on the glass substrate 1, a resist pattern is developed, and the gate electrode 2 is formed. After the resist is removed, as shown in FIG. 19, the gate insulating film 3 and the semiconductor thin film 4, comprising an i layer and n layer, are formed from the bottom, a resist pattern is developed, and the semiconductor thin film 4 is etched. After the resist is removed, an ITO thin film, to become the pixel electrode, is formed, the resist is developed, and the ITO film is etched, whereby the pixel electrode 14 is formed. After the resist is removed, a metal film is formed, a resist pattern is developed, and the source electrode 8 and drain electrode 9 are formed by etching. Furthermore, all of the n layer and part of the i layer on the TFT back channel side are etched (back channel etching) and the insulating film 103 is formed.

[0011] The manufacture and functions of a conventional TFT are explained next using an example. In the case of charging charge to the pixel electrode 14 in the TFT shown in FIG. 18, positive voltage of about 9 V is applied to the source electrode 8 and positive voltage of around 20 V is applied to the gate electrode 2. The TFT is thereby turned on and the drain electrode 9 and pixel electrode 14 are charged to about 9 V. Thereafter, when the potential of the pixel electrode increases sufficiently, negative voltage of about -5 V is applied to the gate electrode 2, the TFT is turned off, and the charge is confined in the pixel.

[0012] However, in addition to the TN (twisted nematic) type, various operating modes are used for the operating modes of a liquid crystal display element, for example, the IPS (in plane switching) type, VA (vertical alignment) type, and so forth for widening the viewing angle. FIG. 20 shows the relationship (V-T characteristics) of the voltage applied to the liquid crystal and the transmittance in the normally white (black display on a white background) mode for a TN-type liquid crystal display element. As shown in FIG. 20, there is a difference of about 1 to 2 V, for example, between the voltage at the start of the change in transmittance (threshold voltage V<sub>th</sub>) and the voltage when the transmittance change has almost ended (saturated voltage V<sub>sat</sub>). In an active matrix-type liquid crystal display element using a TFT as a switching element, a tonal display is made by establishing a number of voltage levels between this V<sub>th</sub> and V<sub>sat</sub>.

[0013] In a TN-type liquid crystal display element, there is a large change in transmittance when changing viewing angle based on the operating principles, and the narrow range of the viewing angle is a problem. The conventional art for improving this problem is discussed in detail next using the drawings.

[0014] A proposed technology is to establish a range where the electrical field intensity applied to the liquid crystal

varies while the same voltage is applied across the pixel electrode and the opposite electrode, opposite thereto and holding the liquid crystal therebetween, in one pixel. In this conventional art, as shown in FIG. 19, the insulating film 103 is formed on the upper layer of the pixel electrode 14, and then a region A where the insulating film 103 is removed and a region B where the film remains are formed on the pixel electrode 14. Actually, because the alignment layer is formed on the TFT array substrate and the opposite substrate, an alignment layer is formed on the pixel region in FIG. 18. The voltage drop occurs in this portion of the alignment layer as well, but at a level that can be ignored, so there is no problem even if this alignment layer is omitted in the following explanation.

[0015] In the region A of the pixel electrode 14 from which the insulating film 103 is removed, voltage applied across the pixel electrode 14 and opposite electrode is applied to the liquid crystal. In the region B where the insulating film 103 remains, a voltage drop occurs in the portion with the insulating film and the voltage applied across the pixel electrode 14 and the opposite electrode is not applied without any change thereto. In other words, in the pixel electrode 14, the regions A and B, having mutually different characteristics of voltage applied across the electrodes to transmittance (V-T characteristics), are established within one pixel.

[0016] FIG. 20(a) shows the V-T characteristics curve of a pixel in the region A with the insulating film 103 removed. FIG. 20(b) shows the V-T characteristics curve occurring in the region B where the insulating film 103 remains. If each pixel is sufficiently small compared to the entire liquid crystal display device, the V-T characteristics curve is projected to an observer as shown in FIG. 20(c) which is the sum of the characteristics in FIG. 20(a) and (b). As a result, the difference between  $V_{th}$  and  $V_{sat}$  is substantially expanded.

[0017] With the expansion of the difference between  $V_{th}$  and  $V_{sat}$ , the narrowness of the viewing angle range during tonal display is also reduced. FIG. 21 shows the situation of the change of the V-T characteristics due to the viewing angle direction. FIG. 21(a) shows the case where there are no regions on the pixel electrode with different electrical field intensities applied. FIG. 21(b) shows the case where there is a region on the liquid crystal with part of the insulating film on the pixel electrode removed as shown in FIG. 18 and with different electric field strength applied.

[0018] In a liquid crystal display element with the TN-type operating mode, the relationship between the applied voltage and transmittance changes from the solid line to the dotted line because of changing the viewing angle direction. As a result, in the case where there are no regions on the pixel electrode with different electric field strength, the transmittance changes greatly with a change in the viewing angle direction as shown in FIG. 21(a). On the other hand, as shown in FIG. 21(b), in the case where there is a region with different electric field strength, the voltage shift width of the V-T characteristics, due to the change in the viewing angle direction when regions of different characteristics are seen as a single pixel, is about the same. As a result, the change in transmittance due to the viewing angle direction is small corresponding to the portion with a gentle change in transmittance due to the applied voltage and the viewing angle range becomes wide.

[0019] On the other hand, various methods are proposed as methods for expanding the viewing angle for liquid crystal display devices using the TN mode. For example, these include a method for expanding viewing angle by varying the array direction of the liquid crystal molecules in the pixels with a multidomain structure, in other words by varying the rising direction of the liquid crystal molecules when voltage is applied; and a method for expanding viewing angle by reducing the slope of the voltage-brightness characteristics by manufacturing the display so that different voltages are applied to the liquid crystal in a single pixel. Furthermore, there is a method for expanding the viewing angle as a result of suppressing the light leakage, in the black display state with voltage applied to the liquid crystal, by inserting an optical compensating film such as a retardation film between the liquid crystal and polarizing plate.

[0020] Gray-scale inversion in half tones gray is an issue for the viewing angle characteristics. In the TN mode, gray-scale inversions do not easily occur upwards and horizontally, but gray-scale inversions occur easily downwards. FIG. 22 shows the relationship between the vertical relative transmittance and angle in the case of applying voltage so that the relative transmittance is 100%, 75%, 50%, 25%, and black display at normal to the panel surface of a conventional TN mode liquid crystal display device. As clear from the drawing, when the half tone gray display is observed at about  $-20^\circ$ , there is an intersection with the relative transmittance curves and it is understood that the display quality markedly deteriorates with the excessively dark image from downwards.

[0021] As discussed above, in order to expand the viewing angle range, it is necessary to create regions with different electric field strength applied to the liquid crystal in one pixel. In order to create such regions, it has been proposed to form a region with an insulating film removed and a region with that film remaining on the pixel electrode. In order to expand the viewing angle range, however, it is necessary to reduce the inclination of the characteristics curve of the total of a plurality of V-T characteristics and it is necessary to form a thick insulating film on and then to remove this thick insulating film from the same pixel electrode.

[0022] By employing this type of constitution, large steps are formed in the insulating film on the same pixel electrode. Consequently, even if an alignment layer is applied in the panel process, the steps cannot be filled in and large steps will be formed in the individual pixels of the liquid crystal display device. The side walls and bases of the steps become a cause of light leakage because they make it difficult to apply the alignment process by rubbing treatment or the like in the panel process and make it difficult for the liquid crystal to align in the desired direction even when voltage is applied. For this reason, a normally white type of liquid crystal display device has a structural problem such that, even if sufficient voltage is applied for the black display state, the brightness of the black state becomes high because of the light leakage and this brings about reduced contrast.

[0023] In recent years, the requirements for high contrast have been increasing more and more with the employment of liquid crystal display devices in television set displays. On the other hand, there are also problems of gray-scale inversion occurring in half tone grays.

## SUMMARY OF THE INVENTION

[0024] In view of the foregoing, it is an object of the present invention to provide a liquid crystal display device with a wide viewing angle range, low black brightness, and high contrast.

[0025] It is another object of the present invention to eliminate light leakage due to the disorder in the orientation of the liquid crystal at step portions or near the base of step portions.

[0026] According to one aspect of the present invention, for achieving the above-mentioned object, there is provided a liquid crystal display device having transistors disposed at the intersections of gate lines and source lines, pixel electrodes connected with the transistors, opposite electrodes opposite to these pixel electrodes, and liquid crystal held between said opposite electrodes and said pixel electrodes, wherein the pixel electrodes comprise a first pixel electrode and a second pixel electrode disposed in a layer above an insulating layer which is itself disposed in a layer above the first pixel electrode, and having a region that does not overlap with the first pixel electrode and the first pixel electrode and second pixel electrode are electrically connected.

[0027] Consequently, it is possible to eliminate light leakage due to the disorder in the orientation of the liquid crystal at step portions or near the base of step portions.

[0028] According to another aspect of the present invention, there is provided a liquid crystal display device having transistors disposed at the intersections of gate lines and source lines, pixel electrodes connected with the drain electrodes of the transistors, opposite electrodes opposite to these pixel electrodes, and liquid crystal held between the opposite electrodes and the pixel electrodes, wherein the pixel electrodes comprise a first pixel electrode and a second pixel electrode disposed in a layer above an insulating layer which is itself disposed in a layer above the first pixel electrode, and having a region that does not overlap with the first pixel electrode, and the first pixel electrode and second pixel electrode are electrically connected with the drain electrode.

[0029] Consequently, it is possible to eliminate light leakage due to the disorder in the orientation of the liquid crystal at step portions or near the base of step portions.

[0030] The above and further objects and novel features of the invention will more fully appear from the following detailed description when the same is read in connection with the accompanying drawings. It is to be expressly understood, however, that the drawings are for purpose of illustration only and are not intended as a definition of the limits of the invention.

## BRIEF DESCRIPTION OF THE DRAWINGS

[0031] FIG. 1 shows a plan view of a pixel in a TFT array substrate.

[0032] FIG. 2 shows a cross sectional view of the pixel in FIG. 1 taken at the line A-A'.

[0033] FIG. 3 shows a cross sectional, structural view of the display portion of the liquid crystal display device comprising a TFT array substrate, opposite substrate, and liquid crystal.

[0034] FIG. 4 is a plan view of a pixel in the TFT array substrate.

[0035] FIG. 5 is a cross sectional view of the pixel in FIG. 4 taken at the line A-A'.

[0036] FIG. 6 is a plan view of a pixel in the TFT array substrate.

[0037] FIG. 7 is a cross sectional view of the pixel in FIG. 6 taken at the line A-A'.

[0038] FIG. 8 shows cross sectional view of an active matrix-type liquid crystal display device relating to the fourth embodiment of the present invention.

[0039] FIG. 9 shows a cross sectional view of an active matrix-type liquid crystal display device relating to the fifth embodiment of the present invention.

[0040] FIG. 10 shows a cross sectional view of an active matrix-type liquid crystal display device relating to the fifth embodiment of the present invention.

[0041] FIG. 11 shows a cross sectional view of an active matrix-type liquid crystal display device relating to the fifth embodiment of the present invention.

[0042] FIG. 12 shows a cross sectional view of an active matrix-type liquid crystal display device relating to the sixth embodiment of the present invention.

[0043] FIG. 13 shows a relationship among the viewing angle, contrast and thickness of insulating film established between the first layer and the second layer of pixel electrode.

[0044] FIG. 14 shows the constitution of the liquid crystal display device relating to the seventh embodiment.

[0045] FIG. 15 shows a relationship between the vertical relative transmittance and angle.

[0046] FIG. 16 shows a relationship between the vertical relative transmittance and angle.

[0047] FIG. 17 shows an equivalent circuit for a TFT array substrate.

[0048] FIG. 18 shows the pixel layout in a conventional TFT array substrate.

[0049] FIG. 19 shows a cross sectional view of the pixel in FIG. 18 at the line A-A'.

[0050] FIG. 20 shows the relationship (V-T characteristics) of the voltage applied to the liquid crystal and the transmittance in the normally white (black display on a white background) mode for a TN-type liquid crystal display element.

[0051] FIG. 21 shows a situation of the change of the V-T characteristics due to the viewing angle direction.

[0052] FIG. 22 shows a relationship between the vertical relative transmittance and angle.

[0053] FIG. 23 shows a relationship between the vertical relative transmittance and angle.

[0054] FIG. 24 shows a relationship between the retardation and polar angle.

[0055] FIG. 25 is an explanatory diagram for index of refraction of discotic liquid crystal.

[0056] FIG. 26 is an explanatory diagram for index of refraction of discotic liquid crystal.

#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0057] The preferred embodiments are explained for the present invention.

[0058] First Embodiment

[0059] The active matrix-type liquid crystal display device relating to the first embodiment of the present invention is explained using FIGS. 1, 2, and 3. FIG. 1 shows a plan view of a pixel in a TFT array substrate. FIG. 2 shows a cross sectional view of the pixel in FIG. 1 taken at the line A-A'. FIG. 3 shows a cross sectional, structural view of the display portion of the liquid crystal display device comprising a TFT array substrate, opposite substrate, and liquid crystal.

[0060] In FIG. 1, 2 shows the gate line, 4 shows the semiconductor thin film, 7 shows the source line, 8 shows the source electrode, 9 shows the drain electrode, 11 shows the Cs line, 14 shows the pixel electrode comprising a second layer of ITO, 15 shows the contact of the drain electrode 9 with the pixel electrode 14, 16 shows the pixel electrode comprising a first layer of ITO, and 17 is the contact of the pixel electrode 16 comprising the first layer of ITO with the pixel electrode 14 comprising the second layer of ITO. Moreover, the contacts 15 and 17 are constituted by extending part of the pixel electrode 14.

[0061] In FIG. 2, 1 is the glass substrate, 3 is the gate insulating film, 5 is the semiconductor thin film, wherein the lower layer is a pure semiconductor layer (hereinafter "i layer") and the upper layer is an n layer having n-type impurities, and 101 is the interlayer insulating film. The constitution to which other symbols are applied is the same constitution, or corresponds to the constitution shown in FIG. 1, and an explanation thereof is therefore omitted.

[0062] In FIG. 3, 21 is the glass substrate, 22 is the shielding film, 23 is the coloring material of a color filter, 24 is the opposite substrate comprising a transparent conductive film such as ITO, 25 is the alignment layer, and 26 is the liquid crystal. As shown in FIGS. 1, 2, and 3, there are two pixel electrodes in the liquid crystal display device relating to the present embodiment: the pixel electrode 14 and pixel electrode 16. This pixel electrode 14 is disposed in a layer above the interlayer insulating film 101 which is disposed in a layer above the pixel electrode. Also, the pixel electrode 14 and pixel electrode 16 comprise a region without mutual overlap as seen from the opposite electrode. Furthermore, the pixel electrode 14 is electrically connected with the drain electrode 9 and is also electrically connected with the pixel electrode 16.

[0063] The method for manufacturing the liquid crystal display device is explained using these FIGS. 1, 2, and 3. First, the gate line 2 and Cs line 11 are formed by the formation of a metal film on the glass substrate 1 with sputtering, and then patterning with photolithography and etching that film. After that, the pixel electrode 16, comprising the first layer of ITO, is formed by the formation with sputtering and then the patterning of the ITO film. The gate insulating film 3 is then formed. Further, the TFT semiconductor thin film 5 is formed by the formation and patterning of a semiconductor thin film. The source line 7, source

electrode 8, and drain electrode 9 are formed by the formation with sputtering and then patterning of a metal film. This TFT is formed by etching in this pattern formation process and removing the part of the n-type semiconductor layer and i layer where there is no metal wiring pattern.

[0064] Furthermore, after the formation of the insulating film 101, the contact hole 15 for connecting the pixel electrode 14 and drain electrode 9, and the contact hole 17 for connecting the pixel electrode 14 and pixel electrode 16 are formed. The pixel electrode 14 is then formed by the formation with sputtering and then patterning of the ITO.

[0065] As shown in FIG. 3, after the formation of the pixel electrode 14, the alignment layer 18 is applied over the pixel electrode 14 in the panel process. Meanwhile, the alignment layer 25 is applied on the opposite substrate side and the alignment layer 18 and 25 are made alignment treatment so that the liquid crystal is twisted about 90°. Then, a normally white mode liquid crystal display device is assembled, as shown in FIG. 3, by placing the liquid crystal 26 between the substrates, and applying one polarizing plate on each side opposite to the surface in contact with the liquid crystal of the upper and lower substrates, so that the absorption axes become about 90° relative to each other. In FIG. 3, because of the large step in the contact portion (Step due to the gate insulating film 3 and insulating film 101) in the contact portion 17 connecting the pixel electrode 14 and pixel electrode 16, it is sometimes the case that light leakage occurs because of the alignment treatment process not being performed satisfactorily due to rubbing or the like. In order to block that light leakage, the contact portion 17 is disposed at a location so as to overlap with the shielding film 22 on the glass substrate 21 on the opposite substrate side.

[0066] Preferably, the thickness of the ITO film in the pixel electrodes 14 and 16 should be small, selected from a range between about 30 to 150 nm, for example, while the thickness of the insulating film 101 should be sufficiently large (for example, selected from a range of 500 nm or greater), so that irregularities caused by the constituent parts on the pixel electrodes are minimized.

[0067] Second Embodiment

[0068] The active matrix-type liquid crystal display device relating to the second embodiment of the present invention is explained using FIGS. 4 and 5. FIG. 4 is a plan view of a pixel in the TFT array substrate; and FIG. 5 is a cross sectional view of the pixel in FIG. 4 taken at the line A-A'. In FIGS. 4 and 5, elements of the constitution having the same symbols as in FIGS. 1 and 2 show identical portions or portions corresponding to the constitution in FIGS. 1 and 2 and an explanation thereof is omitted. In FIGS. 4 and 5, 2a is the adjacent gate line. In this second embodiment, the cumulative capacitance for stabilizing the pixel potential during the holding period is formed between the pixel electrode 14 and the adjacent preceding gate line 2a.

[0069] The method for manufacturing the liquid crystal display device relating to the second embodiment is explained next. The gate lines 2 and 2a are formed by the sputtering and formation of a metal film on the glass substrate 1, and then the patterning with photolithography and etching [of this film]. The pixel electrode 16 is then formed by the formation with sputtering and patterning of the ITO film. The gate insulating film 3 is then formed. The

TFT semiconductor thin film **5** is formed by the formation and patterning of a semiconductor thin film. After that, the source line **7**, source electrode **8**, and drain electrode **9** are formed by the formation with sputtering and patterning of a metal film. The TFT is formed by the removal of the part of the n-type semiconductor layer and i layer where there is no metal wiring pattern by etching in the pattern formation process.

[0070] Furthermore, after the formation of the insulating film **101**, the contact hole **15** for connecting the pixel electrode **14** and drain electrode **9**, and the contact hole **17** for connecting the pixel electrode **14** and pixel electrode **16** are formed. The pixel electrode **14** is then formed by the formation with sputtering and then patterning of the ITO. The pixel electrode **14** is then patterned so that part thereof overlaps the adjacent gate line **2a** and forms the storage capacitance in this overlapping portion.

[0071] Preferably, the thickness of the ITO film that is the pixel electrodes **14** and **16** should be small, selected from a range between about 30 to about 150 nm, for example, while the thickness of the insulating film **101** should be sufficient large (for example, selected from a range of 500 nm or greater), so that irregularities caused by the constituent parts on the pixel electrodes are minimized.

[0072] Third Embodiment

[0073] The active matrix-type liquid crystal display device relating to the third embodiment of the present invention is explained using FIGS. 6 and 7. FIG. 6 is a plan view of a pixel in the TFT array substrate; and FIG. 7 is a cross sectional view of the pixel in FIG. 6 taken at the line A-A'.

[0074] In FIGS. 6 and 7, elements of the constitution having the same symbols as in FIGS. 1 and 2 show identical portions or portions corresponding to the constitution in FIGS. 1 and 2 and an explanation thereof is omitted. In FIGS. 6 and 7, **2a** is the adjacent gate line. In this third embodiment, the storage capacitance for stabilizing the pixel potential during the holding period is formed between the pixel electrode **14** and the Cs line **11**, and between the pixel electrode **14** and the adjacent preceding gate line **2a**.

[0075] The method for manufacturing the liquid crystal display device relating to the third embodiment is explained next. The gate lines **2** and **2a** are formed by the sputtering and formation of a metal film on the glass substrate **1**, and then the patterning with photolithography and etching of this film. The pixel electrode **16**, comprising a first layer of ITO, is then formed by the formation with sputtering and patterning of the ITO film. The gate insulating film **3** is then formed. The TFT semiconductor thin film **5** is formed by the formation and patterning of a semiconductor thin film. After that, the source line **7**, source electrode **8**, and drain electrode **9** are formed by the formation with sputtering and patterning of a metal film. The TFT is formed by the removal of the part of the n-type semiconductor layer and i layer where there is no metal wiring pattern by etching in the pattern formation process.

[0076] Furthermore, after the formation of the insulating film **101**, the contact hole **15** for connecting the pixel electrode **14** and drain electrode **9**, and the contact hole **17** for connecting the pixel electrode **14** and pixel electrode **16** are formed. The pixel electrode **14** is then formed by the formation with sputtering and then patterning of the ITO.

The pixel electrode **14** is then patterned so that part thereof overlaps the preceding gate line **2a**, adjacent to the Cs electrode **11**, and forms the storage capacitance in this overlapping portion.

[0077] Fourth Embodiment

[0078] The active matrix-type liquid crystal display device, relating to the fourth embodiment of the present invention, is explained using the cross sectional view in FIG. 8.

[0079] In FIG. 8, elements of the constitution having the same symbols as in FIGS. 1, 2, and 3 show identical portions or portions corresponding to the constitution in FIGS. 1, 2, and 3 and an explanation thereof is omitted. In FIG. 8, **102** is an insulating film. In this fourth embodiment, the insulating film **102** is established in the layer below the gate line **2** and the pixel electrode **16** is established in the layer below the insulating film **102**. With a preferred embodiment, the thickness of the insulating film **102** is 200 nm, the thickness of the gate insulating film **3** is 400 nm, and the thickness of the interlayer insulating film **101** is 300 nm.

[0080] The method for manufacturing the liquid crystal display device relating to the fourth embodiment is explained next. The pixel electrode **16**, comprising a first layer of ITO, is formed by the formation with sputtering and patterning of the ITO film on the glass substrate **1**. The gate line **2** and Cs line **11** are then formed with the formation of the insulating film **102**, the formation by sputtering of a metal film thereon, and then the patterning by photolithography and etching thereof. The gate insulating film **3** is then formed. The TFT semiconductor thin film **5** is formed by the formation and patterning of a semiconductor thin film. The source electrode **8**, and drain electrode **9** are formed by the formation thereon with sputtering and patterning of a metal film. The TFT is formed by the removal of the part of the n-type semiconductor layer and i layer where there is no metal wiring pattern by etching in the pattern formation process.

[0081] Furthermore, after the formation of the insulating film **101**, the contact hole **15** for connecting the pixel electrode **14** and drain electrode **9**, and the contact hole **17** for connecting the pixel electrode **14** and pixel electrode **16** are formed. The pixel electrode **14** is then formed by the formation with sputtering and then patterning of the ITO.

[0082] Fifth Embodiment

[0083] The active matrix-type liquid crystal display device, relating to the fifth embodiment of the present invention, is explained using the cross sectional view in FIG. 9.

[0084] In FIG. 9, elements of the constitution having the same symbols as in FIGS. 1, 2, and 3 show identical portions or portions corresponding to the constitution in FIGS. 1, 2, and 3 and an explanation thereof is omitted. In this fifth embodiment, in particular, the pixel electrode **16** is established in the same layer as the source electrode **8** and drain electrode **9**.

[0085] The method for manufacturing the liquid crystal display device relating to the fifth embodiment is explained next. The gate line **2** and Cs line **11** are formed with the formation by sputtering of a metal film on the glass substrate **1**, and then the patterning by photolithography and etching

thereof. The gate insulating film **3** is then formed. The TFT semiconductor thin film **5** is formed by the formation and patterning of a semiconductor thin film. The pixel electrode **16**, comprising a first layer of ITO, is then formed by the formation with sputtering and patterning of the ITO film. The source electrode **8**, and drain electrode **9** are formed by the formation thereon with sputtering and patterning of a metal film. The TFT is formed by the removal of the part of the n-type semiconductor layer and i layer where there is no metal wiring pattern by etching in the pattern formation process.

[0086] Furthermore, after the formation of the insulating film **101**, the contact hole **15** for connecting the pixel electrode **14** and drain electrode **9**, and the contact hole **17** for connecting the pixel electrode **14** and pixel electrode **16** are formed. The pixel electrode **14** is then formed by the formation with sputtering and then patterning of the ITO.

[0087] In this example, the contact hole **17** is formed so as to connect the pixel electrode **14** comprising the second layer of ITO with the pixel electrode **16** comprising the first layer of ITO. However, as shown in FIG. 10, the drain electrode **9** and pixel electrode **16** may also be brought into direct contact by forming and patterning the pixel electrode **16**, comprising the first layer of ITO, after the formation of the source electrode **8**, and extending the pixel electrode **16**, comprising the first layer of ITO, to above the drain electrode **9**.

[0088] At this time, the pixel electrode **14** may also be connected with the drain electrode **9** as shown in FIG. 10, but may be connected with the pixel electrode **16** as well, as shown in FIG. 11. The pixel electrode **14** may of course be constituted so as to connect with both the drain electrode **9** and pixel electrode **16**.

[0089] Sixth Embodiment

[0090] The active matrix-type liquid crystal display device, relating to the sixth embodiment of the present invention, is explained using the cross sectional view in FIG. 12.

[0091] In FIG. 12, elements of the constitution having the same symbols as in FIGS. 1, 2, and 3 show identical portions or portions corresponding to the constitution in FIGS. 1, 2, and 3 and an explanation thereof is omitted. In this sixth embodiment, in particular, the gate insulating film **3** is disposed in the layer above the gate line **2**, and the pixel electrode **16** is established in the layer above the gate insulating film **3**. The gate insulating film **103** is then disposed in a layer above the pixel electrode **16** and the interlayer insulating film **101** is disposed in a layer above this gate insulating film **103**. Moreover, the pixel electrode **14** is established in the layer above the interlayer insulating film **101**.

[0092] The method for manufacturing the liquid crystal display device relating to the sixth embodiment is explained next. The gate line **2** and Cs line **11** are formed with the formation by sputtering of a metal film on the glass substrate **1**, and then the patterning by photolithography and etching thereof. The first layer of gate insulating film **3** is then formed. After that, the pixel electrode **16**, comprising a first layer of ITO, is then formed by the formation with sputtering and patterning of the ITO film. The second layer of gate insulating film **103** is then formed. The TFT semiconductor

thin film **5** is formed by the formation and patterning of a semiconductor thin film. The source electrode **8**, and drain electrode **9** are formed by the formation thereon with sputtering and patterning of a metal film. The TFT is formed by the removal of the part of the n-type semiconductor layer and i layer where there is no metal wiring pattern by etching in the pattern formation process.

[0093] Furthermore, after the formation of the insulating film **101**, the contact hole **15** for connecting the pixel electrode **14** and drain electrode **9**, and the contact hole **17** for connecting the pixel electrode **14** and pixel electrode **16** are formed. The pixel electrode **14** is then formed by the formation with sputtering and then patterning of the second layer of ITO.

[0094] Moreover, in the example discussed above, an example wherein the present invention is applied to a transparent liquid crystal display device using a transparent electrode such as ITO for the pixel electrode is explained. However, the present invention is not limited to this and may also be applied to a reflective liquid crystal display device using an electrode such as aluminum (Al) for the pixel electrode.

[0095] Also, in the example discussed above, an example is explained wherein only the alignment layer **18** is disposed in the layer above the pixel electrode **14**, but the present invention is not limited to this and an insulating layer may also be established between the pixel electrode **14** and alignment layer **18**.

[0096] As discussed above, in a liquid crystal display device relating to the present embodiment of the invention, the pixel electrodes have a structure wherein an insulating film is formed in the layer above the first layer transparent electrode, a second layer of transparent electrode is formed in this layer so that part does not overlap the first layer transparent electrode, and the first and second layers of transparent electrode are connected by a contact hole or the like. As a result, it is very easy to set the potential level of the pixel electrode because the first and second layers are connected with the same switching element (TFT) and the same potential level can therefore be supplied thereto.

[0097] Also, in a liquid crystal display device relating to the present embodiment of the invention, each of the pixel electrodes has a structure wherein an insulating film is formed in the layer above the first layer transparent electrode, a second layer of transparent electrode is formed in this layer so that part does not overlap the first layer transparent electrode, and the first and second layers of transparent electrode are connected by a contact hole or the like. As a result, it becomes easy to apply electrical fields of different sizes in regions of the same pixel, as the electric field intensities applied to the liquid crystal between this substrate and the opposite substrate. The difference in the transmittances in the V-T characteristics due to the change in viewing angle, in the case where a plurality of regions with different properties is seen as the same pixel, can be made small and the viewing angle range can therefore be expanded. Furthermore, there is no need to create regions where the insulating film remains and where the insulating film is removed on the same pixel electrode as before. It therefore becomes unnecessary to consider the occurrence of problems such as light leakage or the like due to the disorder in the orientation of the liquid crystal because of structural

steps present at the boundary from the presence of the insulating film. It becomes possible to increase the thickness of the insulating film and the viewing angle range can be greatly expanded.

[0098] Specifically, as shown in FIG. 13(a), the viewing angle improvement effects are mostly not achieved when the thickness of the insulating film established between the first layer of pixel electrode and second layer of pixel electrode is about 400 nm. This thickness must be at least 500 nm to 600 nm and sufficient viewing angle improvement effects can be attained when this thickness is greater than about 800 nm. In FIG. 13(a), the improved viewing angle means the improved angle relative to the gray-scale inversion. The thickness of the insulating film established between the first layer pixel electrode and the second layer pixel electrode changes with the relationships between the anisotropy of the dielectric constant of the liquid crystal normally used in the TN mode and the dielectric constant of the insulating film material, as well as the thickness and dielectric constant of the insulating film disposed on the second pixel electrode. However, this thickness can also be expressed as the ratio of the voltage applied to the liquid crystal layer between the transparent electrode 24 opposite to the first pixel electrode and separated from the liquid crystal, and the transparent electrode 24 opposite to the second pixel electrode and separated from the liquid crystal. The 400 nm in FIG. 13(a) corresponds to a 0.9:1.0 voltage ratio and it is preferable that the abovementioned voltage ratio be greater than this. Also, in terms of the driving voltage, power consumption, cost of the driving circuit, and so forth, the upper limit to the abovementioned voltage ratio is preferably 0.5:1.0.

[0099] In order to widen the viewing angle, it is necessary to apply electrical fields of different sizes depending on the region in the same pixel as the electrical field intensity applied to the liquid crystal between the opposite electrodes. It is also necessary to consider at what percentage the region of liquid crystal, substantially controlled by the first layer pixel electrode and opposite electrode, constitutes each of the pixels. This ratio can be calculated as the ratio between the area of the region without the second layer pixel electrode but with the first layer pixel electrode in the region specified as the opening portion of the pixel, to the area of the region specified as the opening portion of the pixel. In order to stabilize the viewing angle improvement effects as shown in FIG. 13(b), an area of several percent or greater at the least is necessary. When this ratio is made larger, however, contrast has a tendency to decrease and it is therefore necessary to select the optimal ratio for the display characteristics with consideration of the viewing angle improvement effects and the decrease in contrast. This ratio can be selected from a range of several to several tens in percentage, but is preferably in the range of several to 75% in order to attain good viewing angle improvement effects and desirable contrast.

[0100] Also, in the active matrix-type liquid crystal display device relating to this embodiment of the present invention, the individual pixel electrodes have a constitution wherein an insulating film is formed in the layer above the first layer transparent electrode, a second layer transparent electrode is formed on this layer so that part of the region does not overlap the first layer transparent electrode, and the first and second layer transparent electrodes are connected with a contact hole or the like. Because of this, there is no need to create regions where the insulating film remains and where the insulating film is removed on the same pixel electrode as before. Even in the case where the electrical

field intensities applied to the liquid crystal vary greatly depending on the region of the pixel, the thickness of the insulating film present between the first layer and second layer can be made greater. In this case as well, there is no light leakage or the like due to the disorder in the orientation of the liquid crystal because of structural steps present at the boundary from the presence of the insulating film. Because the brightness of black display state can be made small, it becomes possible to realize a liquid crystal display device having a high contrast display function.

[0101] Seventh Embodiment

[0102] FIG. 14 shows the constitution of the liquid crystal display device relating to the seventh embodiment. In this drawing, 301a and 301b are polarizing plates, 302a and 302b are optical compensating films having stabilized the orientation state of discotic liquid crystal, 303a and 303b are substrates, 304 is liquid crystal, 305a and 305b are the rubbing directions for the upper and lower substrates, and 306 is the backlight. The driving circuit for driving the active matrix is not shown.

[0103] Here, the optical compensating films 302a and 302b are optical compensating films wherein the orientation state of the discotic liquid crystal is stabilized. The actual constitution of this optical compensating film is disclosed in Japanese Patent Laid-open Publication Nos. H08-50204, H08-50270, H08-95030, H08-95034, and H09-5524, for example. These optical compensating films 302a and 302b are thought to provide a hybrid orientation to the discotic liquid crystal, wherein the director angle changes continuously in the depthwise direction. For this reason, the absolute value of retardation has a minimum value that is not 0 in any direction and the direction where minimum retardation value shown is inclined at 5° to 50° from the normal to the optical compensating films (see FIG. 24). The anisotropy of the index of refraction is negative for the discotic liquid crystal. In other words, as modeled in FIG. 25, the index of refraction  $n_r$  in the radial direction and the index of refraction  $n_d$  in the depthwise direction of a disk-shaped compound have a relationship where  $n_r > n_d$ . For this reason, as the entire optical compensating film, when  $x$  is the direction of the inclination of the discotic compound as shown in FIG. 26(b) and the indices of refraction in the  $x$ ,  $y$ ,  $z$  directions shown in FIG. 26(a) are  $n_x$ ,  $n_y$ , and  $n_z$  respectively, the index of refraction  $n_x$  and  $n_y$  in the surface and the index of refraction  $n_z$  in the depthwise direction have a relationship wherein  $n_x > n_y > n_z$ . The direction in which this director inclined direction is projected onto the film surface is shown by the arrows on the optical compensating films 302a and 302b in FIG. 14. It could be verified that it is desirable for the product of the birefringence  $\Delta n$  of the liquid crystal 304 and the thickness  $d$  of the liquid crystal layer to satisfy the relationship of  $0.30 \mu\text{m} \leq \Delta n \cdot d \leq 0.50 \mu\text{m}$ . It could be verified that  $0.34 \mu\text{m} \leq \Delta n \cdot d \leq 0.42 \mu\text{m}$  was more preferable.

[0104] Moreover, the actual constitution of the liquid crystal display elements to be applied in the liquid crystal display device relating to the seventh embodiment need not be constituted as explained in the first through sixth embodiments and may also have a structure where different voltages can be applied to the same pixel. For example, [the structure] can also be applied to the conventional structure shown in FIGS. 18 and 19, specifically, to a structure wherein an insulating film is prepared on the pixel electrode, and part of the insulating film is removed from the pixel electrode. Furthermore, by applying potential to the lower pixel elec-

trode and forming a capacitor between the upper and lower pixel electrodes without connecting the two layers of pixel electrode together, different voltages can be applied to the same pixel because the potential of the upper pixel electrode is controlled by coupling. This structure was disclosed in, for example, "Active-matrix LCDs using gray-scale in halftone methods" (Kalluri R. Sarma et al., Phoenix Technology Center, Honeywell, Inc. Phoenix, Ariz., SID 89 Digest P148 to P150).

[0105] The method for manufacturing the liquid crystal display device relating to the seventh embodiment is explained next. First, alignment layer is applied to the substrate **303b** whereon the pixel electrodes are disposed a matrix form and the substrate **303a** forming the opposite electrode (not shown); and this undergoes heat treatment for 30 minutes at 200° C. Next, the rubbing process is performed so that the orientation of the liquid crystal is approximately 90° to the alignment layer. The orientation of the liquid crystal becomes about 90° when the twisting angle of the liquid crystal layer held between the upper and lower substrates is in a range of 70° to 100°. If the twisting angle of the liquid crystal layer is within the abovementioned range, good voltage to transmittance response is attained for the normally white mode. Also, the pretilt angle of the liquid crystal is preferably 3° to 9°. When the pretilt angle is less than 3°, regions of undesired orientation occur easily in the effective display portion of the pixels and this brings about a deterioration in the display quality. On the other hand, if 9° or greater, the transmittance in the white display state decreases and this results in a drop in the display brightness. Next, a spacer comprising plastic beads is applied to one of the substrate so that the thickness of the liquid crystal layer becomes 4.3  $\mu\text{m}$  and the two substrates **303a** and **303b** are laid over each other. At this time, the substrates are surrounded with a sealing material except for part of the circumference and the liquid crystal **304** is injected with a vacuum injection method. The injected liquid crystal **304** is a liquid crystal having a birefringence  $\Delta n=0.089$ . Next, polarizing plates **301a** and **301b** (from Sanritsu Co.), whereon are applied the optical compensating films **302a** and **302b**, are applied to each of the substrates **303a** and **303b**, so that the rubbing direction and the direction of the inclination of the discotic liquid crystal match as shown in **FIG. 14**. The absorption axes of the polarizing plates are about 90° between **301a** and **301b**. Furthermore, the driving circuit is installed, the device is combined with the backlight, and the normally white mode liquid crystal display device is prepared.

[0106] **FIG. 15** shows the relationship between the vertical relative transmittance and angle when voltage is applied so that the relative transmittance becomes 100%, 75%, 50%, 25%, and black display at normal to the panel surface of the liquid crystal display device relating to the present embodiment. As shown in the drawing, the downward half tone gray-scale inversion does not cross by about -50° and inversion does not occur until this angle.

[0107] The angle at which the gray-scale inversion occurs is clearly widened downwards in comparison to the about -25° in the case of applying the structure wherein different voltages are applied to the same pixel in the TN mode and the optical compensating films are not applied, shown in **FIG. 16**.

[0108] Also, the angle at which the gray-scale inversion occurs is clearly widened downwards in comparison to the about -45° in the measured results for the conventional

liquid crystal display device in the TN mode shown in **FIG. 23**, with the optical compensating films, for fixing the orientation of the discotic liquid crystal, inserted between the upper and lower substrates and the polarizing plates.

[0109] The reason why these results were attained is determined as follows. Optical compensation is carried out by combining the liquid crystal layer with the optical compensation film. However, in the case where the liquid crystal display panel in the state of displaying black or half tones gray is viewed from an angle, there is retardation for which optical compensation is not possible depending on this combination. However, by employing a structure wherein two or more voltages are applied in one pixel, there is an additional effect wherein the inclination of the gray-level-brightness characteristics is reduced and the angle at which gray-scale inversion occurs can be widened downwards.

[0110] Any liquid crystal material that is one the market for active matrix applications can be used for the liquid crystal used in the present invention. The viewing angle improvement effects are mostly not attained when the thickness of the insulating film established between the first layer pixel electrode and second layer pixel electrode, in the case where the invention according to the seventh embodiment is applied to the liquid crystal display device according to the first and sixth embodiments, is about 400 nm as shown in **FIG. 13(a)**. [This thickness] must be 500 nm to 600 nm or more at the least and sufficient viewing angle improvement effects can be attained when about 800 nm or greater.

[0111] With the present invention, the method for improving the viewing angle range does not need to be a step structure, due to the presence of insulating films or the like as before, to vary the electrical field strength applied to the liquid crystal depending on the region in each of the pixels. It is therefore possible to eliminate light leakage due to the disorder in the orientation of the liquid crystal at step portions or near the base of step portions, and to realize a liquid crystal display device with a wide viewing angle range, low black brightness, and high contrast.

[0112] While preferred embodiments of the present invention have been described using specific terms, such description is for illustrative purposes only, and it is to be understood that changes and variations may be made without departing from the spirit or scope of the following claims.

What is claimed is:

1. A liquid crystal display device having transistors disposed at the intersections of gate lines and source lines, pixel electrodes connected with the transistors, opposite electrodes opposite to these pixel electrodes, and liquid crystal held between said opposite electrodes and said pixel electrodes:

wherein said pixel electrodes comprise a first pixel electrode and a second pixel electrode disposed in a layer above an insulating film which is itself disposed in a layer above the first pixel electrode, and having a region that does not overlap with the first pixel electrode; and

the first pixel electrode and second pixel electrode are electrically connected.

2. The liquid crystal display device, according to claim 1, wherein the cumulative capacitance for stabilizing the pixel potential during the holding period is formed between said second pixel electrode and a storage capacitance electrode

line and/or between the second pixel electrode and the preceding gate line adjacent thereto.

3. The liquid crystal display device, according to claim 1, wherein said first pixel electrode is disposed in the same layer as the gate line.

4. The liquid crystal display device, according to claim 1, wherein an insulating film is disposed in the layer below said gate line, and said first pixel electrode is disposed in the layer below said insulating film.

5. The active matrix liquid crystal display device, according to claim 1;

wherein a first gate insulating film is disposed in the layer above said gate line;

said first pixel electrode is disposed in the layer above the first gate insulating film;

a second gate insulating film is disposed in the layer above the first pixel electrode;

an interlayer insulating film is disposed in the layer above the second gate insulating film; and

said second pixel electrode is disposed in the layer above the interlayer insulating film.

6. The liquid crystal display device, according to claim 1, wherein said first pixel electrode is disposed in the same layer as the drain lines of said transistors.

7. The liquid crystal display device, according to claim 6, wherein said first pixel electrode is directly connected to the drain electrode of said transistors.

8. The liquid crystal display device, according to claim 1, wherein said second pixel electrode comprises an opening in the central portion, and said insulating film and said liquid crystal are held between said first pixel electrode and opposite electrode in that opening.

9. A liquid crystal display device having transistors disposed at the intersections of gate lines and source lines, pixel electrodes connected with the drain electrodes of the transistors, opposite electrodes opposite to these pixel electrodes, and liquid crystal held between said opposite electrodes and said pixel electrodes:

wherein said pixel electrodes comprise a first pixel electrode and a second pixel electrode disposed in a layer above an insulating layer which is itself disposed in a layer above the first pixel electrode, and having a region that does not overlap with the first pixel electrode; and

the first pixel electrode and second pixel electrode are electrically connected with said drain electrode.

10. The liquid crystal display device, according to claim 1 or 9, wherein the area of the region in said first pixel electrode that does not overlap the second pixel electrode is from several to several tens in percentage with respect to the area of the opening effective for the display.

11. The liquid crystal display device, according to claim 1 or 9, wherein the thickness of said insulating layer is 500 nm or greater.

12. The liquid crystal display device, according to claim 1 or 9, wherein said pixel electrode is a transparent electrode.

13. The liquid crystal display device, according to claim 1 or 9, wherein the ratio of the voltages applied to said first pixel electrode and said second pixel electrode is 0.5:1.0 to 0.9:1.0.

14. A liquid crystal display device, having liquid crystal held between a pair of substrates and comprising a plurality of pixels, wherein said liquid crystal display device further comprises:

means for applying different voltages to one pixel;

an alignment layer disposed on the surface of each substrate in contact with the liquid crystal, for orienting said liquid crystal, and;

a polarizing plate disposed on the surface opposite to the surface of each of said substrates in contact with the liquid crystal; and

optical compensating film disposed between said polarizing plate and said substrate having stabilized the orientation state of the liquid crystal.

15. The liquid crystal display device, according to claim 14, wherein the product of the birefringence  $\Delta n$  of said liquid crystal and the thickness  $d$  of the liquid crystal layer satisfies the relationship of  $0.30 \mu\text{m} \leq \Delta n \cdot d \leq 0.50 \mu\text{m}$ .

16. A method for manufacturing a liquid crystal display device having transistors disposed at the intersections of gate lines and source lines, pixel electrodes connected with the transistors, opposite electrodes opposite to these pixel electrodes, and liquid crystal held between said opposite electrodes and said pixel electrodes, wherein said method comprises:

a step for manufacturing a first pixel electrode;

a step for manufacturing an insulating layer in a layer above the first pixel electrode; and

a step for manufacturing a second pixel electrode in a layer further above the insulating layer, said second pixel electrode having a region that does not overlap the first pixel electrode, and being electrically connected with the first pixel electrode.

17. A method for manufacturing a liquid crystal display device having transistors disposed at the intersections of gate lines and source lines, pixel electrodes connected with the drain electrodes of the transistors, opposite electrodes opposite to these pixel electrodes, and liquid crystal held between said opposite electrodes and said pixel electrodes, wherein said method comprises:

a step for manufacturing a first pixel electrode electrically connected with said drain electrode;

a step for manufacturing an insulating layer in a layer above the first pixel electrode; and

a step for manufacturing a second pixel electrode in a layer further above the insulating layer, said second pixel electrode having a region that does not overlap the first pixel electrode, and being electrically connected with said drain electrode.

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当前申请(专利权)人(译)	三菱电机株式会社		
[标]发明人	NAKAYAMA AKIO NAGANO SHINGO MIZUNUMA MASAYA KOBAYASHI KAZUHIRO KOYAMA HITOSHI		
发明人	NAKAYAMA, AKIO NAGANO, SHINGO MIZUNUMA, MASAYA KOBAYASHI, KAZUHIRO KOYAMA, HITOSHI		
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摘要(译)

本发明公开了一种液晶显示装置，其具有设置在栅极线和源极线的交叉点处的晶体管，与晶体管连接的像素电极，与这些像素电极相对的相对电极，以及保持在所述相对电极和所述像素电极之间的液晶。像素电极包括第一像素电极和第二像素电极。第二像素电极设置在绝缘层上方的层中，绝缘层本身设置在第一像素电极上方的层中。第二像素电极具有不与第一像素电极重叠的区域。第二像素电极与第一像素电极电连接。

