



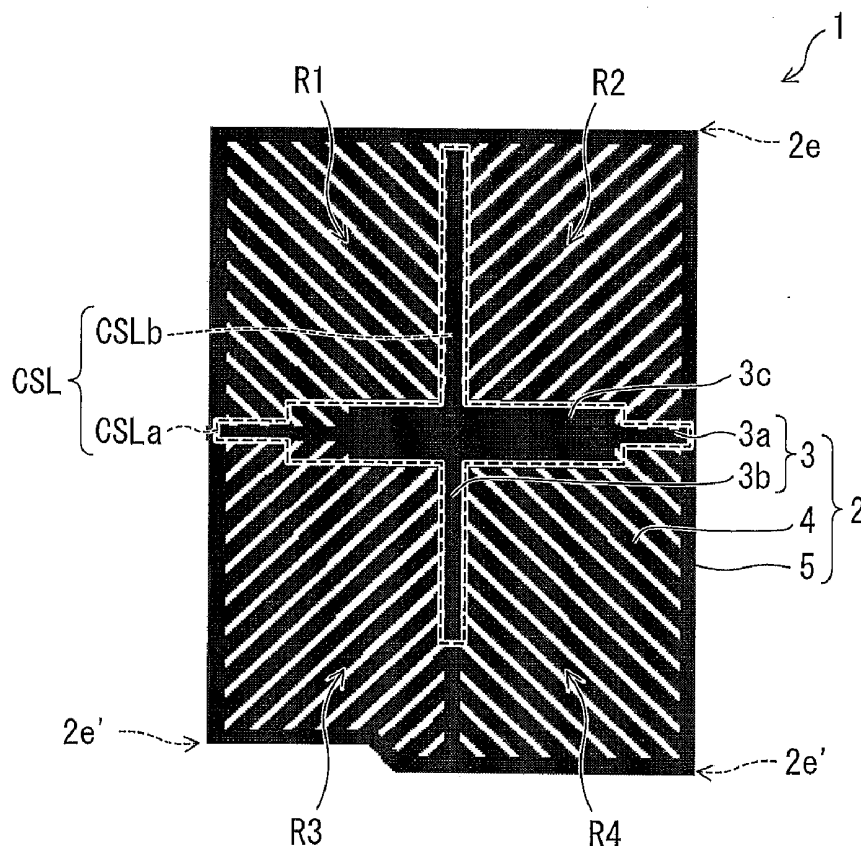
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(19) **United States**(12) **Patent Application Publication**
Iyama et al.(10) **Pub. No.: US 2012/0306731 A1**(43) **Pub. Date: Dec. 6, 2012**(54) **LIQUID CRYSTAL DISPLAY DEVICE****Publication Classification**(75) Inventors: **Yuichi Iyama**, Osaka-shi (JP);
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G09G 3/36 (2006.01)
(52) **U.S. Cl.** **345/87**(73) Assignee: **SHARP KABUSHIKI KAISHA**,
Osaka-shi, Osaka (JP)(21) Appl. No.: **13/577,987**(22) PCT Filed: **Dec. 20, 2010**(86) PCT No.: **PCT/JP2010/072931**§ 371 (c)(1),
(2), (4) Date: **Aug. 9, 2012**(30) **Foreign Application Priority Data**

Feb. 16, 2010 (JP) 2010-031631

(57) **ABSTRACT**

Disclosed is a liquid crystal display device of active matrix type. Each pixel electrode (1) includes either a single regional electrode (2) or two or more regional electrodes (2) which are electrically connected to each other. The or each regional electrode is provided with: a first electrode (cross-shaped electrode) (3) which has a pattern dividing a first region into a plurality of second regions (R1, R2, R3, and R4); and a plurality of stripe electrodes (4) which are provided in each of the second regions so as to extend from the first electrode and so as to be separated from each other by a distance. A storage capacitor line (CSL) is provided facing one of pixel electrodes (1) in a film thickness direction to form a storage capacitor. The storage capacitor line (CSL) is provided so as not to extend facing an edge (2e, 2e') of a first region in the film thickness direction parallelly to the edge (2e, 2e'). The invention eliminates irregular orientation of liquid crystal molecules in polymer sustained alignment.



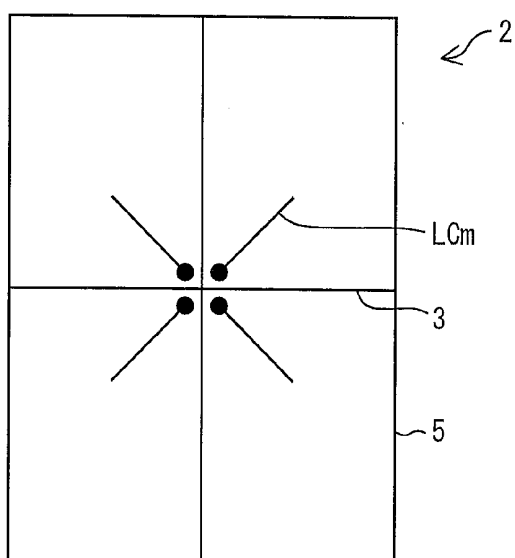


FIG. 3

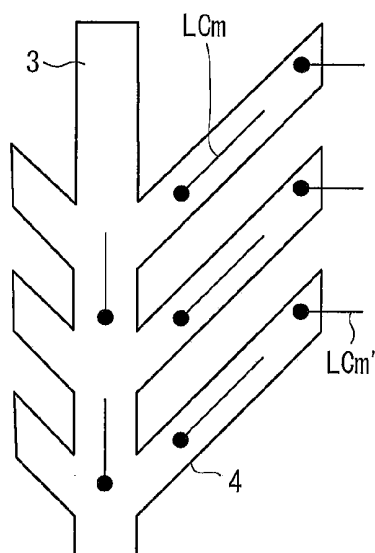


FIG. 4

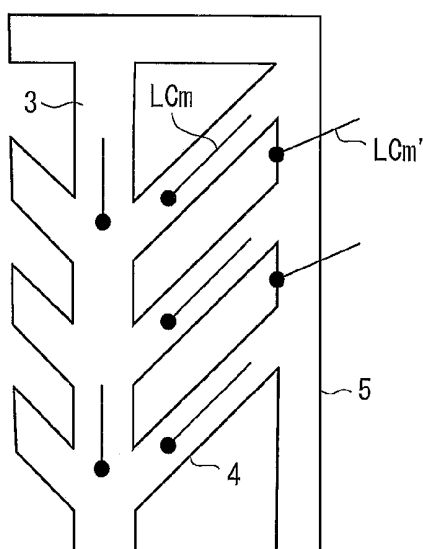


FIG. 5

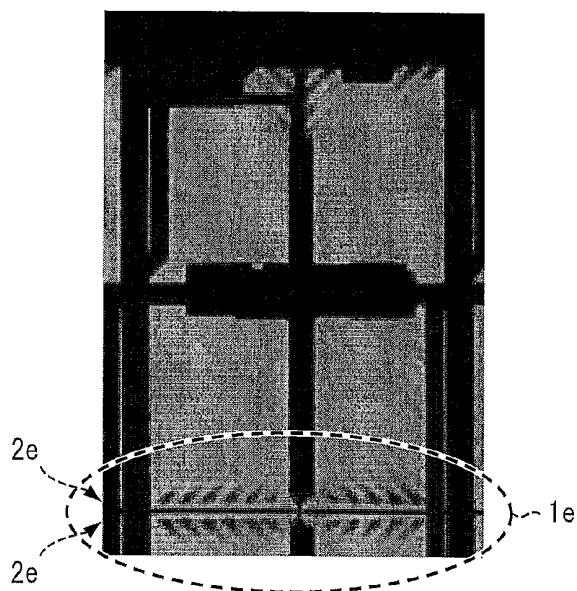


FIG. 6

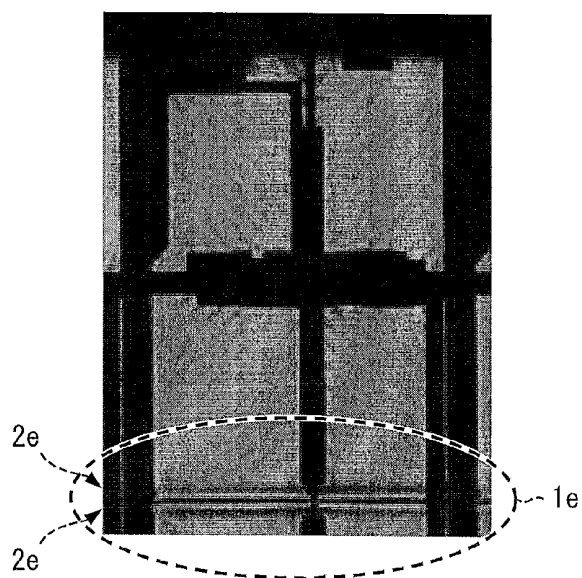
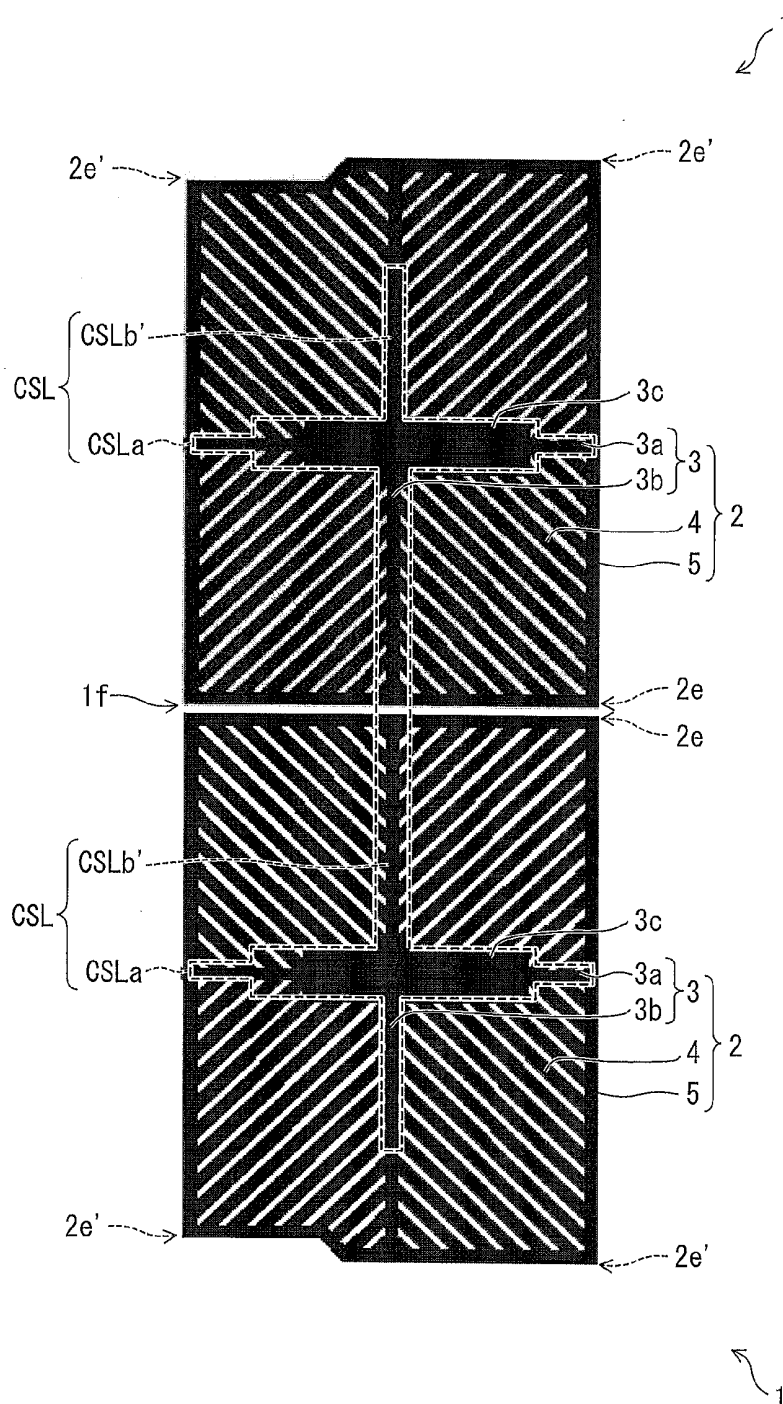


FIG. 7



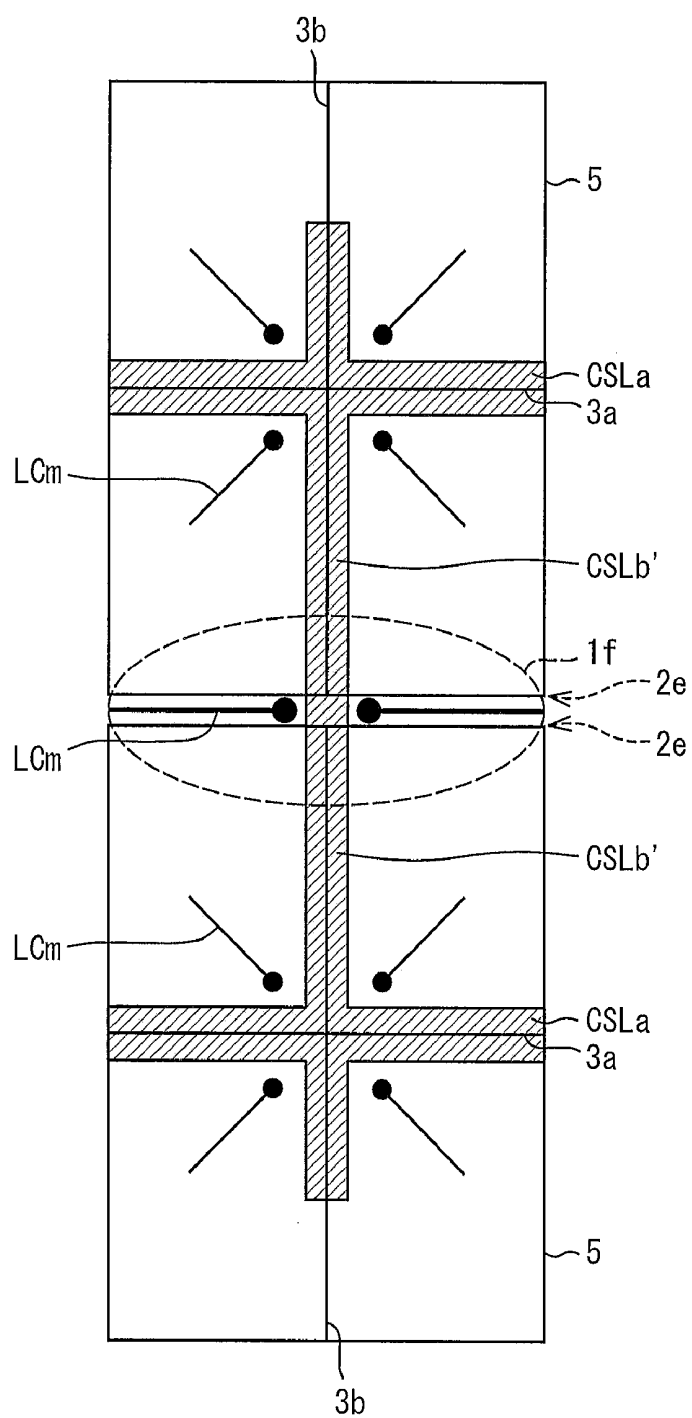


FIG. 9

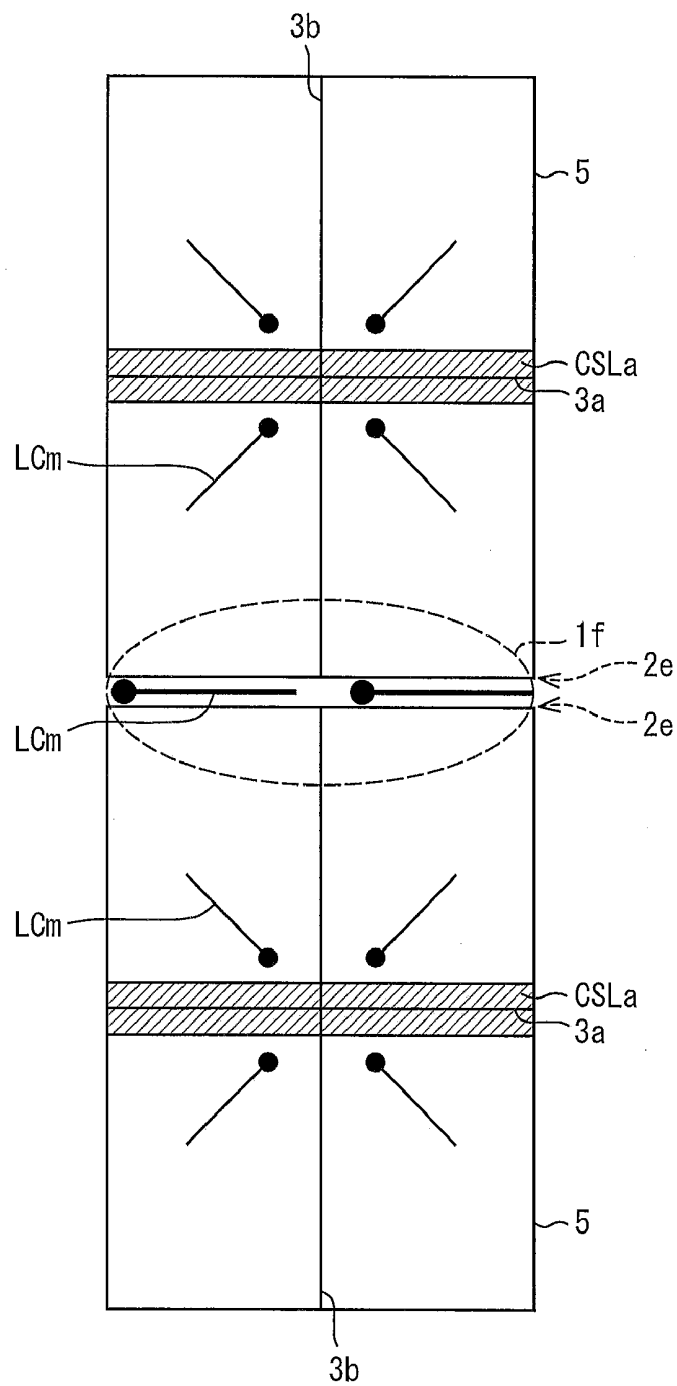


FIG. 10

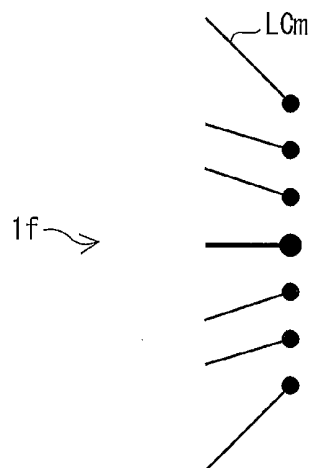


FIG. 11

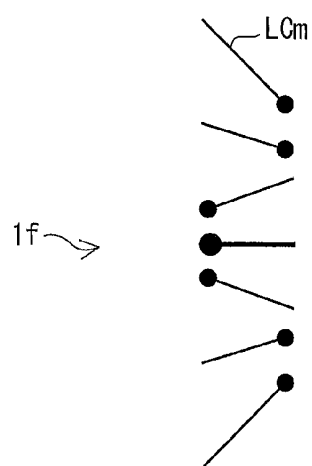


FIG. 12

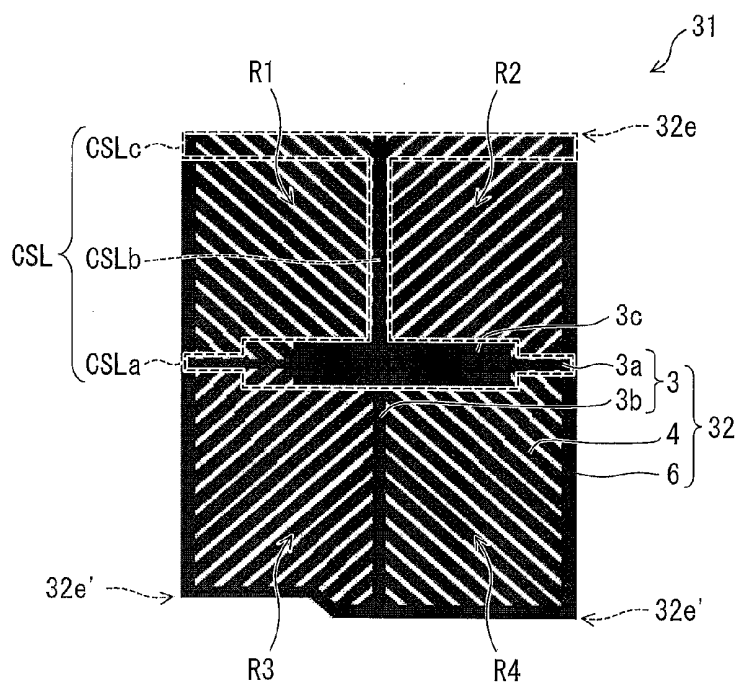


FIG. 13

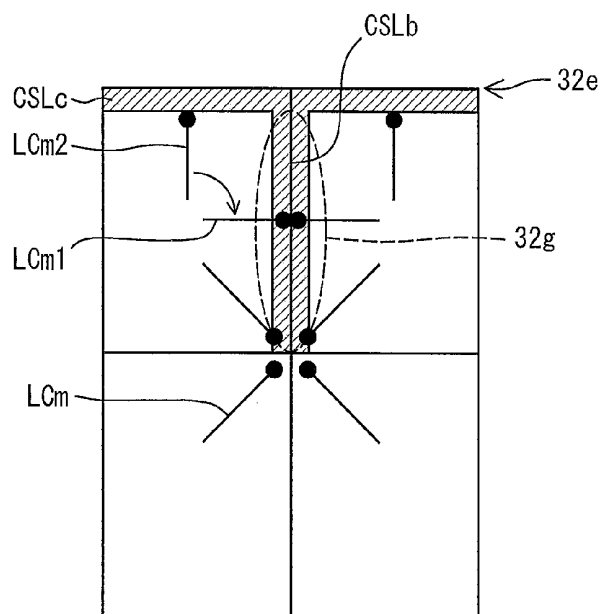


FIG. 14

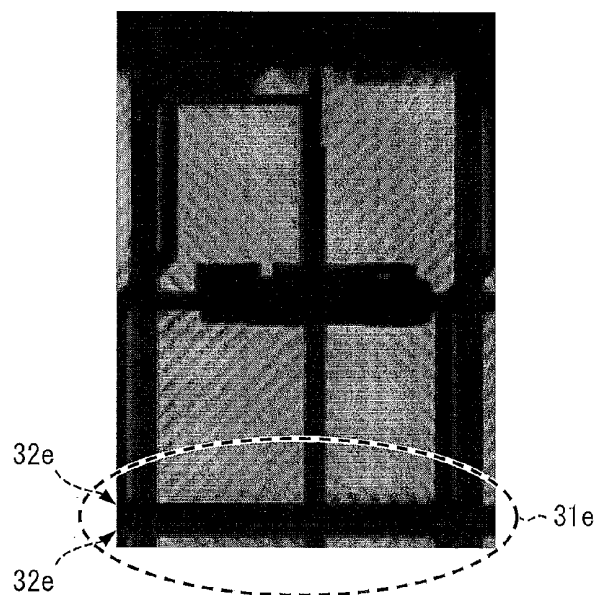


FIG. 15

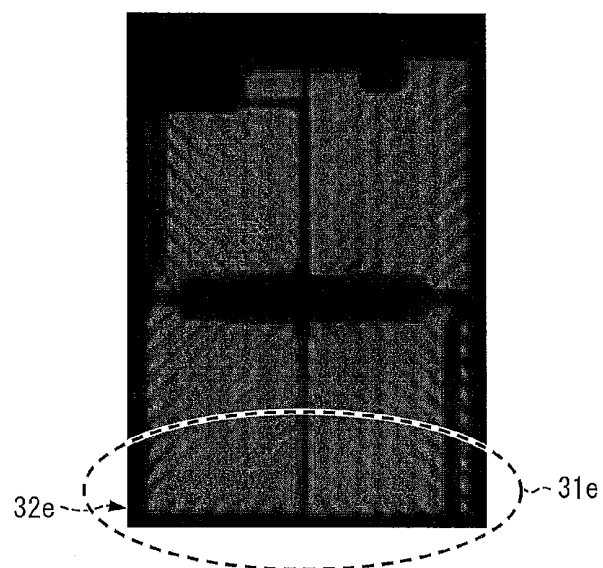


FIG. 16

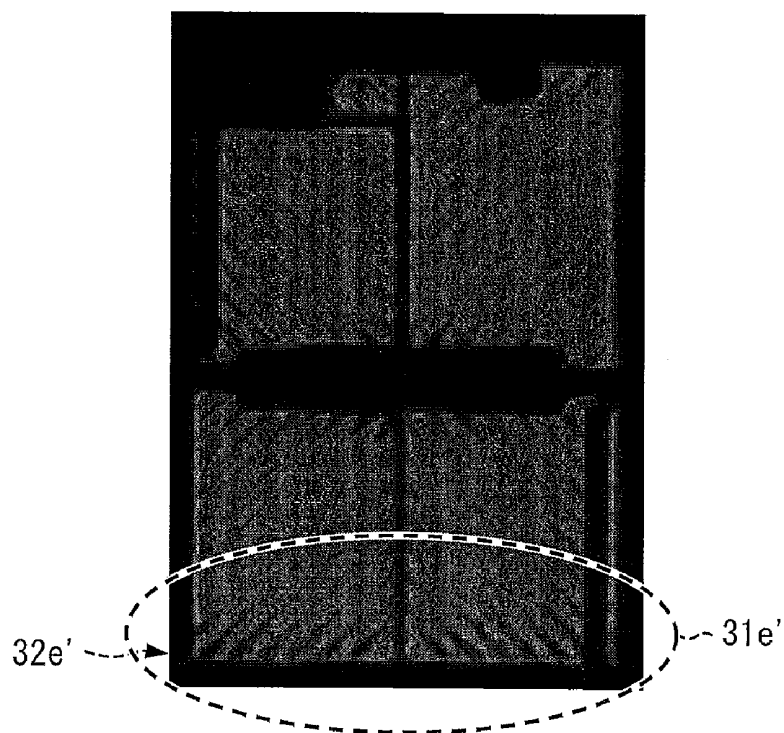


FIG. 17

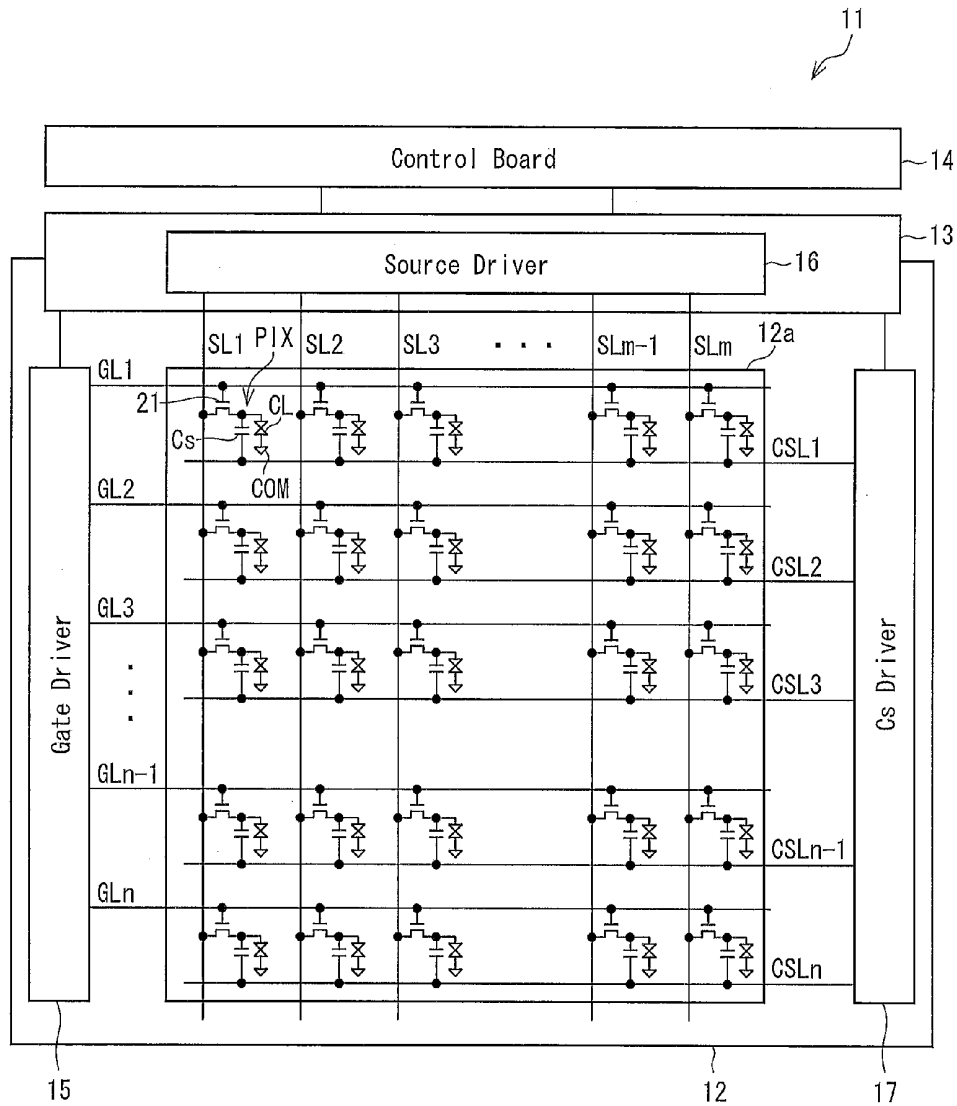


FIG. 18

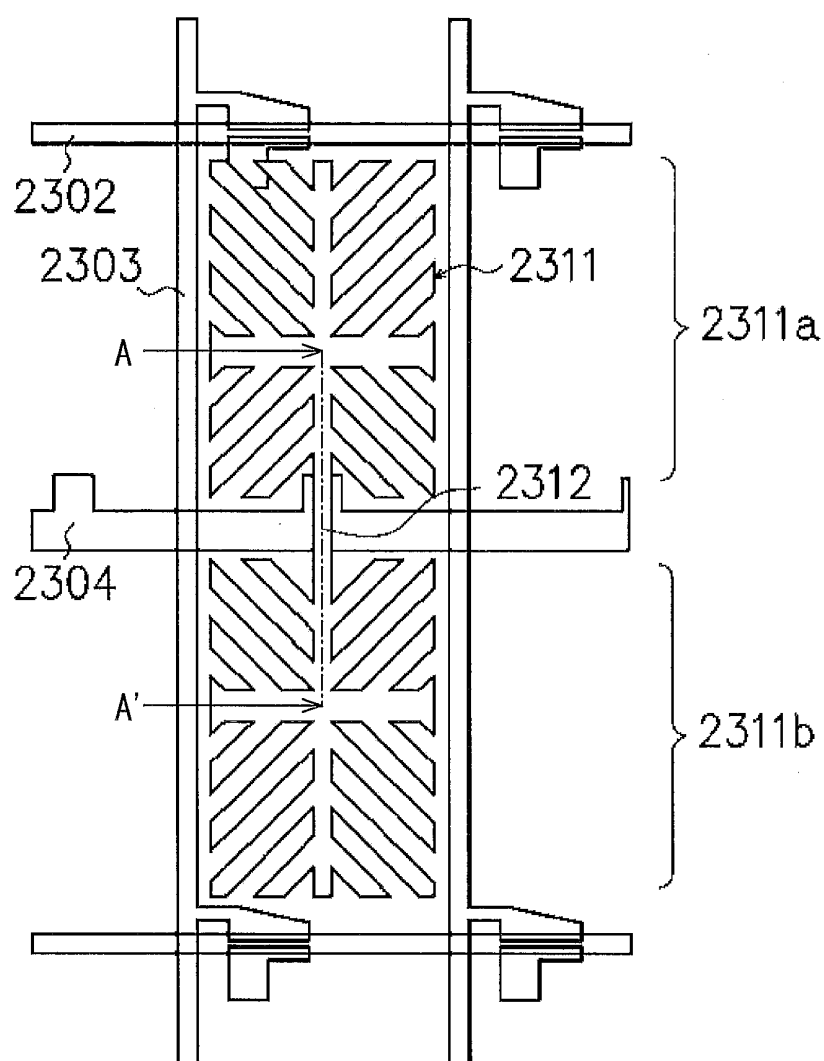


FIG. 19

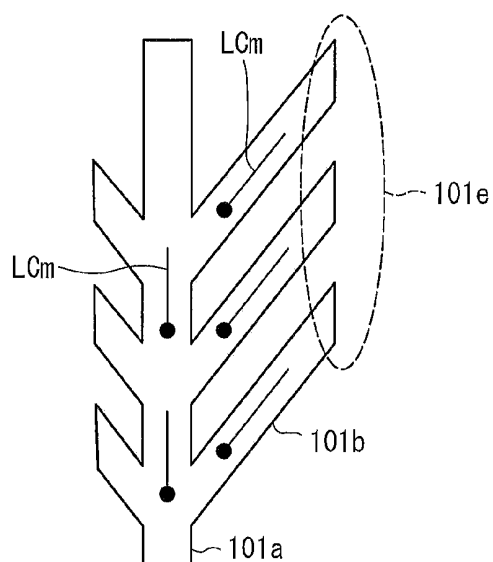


FIG. 20

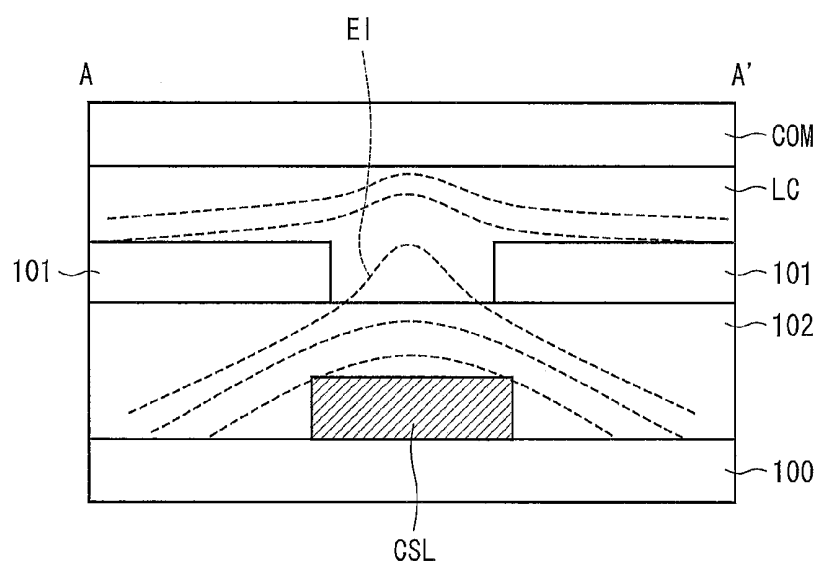


FIG. 21

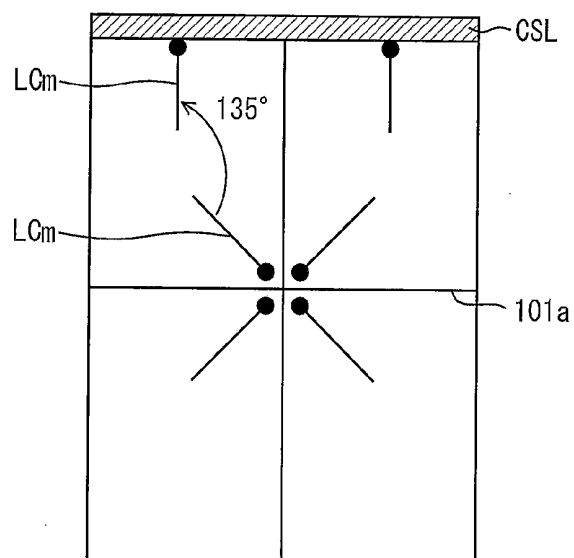
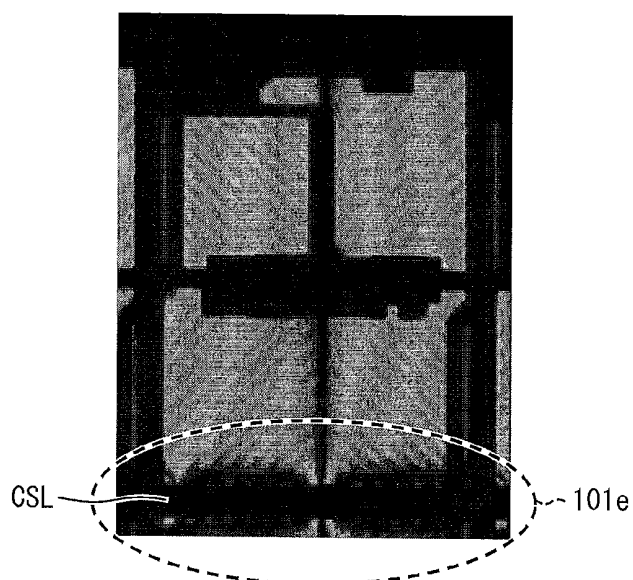


FIG. 22



LIQUID CRYSTAL DISPLAY DEVICE

TECHNICAL FIELD

[0001] The present invention relates to liquid crystal display devices including pixel electrodes including a plurality of stripe electrodes separated from each other by a distance.

BACKGROUND ART

[0002] MVA (Multi-domain Vertical Alignment)-driven liquid crystal display devices are widely known which realize wide viewing angles by dividing a liquid crystal layer into domains and changing the orientation into which liquid crystal molecules tilt from one domain to the other. Some liquid crystal display devices of this type incorporate a so-called “fish bone” pixel electrode. The electrode includes a plurality of stripe electrodes formed in a pattern that runs in different directions in different domains, and the stripe electrodes act as an alignment regulator for the formation of the domains used in MVA driving.

[0003] FIG. 18 represents a pixel arrangement for the liquid crystal display device, described in Patent Literature 1, which includes pixel electrodes including a plurality of stripe electrodes.

[0004] FIG. 18 shows a pixel formed in an area surrounded by two adjacent gate lines 2302 and two adjacent data lines 2303. There is provided a Cs line 2304 parallel to the gate line 2302 so as to divide the pixel area into upper and lower halves. A pixel electrode 2311 is provided in each of the upper half area 2311a and the lower half area 2311b. The two pixel electrodes 2311 are connected to each other by a pixel electrode 2312. The upper and lower pixel electrodes 2311 each include a cross-shaped electrode and a plurality of stripe electrodes which extend at angles from the cross-shaped electrode. The stripe electrodes extend in a different direction in each of the four regions formed by the cross-shaped electrode.

[0005] In the pixel electrode including the stripe electrodes, narrow slits (gaps between the stripe electrodes) as well as the stripe electrodes act as an alignment regulator, so that liquid crystal molecules can orient in the directions of a pattern when voltage is applied across the liquid crystal layer. This feature works as follows. Referring to FIG. 19, the liquid crystal molecules LCm on the stripe electrodes 101b tilt toward a stem of the cross-shaped electrode 101a (as opposed to toward a pixel edge 101e) under the influence of an electric field generated. The liquid crystal molecules therefore tilt in four different directions, one in each domain, when voltage is applied. The feature thus results in wide viewing angles.

[0006] The alignment regulation by use of stripe electrodes and narrow slits, however, does not yield sufficient electric field for regulation of orientation; the liquid crystal molecules fail to orient in a well-organized manner if voltage is applied abruptly across the liquid crystal layer. For this reason, in the manufacturing process, the voltage applied across the liquid crystal layer is changed slowly to achieve stable alignment of the liquid crystal molecules. PVA (polymer sustained alignment) is then performed on the liquid crystal molecules in such a condition. Polymer sustained alignment is a technique to blend liquid crystal with monomer which is subsequently light-irradiated or heated for polymerization, so that the poly-

mer can preserve the pretilt angles of the liquid crystal molecules (see, for example, Patent Literatures 2 and 3).

CITATION LIST

Patent Literature

- [0007] Patent Literature 1
- [0008] Japanese Patent Application Publication, Tokukai, No. 2007-249243A (published Sep. 27, 2007)
- [0009] Patent Literature 2
- [0010] Japanese Patent Application Publication, Tokukai, No. 2003-149647A (published May 21, 2003)
- [0011] Patent Literature 3
- [0012] Japanese Patent Application Publication, Tokukai, No. 2006-215326A (published Aug. 17, 2006)

SUMMARY OF INVENTION

Technical Problem

[0013] Voltage needs to be applied across the liquid crystal layer to perform polymer sustained alignment on a liquid crystal display device provided with the stripe electrodes discussed above. This may be achieved by applying voltage across the pixel electrodes and the common electrode. However, if a pixel receives no voltage because of, for example, a disconnection in the associated source bus line, the polymer sustained alignment is not performed throughout the liquid crystal layer. Voltage is preferably applied across the storage capacitor lines and the common electrode.

[0014] If the storage capacitor line is provided right under the boundary between the pixel electrodes as in the pixel shown in FIG. 18, an electric field E1 is generated which leaks from the storage capacitor line CSL into the gap between pixel electrodes 101 as shown in FIG. 20 (a cross-sectional view of the pixel in FIG. 18 taken along line A-A'). The cross-sectional view shows a liquid crystal layer LC provided between a matrix substrate and a common electrode COM. The matrix substrate is, for example, fabricated by stacking, on a glass substrate 100, storage capacitor lines CSL, an insulating film 102 including a transparent insulating film (JAS), etc., and pixel electrodes 101 including transparent electrodes (ITO), etc. in this order. As illustrated in FIG. 21, the generation of the leaking electric field E1 causes liquid crystal molecules LCm near the center of the pixel to orient toward the center of the pixel, and in contrast, causes liquid crystal molecules LCm around the storage capacitor lines CSL to orient toward the storage capacitor lines CSL under the influence of the leaking electric field E1. This results in the liquid crystal molecules LCm near the center of the pixel and those around the storage capacitor lines CSL to tilt in directions which differ by 135°. The orientation of the liquid crystal molecules LCm thus becomes irregular. Transmittance decreases where the orientation of the liquid crystal molecules LCm is irregular, thereby leading to poor display quality.

[0015] FIG. 22 is a photograph of a pixel in which the orientation has such irregularity. The pixel is a standard cell in a 40-inch TV display panel with full-specification HD resolution. As encircled by a broken line, the liquid crystal molecules on the pixel edge 101e, right under which a storage capacitor line CSL is located, exhibit irregular orientation, which in turn causes a dark line on the pixel edge 101e.

[0016] The present invention, conceived in view of these conventional problems, has an object to realize a liquid crys-

tal display device which includes pixel electrodes including a plurality of stripe electrodes separated from each other by a distance and which is capable of providing a solution to irregular orientation of liquid crystal molecules in polymer sustained alignment.

Solution to Problem

[0017] In order to address the problems, a liquid crystal display device in accordance with the present invention is a liquid crystal display device of active matrix type, including: a liquid crystal layer; pixel electrodes; a common electrode; and storage capacitor lines, wherein the liquid crystal layer is located between the pixel electrodes and the common electrode, each of the pixel electrodes includes either a single regional electrode or two or more regional electrodes which are electrically connected to each other, the or each regional electrode being provided with: a first electrode which has a pattern dividing a first region where the regional electrode is formed into a plurality of second regions; and a plurality of stripe electrodes which are provided in each of the plurality of second regions so as to extend from the first electrode and so as to be separated from each other by a distance, and each of the storage capacitor lines is provided facing one of the pixel electrodes in a film thickness direction, so as not to extend facing an edge of the first region in the film thickness direction parallelly to the edge, the storage capacitor line being allocated to that pixel electrode to form a storage capacitor.

[0018] According to the invention, no storage capacitor line is provided to extend in such a manner that it faces, parallel to an edge of the area where the regional electrode is provided, the edge in the film thickness direction. For this reason, the storage capacitor line generates no leaking electric field which may disturb the orientation of liquid crystal molecules in the polymer sustained alignment process. Therefore, the liquid crystal molecules assume an intended normal orientation.

[0019] The invention hence advantageously realizes a liquid crystal display device which includes pixel electrodes each of which is provided with a plurality of stripe electrodes separated from each other by a distance, the liquid crystal display device successfully addressing irregular orientation of liquid crystal molecules in polymer sustained alignment.

[0020] Another liquid crystal display device in accordance with the present invention, in order to address the problems, is a liquid crystal display device of active matrix type, including: a liquid crystal layer; pixel electrodes; a common electrode; and storage capacitor lines, wherein the liquid crystal layer is located between the pixel electrodes and the common electrode, each of the pixel electrodes includes either a single regional electrode or two or more regional electrodes which are electrically connected to each other, the or each regional electrode being provided with: a first electrode which has a pattern dividing a first region where the regional electrode is formed into a plurality of second regions; and a plurality of stripe electrodes which are provided in each of the plurality of second regions so as to extend from the first electrode and so as to be separated from each other by a distance, and each of the storage capacitor lines is provided for one of the pixel electrodes to which that storage capacitor line is allocated to form a storage capacitor, the storage capacitor line being provided with: a first wiring section which extends facing an edge of the first region parallelly to the edge; and a second wiring section which extends from the first wiring section in

a direction intersecting with the first wiring section in an area which is located facing the first region.

[0021] According to the invention, the provision of the first wiring section and the second wiring section causes the liquid crystal molecules on edges to tilt toward the first wiring section and in contrast, causes those in the peripheral region of the second wiring section to tilt toward the second wiring section. Therefore, the liquid crystal molecules readily tilt in the mid-direction. The liquid crystal molecules, as a whole, readily tilt in an intended direction.

[0022] The invention hence advantageously realizes a liquid crystal display device which includes pixel electrodes each of which is provided with a plurality of stripe electrodes separated from each other by a distance, the liquid crystal display device successfully addressing irregular orientation of liquid crystal molecules in polymer sustained alignment.

Advantageous Effects of Invention

[0023] A liquid crystal display device in accordance with the present invention, as described in the foregoing, is a liquid crystal display device of active matrix type, including: a liquid crystal layer; pixel electrodes; a common electrode; and storage capacitor lines, wherein the liquid crystal layer is located between the pixel electrodes and the common electrode, each of the pixel electrodes includes either a single regional electrode or two or more regional electrodes which are electrically connected to each other, the or each regional electrode being provided with: a first electrode which has a pattern dividing a first region where the regional electrode is formed into a plurality of second regions; and a plurality of stripe electrodes which are provided in each of the plurality of second regions so as to extend from the first electrode and so as to be separated from each other by a distance, and each of the storage capacitor lines is provided facing one of the pixel electrodes in a film thickness direction, so as not to extend facing an edge of the first region in the film thickness direction parallelly to the edge, the storage capacitor line being allocated to that pixel electrode to form a storage capacitor.

[0024] Another liquid crystal display device in accordance with the present invention, as described in the foregoing, is a liquid crystal display device of active matrix type, including: a liquid crystal layer; pixel electrodes; a common electrode; and storage capacitor lines, wherein the liquid crystal layer is located between the pixel electrodes and the common electrode, each of the pixel electrodes includes either a single regional electrode or two or more regional electrodes which are electrically connected to each other, the or each regional electrode being provided with: a first electrode which has a pattern dividing a first region where the regional electrode is formed into a plurality of second regions; and a plurality of stripe electrodes which are provided in each of the plurality of second regions so as to extend from the first electrode and so as to be separated from each other by a distance, and each of the storage capacitor lines is provided for one of the pixel electrodes to which that storage capacitor line is allocated to form a storage capacitor, the storage capacitor line being provided with: a first wiring section which extends facing an edge of the first region parallelly to the edge; and a second wiring section which extends from the first wiring section in a direction intersecting with the first wiring section in an area which is located facing the first region.

[0025] The invention hence advantageously realizes a liquid crystal display device which includes pixel electrodes each of which is provided with a plurality of stripe electrodes

separated from each other by a distance, the liquid crystal display device successfully addressing irregular orientation of liquid crystal molecules in polymer sustained alignment.

BRIEF DESCRIPTION OF DRAWINGS

[0026] FIG. 1, representing an embodiment of the present invention, is a plan view of an arrangement of a pixel electrode and a storage capacitor line in Example 1.

[0027] FIG. 2 is a diagram illustrating behavior of liquid crystal molecules in the arrangement in FIG. 1.

[0028] FIG. 3 is a diagram illustrating behavior of liquid crystal molecules in the arrangement in FIG. 1, with no second electrode being provided.

[0029] FIG. 4 is a diagram illustrating behavior of liquid crystal molecules in the arrangement in FIG. 1, with a second electrode being provided.

[0030] FIG. 5 is a drawing showing reduced irregular orientation of liquid crystal molecules in the arrangement in FIG. 1, with no second electrode being provided.

[0031] FIG. 6 is a drawing showing reduced irregular orientation of liquid crystal molecules in the arrangement in FIG. 1, with a second electrode being provided.

[0032] FIG. 7, representing an embodiment of the present invention, is a plan view of an arrangement of pixel electrodes and storage capacitor lines in Example 2.

[0033] FIG. 8 is a diagram illustrating behavior of liquid crystal molecules in the arrangement in FIG. 7.

[0034] FIG. 9 is a diagram illustrating behavior of liquid crystal molecules in the arrangement in FIG. 7, with no connecting wire being provided.

[0035] FIG. 10 is a diagram illustrating behavior of the liquid crystal molecules in FIG. 8 in detail.

[0036] FIG. 11 is a diagram illustrating behavior of the liquid crystal molecules in FIG. 9 in detail.

[0037] FIG. 12, representing an embodiment of the present invention, is a plan view of an arrangement of a pixel electrode and a storage capacitor line in Example 3.

[0038] FIG. 13 is a diagram illustrating behavior of liquid crystal molecules in the arrangement in FIG. 12.

[0039] FIG. 14 is a drawing showing reduced irregular orientation of liquid crystal molecules in the arrangement of FIG. 13, with no second electrode being provided.

[0040] FIG. 15 is a drawing showing reduced irregular orientation of liquid crystal molecules in the arrangement of FIG. 13, with a second electrode being provided.

[0041] FIG. 16 is a drawing showing irregular orientation of liquid crystal molecules in an arrangement which is a comparative example for FIG. 15.

[0042] FIG. 17, representing an embodiment of the present invention, is a block diagram of a display device.

[0043] FIG. 18, representing conventional art, is a plan view of a pixel.

[0044] FIG. 19, representing conventional art, is a diagram illustrating behavior of liquid crystal molecules on a pixel electrode including stripe electrodes.

[0045] FIG. 20 is a cross-sectional view of FIG. 18 taken along line A-A'.

[0046] FIG. 21, representing conventional art, is a diagram illustrating problems in behavior of liquid crystal molecules on a pixel electrode including stripe electrodes.

[0047] FIG. 22 is a drawing showing irregular orientation caused by the behavior of liquid crystal molecules in FIG. 21.

DESCRIPTION OF EMBODIMENTS

[0048] The following will describe embodiments of the present invention in reference to FIGS. 1 to 17.

[0049] FIG. 17 represents an arrangement of a liquid crystal display device 11 which is a display device in accordance with the present embodiment.

[0050] The liquid crystal display device 11 includes a display panel 12, a flexible printed circuit board 13, and a control board 14.

[0051] The display panel 12 is an active matrix display panel in which there are provided, on a glass substrate, a display area 12a, gate bus lines (scan signal lines) GL, source bus lines (data signal lines) SL, and storage capacitor bus lines (storage capacitor lines) CSL, using amorphous silicon-based TFTs. Alternatively, the display panel 12 may be fabricated using, for example, polycrystalline silicon-, CG silicon-, microcrystal silicon-, or IGZO (In—Ga—Zn—O)-based TFTs.

[0052] The display panel 12 further includes a gate driver (scan signal line driver circuit) 15 and a Cs driver (storage capacitor line driver circuit) 17. The gate driver 15 and the Cs driver 17 are not necessarily provided on the display panel 12. Alternatively, the drivers 15 and 17 may be located respectively on dedicated boards, formed monolithically on the display panel 12, mounted on the flexible printed circuit board 13, or provided in any other proper form.

[0053] The display area 12a is an area where a matrix of pixels PIX is provided. Conceptually, the pixel PIX encompasses a subpixel. Each pixel PIX is provided with a TFT (pixel selecting element) 21, a liquid crystal capacitor CL, and a storage capacitor Cs. The TFT 21 has a gate terminal connected to one of the gate bus lines GL and a source terminal connected to one of the source bus lines SL. An end of the liquid crystal capacitor CL and an end of the storage capacitor Cs are composed of a pixel electrode which is connected to the drain of the TFT 21. The other end of the liquid crystal capacitor CL is composed of a common electrode COM. The other end of the storage capacitor Cs is composed of one of the storage capacitor bus lines CSL. The liquid crystal layer contains liquid crystal molecules which have a negative dielectric anisotropy and which are vertically aligned to realize MVA driving. The liquid crystal layer is subjected to polymer sustained alignment during manufacture of the liquid crystal display device 11. A pretilt angle is imparted to the vertically aligned liquid crystal molecules by use of a polymer.

[0054] The gate bus lines GL include gate bus lines GL1, GL2, GL3, . . . , and GLn. A gate bus line, GLi, in the i-th row (i is such an integer that $1 \leq i \leq n$) is connected to an i-th row output of the gate driver 15. The source bus lines SL include source bus lines SL1, SL2, SL3, . . . , and SLm. A source bus line, SLj, in the j-th column (j is such an integer that $1 \leq j \leq m$) is connected to a j-th column output of a source driver 16 (which will be described later in detail). The storage capacitor bus lines CSL include storage capacitor bus lines CSL1, CSL2, CSL3, . . . , and CSLn. A storage capacitor bus line, CSLi, in the i-th row (i is such an integer that $1 \leq i \leq n$) may be connected to an output of the Cs driver 17 which is provided either commonly for all rows or solely for the i-th row, or may be otherwise arranged in a suitable manner depending on the

form of the storage capacitor voltage applied to the end of the storage capacitor Cs composed of the storage capacitor bus line CSLi.

[0055] The gate driver **15** supplies gate pulses (scan pulses) to the gate bus lines GL sequentially. The Cs driver **17** supplies a storage capacitor voltage to the storage capacitor bus lines CSL.

[0056] The flexible printed circuit board **13** includes the source driver **16** which supplies a data signal to the source bus lines SL. The source driver **16** may be provided on the display panel **12** monolithically to the display area **12a**. The control board **14** is connected to the flexible printed circuit board **13** to supply necessary signals and power to the gate driver **15** and the source driver **16**. The signals and power to be supplied from the control board **14** to the gate driver **15** are delivered via the flexible printed circuit board **13** and from the display panel **12** to the gate driver **15**.

[0057] The following will describe examples of the pixels PIX in accordance with the present embodiment.

Example 1

[0058] FIG. 1 is a plan view of an arrangement of a pixel electrode **1** and a storage capacitor bus line CSL in a pixel PIX in accordance with Example 1.

[0059] The pixel electrode **1** is provided with a regional electrode **2**. The pixel electrode **1** consists of a regional electrode **2** in Example 1; the pixel electrode **1** may include other members. In addition, the pixel electrode **1** is composed of a transparent electrode (ITO) in Example 1; alternatively, the pixel electrode **1** may be made of any other material.

[0060] The regional electrode **2** includes a cross-shaped electrode (first electrode) **3**, stripe electrodes **4**, and a peripheral electrode (second electrode) **5**.

[0061] The cross-shaped electrode **3** has a cross-shaped wiring pattern which divides a region (first region) where the regional electrode **2** is formed into four regions (second regions) R1, R2, R3, and R4. The cross-shaped electrode **3** is provided with a stem electrode **3a** which extends in the row direction and a stem electrode **3b** which extends in the column direction. The stem electrode **3a** includes, at the central part of the regional electrode **2**, a storage capacitor electrode pad **3c** where the stem width is expanded, especially, to increase the area located facing the storage capacitor bus line CSL. A large-area segment like the storage capacitor electrode pad **3c** may have a contact with the drain of a TFT **21** (selecting element for the pixel PIX) through a contact hole. Each second region is provided as a domain for MVA driving. The first electrode may therefore divide the first region into a suitable number of second regions (generally two or more second regions) in accordance with the domain arrangement. The first electrode is hence not necessarily shaped like a cross and may be formed in a linear pattern which divides the first region into two second regions, a star-shaped pattern which divides the first region into three second regions, or any other dividing pattern.

[0062] The stripe electrodes **4** are provided, separated from each other by a distance, to extend from the cross-shaped electrode **3** toward pixel edges. The stripe electrodes **4** are positioned, in this example, at 45° (to the upward direction) in two of the regions R1 to R4 and at 135° in the two other regions in such a manner that the stripe electrodes **4** in each second region have a different inclination angle from that of the stripe electrodes **4** in a second region adjacent to it in either the column direction or the row direction. Those seg-

ments which provide the distance separating the stripe electrodes **4** are also included in the first region. The inclination angles are arbitrary and may be equal to zero in one or more domains.

[0063] Each storage capacitor bus line CSL is allocated to a pixel electrode **1**, as indicated by broken lines in FIG. 1, to form a storage capacitor Cs between itself and the pixel electrode **1**. The storage capacitor bus line CSL is provided with a stem line section CSLa and a branch line section CSLb in an area of the pixel electrode **1** to which the storage capacitor bus line CSL is allocated. The stem line section CSLa extends in the row direction, facing the stem electrode **3a** of the cross-shaped electrode **3**. The branch line section CSLb extends in the column direction, facing the stem electrode **3b** of the cross-shaped electrode **3**. The stem line section CSLa and the branch line section CSLb are connected to each other at a position which faces, in a film thickness direction, the center of the cross-shaped electrode **3**. The storage capacitor Cs is formed by positioning the storage capacitor bus line CSL and the cross-shaped electrode **3** so that they face each other. The storage capacitor bus line CSL includes, at the central part of the regional electrode **2**, a wide-width section where the line width is expanded to contain the area located facing the storage capacitor electrode pad **3c**.

[0064] As mentioned above, the storage capacitor bus line CSL is provided, in the pixel electrode **1** to which the storage capacitor bus line CSL is allocated, so as to face, in the film thickness direction, those parts of the area where the regional electrode **2** is provided, except for edges **2e** and **2e'** extending parallel to the storage capacitor bus line CSL. In other words, the storage capacitor bus line CSL is provided so as to overlap projections of edges in the film thickness direction. In contrast, in a conventional arrangement, the storage capacitor line CSL is provided at a position which faces, in the film thickness direction, the edge of a pixel electrode **101** parallel to which the storage capacitor line CSL extends, as illustrated in FIG. 20.

[0065] Conventionally, the edge **2e** is located so as to face the storage capacitor bus line CSL, and the edge **2e'** is located across from the edge **2e**. In the present example, the edge **2e'** is the one on which the TFTs **21** and the gate bus lines GL in FIG. 17 are located.

[0066] The peripheral electrode **5** forms a periphery for the entire area where the regional electrode **2** is provided. The cross-shaped electrode **3** and the stripe electrodes **4** of each regional electrode **2** are connected to the peripheral electrode **5** in the periphery. The peripheral electrode **5** may not be provided.

[0067] According to the arrangement of the pixel electrode **1** and the storage capacitor bus line CSL discussed above, none of the storage capacitor line CSL is provided to extend in such a manner that it faces, parallel to an edge of the area where the regional electrode **2** is provided, the edge in the film thickness direction. For this reason, the storage capacitor bus line CSL generates no leaking electric field which may disturb the orientation of the liquid crystal molecules LCm in the polymer sustained alignment process. Therefore, the liquid crystal molecules LCm assume an intended normal orientation: for example, they tilt uniformly toward the center of the regional electrode **2** as illustrated in FIG. 2.

[0068] The provision of the peripheral electrode **5** to the pixel electrode **1** enhances uniform orientation of the liquid crystal molecules LCm. The liquid crystal molecules LCm' near an edge of the regional electrode **2** can, in the absence of

the peripheral electrode **5**, tilt perpendicular to the edge of the regional electrode **2** as illustrated in FIG. 3. When this is the case, the liquid crystal molecules LCm' near the edge of the regional electrode **2** tilt differently from the liquid crystal molecules LCm at the central part of the pixel (i.e., they are displaced from the inclination angle of 45° or 135°). Consequently, transmission loss grows, and transmittance decreases. When the peripheral electrode **5** is provided as illustrated in FIG. 4, the edges of the regional electrode **2** give less impact on electric field because the edges of the cross-shaped electrode **3** and the edges of the stripe electrodes **4** are interconnected. Accordingly, the liquid crystal molecules LCm' near the edges of the regional electrode **2** will more likely tilt in the same inclination direction of 45° or 135° as will the liquid crystal molecules LCm at the central part of the pixel, than in the case of FIG. 3. Therefore, transmission loss decreases, and transmittance grows.

[0069] FIG. 5 shows, in a photograph, how the orientation of liquid crystal molecules on a pixel edge **1e** improves due to the non-provision of the storage capacitor bus line CSL on the edge **2e**. This pixel incorporates the pixel arrangement in accordance with the present example and is fabricated under similar manufacturing conditions to the case of FIG. 22. No peripheral electrode **5** is formed. FIG. 5 shows a smaller dark area (an indicator of irregular orientation) on the pixel edge **1e** than FIG. 22 does on the pixel edge **101e**.

[0070] FIG. 6 shows, in a photograph, how the orientation of liquid crystal molecules improves due to the addition of the peripheral electrode **5** to the pixel in FIG. 5. The irregular orientation on the pixel edge **1e** is further reduced. FIG. 6 shows even fewer dark lines (an indicator of irregular orientation) than FIG. 5.

Example 2

[0071] Example 2 describes arrangements for variations of example 1.

[0072] FIG. 7 is a plan view of an arrangement of pixel electrodes **1** and storage capacitor bus lines CSL in pixels PIX in accordance with a first variation example.

[0073] Each pixel electrode **1** is arranged similarly to the pixel electrode **1** in FIG. 1, and description of its detailed arrangement is omitted.

[0074] Each storage capacitor bus line CSL is identical to the storage capacitor bus line CSL in FIG. 1 except that the branch line section CSLb is replaced by a branch line section (connecting wire) CSLb'. The branch line sections CSLb' of the two storage capacitor bus lines CSL allocated to the pixel electrodes **1** of two pixels PIX which are adjacent to each other in the column direction are connected to each other in a pixel boundary **1f** which makes up a boundary between the pixel electrodes **1**. Each branch line section CSLb' is provided so as to face a stem electrode **3b** in the film thickness direction.

[0075] The arrangement causes the liquid crystal molecules LCm in the region **1f** between the pixel electrodes **1** of the two pixels PIX to tilt toward the branch line sections CSLb' in a stable manner as illustrated in FIG. 8. In this situation, if the liquid crystal molecules LCm in one of the two domains on the sides of the pixel boundary **1f** tilt in a 45° direction, and those in the other domain tilt in a 135° direction, the liquid crystal molecules LCm in the region **1f** readily tilt in the mid-direction of these angles (i.e., 90° direction) in a stable manner for the following reason. The provision of the branch line section CSLb in the pixel boundary if changes the orientation into which both the liquid crystal molecules LCm in the pixel boundary and those in its surroundings tilt to a predetermined orientation as illustrated in FIG. 10.

[0076] In contrast, as illustrated in FIG. 9, in the absence of the branch line sections CSLb' and an arrangement connecting the branch line sections CSLb', the orientation into which the liquid crystal molecules LCm in the region **1f** tilt is indefinite, and the liquid crystal molecules LCm may possibly orient in an opposite direction (270° direction). Therefore, in this arrangement, the orientation of the liquid crystal molecules LCm in the region **1f** tends to be indefinite, and the liquid crystal molecules LCm are unlikely to orient properly as illustrated in FIG. 11.

[0077] Next, a second variation example is described.

[0078] The second variation example differs from the arrangement in FIG. 2 in that the stem electrodes **3b** of, for example, the two regional electrodes **1** are connected together, and at least the stem line section CSLa of either one of the storage capacitor lines CSL is missing, so that each pixel electrode includes two regional electrodes **2**. There may or may not be provided a branch line section CSLb'. If a branch line section CSLb' is allocated to, and provided in, one of the regional electrodes **2** which does not have a stem line section CSLa, the branch line section CSLb' may be connected to the storage capacitor bus line CSL in the regional electrode **2** to which the branch line section CSLb' is allocated.

[0079] According to this arrangement, similarly to Example 1, none of the storage capacitor line CSL is provided to extend in such a manner that it faces, parallel to an edge of the area where the regional electrode **2** is provided, the edge in the film thickness direction. For this reason, the storage capacitor bus line CSL generates no leaking electric field which may disturb the orientation of the liquid crystal molecules LCm in the polymer sustained alignment process.

[0080] Each pixel electrode includes two regional electrodes **2** in this example. Generally, a pixel electrode may include two or more regional electrodes **2** which are electrically connected to each other.

[0081] Alternatively, any number of regional electrodes **2** may be electrically connected to an electrode arranged differently from the regional electrode **2**, so as to form a single pixel electrode.

Example 3

[0082] FIG. 12 is a plan view of an arrangement of a pixel electrode **31** and a storage capacitor bus line CSL in a pixel PIX in accordance with Example 3.

[0083] The pixel electrode **31** is provided with a regional electrode **32**. The pixel electrode **31** consists of a regional electrode **32** in Example 3; the pixel electrode **31** may include other members. In addition, the pixel electrode **31** is composed of a transparent electrode (ITO) in Example 3; alternatively, the pixel electrode **31** may be made of any other material.

[0084] The regional electrode **32** includes a cross-shaped electrode (first electrode) **3**, stripe electrodes **4**, and a peripheral electrode (second electrode) **6**.

[0085] The cross-shaped electrode **3** has a cross-shaped pattern which divides a region (first region) where the regional electrode **32** is formed into four regions (second regions) R1, R2, R3, and R4. The cross-shaped electrode **3** is provided with a stem electrode **3a** which extends in the row direction and a stem electrode **3b** which extends in the column

direction. The stem electrode **3a** includes, at the central part of the regional electrode **32**, a storage capacitor electrode pad **3c** where the stem width is expanded, especially, to increase the area located facing the storage capacitor bus line CSL. A large-area segment like the storage capacitor electrode pad **3c** may have a contact with the drain of a TFT **21** (selecting element for the pixel PIX) through a contact hole. Each second region is provided as a domain for MVA driving. The first electrode may therefore divide the first region into a suitable number of second regions (generally two or more second regions) in accordance with the domain arrangement. The first electrode is hence not necessarily shaped like a cross and may be formed in a linear pattern which divides the first region into two second regions, a star-shaped pattern which divides the first region into three second regions, or any other dividing pattern.

[0086] The stripe electrodes **4** are provided, separated from each other by a distance, to extend from the cross-shaped electrode **3** toward pixel edges. The stripe electrodes **4** are positioned, in this example, at 45° (to the upward direction) in two of the regions **R1** to **R4** and at 135° in the two other regions in such a manner that the stripe electrodes **4** in each second region have a different inclination angle from that of the stripe electrodes **4** in a second region adjacent to it in either the column direction or the row direction. Those segments which provide the distance separating the stripe electrodes **4** are also included in the first region. The inclination angles are arbitrary and may be equal to zero in one or more domains.

[0087] Each storage capacitor bus line CSL is allocated to a pixel electrode **31**, as indicated by broken lines in FIG. 12, to form a storage capacitor Cs between itself and the pixel electrode **31**. The storage capacitor bus line CSL is provided with a stem line section CSLa, a branch line section (second wiring section) CSLb, and a branch line section (first wiring section) CSLc in an area of the pixel electrode **31** to which the storage capacitor bus line CSL is allocated. The stem line section CSLa extends in the row direction, facing the stem electrode **3a** of the cross-shaped electrode **3**. The branch line section CSLb extends in the column direction from the stem line section CSLa toward an edge **32e**, facing the stem electrode **3b** of the cross-shaped electrode **3**. Conventionally, the edge **32e** extends in a direction in which the storage capacitor bus line CSL extends and is located facing the storage capacitor bus line CSL. In the present example, an edge **32e'**, located across from the edge **32e**, is an edge on which the TFTs **21** and the gate bus lines GL in FIG. 17 are provided. The branch line section CSLc is provided to extend parallel to the edge **32e**, facing the edge **32e** in the film thickness direction. The branch line section CSLb generally only needs to extend in a direction intersecting with the branch line section CSLc in an area which is located facing an area where the regional electrode **2** is provided.

[0088] The stem line section CSLa includes, at the central part of the regional electrode **2**, a wide-width section where the line width is expanded to contain the area located facing the storage capacitor electrode pad **3c**.

[0089] The peripheral electrode **6** has an angular C shape, making up the periphery of the area where the regional electrode **2** is provided, except for the edge **32e** located facing the branch line section CSLc. The cross-shaped electrode **3** and the stripe electrodes **4** of each regional electrode **2** are connected to the peripheral electrode **6** in the periphery. The peripheral electrode **6** may not be provided.

[0090] According to the arrangement of the pixel electrode **31** and the storage capacitor bus line CSL discussed above, the storage capacitor bus line CSL is provided with a branch line section CSLc which extends so as to face the edge **32e** and a branch line section CSLb which extends from the branch line section CSLc in a direction intersecting with the branch line section CSLc. Therefore, as illustrated in FIG. 13, the liquid crystal molecules LCm2 on the edge **32e** will tilt toward the branch line section CSLc, whereas the liquid crystal molecules LCm1 in a region **32g** on the periphery of the branch line section CSLb will tilt toward the branch line section CSLb. As a result, the liquid crystal molecules LCm1 and LCm2 readily tilt in the mid-direction of these directions. The liquid crystal molecules LCm, including the liquid crystal molecules LCm1 and LCm2, readily tilt in an intended direction.

[0091] FIG. 14 shows, in a photograph, how the orientation of liquid crystal molecules on a pixel edge **31e** improves. This pixel incorporates the pixel arrangement in accordance with the present example and is fabricated under similar manufacturing conditions to the case of FIG. 22. No peripheral electrode **6** is formed. FIG. 14 shows fewer dark lines (an indicator of irregular orientation) on the pixel edge **31e** than FIG. 22 does on the pixel edge **101e**.

[0092] FIG. 15 shows, in a photograph, how the orientation of liquid crystal molecules improves due to the addition of the peripheral electrode **6** to the pixel in FIG. 14. The edges of the regional electrode **32** give less impact on electric field because the edges of the cross-shaped electrode **3** and the edges of the stripe electrodes **4** are interconnected. Accordingly, the liquid crystal molecules near the edges of the regional electrode **32** readily tilt in an intended inclination direction. Therefore, transmission loss decreases, and transmittance grows. In addition, no peripheral electrode is provided on the edge **32e** because a peripheral electrode on the edge **32e** will likely cause irregular orientation under the influence of the branch line section CSLc. Therefore, the irregular orientation on the pixel edge **31e** is further reduced. FIG. 15 shows even fewer dark lines (an indicator of irregular orientation) than FIG. 14. FIG. 16 shows, for comparison, orientation on a pixel edge **31e'** which is provided with an edge **32e'** obtained by forming a peripheral electrode on the edge **32e** so as to enclose the entire periphery of the regional electrode **2** with the electrode. The provision of the peripheral electrode on the edge **32e'** causes greater irregular orientation than in the case of FIG. 15. Consequently, transmission loss grows, and transmittance decreases.

[0093] Various embodiments have been described so far.

[0094] As described in the foregoing, a liquid crystal display device in accordance with the present invention is a liquid crystal display device of active matrix type, including: a liquid crystal layer; pixel electrodes; a common electrode; and storage capacitor lines, wherein the liquid crystal layer is located between the pixel electrodes and the common electrode, each of the pixel electrodes includes either a single regional electrode or two or more regional electrodes which are electrically connected to each other, the or each regional electrode being provided with: a first electrode which has a pattern dividing a first region where the regional electrode is formed into a plurality of second regions; and a plurality of stripe electrodes which are provided in each of the plurality of second regions so as to extend from the first electrode and so as to be separated from each other by a distance, and each of the storage capacitor lines is provided facing one of the pixel

electrodes in a film thickness direction, so as not to extend facing an edge of the first region in the film thickness direction parallelly to the edge, the storage capacitor line being allocated to that pixel electrode to form a storage capacitor.

[0095] According to the invention, no storage capacitor line is provided to extend in such a manner that it faces, parallel to an edge of the area where the regional electrode is provided, the edge in the film thickness direction. For this reason, the storage capacitor line generates no leaking electric field which may disturb the orientation of liquid crystal molecules in the polymer sustained alignment process. Therefore, the liquid crystal molecules assume an intended normal orientation.

[0096] The invention hence advantageously realizes a liquid crystal display device which includes pixel electrodes each of which is provided with a plurality of stripe electrodes separated from each other by a distance, the liquid crystal display device successfully addressing irregular orientation of liquid crystal molecules in polymer sustained alignment.

[0097] The liquid crystal display device in accordance with the present invention may be arranged so that: each of the regional electrodes is provided with a second electrode forming an entire periphery of the first region; and the first electrode and the plurality of stripe electrodes of each of the regional electrodes are connected to the second electrode in the periphery.

[0098] According to this aspect of the invention, the edges of the regional electrode(s) give less impact on electric field because the edges of the first electrode and the edges of the stripe electrodes are interconnected by the second electrode provided. Accordingly, the liquid crystal molecules near the edges of the regional electrode(s) readily tilt in an intended inclination direction. Therefore, transmission loss decreases, and transmittance grows. Therefore, the irregular orientation on pixel edges is further reduced, which in turn brings about an advantage of further reducing irregular orientation on the whole.

[0099] The liquid crystal display device in accordance with the present invention may be arranged so that the storage capacitor line is provided so as to face the first electrode in the film thickness direction.

[0100] According to this aspect of the invention, a storage capacitor is advantageously formed between the storage capacitor lines and the first electrode.

[0101] The liquid crystal display device in accordance with the present invention may be arranged so that those different ones of the storage capacitor lines which are allocated respectively to two adjacent ones of the pixel electrodes are connected to each other by a connecting wire which passes through an area located facing the two pixel electrodes in the film thickness direction.

[0102] According to this aspect of the invention, the provision of the connecting wire in a pixel boundary changes the orientation into which both the liquid crystal molecules in the pixel boundary and those in its surroundings tilt to a predetermined orientation. This in turn brings about an advantage of tilting the liquid crystal molecules into a predetermined orientation in a stable manner.

[0103] The liquid crystal display device in accordance with the present invention may be arranged so that the connecting wire is provided so as to face the first electrodes for the two pixel electrodes in the film thickness direction.

[0104] According to this aspect of the invention, a storage capacitor is advantageously formed between the connecting wire and the first electrodes.

[0105] A liquid crystal display device in accordance with the present invention is a liquid crystal display device of active matrix type, including: a liquid crystal layer; pixel electrodes; a common electrode; and storage capacitor lines, wherein the liquid crystal layer is located between the pixel electrodes and the common electrode, each of the pixel electrodes includes either a single regional electrode or two or more regional electrodes which are electrically connected to each other, the or each regional electrode being provided with: a first electrode which has a pattern dividing a first region where the regional electrode is formed into a plurality of second regions; and a plurality of stripe electrodes which are provided in each of the plurality of second regions so as to extend from the first electrode and so as to be separated from each other by a distance, and each of the storage capacitor lines is provided for one of the pixel electrodes to which that storage capacitor line is allocated to form a storage capacitor, the storage capacitor line being provided with: a first wiring section which extends facing an edge of the first region parallelly to the edge; and a second wiring section which extends from the first wiring section in a direction intersecting with the first wiring section in an area which is located facing the first region.

[0106] According to the invention, the provision of the first wiring section and the second wiring section causes the liquid crystal molecules on edges to tilt toward the first wiring section and in contrast, causes those in the peripheral region of the second wiring section to tilt toward the second wiring section. Therefore, the liquid crystal molecules readily tilt in the mid-direction. The liquid crystal molecules, as a whole, readily tilt in an intended direction.

[0107] The invention hence advantageously realizes a liquid crystal display device which includes pixel electrodes each of which is provided with a plurality of stripe electrodes separated from each other by a distance, the liquid crystal display device successfully addressing irregular orientation of liquid crystal molecules in polymer sustained alignment.

[0108] The liquid crystal display device in accordance with the present invention may be arranged so that the second wiring section is located so as to face the first electrode in the film thickness direction.

[0109] According to this aspect of the invention, a storage capacitor is advantageously formed between the storage capacitor lines and the second wiring section.

[0110] The liquid crystal display device in accordance with the present invention may be arranged so that: each of the regional electrodes is provided with a second electrode forming a periphery of the first region except for the edge; and the first electrode and the plurality of stripe electrodes of each of the regional electrodes are connected to the second electrode in the periphery.

[0111] According to this aspect of the invention, the edges of the regional electrode(s) give less impact on electric field because the edges of the first electrode and the edges of the stripe electrodes are interconnected by the second electrode provided. Accordingly, the liquid crystal molecules near the edges of the regional electrode(s) readily tilt in an intended inclination direction. Therefore, transmission loss decreases, and transmittance grows. In addition, the non-provision of the second electrode on the edge where the first wiring section is

provided prevents irregular orientation which could otherwise occur under the influence of the second wiring.

[0112] Therefore, the irregular orientation on pixel edges is further reduced, which in turn brings about an advantage of further reducing irregular orientation on the whole.

[0113] The liquid crystal display device in accordance with the present invention may be arranged so that the liquid crystal layer contains liquid crystal molecules which have a negative dielectric anisotropy and to which a pretilt angle is imparted by use of a polymer.

[0114] According to this aspect of the invention, polymer sustained alignment can be performed without causing irregular orientation of liquid crystal molecules. This in turn brings about an advantageous supplement to insufficient regulation of orientation through electric field, to achieve well-organized orientation.

[0115] The liquid crystal display device in accordance with the present invention may be arranged so that: the pattern of the first electrode is a wiring pattern extending in a row direction and in a column direction; and the plurality of stripe electrodes each extend in a direction at an angle to the first electrode.

[0116] According to this aspect of the invention, each second region advantageously acts as a domain for MVA driving.

[0117] The liquid crystal display device in accordance with the present invention may be arranged so that in those of the plurality of second regions which, in each of the regional electrodes, are adjacent either in the row direction or in the column direction with the first electrode intervening between those second regions, the plurality of stripe electrodes extend in a direction which differs from one second region to another.

[0118] According to this aspect of the invention, each second region advantageously acts as an independent domain for MVA driving.

[0119] The present invention is not limited to the description of the embodiments and examples above. Proper variations and combinations of the embodiments and examples in view of general technical knowledge are encompassed in the technical scope of the present invention.

INDUSTRIAL APPLICABILITY

[0120] The present invention is preferably applicable to active matrix display devices.

REFERENCE SIGNS LIST

- [0121] 1, 31 Pixel Electrode
- [0122] 2, 32 Regional Electrode
- [0123] 3 Cross-shaped Electrode (First Electrode)
- [0124] 4 Stripe Electrode
- [0125] 5, 6 Peripheral Electrode (Second Electrode)
- [0126] 11 Liquid Crystal Display Device
- [0127] 2e, 32e Edge
- [0128] R1, R2, R3, R4 Region (Second Region)
- [0129] LC Liquid Crystal Layer
- [0130] COM Common Electrode
- [0131] Cs Storage Capacitor
- [0132] CSL Storage Capacitor Bus Line (Storage Capacitor Line)
- [0133] CSLc Branch Line Section (First Wiring Section)
- [0134] CSLb Branch Line Section (Second Wiring Section)
- [0135] CSLb' Branch Line Section (Connecting Wire)

1. A liquid crystal display device of active matrix type, comprising:

a liquid crystal layer;
pixel electrodes;
a common electrode; and
storage capacitor lines,

wherein

the liquid crystal layer is located between the pixel electrodes and the common electrode,

each of the pixel electrodes includes either a single regional electrode or two or more regional electrodes which are electrically connected to each other, the or each regional electrode being provided with: a first electrode which has a pattern dividing a first region where the regional electrode is formed into a plurality of second regions; and a plurality of stripe electrodes which are provided in each of the plurality of second regions so as to extend from the first electrode and so as to be separated from each other by a distance, and

each of the storage capacitor lines is provided facing one of the pixel electrodes in a film thickness direction, so as not to extend facing an edge of the first region in the film thickness direction parallelly to the edge, the storage capacitor line being allocated to that pixel electrode to form a storage capacitor.

2. The liquid crystal display device as set forth in claim 1, wherein:

each of the regional electrodes is provided with a second electrode forming an entire periphery of the first region; and

the first electrode and the plurality of stripe electrodes of each of the regional electrodes are connected to the second electrode in the periphery.

3. The liquid crystal display device as set forth in claim 1, wherein the storage capacitor line is provided so as to face the first electrode in the film thickness direction.

4. The liquid crystal display device as set forth in claim 1, wherein those different ones of the storage capacitor lines which are allocated respectively to two adjacent ones of the pixel electrodes are connected to each other by a connecting wire which passes through an area located facing the two pixel electrodes in the film thickness direction.

5. The liquid crystal display device as set forth in claim 4, wherein the connecting wire is provided so as to face the first electrodes for the two pixel electrodes in the film thickness direction.

6. A liquid crystal display device of active matrix type, comprising:

a liquid crystal layer;
pixel electrodes;
a common electrode; and
storage capacitor lines,

wherein

the liquid crystal layer is located between the pixel electrodes and the common electrode,

each of the pixel electrodes includes either a single regional electrode or two or more regional electrodes which are electrically connected to each other, the or each regional electrode being provided with: a first electrode which has a pattern dividing a first region where the regional electrode is formed into a plurality of second regions; and a plurality of stripe electrodes which are provided in each of the plurality of second regions so as to extend from the first electrode and so as to be separated from each other by a distance, and

each of the storage capacitor lines is provided for one of the pixel electrodes to which that storage capacitor line is allocated to form a storage capacitor, the storage capacitor line being provided with: a first wiring section which extends facing an edge of the first region parallelly to the edge; and a second wiring section which extends from the first wiring section in a direction intersecting with the first wiring section in an area which is located facing the first region.

7. The liquid crystal display device as set forth in claim 6, wherein the second wiring section is located so as to face the first electrode in the film thickness direction.

8. The liquid crystal display device as set forth in claim 6, wherein:

each of the regional electrodes is provided with a second electrode forming a periphery of the first region except for the edge; and

the first electrode and the plurality of stripe electrodes of each of the regional electrodes are connected to the second electrode in the periphery.

9. The liquid crystal display device as set forth in claim 1, wherein the liquid crystal layer contains liquid crystal molecules which have a negative dielectric anisotropy and to which a pretilt angle is imparted by use of a polymer.

10. The liquid crystal display device as set forth in claim 1, wherein:

the pattern of the first electrode is a wiring pattern extending in a row direction and in a column direction; and

the plurality of stripe electrodes each extend in a direction at an angle to the first electrode.

11. The liquid crystal display device as set forth in claim 10, wherein in those of the plurality of second regions which, in each of the regional electrodes, are adjacent either in the row direction or in the column direction with the first electrode intervening between those second regions, the plurality of stripe electrodes extend in a direction which differs from one second region to another.

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摘要(译)

公开了一种有源矩阵型液晶显示装置。每个像素电极（1）包括单个区域电极（2）或两个或更多个区域电极（2），它们彼此电连接。该区域电极或每个区域电极具有：第一电极（十字形电极）（3），其具有将第一区域分成多个第二区域（R1，R2，R3和R4）的图案；多个条形电极（4）设置在每个第二区域中，以便从第一电极延伸并且彼此分开一段距离。存储电容器线（CSL）在膜厚度方向上面对像素电极（1）中的一个设置，以形成存储电容器。存储电容器线（CSL）被设置为不平行于边缘（2e，2e'）在膜厚方向上面向第一区域的边缘（2e，2e'）延伸。本发明消除了聚合物持续排列中液晶分子的不规则取向。

