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Lee et al.

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(54) **MULTI-DOMAIN LIQUID CRYSTAL
DISPLAY DEVICE**

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2000, now Pat. No. 6,680,769.

(30) Foreign Application Priority Data

Feb. 8, 1999 (KR) P1999-004195
Apr. 22, 1999 (KR) P1999-014492

(51) Int. Cl.⁷ **G02F 1/1377**

(52) U.S. Cl. **349/129; 349/38**

(58) Field of Search 349/38, 39, 129,
349/123, 143

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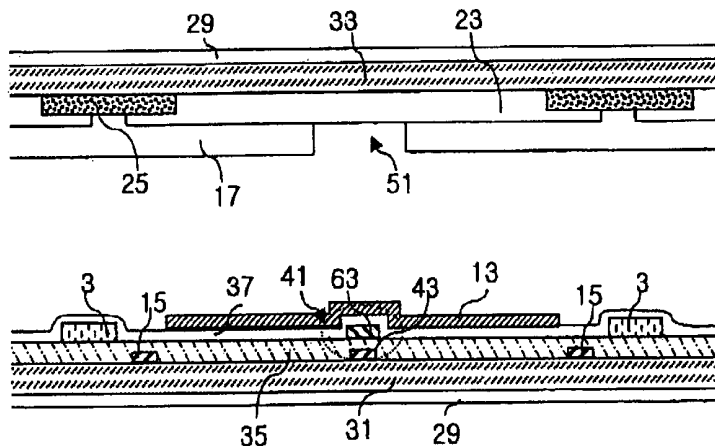
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LLP

(57) ABSTRACT

A multi-domain liquid crystal display device includes first
and second substrates facing each other and a liquid crystal
layer between the first and second substrates. A conductive
layer distorting electric field is on the first substrate, and a
common-auxiliary electrode is on a same layer whereon the
conductive layer distorting electric field is formed. A com-
mon electrode is on the second substrate, and a storage
electrode is in an electric field inducing region that divides
the liquid crystal layer into at least two domains.

12 Claims, 29 Drawing Sheets



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FIG. 1A
PRIOR ART

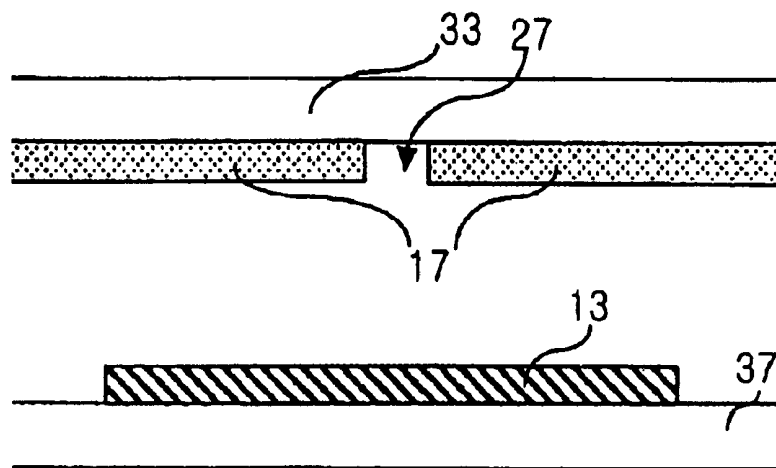


FIG. 1B
PRIOR ART

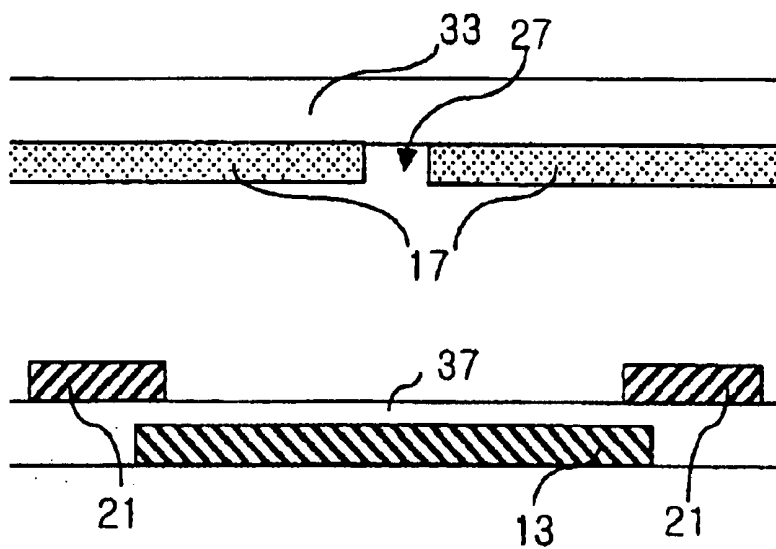


FIG. 2A

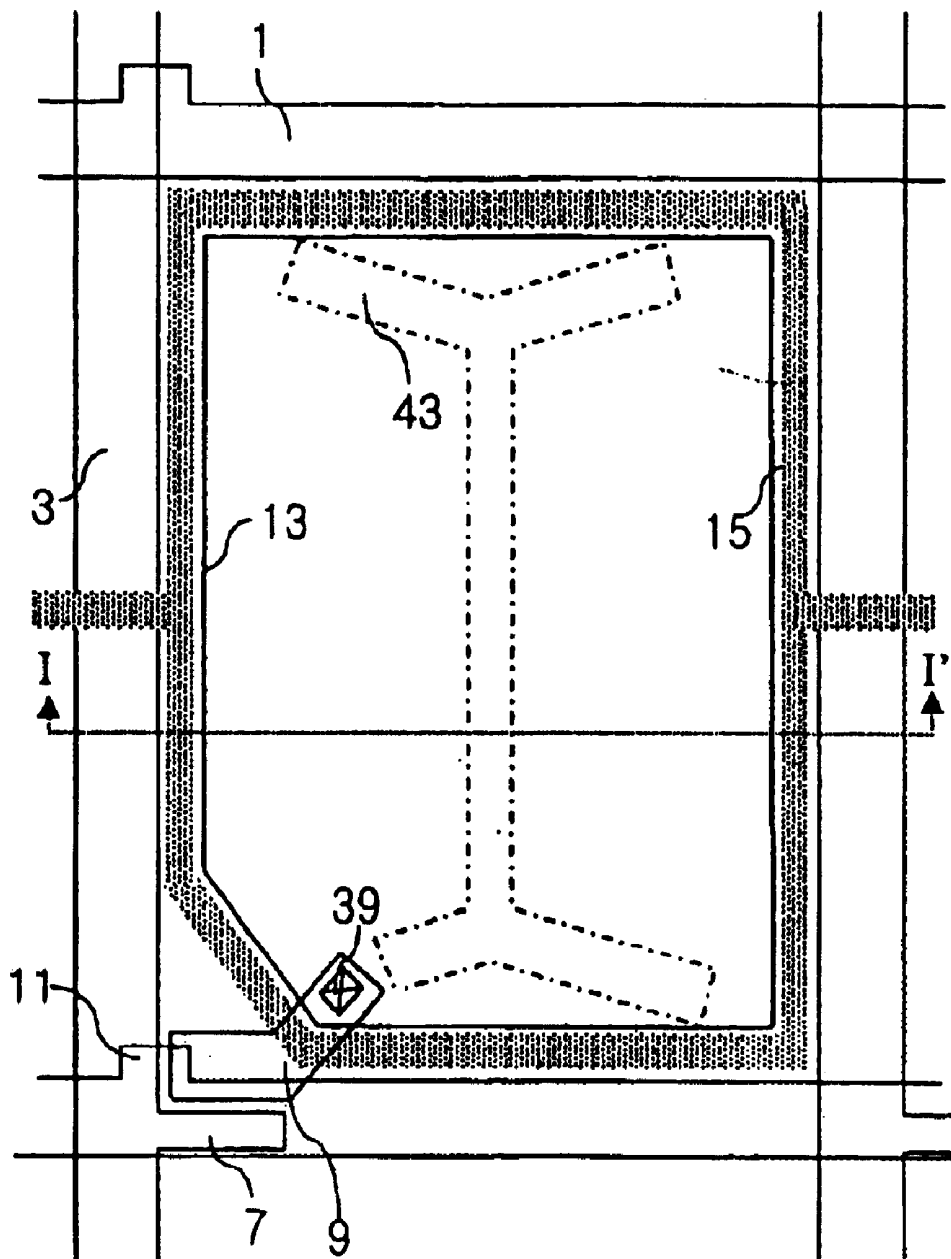


FIG. 2B

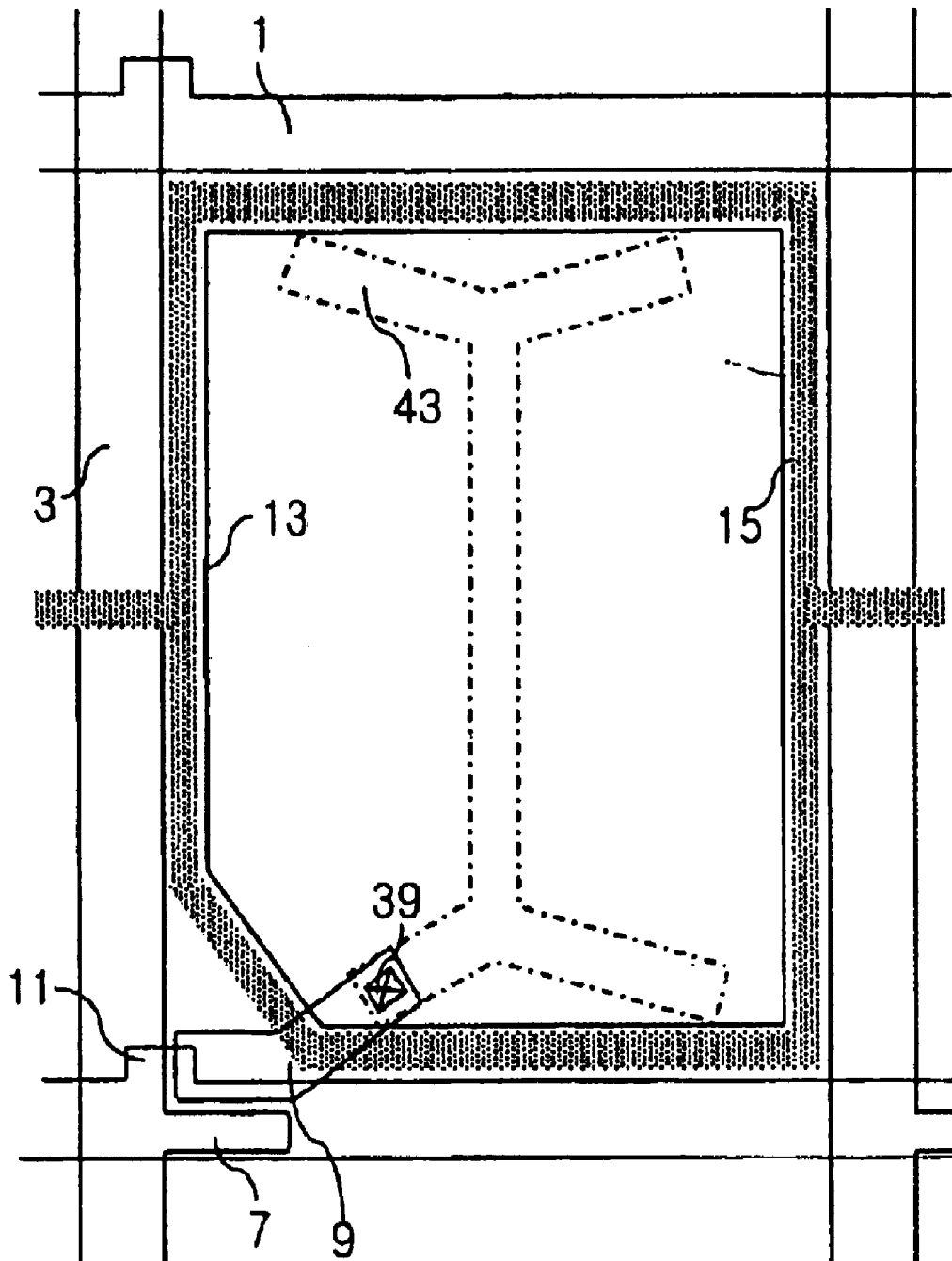


FIG. 2C

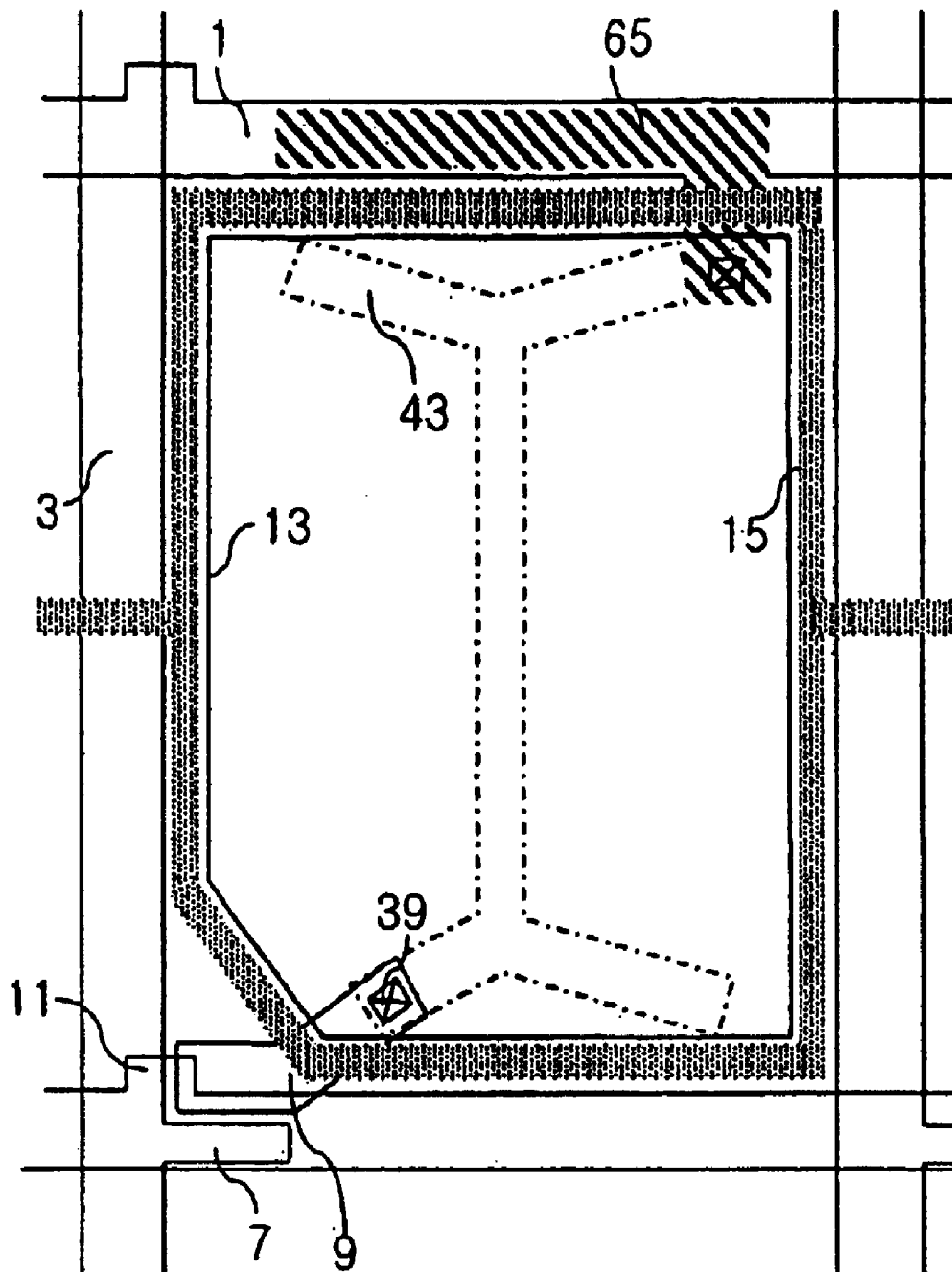


FIG. 2D

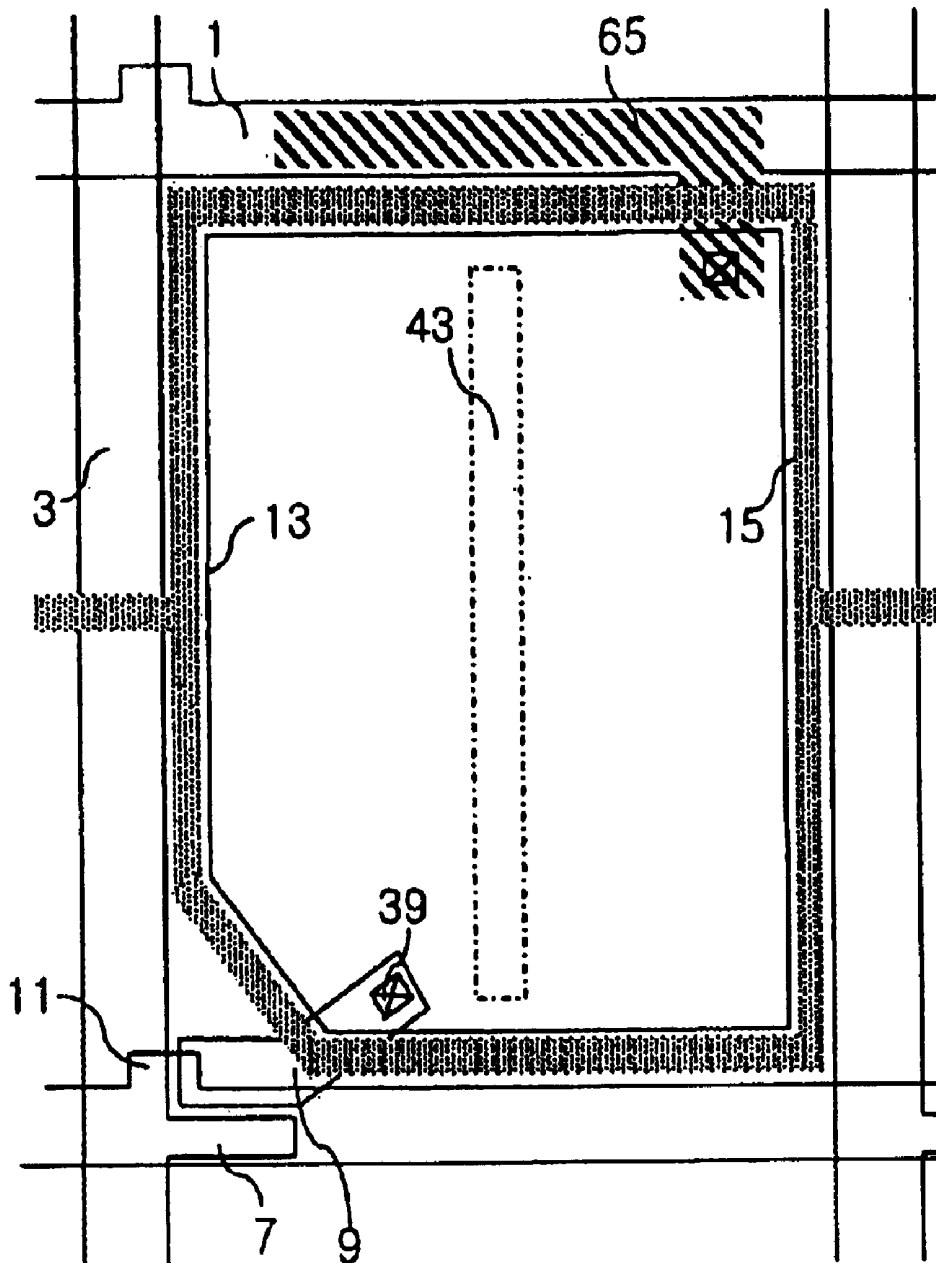


FIG. 2E

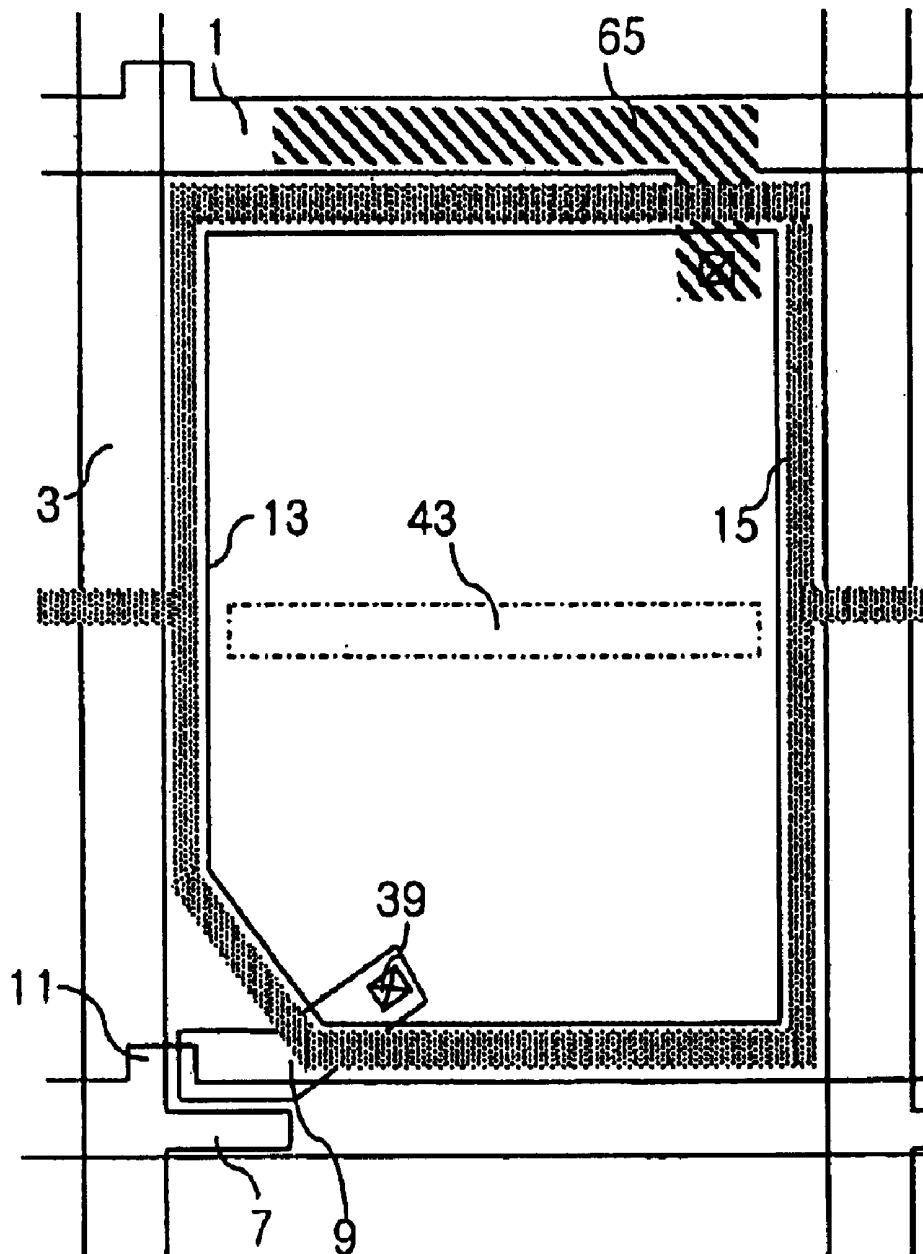


FIG. 2F

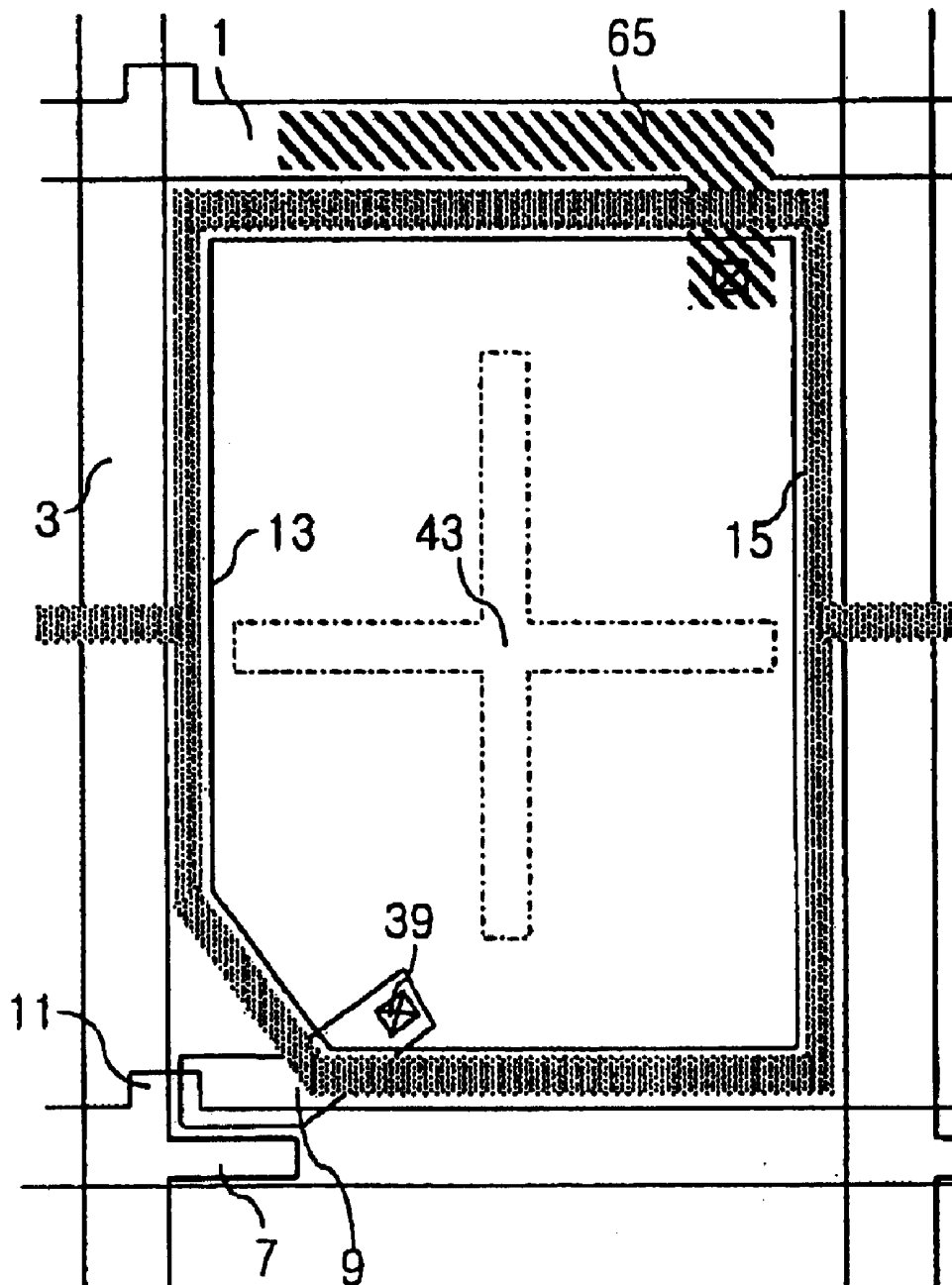


FIG. 2G

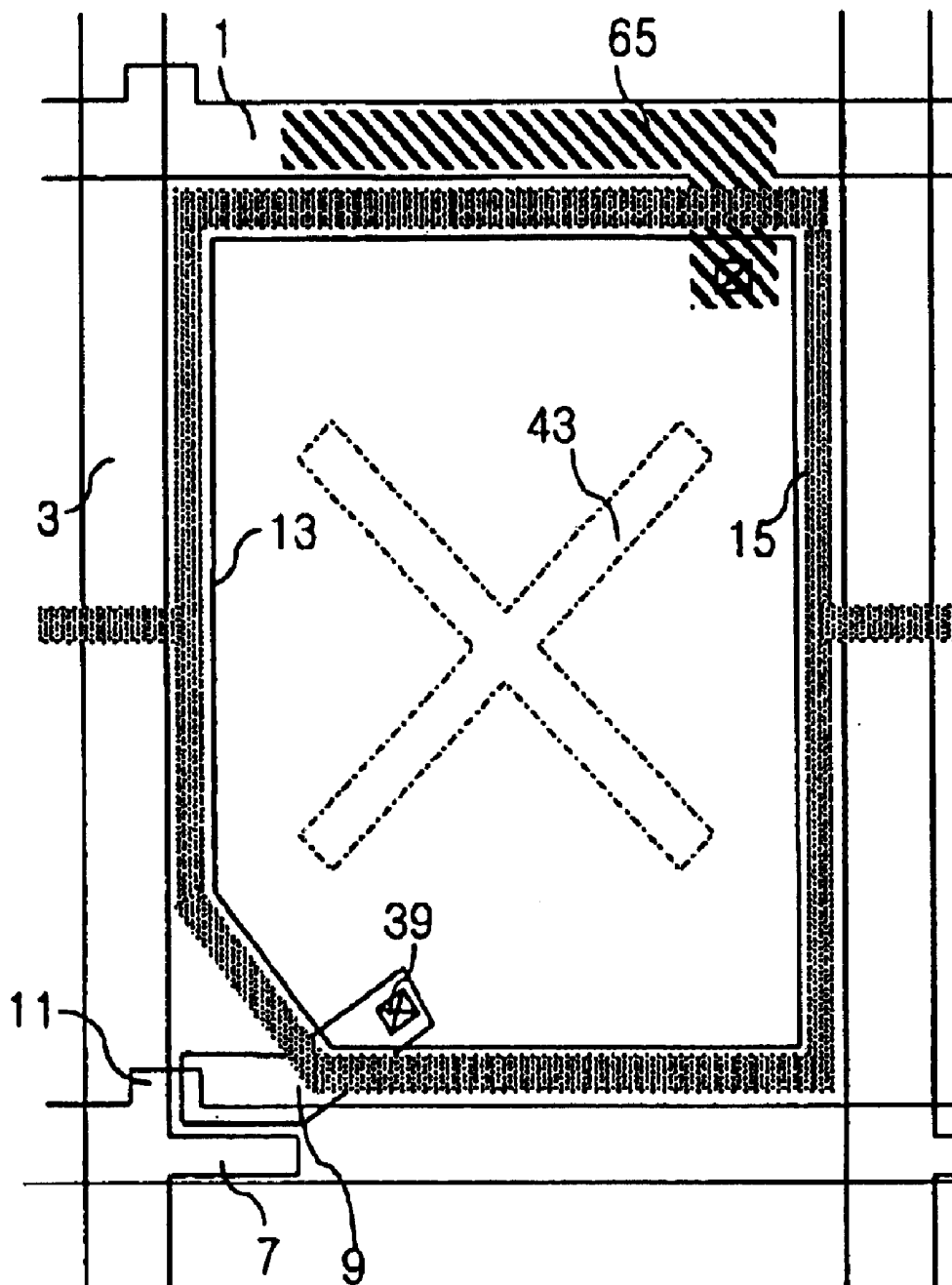


FIG. 2H

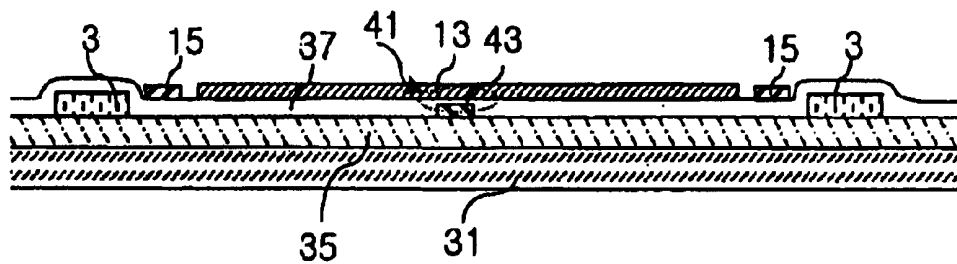


FIG. 2I

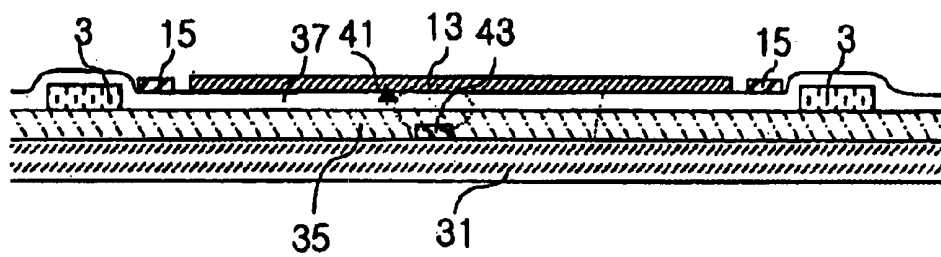


FIG. 2J

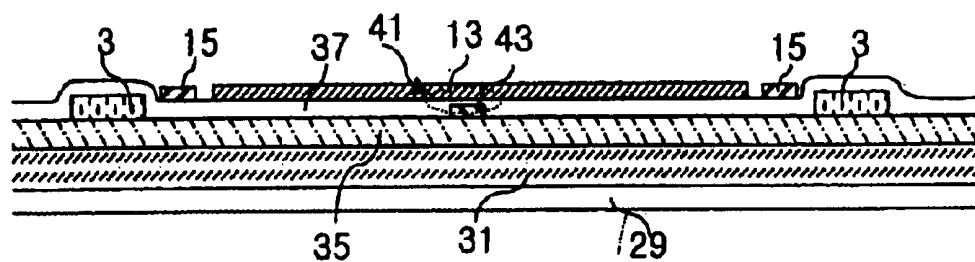
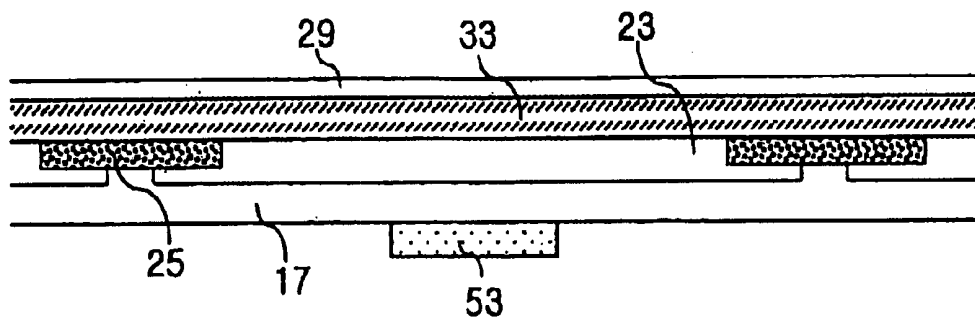


FIG. 2K

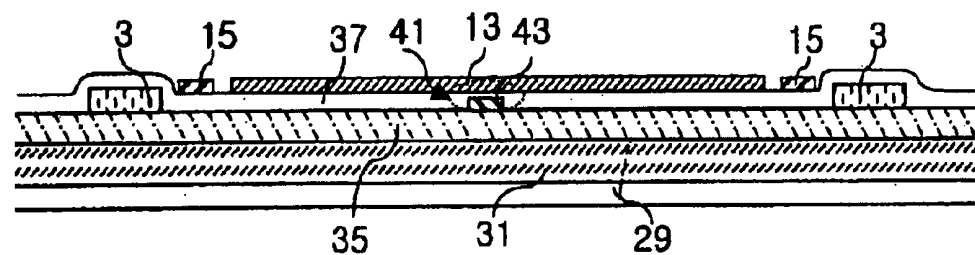
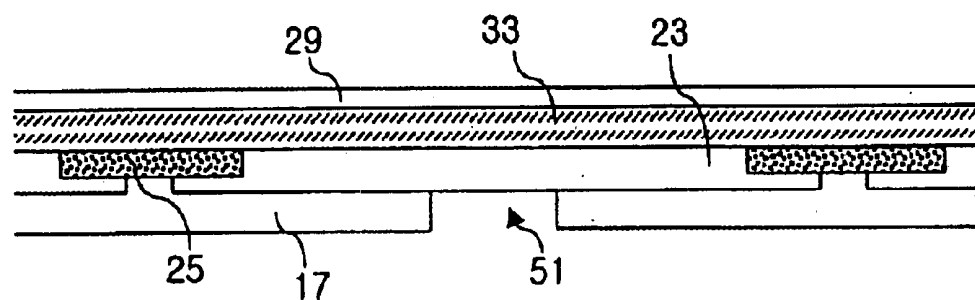


FIG. 3A

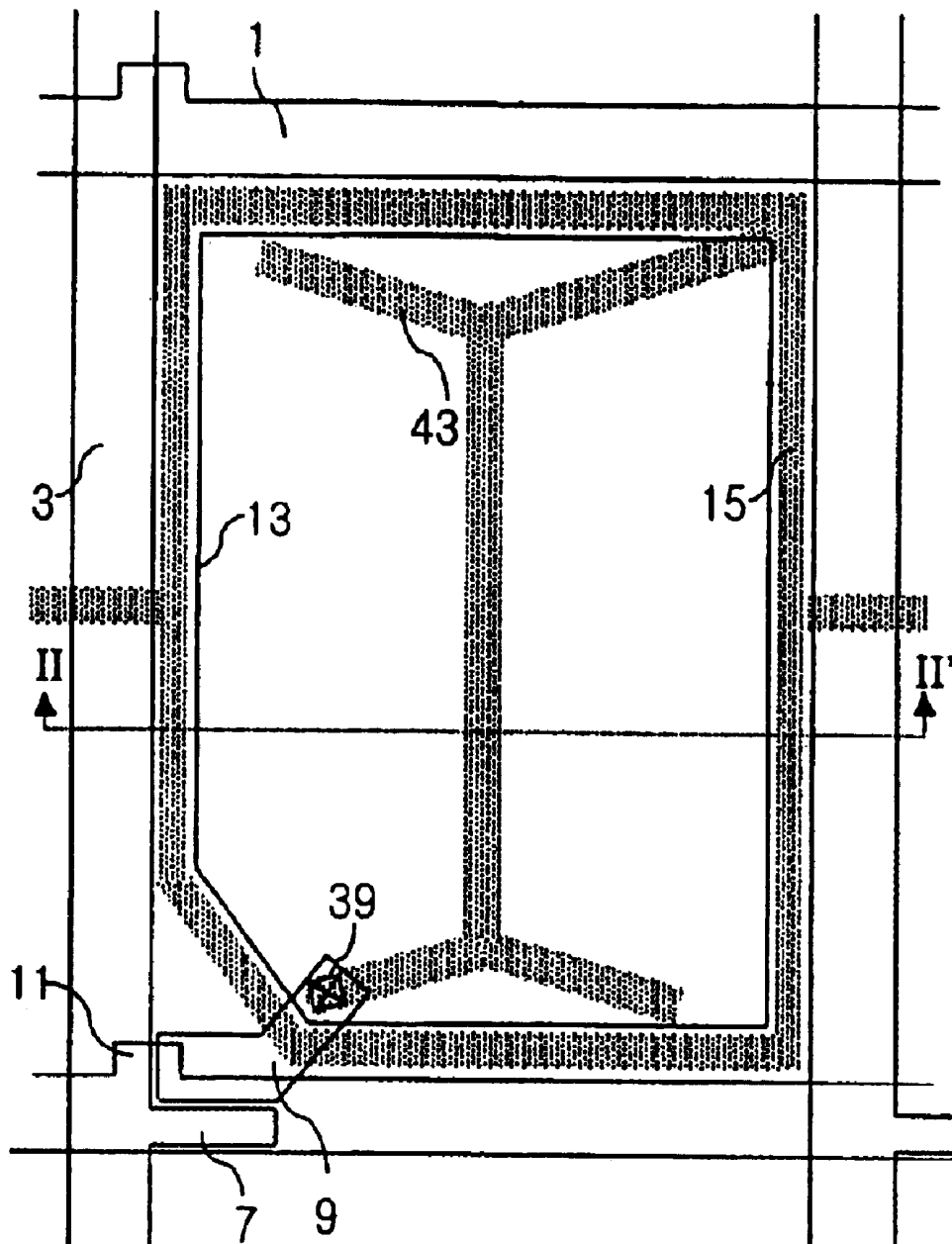


FIG. 3B

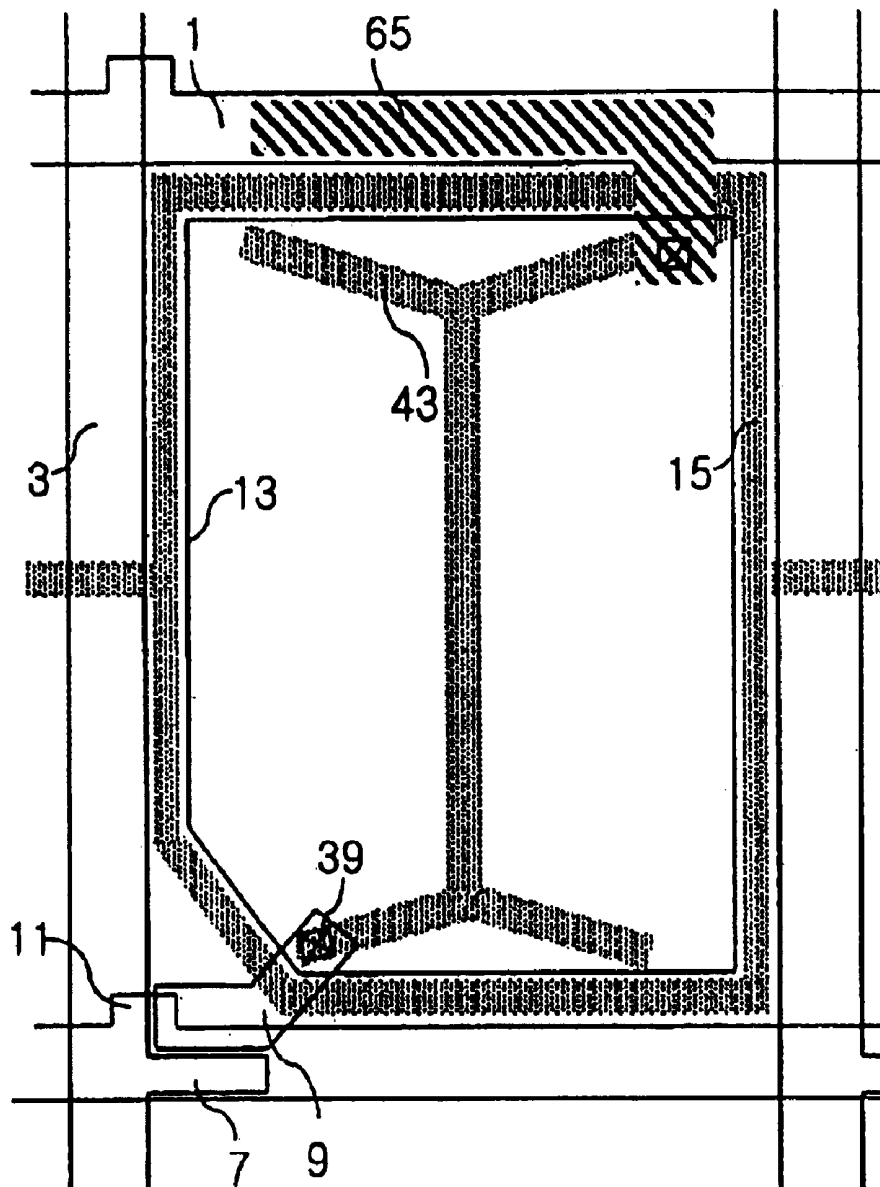


FIG. 3C

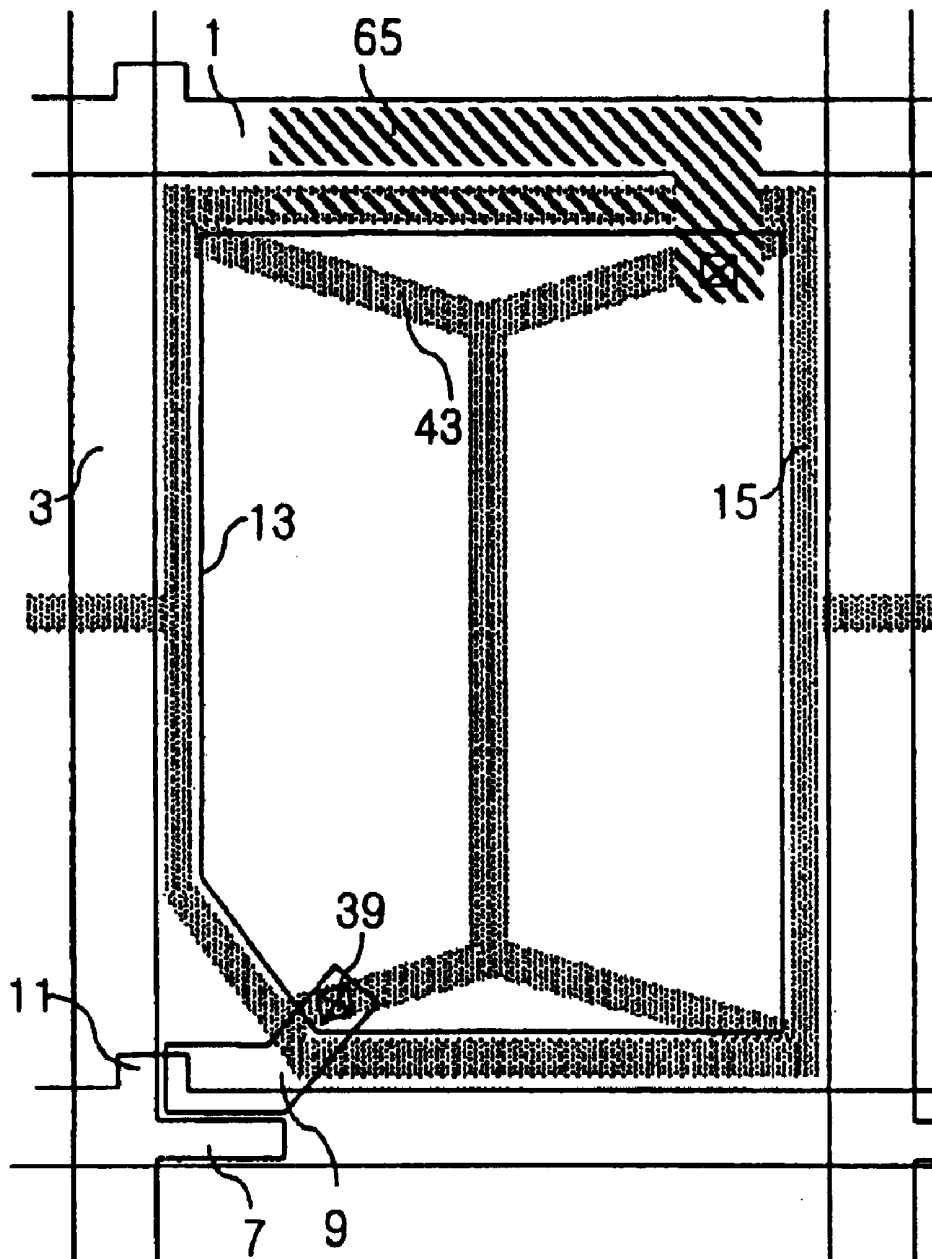


FIG. 3D

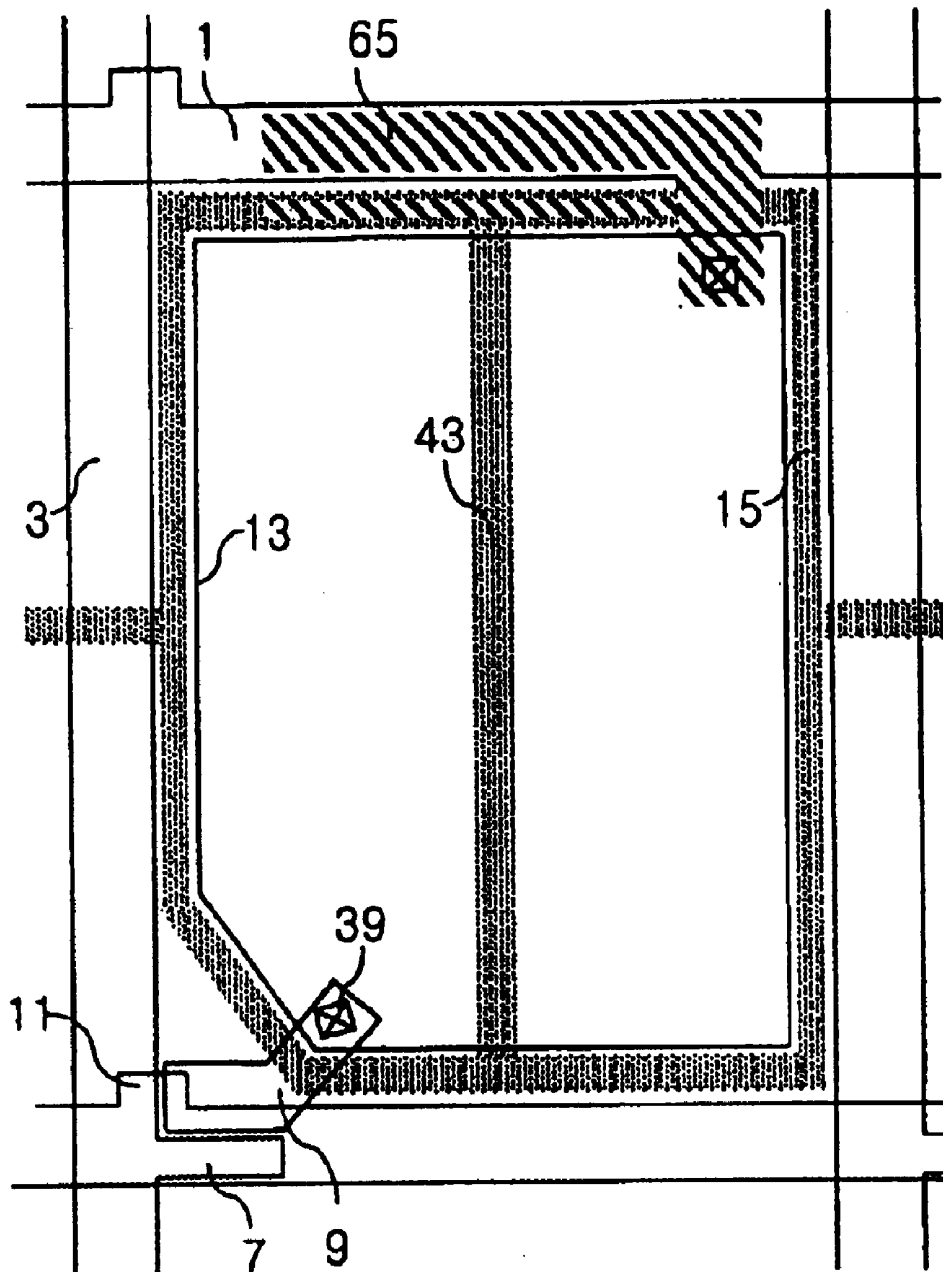


FIG. 3E

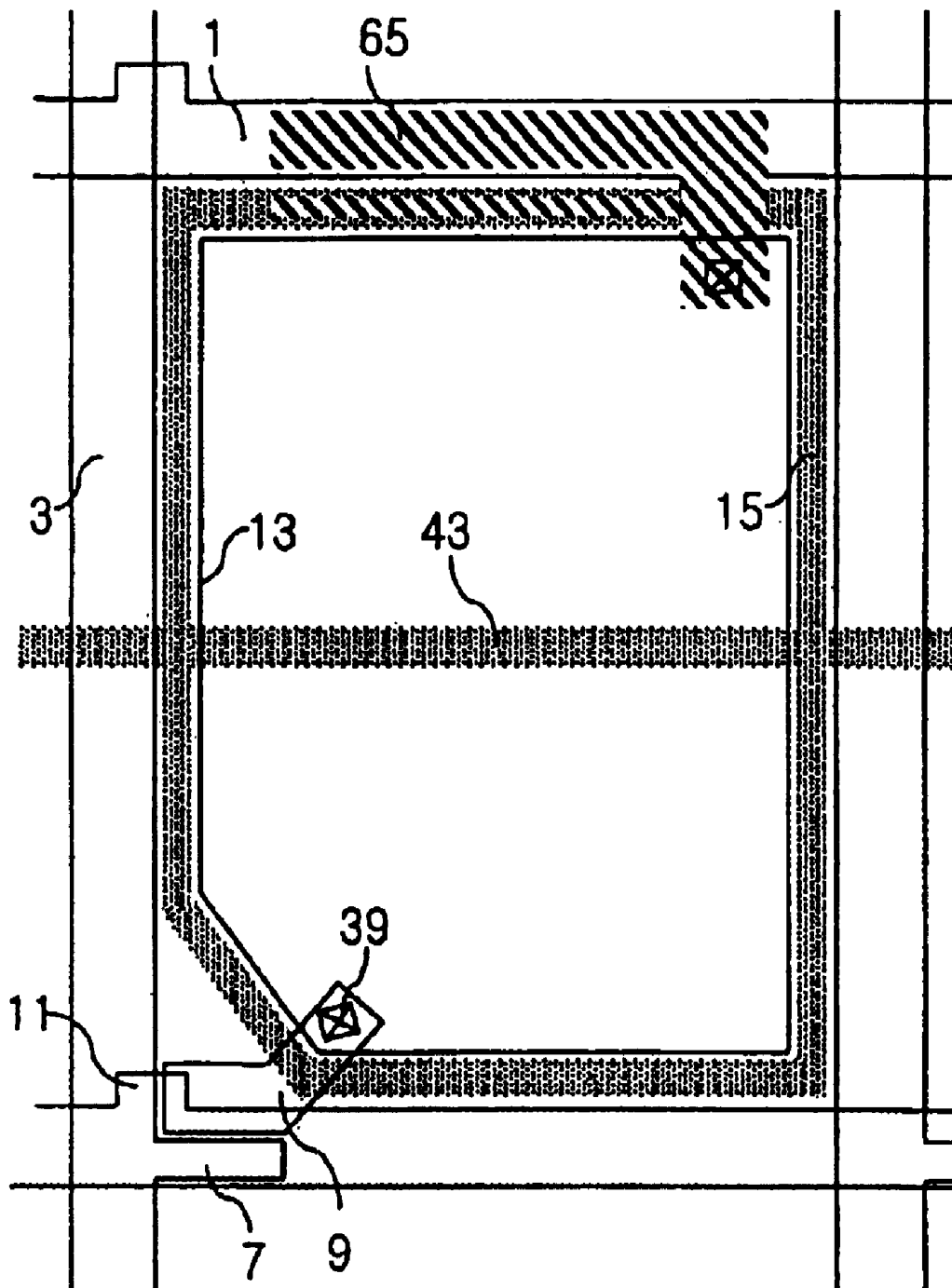


FIG. 3F

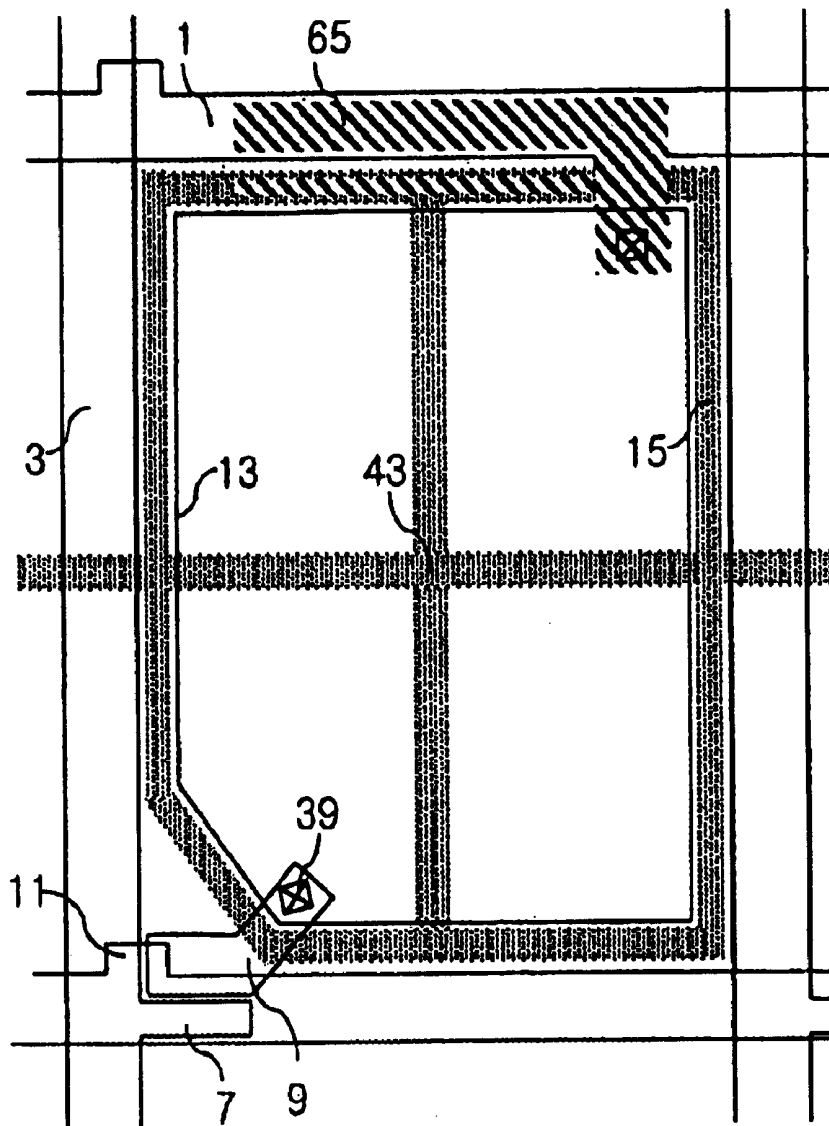


FIG. 3G

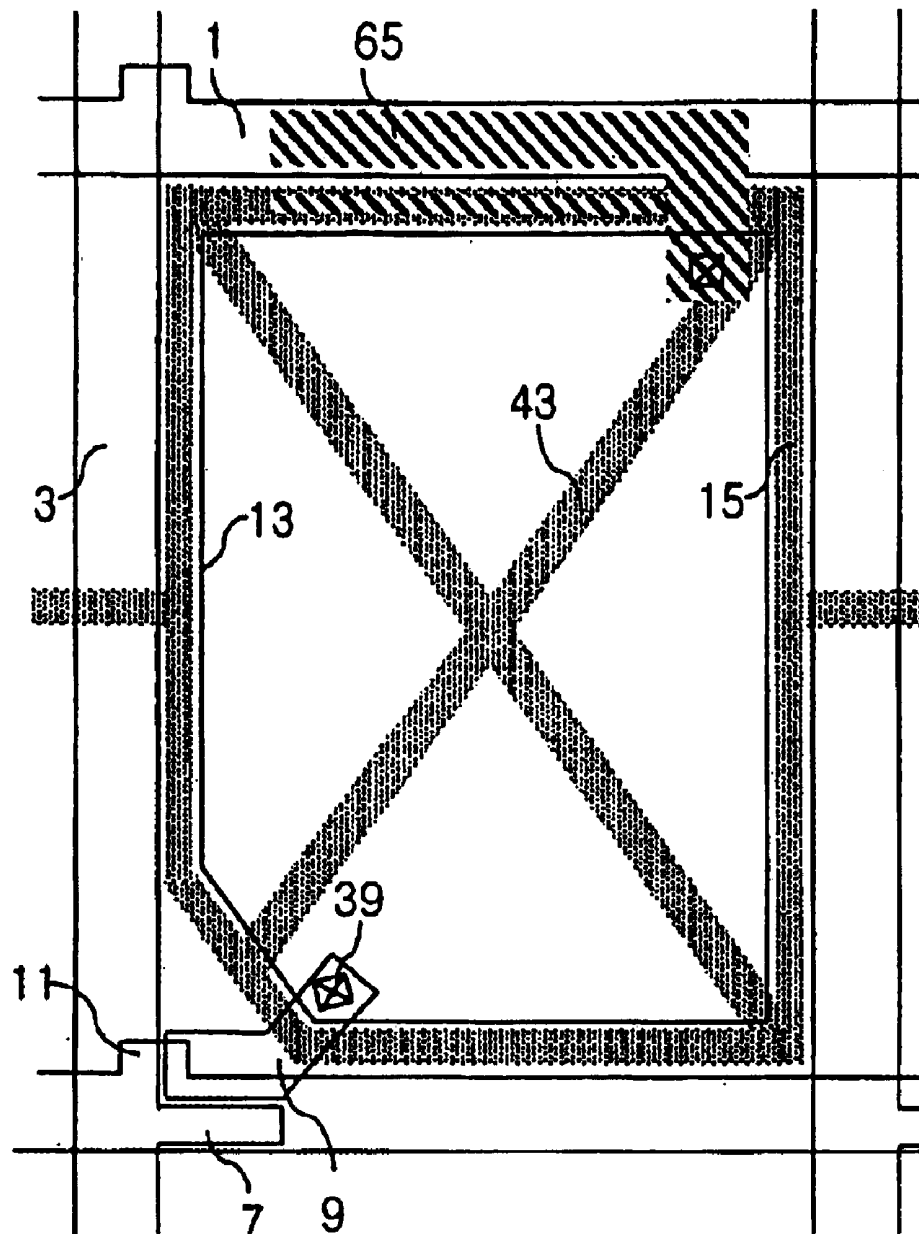


FIG. 3H

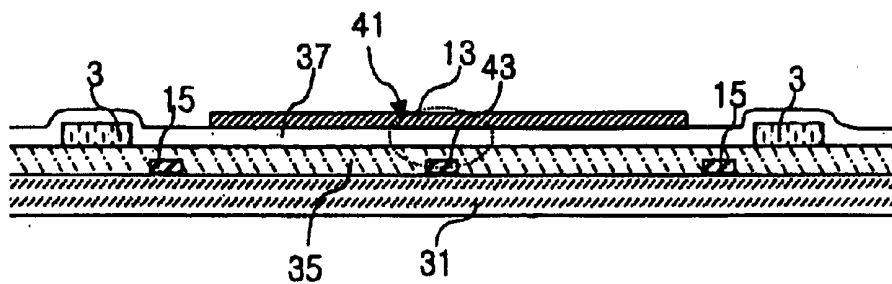


FIG. 3I

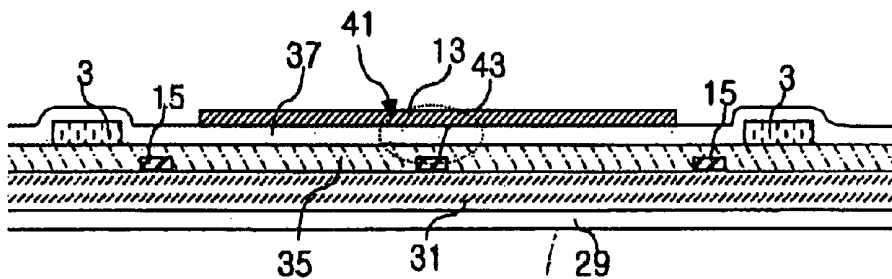
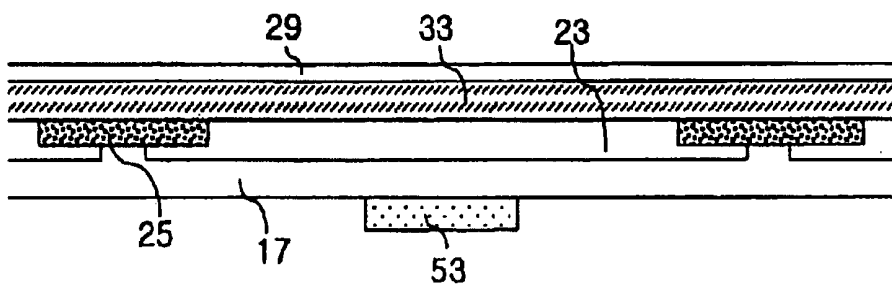


FIG. 3J

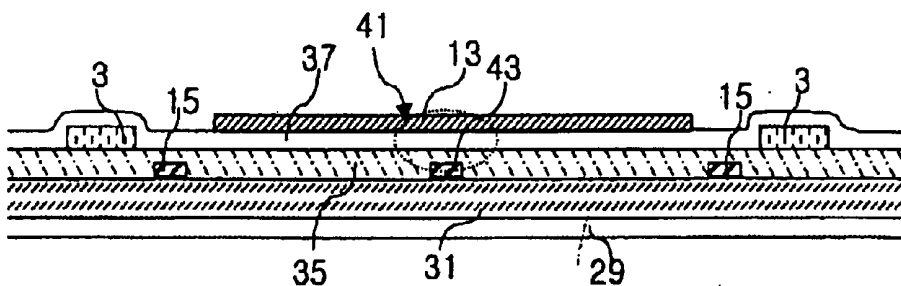
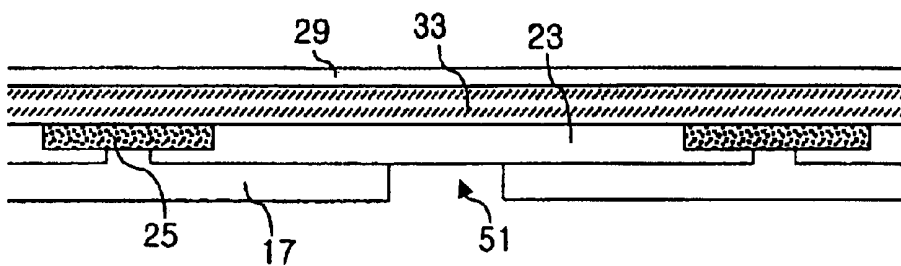


FIG. 4A

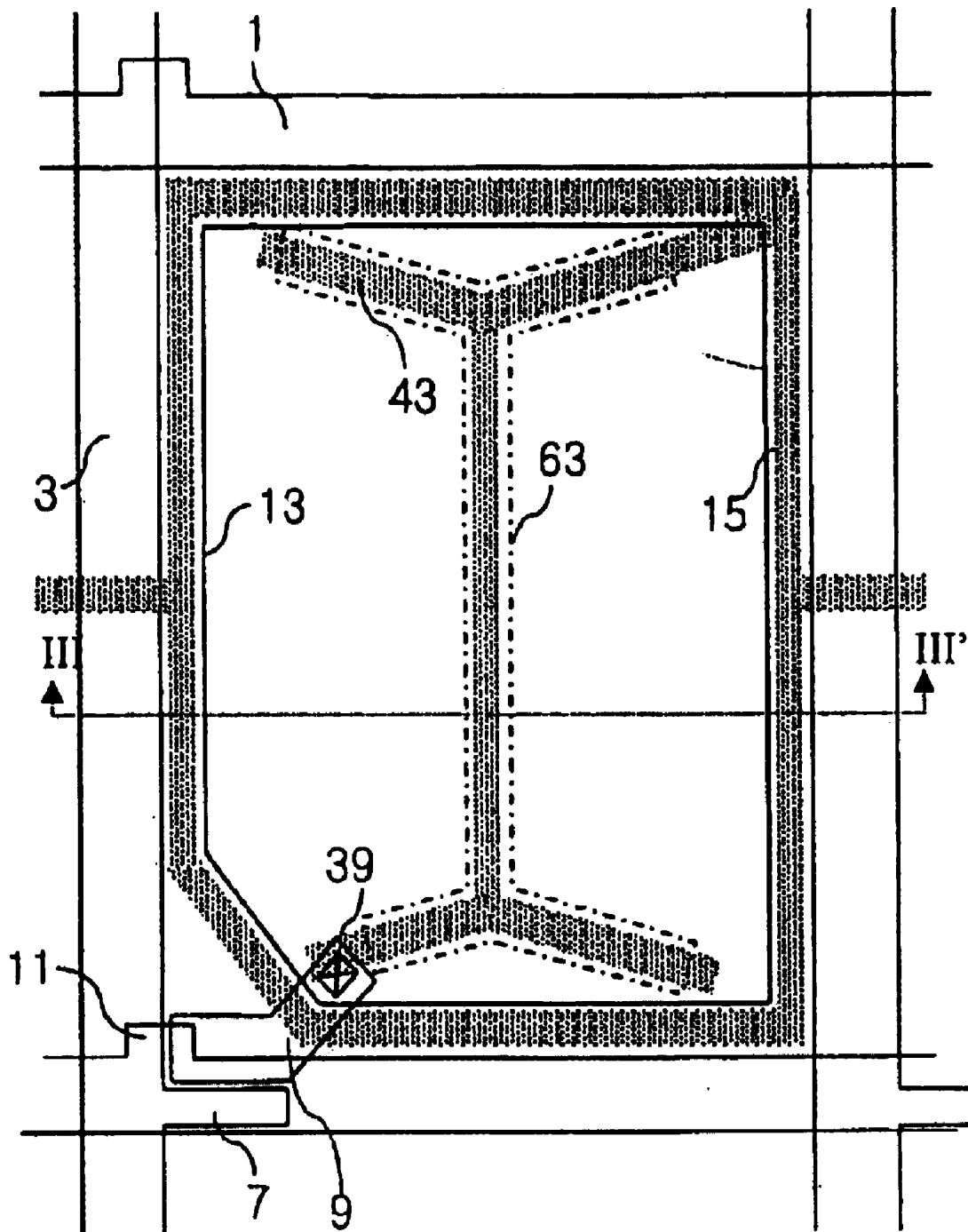


FIG. 4B

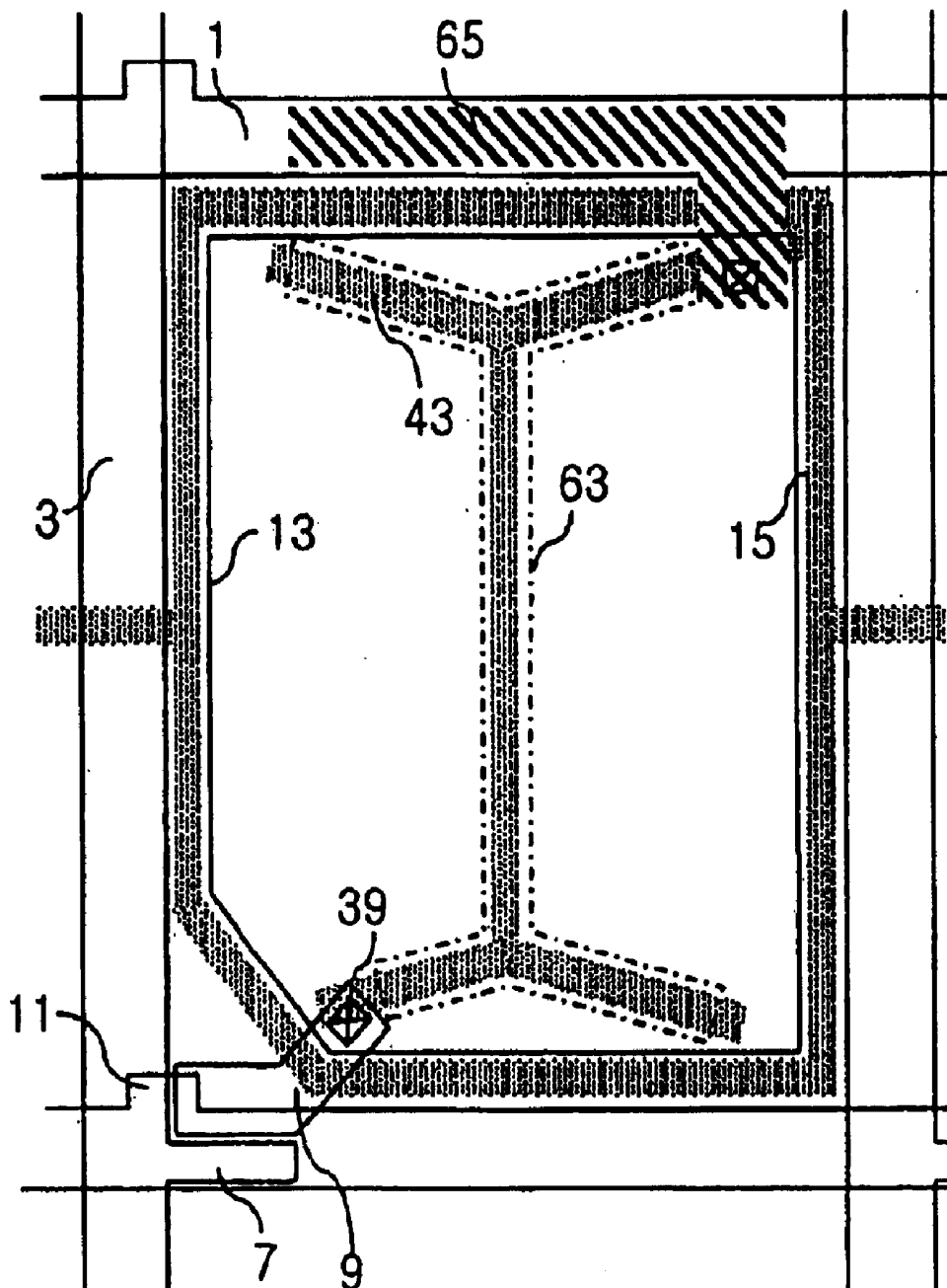


FIG. 4C

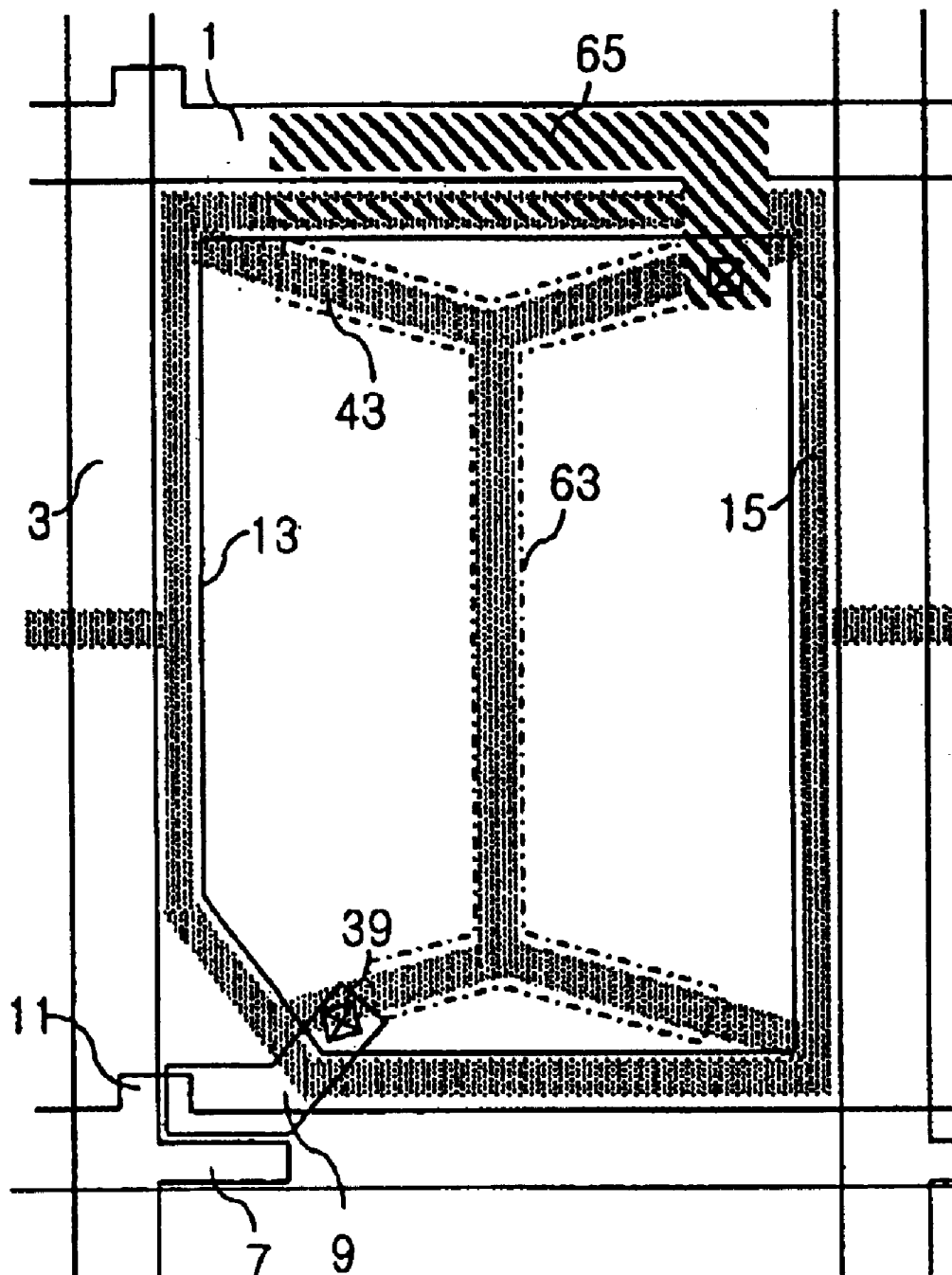


FIG. 4D

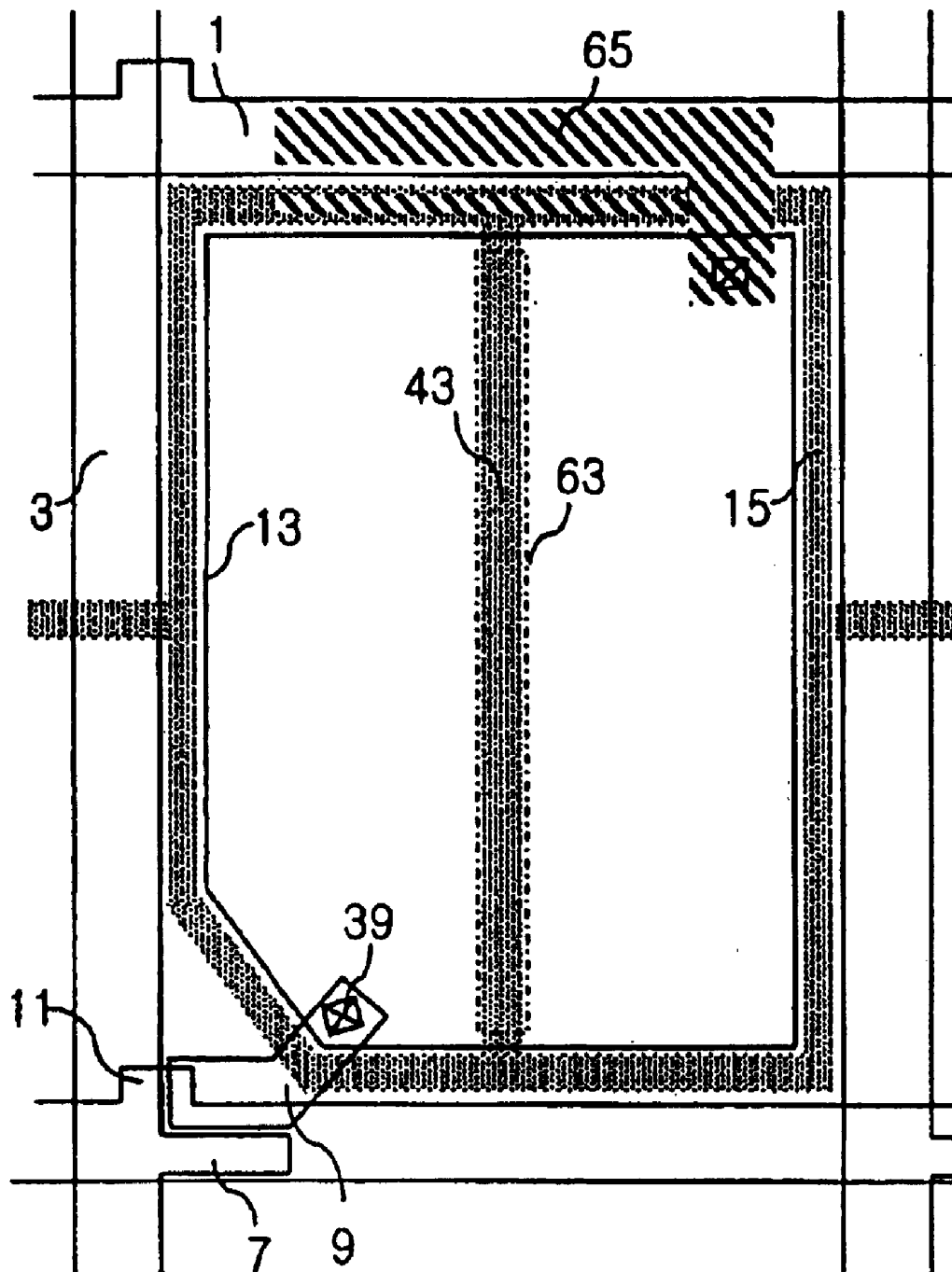


FIG. 4E

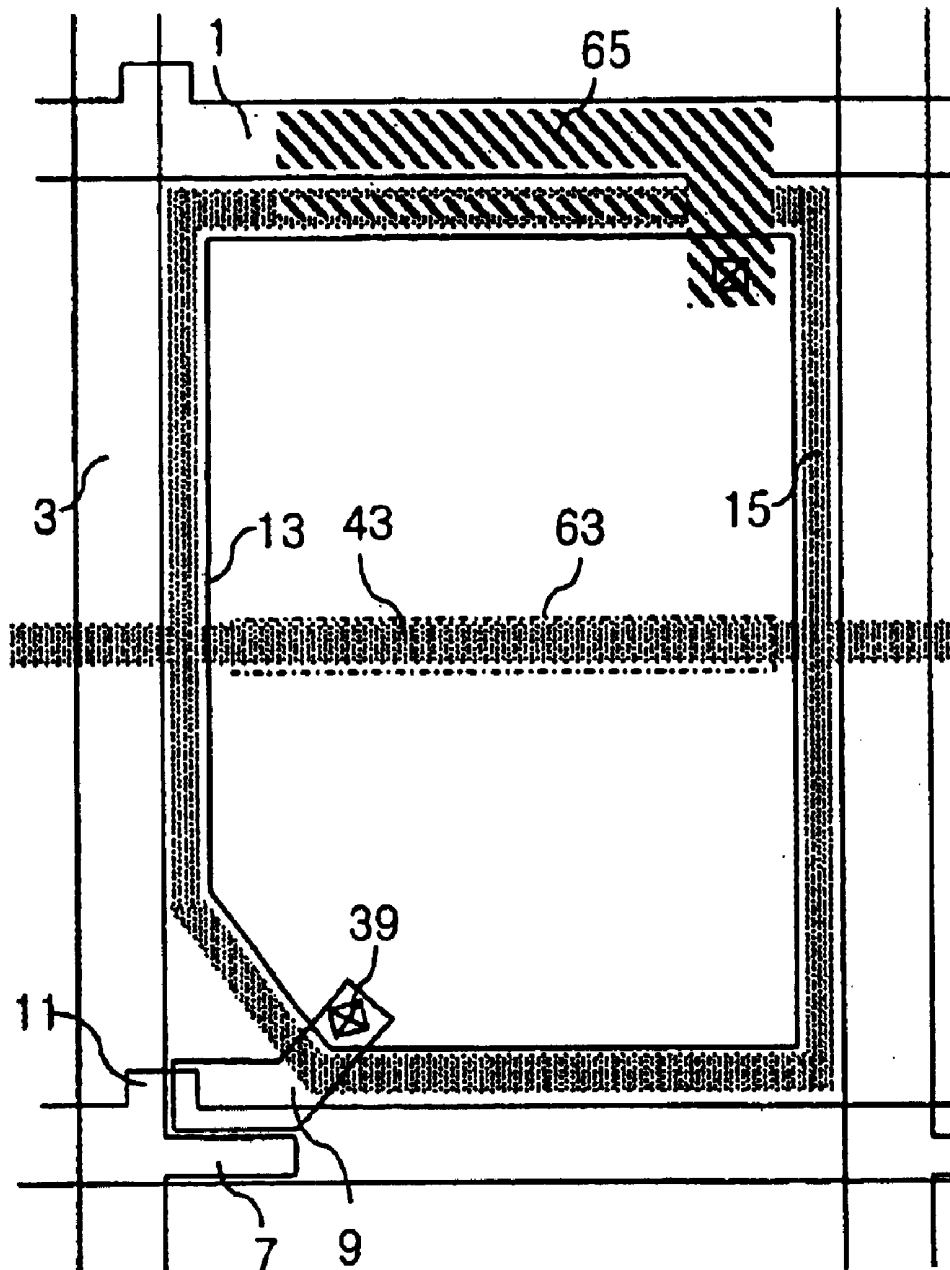


FIG. 4F

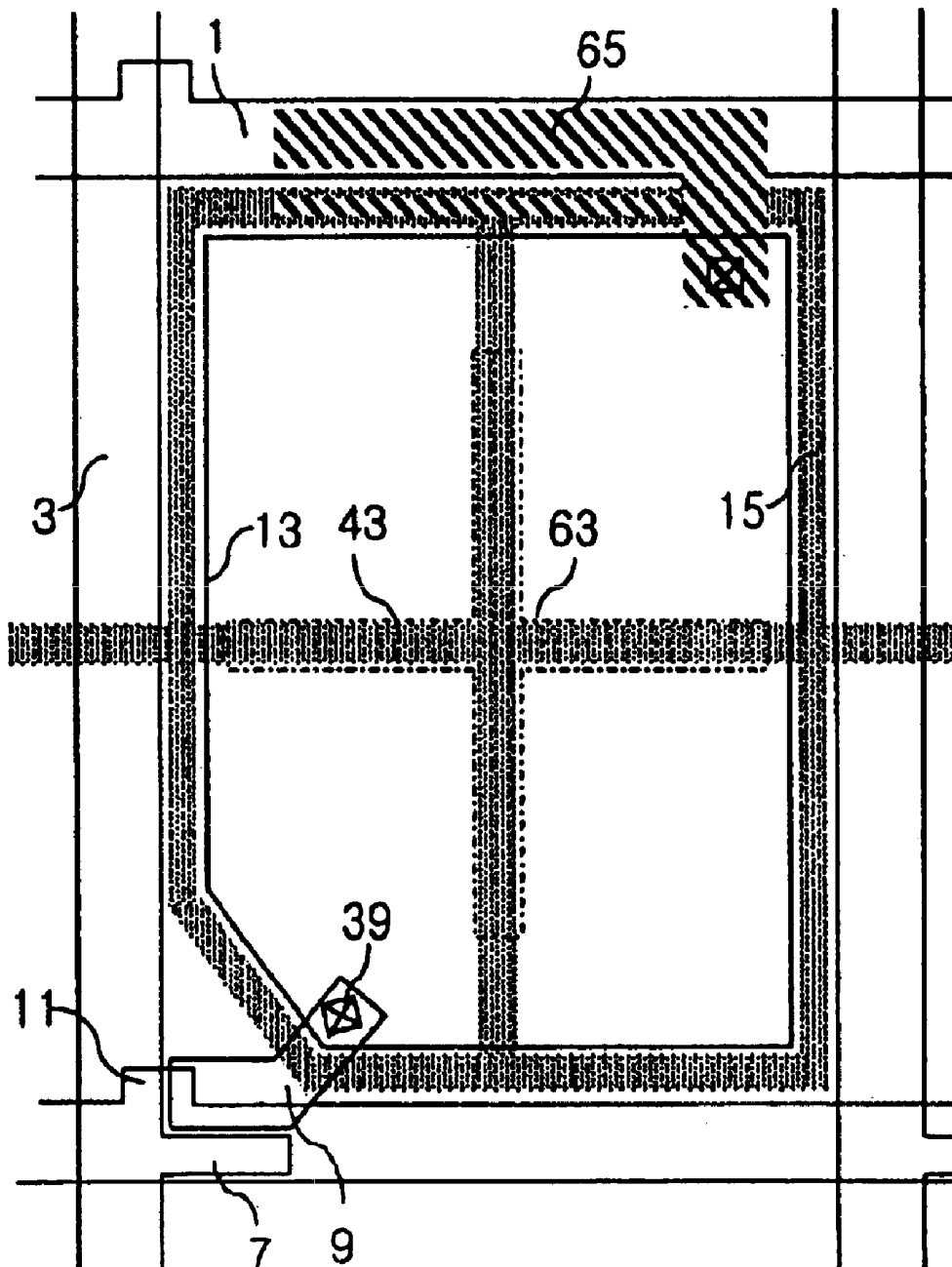


FIG. 4G

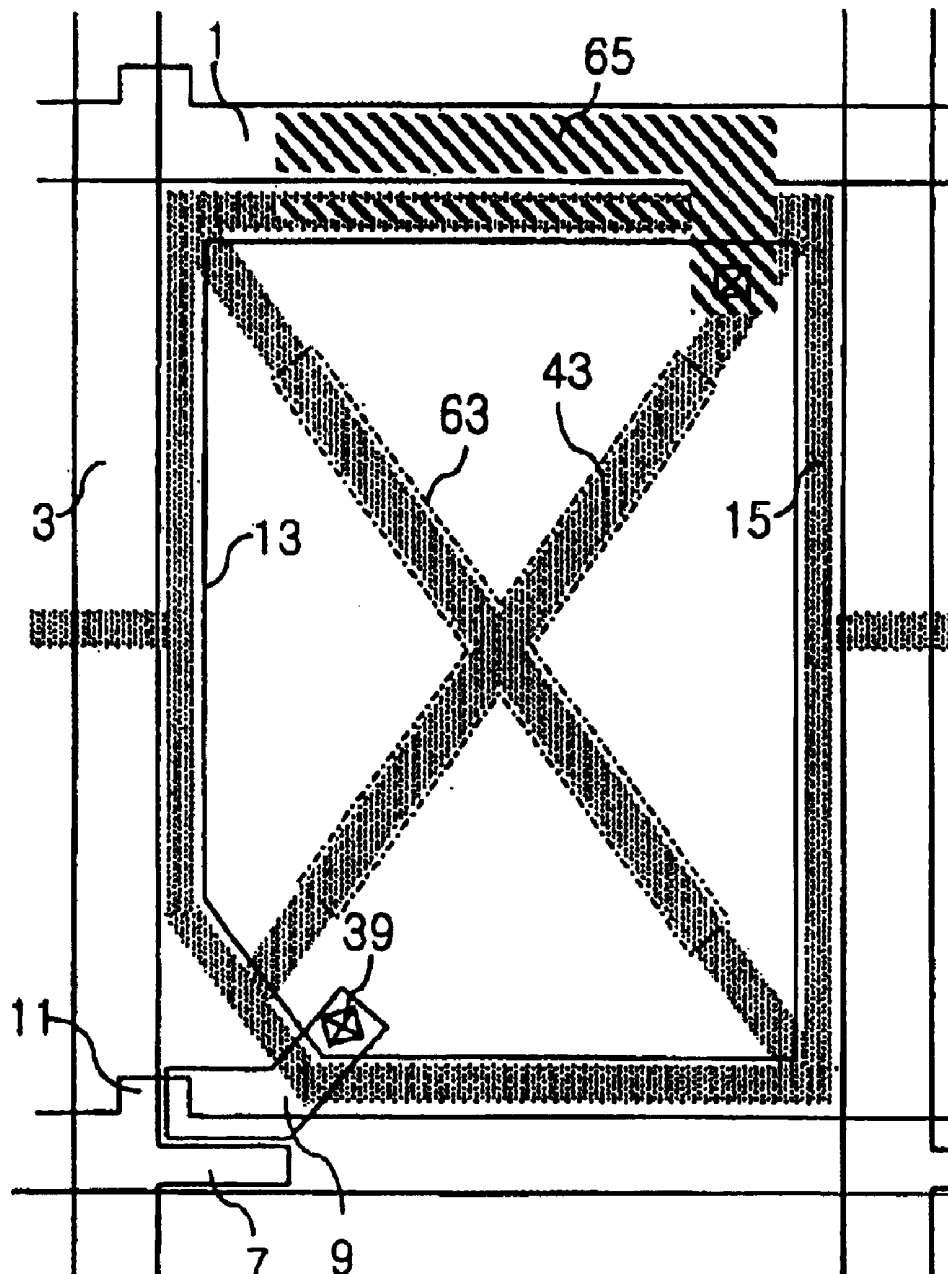


FIG. 4H

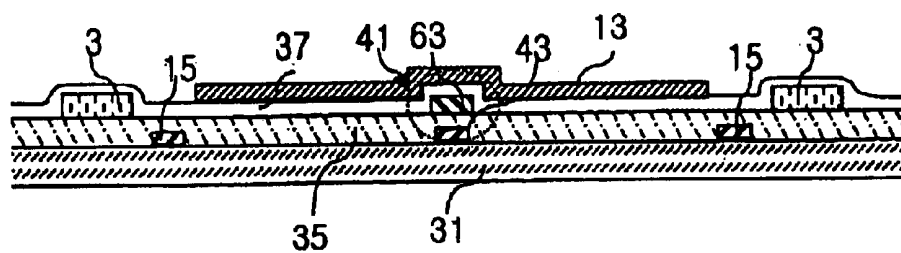


FIG. 4I

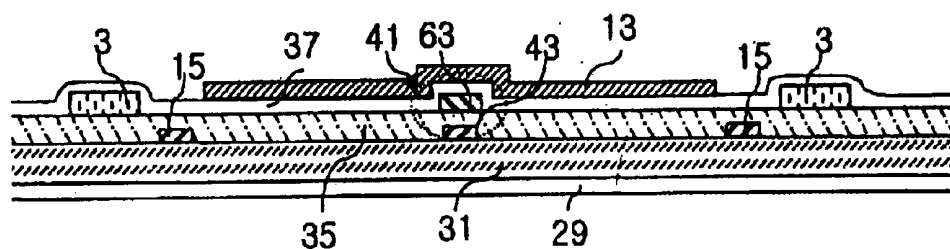
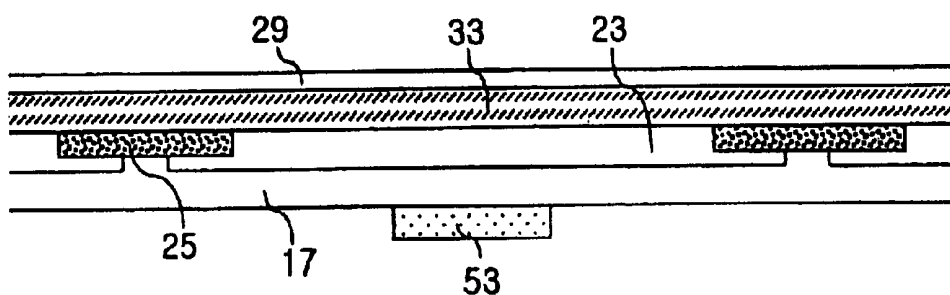


FIG. 4J

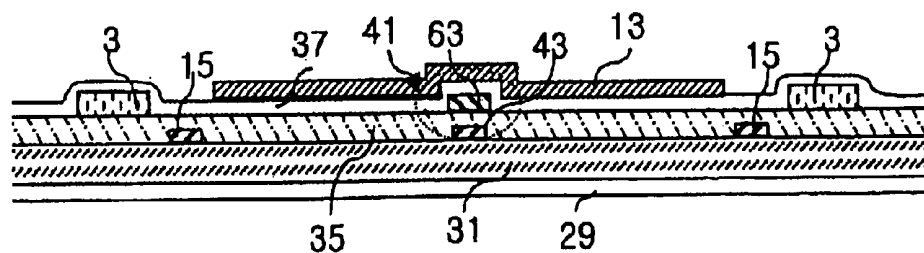
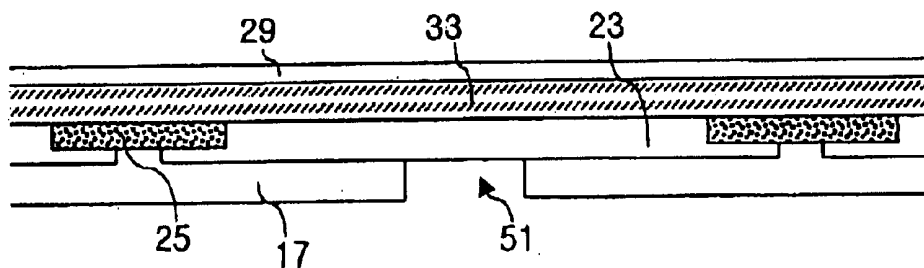


FIG. 5A

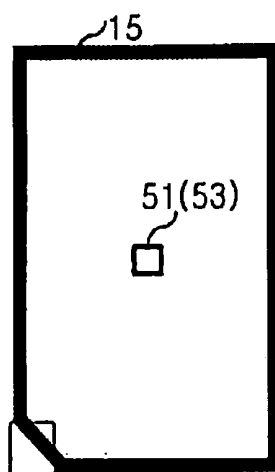


FIG. 5B

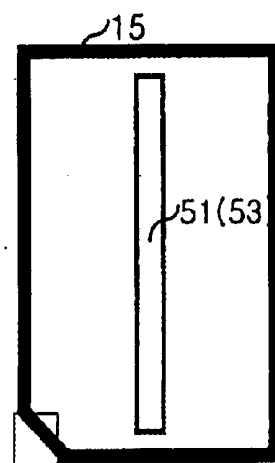


FIG. 5C

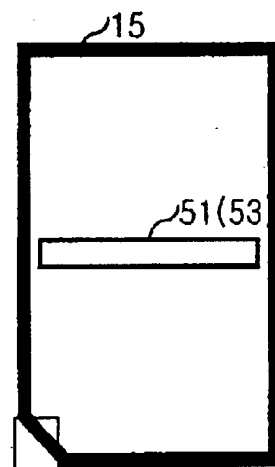


FIG. 5D

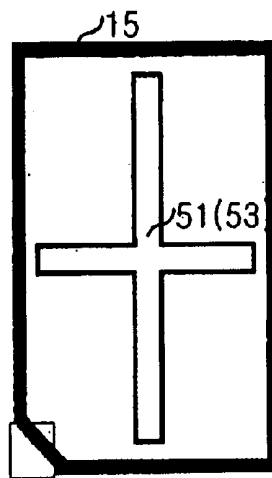


FIG. 5E

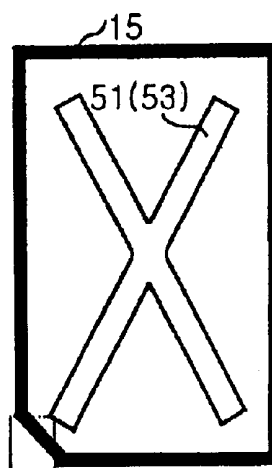


FIG. 5F

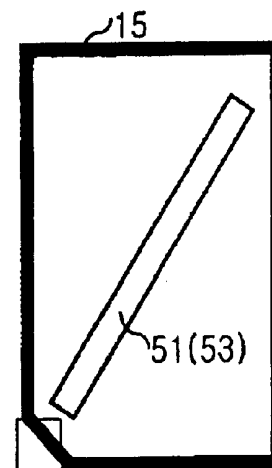
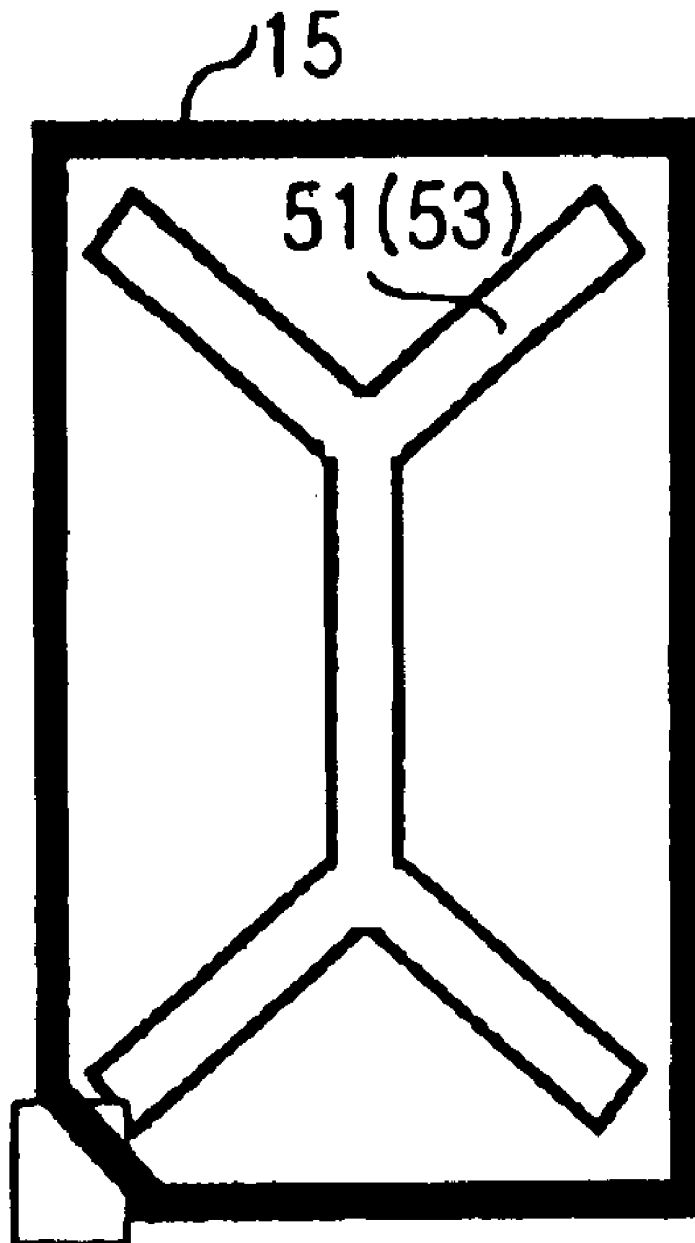


FIG. 5G



MULTI-DOMAIN LIQUID CRYSTAL DISPLAY DEVICE

CROSS-REFERENCE TO RELATED APPLICATIONS

This application is a divisional application of patent application Ser. No.: 09/497,507, filed Feb. 4, 2000 now U.S. Pat. No. 6,680,769, now allowed; which claims priority to Korean Patent Application No.: P1999-4195, filed Feb. 8, 1999 and Korean Patent Application No.: P1999-14492, filed Apr. 22, 1999, all of which are incorporated by reference for all purposes as if fully set forth herein.

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates to a liquid crystal display device (LCD), and more particularly, to a liquid crystal display device having common-auxiliary electrodes surrounding pixel region and storage electrode in the pixel region.

2. Description of the Related Art

Recently, an LCD has been proposed where the liquid crystal is not aligned and the liquid crystal is driven by side electrodes insulated from pixel electrodes. FIGS. 1a and 1b are sectional views of pixel unit of the conventional LCDs.

Regarding conventional LCDs, a plurality of gate bus lines arranged in a first direction on a first substrate and a plurality of data bus lines arranged in a second direction on the first substrate divide the first substrate into a plurality of pixel regions.

A thin film transistor (TFT) applies image signal delivered from the data bus line to a pixel electrode 13 on a passivation layer 37. The TFT is formed on each pixel region and comprises a gate electrode, a gate insulator, a semiconductor layer, a source electrode, and a drain electrode, etc.

Pixel electrode 13 is formed on the gate insulator, thereon passivation layer 37 is formed over the whole first substrate, and side electrode 21 is formed surrounding the pixel electrode 13 and a part of pixel electrode 13 overlaps side electrode 21. (FIG. 1A)

Alternatively, pixel electrode 13 is formed on the gate insulator, and passivation layer 37 is formed over the whole first substrate.

On second substrate, a light shielding layer is formed to shield any light leakage from gate and data bus lines, and the TFT, a color filter layer is formed on the light shielding layer, an over coat layer is formed on the color filter layer, a common electrode 17 is formed to have open area 27 on the over coat layer, and a liquid crystal layer is formed between the first and second substrates.

The common electrode 17 is formed and applies electric field to a liquid crystal layer together with pixel electrode 13. Side electrode 21 and open area (slit) 27 distort the electric field applied to the liquid crystal layer.

Then, in a unit pixel liquid crystal molecules are driven variously. This means that when voltage is applied to the LCD, dielectric energy due to the distorted electric field arranges the liquid crystal directors in needed or desired positions.

In the LCDs, however, open area 27 in common electrode 17 or pixel electrode 13 is necessary, and the liquid crystal molecules could be driven stably when the open area is wider. If the electrodes do not have an open area or the width of the open area is narrow, the electric field distortion needed

to divide the pixel region becomes weak. Then, when voltage over a threshold voltage, V_{th} , is applied, the time needed to stabilize the liquid crystal directors increases.

At this time, disclination occurs from the area where the liquid crystal directors are parallel with a transmittance axis of the polarizer, which results in a decrease in brightness. Further, according to the surface state of LCDs, the liquid crystal texture has an irregular structure.

SUMMARY OF THE INVENTION

Accordingly, the present invention is directed to an LCD that substantially obviates one or more problems due to limitations and disadvantages of the related art.

An object of the present invention is to provide a multi-domain LCD having high response time characteristics and high brightness by stable arrangement of liquid crystal molecules.

Additional features and advantages of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the invention. The objectives and other advantages of the invention will be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

To achieve the objects and in accordance with the purpose of the invention, as embodied and broadly described herein, a multi-domain liquid crystal display device comprises first and second substrates facing each other; a conductive layer distorting electric field on the first substrate; a common-auxiliary electrode on a same layer whereon the conductive layer distorting electric field is formed; a common electrode on the second substrate; a liquid crystal layer between the first and second substrates; and a storage electrode in an electric field inducing region that divides the liquid crystal layer into at least two domains.

The liquid crystal layer includes liquid crystal molecules having positive or negative dielectric anisotropy.

The multi-domain liquid crystal display device further comprises a negative uniaxial or negative biaxial film on at least one substrate.

The liquid crystal layer includes chiral dopants.

It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE DRAWINGS

The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of the specification, illustrates embodiments of the invention and together with description serve to explain the principles of the invention.

In the drawings:

FIGS. 1A and 1B are sectional views of the liquid crystal display devices in the related art;

FIGS. 2A, 2B, 2C, 2D, 2E, 2F, and 2G are plan views of the multi-domain liquid crystal display device according to the first embodiment of the present invention, and FIGS. 2H, 2I, and 2J, 2K are sectional views taken along the lines I-I' of FIG. 2A;

FIGS. 3A, 3B, 3C, 3D, 3E, 3F, and 3G are plan views of the multi-domain liquid crystal display device according to the second embodiment of the present invention, and FIGS. 3H, and 3I, 3J are sectional views taken along the lines II-II' of FIG. 3A;

FIGS. 4A, 4B, 4C, 4D, 4E, 4F, and 4G are plan views of the multi-domain liquid crystal display devices according to the third embodiment of the present invention, and FIGS. 4H, and 4I, 4J are sectional views taken along the lines III-III' of FIG. 4A;

FIGS. 5A, 5B, 5C, 5D, 5E, 5F, and 5G are plan views of the multi-domain liquid crystal display devices showing various electric field inducing window or dielectric frame according to the embodiments of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Hereinafter, the multi-domain liquid crystal display device of the present invention is explained in detail by accompanying the drawings.

FIGS. 2A, 2B, 2C, 2D, 2E, 2F, and 2G are plan views of the multi-domain liquid crystal display device according to the first embodiment of the present invention, FIGS. 2H, 2I are sectional views taken along the lines I-I' of FIG. 2A, and FIGS. 2J, 2K are entire sectional views according to the FIG. 2H.

As shown in the figures, the present invention comprises first and second substrates 31, 33, a plurality of gate bus lines 1 arranged in a first direction on the first substrate and a plurality of data bus lines 3 arranged in a second direction on the first substrate, a TFT, a passivation layer 37, a pixel electrode 13, and a common-auxiliary electrode 15.

Data bus lines 3 and gate bus lines 1 divide the first substrate 31 into a plurality of pixel regions. The common-auxiliary electrode 15 distorts electric field on a same layer whereon the pixel electrode 13 is formed. The TFT is formed on each pixel region and comprises a gate electrode 11, a gate insulator 35, a semiconductor layer, an ohmic contact layer, and source/drain electrodes 7, 9.

Passivation layer 37 is formed on the whole first substrate 31. Pixel electrode 13 is coupled to the drain electrode 9 by a contact hole 39.

On the second substrate 33, a light shielding layer 25 is formed to shield the light leaked from gate and data bus lines 1, 3, and the TFT, a color filter layer 23 is formed on the light-shielding layer 25, a common electrode 17 is formed on the color filter layer 23 and electrically connects to the common-auxiliary electrode 15, and a liquid crystal layer is formed between the first and second substrates 31, 33.

The common electrode 17 has dielectric frames 53 thereon, or has electric field inducing window 51 like hole or slit in its inner part.

In the first embodiment of the present invention, a first storage electrode 43 is formed on a same layer whereon the gate bus line is formed (refer to the FIG. 2I), or on a same layer whereon the source/drain electrodes 7, 9 are formed (refer to the FIG. 2H), and then the first storage electrode 43 forms a storage capacitor 41 with the pixel electrode 13. At this time, since the first storage electrode 43 is patterned as shown in the figures, the portions of disclination in the pixel region are covered.

Further, as shown in the FIG. 2C, a third storage electrode 65 is additionally formed to enlarge the capacitance of liquid crystal display device on the gate bus line 1, so that a image flicker or a residual image could be removed.

In the FIGS. 2D, 2E, 2F, and 2G, the first storage electrode 43 are patterned horizontally, vertically, and/or diagonally, or in a shape of "+" or "x", and in these embodiments, the dielectric frame 53 or electric field inducing window 51 on the second substrate is patterned as above.

In the FIGS. 2J and 2K, the common electrode 17 has dielectric frames 53 thereon, and has electric field inducing window 51 like hole or slit therein.

FIGS. 3A, 3B, 3C, 3D, 3E, 3F, and 3G are plan views of the multi-domain liquid crystal display device according to the second embodiment of the present invention, FIG. 3H is sectional view taken along the lines II-II' of FIG. 3A, and FIGS. 3I, 3J are entire sectional views according to the FIG. 3H.

As shown in the figures, the present invention comprises first and second substrates 31, 33, a plurality of gate bus lines 1 arranged in a first direction on the first substrate 31 and a plurality of data bus lines 3 arranged in a second direction on the first substrate 31, a common-auxiliary electrode 15, a first storage electrode 43, a TFT, a passivation layer 37, and a pixel electrode 13.

Data bus lines 3 and gate bus lines 1 divide the first substrate 31 into a plurality of pixel regions. The first storage electrode 43 is in the pixel region and on a same layer whereon the gate bus line 1 is formed, and is connected to or independently formed with the common-auxiliary electrode 15 to make a storage capacitor with the pixel electrode 13.

The common-auxiliary electrode 15 surrounds the pixel region and distorts electric field on a same layer whereon the gate bus line is formed.

The TFT is formed on each pixel region and comprises a gate electrode 11, a gate insulator 35, a semiconductor layer, an ohmic contact layer, and source/drain electrodes 7, 9. Passivation layer 37 is formed on the whole first substrate 31. Pixel electrode 13 is coupled to the drain electrode 9 by a contact hole 39.

On the second substrate 33, a light shielding layer 25 is formed to shield the light leaked from gate and data bus lines 1, 3, and the TFT, a color filter layer 23 is formed on the light shielding layer 25, a common electrode 17 is formed on the color filter layer 23 and electrically connects to the common-auxiliary electrode 15, and a liquid crystal layer is formed between the first and second substrates 31, 33.

The common electrode 17 has dielectric frames 53 thereon, or has electric field inducing window 51 like hole or slit in its inner part.

Further, as shown in the FIG. 3B, a third storage electrode 65 is additionally formed to enlarge the capacitance of liquid crystal display device on the gate bus line 1, so that a image flicker or a residual image could be removed.

In the FIG. 3C, all of the first storage electrode 43 are connected to the common-auxiliary electrode 15 and the third storage electrode 65 is formed on the common-auxiliary electrode 15 as well as the gate bus line 1, which obtains more enlarged capacitance than that in the embodiment in the FIG. 3B.

In the FIGS. 3D, 3E, 3F, and 3G, the first storage electrode 43 are patterned horizontally, vertically, and/or diagonally, or in a shape of "+" or "x", and in these embodiments, the dielectric frame 53 or electric field inducing window 51 on the second substrate is patterned as above. At this time, since the first storage electrode 43 is patterned as shown in the figures, the portions of disclination in the pixel region are covered.

In the FIGS. 3I and 3J, the common electrode 17 has dielectric frames 53 thereon, and has electric field inducing window 51 like hole or slit therein.

FIGS. 4A, 4B, 4C, 4D, 4E, 4F, and 4G are plan views of the multi-domain liquid crystal display device according to

the second embodiment of the present invention, FIG. 4H is sectional view taken along the lines III-III' of FIG. 4A, and FIGS. 4I, 4J are entire sectional views according to the FIG. 4H.

In the third embodiment, a second storage electrode **63** is additionally formed on a same layer whereon the data bus line **3** is formed to the structure in the second embodiment. The second storage electrode **63** is formed at a time when the source/drain electrodes **7, 9** are formed and have a shape like the first storage electrode **43** in the pixel region.

When the second storage electrode **63** is connected to the drain electrode **9**, the same voltage with the pixel electrode is applied to the second storage electrode, and then the second storage electrode forms a storage capacitor with the first storage electrode **43** related to the common-auxiliary electrode **15**. When the second storage electrode **63** is independently formed, this forms a storage capacitor with the pixel electrode **13** and the first storage electrode **43**.

Further, as shown in the FIG. 4B, a third storage electrode **65** is additionally formed to enlarge the capacitance of liquid crystal display device on the gate bus line **1**, so that a image flicker or a residual image could be removed.

In the FIG. 4C, all ends of the first storage electrode **43** are connected to the common-auxiliary electrode **15** and the third storage electrode **65** is formed on the common-auxiliary electrode **15** as well as the gate bus line **1**, which obtains more enlarged capacitance than that in the embodiment in the FIG. 4B.

In the FIGS. 4D, 4E, 4F, and 4G, the first storage electrode **43** are patterned horizontally, vertically, and/or diagonally, or in a shape of "+" or "x", and in these embodiments, the dielectric frame **53** or electric field inducing window **51** on the second substrate is patterned also as above. At this time, since the first storage electrode **43** is patterned as shown in the figures, the portions of disclination in the pixel region are covered.

In the FIGS. 4I and 4J, the common electrode **17** has dielectric frames **53** thereon, and has electric field inducing window **51** like hole or slit therein.

To manufacture the multi-domain LCD of the present invention, in each pixel region on the first substrate **31**, a TFT is formed comprising gate electrode **11**, gate insulator **35**, semiconductor layer, ohmic contact layer and source/drain electrodes **7, 9**. At this time, a plurality of gate bus lines **1** and a plurality of data bus lines **3** are formed to divide the first substrate **31** into a plurality of pixel regions.

Gate electrode **11** and gate bus line **1** are formed by sputtering and patterning a metal such as Al, Mo, Cr, Ta, or Al alloy, etc. Alternatively, it is possible to form the gate electrode and gate bus line as a double layer, the double layer is formed from different materials.

At this time, common-auxiliary electrode **15** is formed surrounding the pixel region. And, the first storage electrode **43** is formed by sputtering and patterning a metal such as ITO, Al, or Cr in the pixel region. The gate insulator **35** is formed by depositing SiN_x , SiO_x , or BCB (BenzoCycloButene), acrylic resin using PECVD thereon.

The semiconductor layer and the ohmic contact layer are formed by depositing with PECVD (Plasma Enhanced Chemical Vapor Deposition), and patterning amorphous silicon (a-Si) and doped amorphous silicon (n^+ a-Si), respectively. Also, gate insulator **35**, amorphous silicon (a-Si), and doped amorphous silicon (n^+ a-Si) could be formed by PECVD and patterned simultaneously.

Data bus line **3** and source/drain electrodes **7, 9** are formed by sputtering and patterning a metal such as Al, Mo,

Cr, Ta, or Al alloy, etc. Alternatively, it is possible to form the data bus line and source/drain electrodes as a double layer, the double layer is formed from different materials.

At this time, A storage electrode is additionally formed in the pixel region, on the gate bus line **1** and/or common-auxiliary electrode **15** at the same time, the storage electrode makes a storage capacitor with gate bus line **1** and/or common-auxiliary electrode **15**.

Subsequently, passivation layer **37** is formed with BCB (BenzoCycloButene), acrylic resin, polyimide based material, SiN_x or SiO_x on the whole first substrate **31**. Pixel electrode **13** is formed by sputtering and patterning a metal such as ITO (indium tin oxide). A contact hole **39** is formed to connect the pixel electrode **13** to the drain and storage electrodes by opening and patterning a part of the passivation layer **37** on the drain electrode.

When common-auxiliary electrode **15**, and gate bus line **1** or pixel electrode **13** are formed with the same material, the common-auxiliary and gate electrodes are simultaneously formed with one mask and connected electrically to common electrode **17**, or it is possible to form them with different materials or double layer with additional masks.

On the second substrate **33**, a light shielding layer **25** is formed to shield any light leakage from gate and data bus lines **1, 3**, the TFT, and common-auxiliary electrode **15**. A color filter layer **23** is formed R, G, B (red, green, blue) elements to alternate on the light shielding layer **25**.

A common electrode **17** is formed with ITO on the color filter layer **23**, and a liquid crystal layer is formed by injecting liquid crystal between the first and second substrates **31, 33**. The liquid crystal layer may include liquid crystal molecules having positive or negative dielectric anisotropy. Also, the liquid crystal layer may include chiral dopants.

On the common electrode **17**, dielectric frames **53** are formed by depositing photosensitive material and patterning in various shapes using photolithography. The dielectric frame **53** includes material of which dielectric constant is same or smaller than that of the liquid crystal, and the dielectric constant thereof is preferably below **3**, for example, photoacrylate or BCB (BenzoCycloButene).

Furthermore, the dielectric frame **53** is formed on at least one substrate between the first and second substrates.

Alternatively, an electric field inducing window **51** is formed on at least one substrate between the first and second substrates **31, 33**. At this time, the dielectric frame **53** and electric field inducing window are formed on same substrate together. The electric field inducing window **51** is formed by patterning hole or slit in the common electrode **17** or pixel electrode **13**.

To apply voltage (V_{com}) to common-auxiliary electrode **15**, Ag-dotting part is formed in each corner of driving area on the first substrate **31**, electric field is applied with the second substrate **33**, and the liquid crystal molecules are driven by the potential difference. A voltage (V_{com}) is applied to common-auxiliary electrode **15** by connecting the Ag-dotting part to the common-auxiliary electrode **15**, which is accomplished simultaneously by forming the common-auxiliary electrode **15**.

On at least one substrate, a compensation film **29** is formed with polymer. The compensation film **29** is a negative uniaxial film, which has one optical axis, and compensates the phase difference of the direction according to viewing-angle. Hence, it is possible to compensate effectively the right-left viewing-angle by widening the area

without gray inversion, increasing contrast ratio in an inclined direction, and forming one pixel to multi-domain.

In the present multi-domain liquid crystal display device, it is possible to form a negative biaxial film as the compensation film, which has two optical axes and wider viewing-angle characteristics as compared with the negative uniaxial film. The compensation film could be formed on both substrates or on one of them.

After forming the compensation film, polarizer is formed on at least one substrate. At this time, the polarization axis of the polarizer is 45°, 135° against the alignment axis of the liquid crystal molecules, and the compensation film and polarizer are preferably composed as one. Also, it is possible to form that the optic axis of the compensation film and the polarization axis of the polarizer are parallel.

In the multi-domain LCD of the present invention, the aperture ratio is enhanced by an optimum structure design of a "n-line" thin film transistor (U.S. Pat. No. 5,694,185) so as to reduce power consumption, increase luminance, and lower reflection, thus improving contrast ratio.

Aperture ratio is increased by forming the TFT above the gate line and providing a "n-line" TFT. The parasitic capacitor, occurring between the gate bus line and the drain electrode, can be reduced when a TFT having the same channel length as the symmetrical TFT structure is manufactured due to effect of channel length extension.

FIGS. 5A, 5B, 5C, 5D, 5E, 5F, and 5G are plan views of the multi-domain liquid crystal display devices showing various electric field inducing window or dielectric frame according to the embodiments of the present invention.

The multi-domain LCD of the present invention has a dielectric frame 53 on the pixel electrode 13, common electrode, and/or color filter layer 23, or an electric field inducing window 51 like a hole or slit in the pixel electrode 13, passivation layer 37, gate insulator 35, color filter layer 23, and/or common electrode by patterning, thereby electric field distortion effect and multi-domain are obtained.

That is, from forming electric field inducing window 51 or dielectric frame 53, the multi-domain is obtained by dividing each pixel into four domains such as in a "+", "x", or "double Y" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate.

In multi-domain LCD of the present invention, an alignment layer(not shown in the figure) is formed over the whole first and/or second substrates. The alignment layer includes a material such as polyamide or polyimide based materials, PVA (polyvinylalcohol), polyamic acid or SiO₂. When rubbing is used to determine an alignment direction, it should be possible to apply any material suitable for the rubbing treatment.

Moreover, it is possible to form the alignment layer with a photosensitive material such as PVCN (polyvinylcinnamate), PSCN (polysiloxanecinnamate), and CelCN (cellulosecinnamate) based materials. Any material suitable for the photo-aligning treatment may be used.

Irradiating light once on the alignment layer determines the alignment or pretilt direction and the pretilt angle.

The light used in the photo-alignment is preferably a light in a range of ultraviolet light, and any of unpolarized light, linearly polarized light, and partially polarized light can be used.

In the photo-alignment treatment, it is possible to apply one or both of the first and second substrates, and to apply different aligning-treatment on each substrate.

From the aligning-treatment, a multi-domain LCD is formed with at least two domains, and LC molecules of the LC layer are aligned differently one another on each domain. That is, the multi-domain is obtained by dividing each pixel into four domains such as in a "+" or "x" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate.

It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

Consequently, since the multi-domain LCD of the present invention forms the common-auxiliary electrode surrounding the pixel region and the storage electrode on a same layer whereon the gate bus line and/or the data bus line in the pixel region, it improves the aperture ratio and storage capacitor, which obtains wide viewing angle and multi-domain effect.

Moreover, when the common-auxiliary electrode is on a same layer with the gate bus line, the short between the pixel and common-auxiliary electrodes is removed, and then the yield is improved.

It will be apparent to those skilled in the art that various modifications can be made in the liquid crystal display device of the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention covers the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

What is claimed is:

1. A multi-domain liquid crystal display device, comprising:

- first and second substrates facing each other;
- a conductive layer distorting electric field on said first substrate;
- a common-auxiliary electrode on a same layer whereon said conductive layer distorting electric field is formed;
- a common electrode on said second substrate;
- a liquid crystal layer between said first and second substrates;
- a storage electrode in an electric field inducing region that divides said liquid crystal layer into at least two domains; and
- a pixel electrode on said conductive layer distorting electric field.

2. The multi-domain liquid crystal display device according to claim 1, wherein said conductive layer distorting electric field is a gate bus line.

3. The multi-domain liquid crystal display device according to claim 2, wherein said gate bus line is formed on a same layer whereon said storage electrode is formed.

4. The multi-domain liquid crystal display device according to claim 3, further comprising:

- a supplementary storage electrode on said storage electrode to form a storage capacitor.

5. The multi-domain liquid crystal display device according to claim 4, wherein said supplementary storage electrode is source and drain electrodes.

6. The multi-domain liquid crystal display device according to claim 3, further comprising:

- a supplementary storage electrode on said gate bus line and/or said common-auxiliary electrode.

7. The multi-domain liquid crystal display device according to claim 3, wherein said storage electrode is electrically connected to said common-auxiliary electrode.

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8. The multi-domain liquid crystal display device according to claim 1, wherein said pixel electrode overlaps said common-auxiliary electrode.

9. The multi-domain liquid crystal display device according to claim 1, wherein said pixel electrode is not overlapping said common-auxiliary electrode. 5

10. The multi-domain liquid crystal display device according to claim 1, wherein said electric field inducing region is an electric field inducing window in said pixel electrode.

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11. The multi-domain liquid crystal display device according to claim 1, wherein said electric field inducing region is a dielectric frame on said pixel electrode.

12. The multi-domain liquid crystal display device according to claim 1, wherein said pixel electrode includes a material selected from the group consisting of ITO (indium tin oxide), aluminum, and chromium.

* * * * *

专利名称(译)	多域液晶显示装置		
公开(公告)号	US6940574	公开(公告)日	2005-09-06
申请号	US10/725258	申请日	2003-12-02
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摘要(译)

一种多畴液晶显示装置，包括彼此面对的第一和第二基板以及在第一和第二基板之间的液晶层。扭曲电场的导电层位于第一基板上，并且公共辅助电极位于形成导电层扭曲电场的同一层上。公共电极位于第二基板上，并且存储电极位于电场诱导区域中，该区域将液晶层分成至少两个区域。

