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Araki et al.(10) **Pub. No.: US 2007/0146620 A1**(43) **Pub. Date: Jun. 28, 2007**(54) **LIQUID CRYSTAL DISPLAY ELEMENT****Publication Classification**(75) Inventors: **Toshio Araki**, Kikuchi-gun (JP);
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Tomoki Nakamura, Chiyoda-ku (JP)(51) **Int. Cl.**
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G02F 1/1333 (2006.01)(52) **U.S. Cl.** **349/156; 349/110**

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NEUSTADT, P.C.****1940 DUKE STREET****ALEXANDRIA, VA 22314 (US)**(57) **ABSTRACT**

A liquid crystal display element includes: a first substrate having a first surface and including a pixel electrode; a second substrate having a second surface opposing to the first surface, and including a light-shielding film, a thickness adjusting film and a counter electrode arranged in sequence in a direction to the second surface; a sealing material having a frame-shape to bond the first substrate and the second substrate; a liquid crystal layer provided in a region surrounded by the sealing material and between the first substrate and the second substrate, wherein the thickness adjusting film is provided to cover the light-shielding film to define the thickness of the liquid crystal layer, and has a contact hole, wherein the counter electrode is provided to oppose to the pixel electrode, and electrically connected to the light shielding film via the contact hole.

(73) Assignee: **MITSUBISHI ELECTRIC CORPORATION**, Chiyoda-ku (JP)(21) Appl. No.: **11/615,169**(22) Filed: **Dec. 22, 2006**(30) **Foreign Application Priority Data**

Dec. 23, 2005 (JP) 2005-371173

Sep. 5, 2006 (JP) 2006-240198

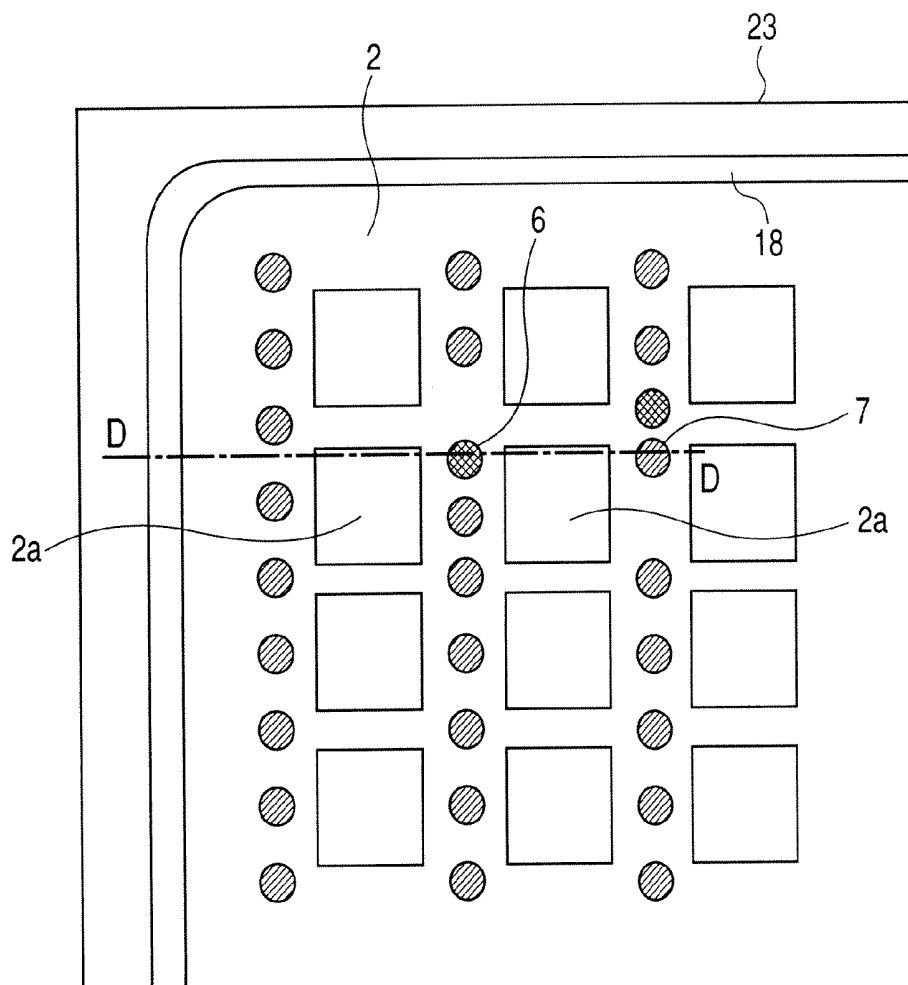


FIG. 1

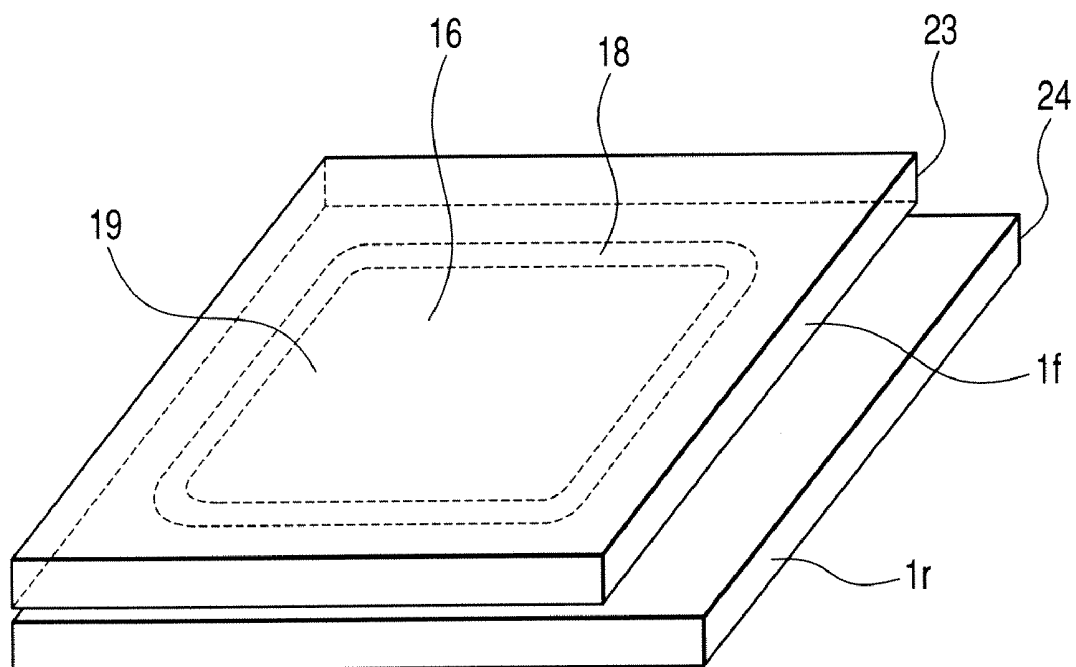


FIG. 2A

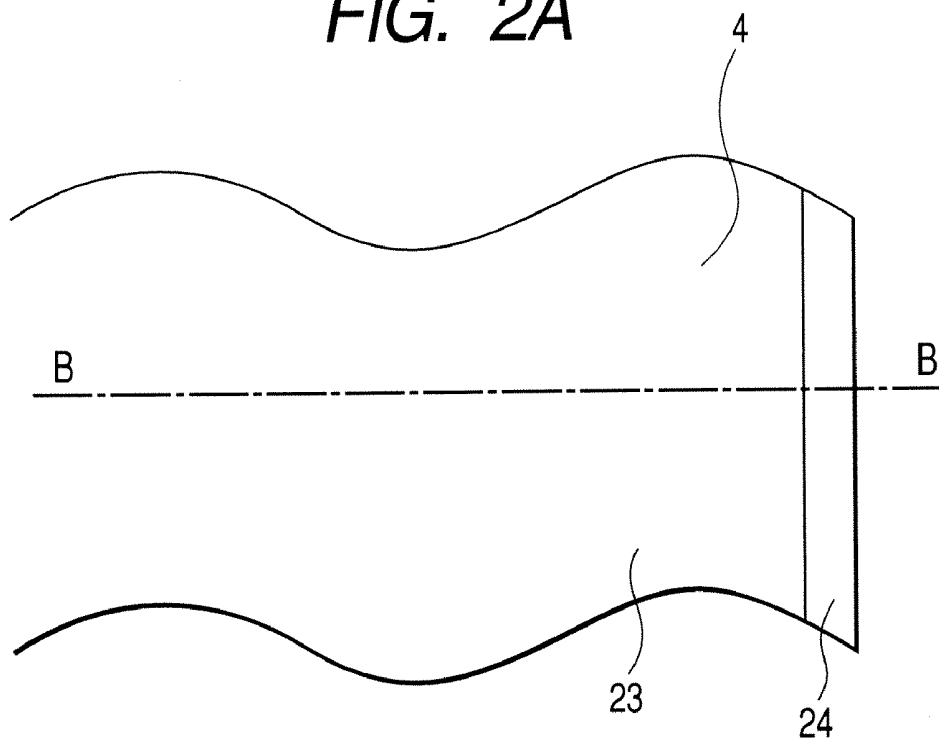


FIG. 2B

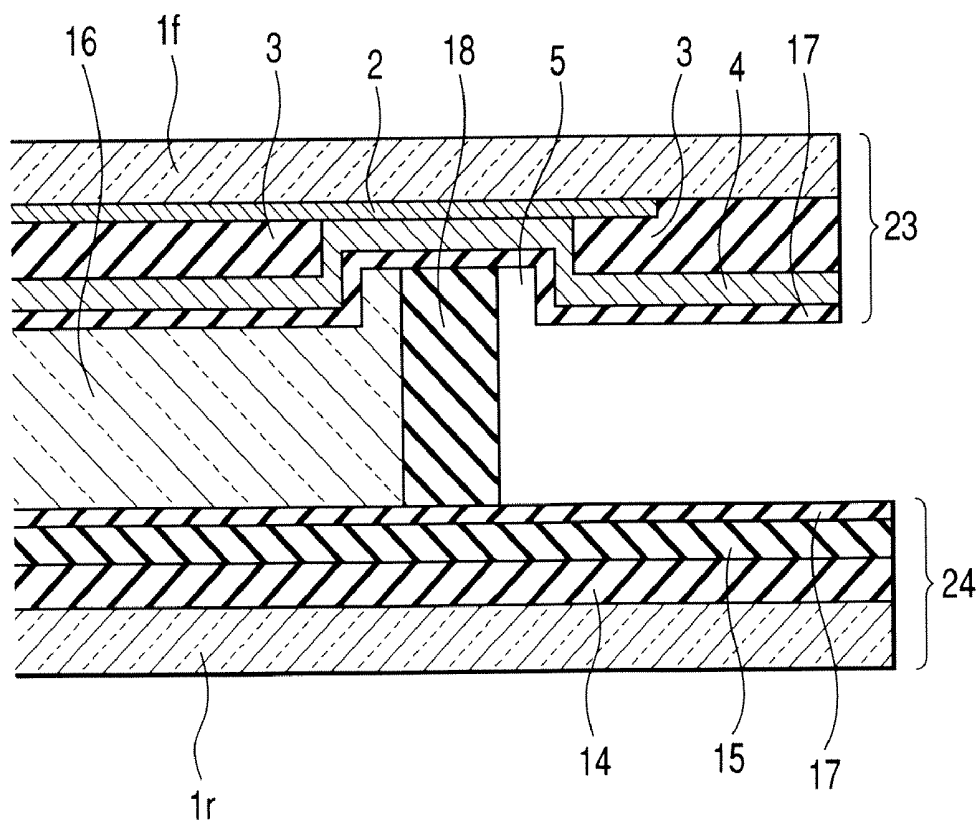


FIG. 3A

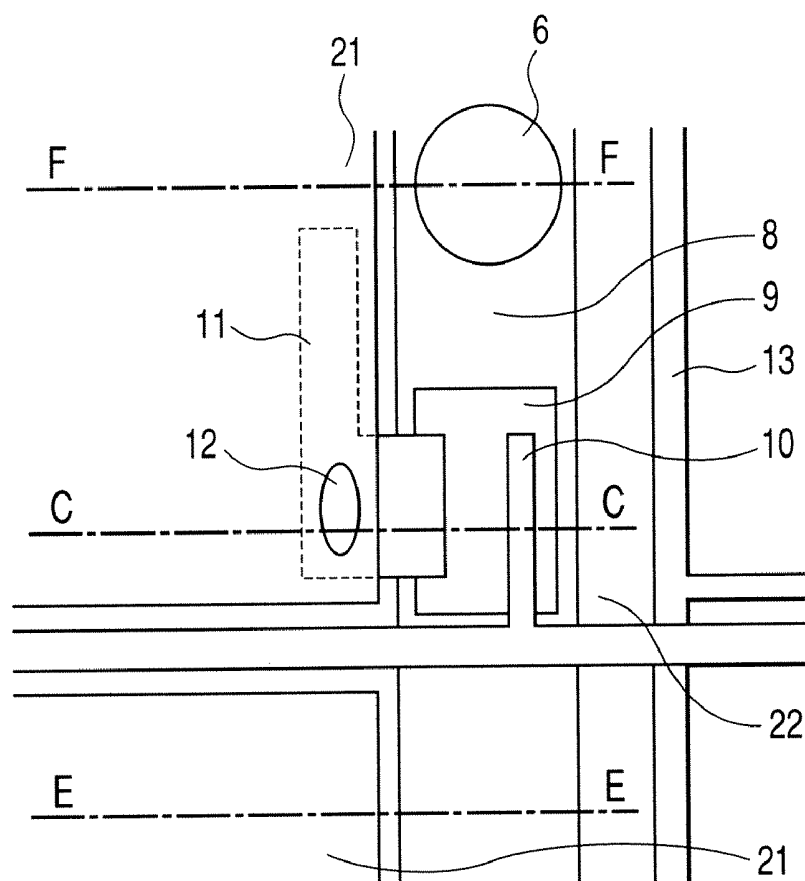


FIG. 3B

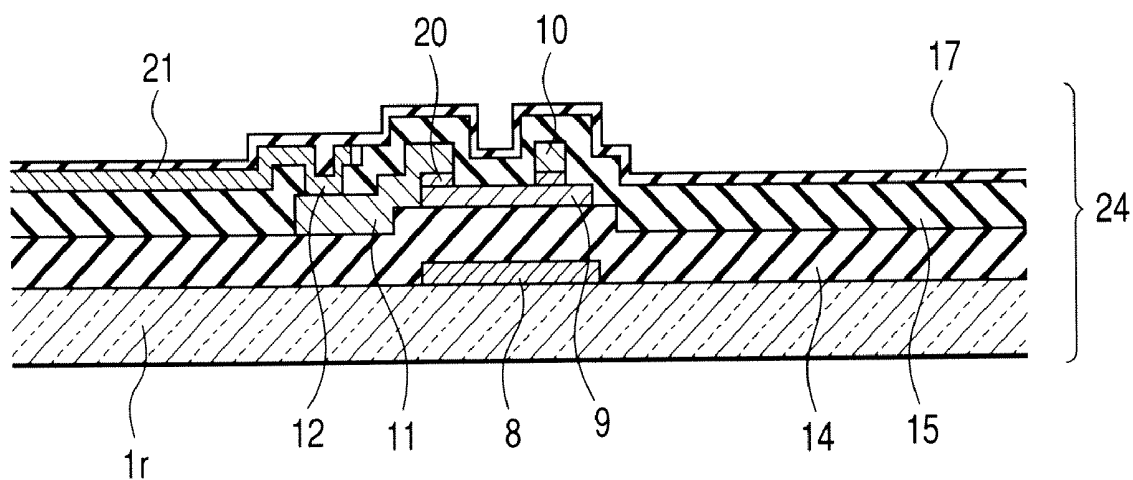


FIG. 5A

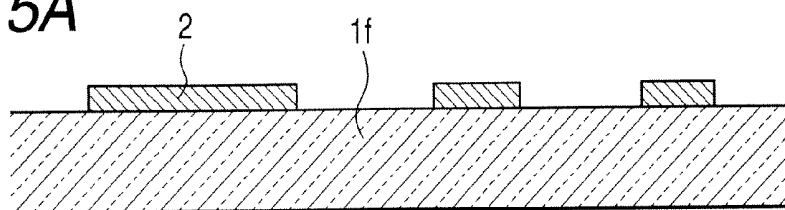


FIG. 5B

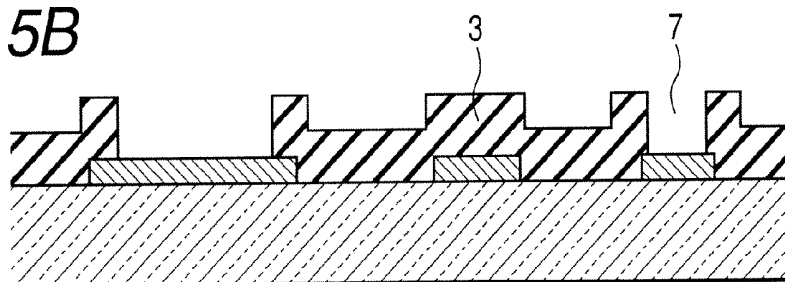


FIG. 5C

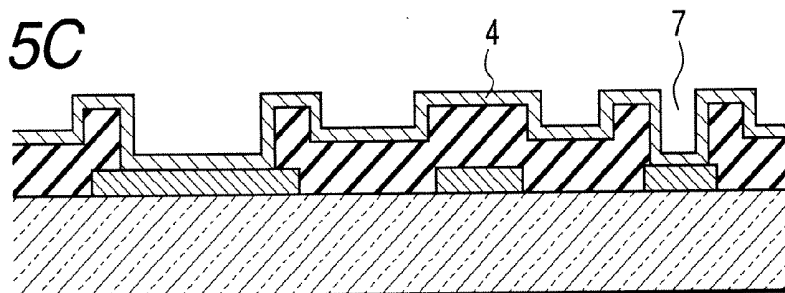


FIG. 5D

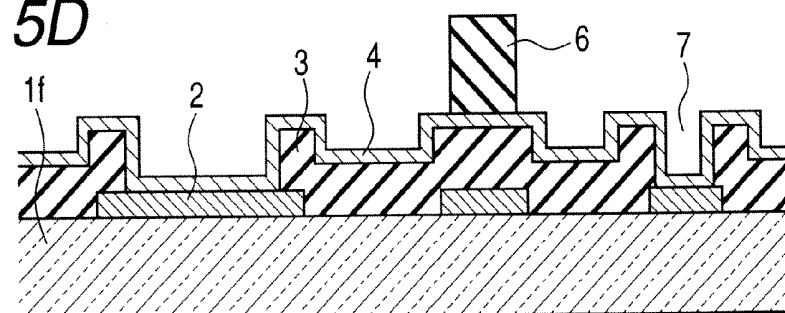


FIG. 5E

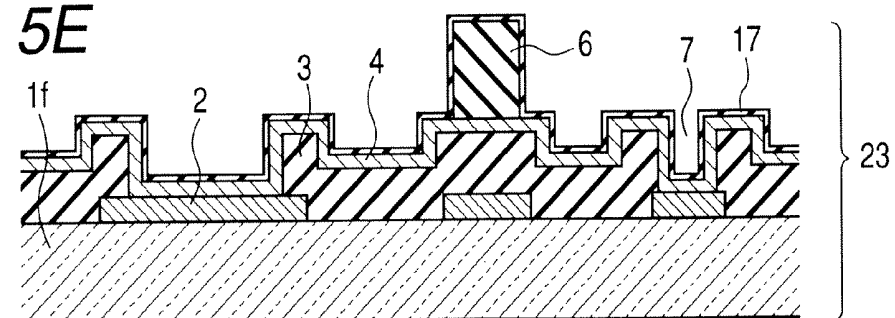


FIG. 6A

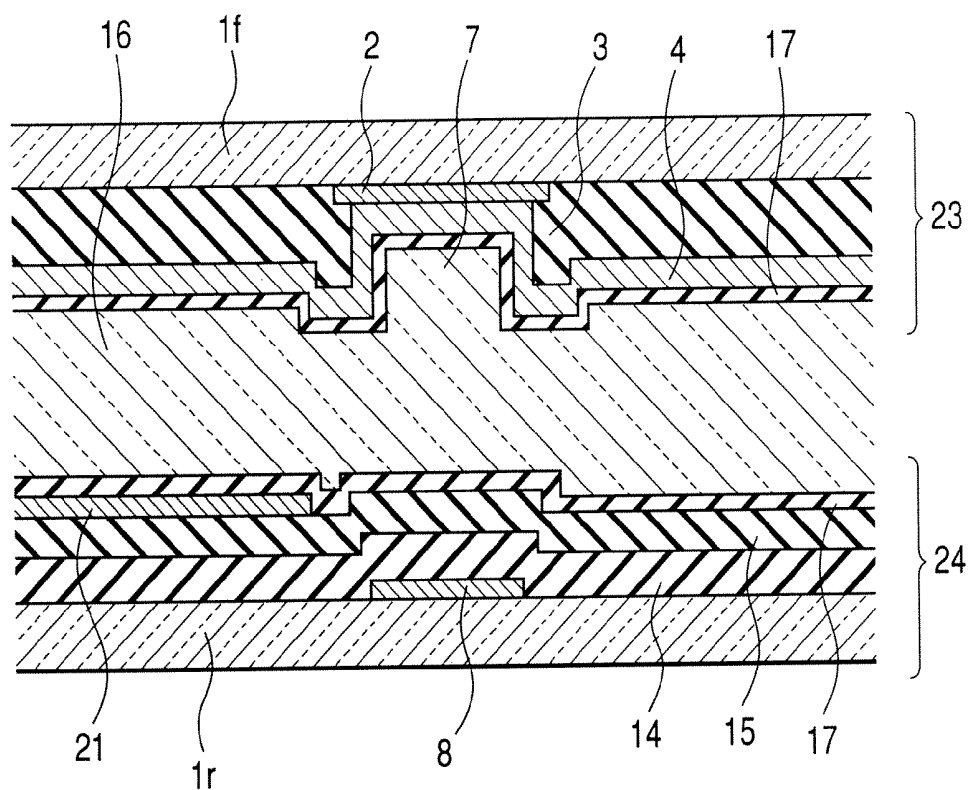


FIG. 6B

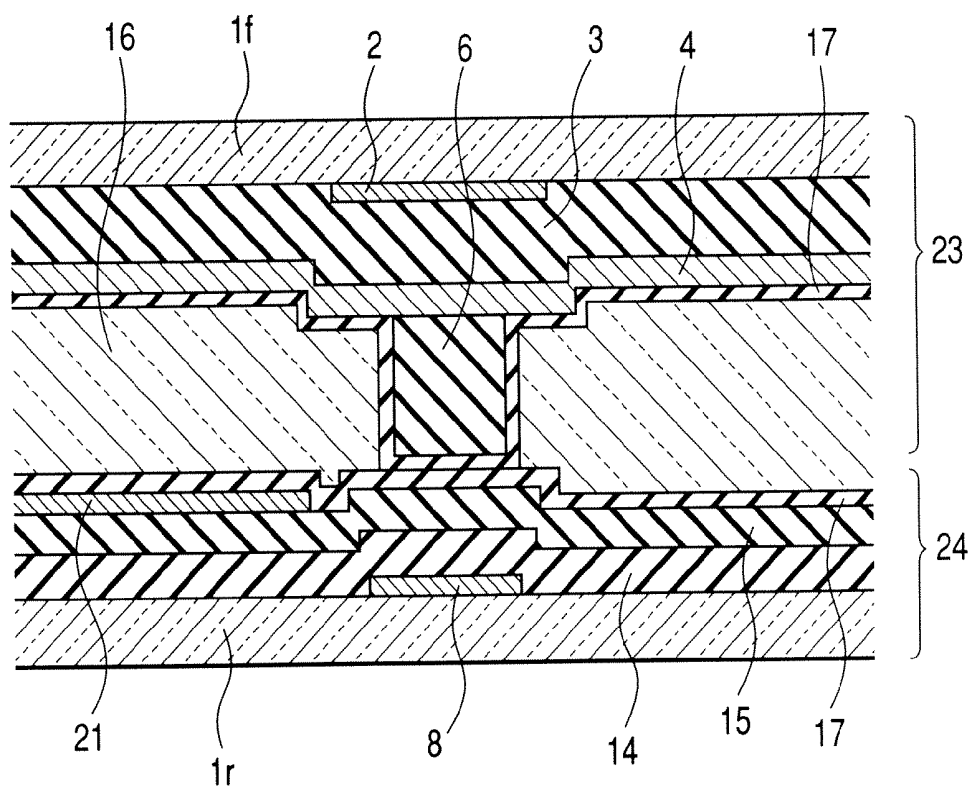
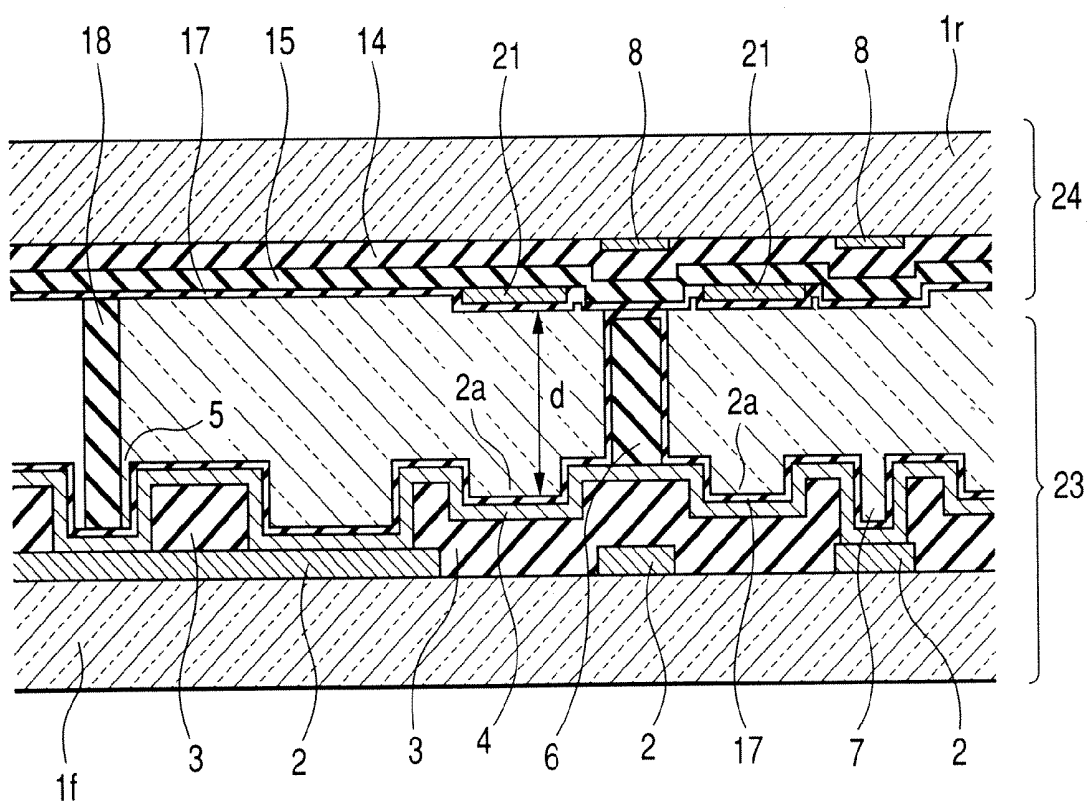


FIG. 7



LIQUID CRYSTAL DISPLAY ELEMENT

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] This application is based upon and claims the benefit of priority from Japanese Patent Application No. 2005-371173 filed on Dec. 23, 2005, and Japanese Patent Application No. 2006-240198 filed on Sep. 5, 2006, the entire contents of which are incorporated herein by reference.

BACKGROUND OF THE INVENTION

[0002] 1. Field

[0003] The present invention relates to a liquid crystal display element and in particular to a liquid crystal display element of the field sequential system.

[0004] 2. Description of the Related Art

[0005] In recent years, a field sequential liquid crystal display element that does not require a color filter has been developed. In the field sequential system, light sources of three colors RGB are switched at high speed to provide a color display. Thus, the field sequential system does not use a color filter for a typical liquid crystal display element but uses a counter substrate including; a counter electrode containing a transparent conductive film; and a light-shielding film containing a Cr film. The counter electrode is extended to the exterior of a pixel region and electrically connected to an electrode and a wiring provided on the alley substrate.

[0006] Since fast response is required in the field sequential liquid crystal display element, a ferroelectric liquid crystal is used as a liquid crystal enclosed between a pair of substrates forming a liquid crystal display element. To enclose a liquid crystal, there has been known a manufacturing method including dropping a liquid crystal on one substrate, applying a sealing material on the periphery of the pair of substrates, laminating the pair of substrates, and curing the sealing material with heat or light to enclose a liquid crystal. In case a ferroelectric liquid crystal is used as a liquid crystal material, a liquid crystal layer between a pair of substrates forming a liquid crystal display element has an optimum thickness range of 1 to 3 μm which is smaller than that of a typical liquid crystal layer. Thus, in some cases, a spacer is arranged to keep uniform the thickness of a liquid crystal layer in a pixel region.

[0007] In the process of laminating a pair of substrates, in case the gap between the pair of substrates is small, the sealing material between the pair of substrates spreads and the uncured resin of the sealing material extends to a region where the liquid crystal is enclosed thus contaminating the liquid crystal and degrading the display quality. While this phenomenon does not occur when the viscosity of the sealing material is increased, the sealing material does not sufficiently squash at a time of laminating the pair of substrates; therefore, the gap on the periphery of the panel increases. This results in the difference in the liquid crystal layer between the periphery and center of the pixel region, thus causing uneven display (for example, see JP-A-2003-280007 (FIG. 6)). That is, reduction of the thickness of a liquid crystal layer than a typical thickness may cause the degrading of display quality or uneven display.

[0008] There has been a technique for, reducing the thickness of a liquid crystal layer in a pixel region while maintaining good curing of a sealing material. In this technique, a liquid crystal layer thickness adjusting film is provided for adjusting the thickness of a liquid crystal layer in a region surrounded by a sealing material applied to the periphery of a counter substrate and forming thereon a counter electrode (for example, see JP-A-2003-264606 (FIG. 4)).

[0009] With the above approach, it is possible to avoid phenomenon concerning curing of a sealing material while reducing the thickness of a liquid crystal layer in a pixel region surrounded by the sealing material, but may cause a crosstalk. Use of this approach makes it necessary to form an insulating film serving as a liquid crystal layer thickness adjusting film between a light-shielding film and a counter electrode on a counter substrate. This is because the counter electrode is used to apply a voltage on a liquid crystal and is preferably provided at the top of layers (the remotest layer from a glass substrate) except an alignment layer, and a light-shielding film is preferably formed on a glass substrate from the viewpoint of adhesion to the base material and optimization of the reflection prevention effect.

[0010] While the aforementioned phenomenon is prevented by this structure, horizontal crosstalk may occur. Since the specific resistance of a transparent conductive film used for a counter electrode is about 0.00002 Ωcm and its thickness is a mere 0.1 μm , the resistance of the counter electrode is far from low. Thus, in a portion remote from the joint with an array substrate external to a pixel region, that is, near the center of the pixel region, the resistance component of the counter electrode is added between the pixel region and the array substrate, which may result in defective display called horizontal crosstalk. A transparent conductive film on a typical counter substrate is laminated on a low-resistance metal layer such as a light-shielding film thus suppressing horizontal crosstalk caused by an increase in the resistance component.

[0011] With the above approach, the laminating structure of a counter electrode and a light-shielding film in a pixel region is interrupted by the formation of an insulating film in the pixel region as the solution means. Thus, the low-conductive metal film in a light-shielding film does not fully contribute to the conductivity of the counter electrode. As a result, the counter electrode has high resistance thus causing horizontal crosstalk.

SUMMARY OF THE INVENTION

[0012] An object of the invention is to provide a liquid crystal display element having a structure where sealing formation is excellently performed on the periphery of a pixel region and the thickness of a liquid crystal layer is reduce, capable of suppressing the horizontal crosstalk and having a good display characteristic

[0013] According to an aspect of the invention, a liquid crystal display element includes: a first substrate having a first surface and including a pixel electrode; a second substrate having a second surface opposing to the first surface, and including a light-shielding film, a thickness adjusting film and a counter electrode arranged in sequence in a direction to the second surface; a sealing material having a frame-shape to bond the first substrate and the second substrate; a liquid crystal layer provided in a region sur-

rounded by the sealing material and between the first substrate and the second substrate, wherein the thickness adjusting film is provided to cover the light-shielding film to define the thickness of the liquid crystal layer, and has a contact hole, wherein the counter electrode is provided to oppose to the pixel electrode, and electrically connected to the light shielding film via the contact hole.

[0014] The liquid crystal display element according to the aspect of the invention has is advantageous in that the liquid crystal display element has a favorable display characteristic without liquid crystal contamination, improper peripheral gap or horizontal crosstalk while having a structure where the thickness of a liquid crystal layer in a pixel region is reduced by forming an insulating film in the pixel region of a counter substrate and forming a joint part of the transparent conductive film of the counter electrode and a light-shielding film.

BRIEF DESCRIPTION OF THE DRAWINGS

[0015] FIG. 1 is an external view of a liquid crystal display element according to an embodiment of the invention;

[0016] FIG. 2A is a plan view of a liquid crystal display element according to the embodiment of the invention near a sealing material;

[0017] FIG. 2B is a cross sectional view taken along line B-B shown in FIG. 2A;

[0018] FIG. 3A is a plan view of the rear substrate of a liquid crystal display element according to the embodiment of the invention;

[0019] FIG. 3B is a cross sectional view taken along line C-C shown in FIG. 3A;

[0020] FIG. 4A is a plan view of the front substrate of a liquid crystal display element according to the embodiment of the invention;

[0021] FIG. 4B is a cross sectional view taken along line D-D shown in FIG. 4A;

[0022] FIGS. 5A to 5E are cross sectional views showing the manufacturing process of the front substrate of a liquid crystal display element according to the embodiment of the invention;

[0023] FIG. 6A is a cross sectional view of the front and rear substrates of a liquid crystal display element according to the embodiment of the invention taken along line E-E shown in FIG. 3A;

[0024] FIG. 6B is a cross sectional view taken along line F-F shown in FIG. 3A; and

[0025] FIG. 7 is a cross sectional view of the front and rear substrates of a liquid crystal display element according to the embodiment of the invention taken along line D-D shown in FIG. 4A.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

[0026] The external view of a liquid crystal display element according to an embodiment of the invention is shown in FIG. 1. The liquid crystal display element shown in FIG. 1 includes a rear substrate 24 as a first substrate positioned, and a front substrate 23 as a second substrate positioned on

a front side of the rear substrate 24 viewed from the user. The rear substrate 24 and the front substrate 23 are laminated together via a sealing material formed in the shape of a frame between the pair of substrates. A liquid crystal 16 is enclosed in the region surrounded by the sealing material 18, the rear substrate 24 and the front substrate 23. The sealing material 18 is formed on the outer side of the pixel region 19. On an inner surface of the rear substrate 24 within the pixel region 19, TFT (Thin Film Transistor) described later is formed.

[0027] A plan view of the liquid crystal display element near the sealing material 18 is shown in FIG. 2A. A cross section of the front substrate 23 and rear substrate 24 taken along line B-B in FIG. 2A is shown in FIG. 2B.

[0028] The liquid crystal display element of the embodiment is used for a field sequential liquid crystal display element and does not include a color filter. The liquid crystal display element is an active matrix liquid crystal display element that uses a TFT as an active element. Basically, a pair of transparent glass substrates is joined via the sealing material 18. A liquid crystal layer 16 is enclosed in a region surrounded by the sealing part of the sealing material 18. In close proximity to a sealing material 18 on the inner surface of the glass substrate Ir of the rear substrate 24, a gate insulating film 14, a passivation film 15 and an alignment film 17 as components of a TFT described later are formed. On the inner surface of the glass substrate Ir of the front substrate 23, a light-shielding film 2 with electric conductivity formed of laminated layers respectively including for example chrome oxide (CrOx: x is a positive number) and chromium (Cr), a liquid crystal layer thickness adjusting film 3 (referred to as "thickness adjusting film") containing for example silicon nitride (SiNx: x is a positive number), a transparent conductive film 4 containing for example ITO (indium tin oxide) (hereinafter referred to as "ITO film"), and the alignment film 17 are formed. A concave part 5 is formed in the thickness adjusting film 3 at a position where the sealing material 18 is formed.

[0029] Next, the TFT and the pixel electrode provided on the inner surface of the rear substrate 24 will be described. A plan view of the rear substrate 24 where the TFT is formed as seen from the inner surface is shown in FIG. 3A. A cross section of the rear substrate 24 taken along line C-C in FIG. 3A is shown in FIG. 3B. Referring to FIGS. 3A) and 3B, the TFT 22 provided on the inner surface of the rear substrate 24 includes: common CS electrode wiring 13 and gate wiring 8 formed on the surface of the rear substrate 24; a transparent gate insulating film 14 formed across the entire region of the substrate while covering the gate wiring 8; an i-type semiconductor film 9 formed while being confronted with the gate wiring 8; a source electrode 10 and a drain electrode 11 formed on the i-type semiconductor film 9 via an n-type semiconductor film 20; and a passivation film 15 covering the i-type semiconductor film 9, the source electrode 10 and the drain electrode 11. In the passivation film 15, a TFT contact hole 12 is formed. Each of a plurality of pixel electrodes 21 contains a transparent conductive film such as an ITO and is formed on the passivation film 15. The pixel electrodes 21 are connected, at one end of the passivation film 15, to the drain electrode 11 of the TFT 22 corresponding to each of the pixel electrodes 21. The alignment film 17 is formed while covering the TFT 22 and the passivation film 15. On the gate wiring 8 on line F-F in FIG. 3A, a

columnar spacer 6 is formed. Further, in the counter substrate (front substrate 23) positioned on the gate wiring 8 on line E-E in FIG. 3A, a contact hole 7 (not shown) is formed as described later.

[0030] Next, the front substrate 23 will be described. A plan view of the front substrate 23 as seen from the inner surface is shown in FIG. 4A. A cross section of the front substrate 23 taken along line D-D in FIG. 4A is shown in FIG. 4B. Referring to FIG. 4A, the light-shielding film 2 of the rear substrate 24 has openings 2a punched in an almost rectangular shape at regions in correspondence to the respective pixel electrodes 21 arranged in a matrix shape. On the light-shielding film 2, the thickness adjusting film 3 containing a SiNx film is formed to cover the light-shielding film as shown in FIG. 4B. On the thickness adjusting film 3, is provided the ITO film 4 as a counter electrode. The ITO film 4 is electrically connected to the light-shielding film 2 via the contact hole 7 provided in the thickness adjusting film 3. The pattern of the light-shielding film 2 has a continuously linked shape except in the regions of the openings 2a. On the ITO film 4, the columnar spacer 6 containing a photosensitive resin film is formed. When the columnar spacer 6 overlaps the contact hole 7, it is difficult to keep uniform the thickness of the liquid crystal layer. The columnar spacer 6 is preferably formed at a region except where the contact hole 7 is formed, for example as shown in FIG. 4A. The alignment film 17 is applied and formed thereon.

[0031] Next, a method for manufacturing the front substrate 23 according to the embodiment of the invention will be described referring to FIGS. 5A to 5E. FIGS. 5A to 5E are process sectional view at sections corresponding to FIG. 4B. In FIG. 5A, a CrOx film of 10 nm thickness and a Cr film of 150 nm thickness are formed continuously on a glass substrate 1f with the sputtering method, and then a photoengraving process, etching, and a resist removing process are performed to form a pattern of the light-shielding film 2. In order to enhance the concentration of the light-shielding film 2 and improve the light-shielding effect, chrome nitride (CrNx) may be used instead of Cr to provide a laminate structure of CrOx and CrNx. In this case, it is possible to obtain a sufficient light-shielding effect by way of a reduced film thickness compared with Cr.

[0032] In FIG. 5B, a SiNx (silicon nitride) film of 700 nm thickness is formed. A photoengraving process, etching, and a resist removing process are performed to form the thickness adjusting film 3 and a contact hole 7. In this case, as shown in FIG. 2B, a SiNx film beneath the position where the sealing material 18 is to be formed may be removed to form the concave part 5.

[0033] In FIG. 5C, the ITO film 4 as a counter electrode is formed on the entire surface by using the sputtering method. In this case, the electric resistance of the counter electrode can be reduced by forming the ITO film 4 so that the light-shielding film 2 and the ITO film 4 will be electrically conducted via the contact hole 7. For example, an entire part of the thickness adjusting film 3 on the light-shielding film 2 may be removed in order to increase the area of the contact hole 7 used to connect the light-shielding film 2 and the ITO film 4 although this approach results in an increase in the etching time and reduced productivity. In case the contact hole 7 is made like as this

embodiment, it is possible to suppress an increase in the area used for etching and reduce the etching time. In a typical case, it suffices to form more than one contact hole 7 in order to reduce the electric resistance of the counter electrode.

[0034] In FIG. 5D, a photosensitive resin film of about 2 μ m thickness (not shown) is applied and a photoengraving process is performed, then a columnar spacer 6 having a predetermined height (for example 5 μ m) is formed. The front substrate 23 includes at least one contact hole 7 for electrically connecting the ITO film 4 as a counter electrode and the light-shielding film 2 formed in the thickness adjusting film 3 in the display pixel region. In FIG. 5E, the alignment film 17 is applied and formed to complete the front substrate 23 according to the embodiment of the invention.

[0035] While the rear substrate 24 and the front substrate 23 have been described, in this example, a cross section of the front substrate 23 and the rear substrate 24 taken along line E-E in FIG. 3A is shown in FIG. 6A in order to define the positional relationship between the position of the contact hole 7 formed in the front substrate 23 and the TFT 22 formed in the rear substrate 24 assumed in case the two substrates are bonded to each other while confronted with each other by using the columnar spacer 6 and the sealing material 18 as shown in FIGS. 1, 2A and 2B. As shown in FIG. 3A and FIG. 6A, the contact hole 7 in the front substrate 23 is preferably formed opposite on the gate wiring 8 with flat surface on the rear substrate 24 rather than formed opposite on the TFT 22 having a distorted surface shape. This is because alignment control by the rubbing process on the alignment film 17 is assured.

[0036] A cross section of the front substrate 23 and the rear substrate 24 taken along line F-F in FIG. 3A is shown in FIG. 6B in order to define the positional relationship between the position of the columnar spacer 6 of the front substrate 23 and the TFT 22. As shown in FIG. 3A and FIG. 6B, the columnar spacer 6 of the front substrate 23 is preferably formed opposite on the gate wiring 8 with flat surface on the rear substrate 24 rather than formed opposite on the TFT 22 having a distorted surface shape. This is because it is readily possible to control the liquid crystal layer thickness.

[0037] A cross section of the front substrate 23 and the rear substrate 24 taken along line D-D in FIG. 4A is shown in FIG. 7 in order to define the positional relationship between the opening 2a formed in the light-shielding film 2 of the front substrate 23 and the pixel electrode 21 of the rear substrate 24. The sealing material 18 holds the front substrate 23 and the rear substrate 24 as described above, and the columnar spacer 6 comes into contact with the inner surface of the rear substrate 24 to maintain the liquid crystal layer thickness. The contact hole 7 and the columnar spacer 6 are formed in separate positions to avoid overlapping. The pixel electrode 21 of the rear substrate 24 is formed in a position opposite to the opening 2a in the light-shielding film 2 of the front substrate 23. The thickness d of the liquid crystal layer 16 held in this position (distance between inner surfaces of the front substrate 23 and rear substrate 24; gap) is specified mainly with the sealing material 18 and the thickness adjusting film 3.

[0038] According to the result of measuring the electric resistance of the counter electrode assumed in case an electrical connection part is formed by the contact hole 7, the

value obtained is on the order of one tenth or one hundredth the value obtained in the absence of the contact hole 7, that is, in case the counter electrode is formed by the ITO film 4 alone. In order to obtain a low electric resistance value than in a case where the counter electrode is formed by the ITO film 4 alone, the light-shielding film 2 is preferably formed of a material having a lower electric resistance value than that of the ITO film 4. It has been verified that a liquid crystal display element provided by forming the frame-shaped sealing material 18 between the front substrate 23 and the rear substrate 24 and bonding the substrates with the liquid crystal 16 enclosed therebetween is free from liquid crystal contamination or improper peripheral gap as well as defective display called horizontal crosstalk caused by an increase in the electric resistance of the counter electrode.

[0039] At least one contact hole 7 for electrically connecting the ITO film 4 as a counter electrode and the light-shielding film 2 is preferably formed in the thickness adjusting film 3 in the display pixel region. Further, as shown in FIG. 4A, at least one contact hole 7 is preferably formed in association with each of the plurality of pixel electrodes 21 in the display pixel region. In this case, the electric resistance values of individual counter electrodes (the light-shielding film 2, the ITO film 4) respectively corresponding to individual pixel electrodes 21 can be almost uniform in the display pixel region. It is thus possible to suppress uneven display caused by variations in display of the horizontal crosstalk as well as widen the margin of defective display.

[0040] While a SiNx film 700 nm thick is formed as the thickness adjusting film 3 in this embodiment, the invention is not limited thereto but another type of transparent insulating film, for example a SiOx film may be used. Or, a transparent insulating resin film may be applied to form the thickness adjusting film 3. In this case, it is possible to smooth the surface of the thickness adjusting film 3 so that at least the thickness of the liquid crystal layer 16 in the region opposite to the pixel electrodes 21 may be made uniform and uniform rubbing of the alignment film 17 is made possible, which improves the display quality. Further, by using a photosensitive resin, it is readily possible to form a contact hole 7 by using a photoengraving process.

[0041] A liquid crystal display element according to the embodiment of the invention has the thickness adjusting film 3 formed therein. Even in case the thickness of a liquid crystal layer in a pixel region 19 is small, the sealing material 18 is formed without liquid crystal contamination or improper peripheral gap. Further, the ITO film 4 and the light-shielding film 2 are electrically connected to each other via the contact hole 7 provided in the thickness adjusting film 3 in the pixel region 19, so that there is no fear of defective display caused by an increase in the resistance of a counter electrode.

What is claimed is:

1. A liquid crystal display element comprising:

- a first substrate having a first surface and including a pixel electrode;
- a second substrate having a second surface opposing to the first surface, and including a light-shielding film, a thickness adjusting film and a counter electrode arranged in sequence in a direction to the second surface;
- a sealing material having a frame-shape to bond the first substrate and the second substrate;
- a liquid crystal layer provided in a region surrounded by the sealing material and between the first substrate and the second substrate,

wherein the thickness adjusting film is provided to cover the light-shielding film to define the thickness of the liquid crystal layer, and has a contact hole,

wherein the counter electrode is provided to oppose to the pixel electrode, and electrically connected to the light shielding film via the contact hole.

2. The liquid crystal display element according to claim 1, wherein the contact hole is arranged in a region surrounded by the sealing material.

3. The liquid crystal display element according to claim 1, wherein:

the first substrate includes a plurality of the pixel electrodes arranged; and

the light-shielding film has openings provided at regions opposing to the pixel electrodes.

4. The liquid crystal display element according to claim 3, wherein the light-shielding film has an electric conductivity and the pattern of the light-shielding film has a continuously linked shape except in the regions of the openings.

5. The liquid crystal display element according to claim 3, wherein the thickness adjusting film has a plurality of the contact holes, and a number of the contact holes is equal to or above a number of the pixel electrodes.

6. The liquid crystal display element according to claim 1, wherein the second substrate further includes a columnar spacer provided on the counter electrode to come in contact with the first surface of the first substrate.

7. The liquid crystal display element according to claim 6, wherein the contact hole is formed in a position where the contact hole does not overlap the columnar spacer.

8. The liquid crystal display element according to claim 1, wherein the first substrate further includes a thin-film transistor connected to the pixel electrode, and a gate wire and a source wire connected to the thin-film transistor to supply a gate signal and a data signal thereto.

9. The liquid crystal display element according to claim 8, wherein the contact hole is opposed to the gate wire.

* * * * *

专利名称(译)	液晶显示元件		
公开(公告)号	US20070146620A1	公开(公告)日	2007-06-28
申请号	US11/615169	申请日	2006-12-22
[标]申请(专利权)人(译)	三菱电机株式会社		
申请(专利权)人(译)	三菱电机株式会社		
当前申请(专利权)人(译)	三菱电机株式会社		
[标]发明人	ARAKI TOSHIO HIRAKAWA MAKOTO NAKAMURA TOMOKI		
发明人	ARAKI, TOSHIO HIRAKAWA, MAKOTO NAKAMURA, TOMOKI		
IPC分类号	G02F1/1339 G02F1/1333		
CPC分类号	G02F1/1339 G02F1/13394 G02F1/1345		
优先权	2006240198 2006-09-05 JP 2005371173 2005-12-23 JP		
其他公开文献	US7671939		
外部链接	Espacenet USPTO		

摘要(译)

一种液晶显示元件，包括：第一基板，具有第一表面并包括像素电极；第二基板，具有与第一表面相对的第二表面，并且包括在与第二表面的方向上依次布置的遮光膜，厚度调节膜和对电极；密封材料，具有框架形状以粘合第一基板和第二基板；液晶层，设置在由密封材料围绕的区域中并位于第一基板和第二基板之间，其中设置厚度调节膜以覆盖光屏蔽膜以限定液晶层的厚度，并且具有接触孔，其中对电极设置成与像素电极相对，并通过接触孔电连接到遮光膜。

