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(54) **ACTIVE MATRIX LIQUID-CRYSTAL
DISPLAY DEVICE**

Publication Classification

(75) Inventors: **Michiaki Sakamoto**, Tokyo (JP); **Choei Sugitani**, Tokyo (JP)

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Correspondence Address:
SUGHRUE MION, PLLC
2100 Pennsylvania Avenue, NW
Washington, DC 20037-3213 (US)

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(73) Assignee: **NEC CORPORATION**

(57) **ABSTRACT**

(21) Appl. No.: **10/056,036**

In an active matrix liquid-crystal display device, dummy contact holes are provided between neighboring terminals, so as to prevent a poor connection between the lower metal layer of a terminal and a TAB caused by conductive particles of an ACF remaining on an organic film, and contact holes that make connections between an upper layer transparent electrode of a terminal and a lower metal layer are formed by a plurality of fine via holes, so that sufficient conductive particles of the ACF remain on the upper transparent electrode and a good connection is made to TAB connection lines.

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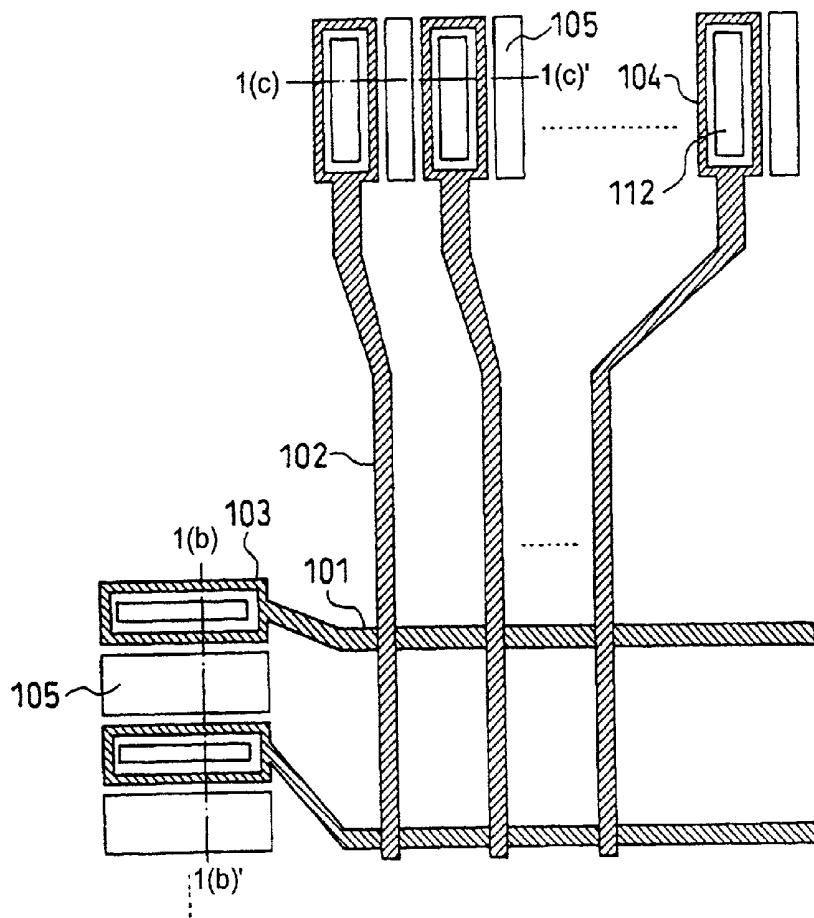


Fig. 1(a)

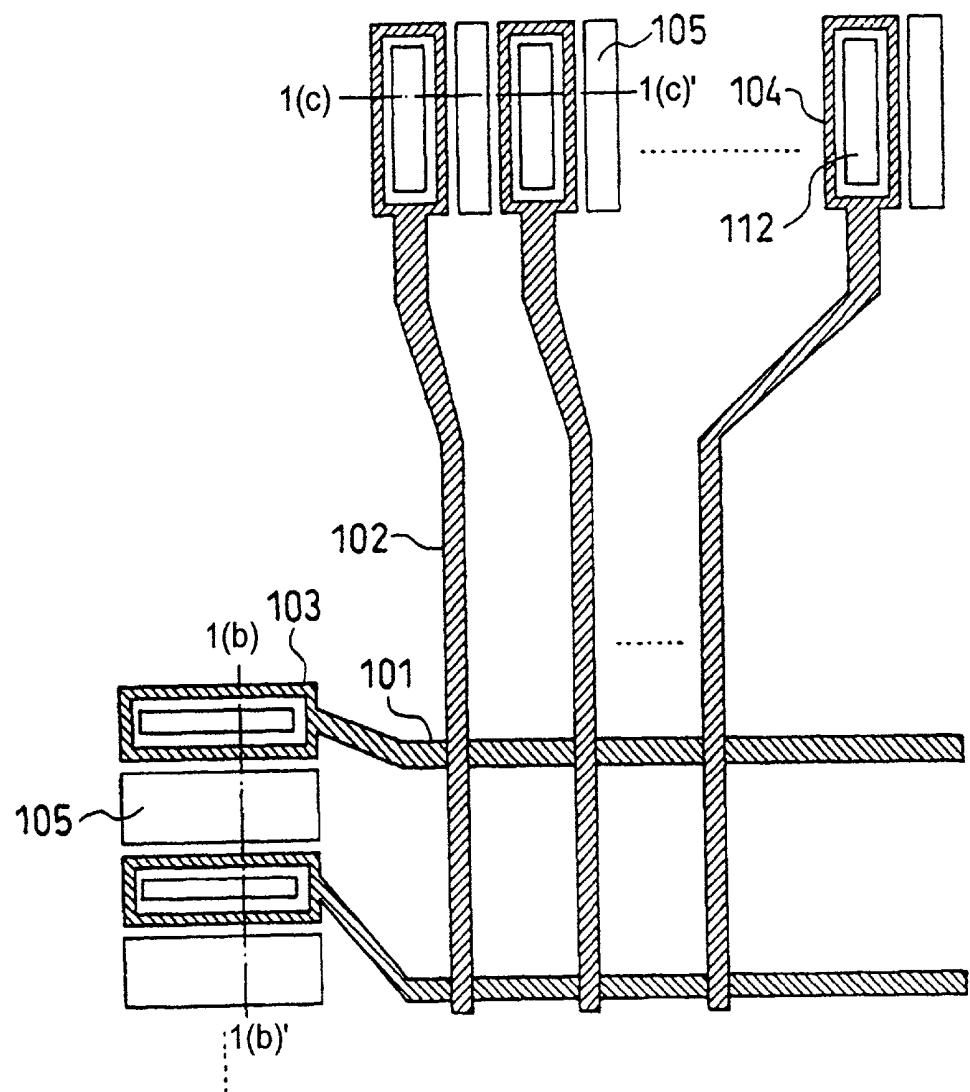


Fig. 1(b)

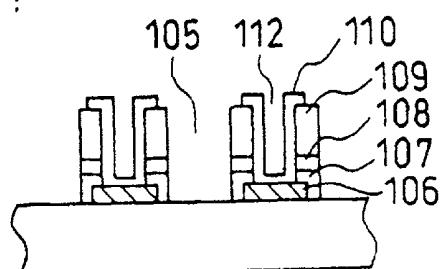


Fig. 1(c)

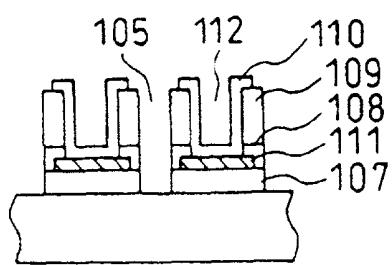


Fig. 2(a)

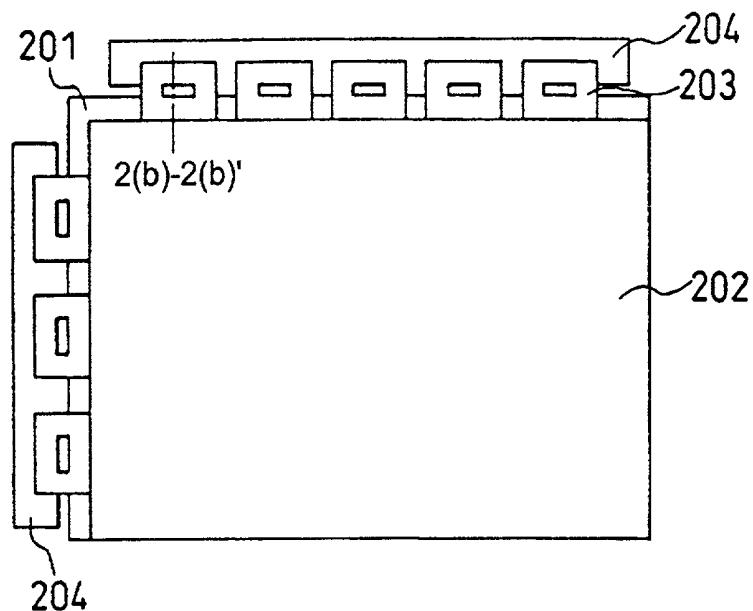


Fig. 2(b)

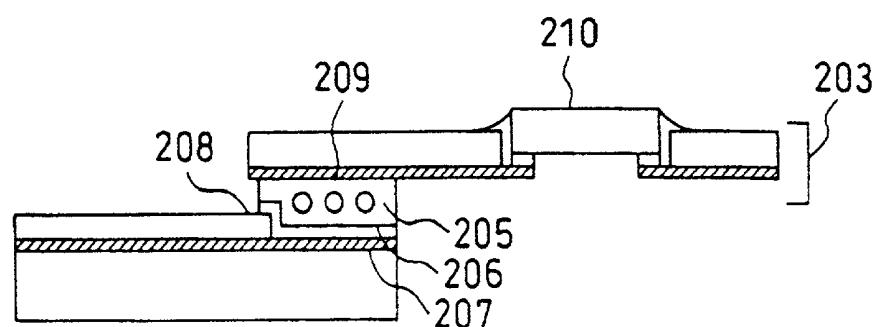


Fig. 3(a)

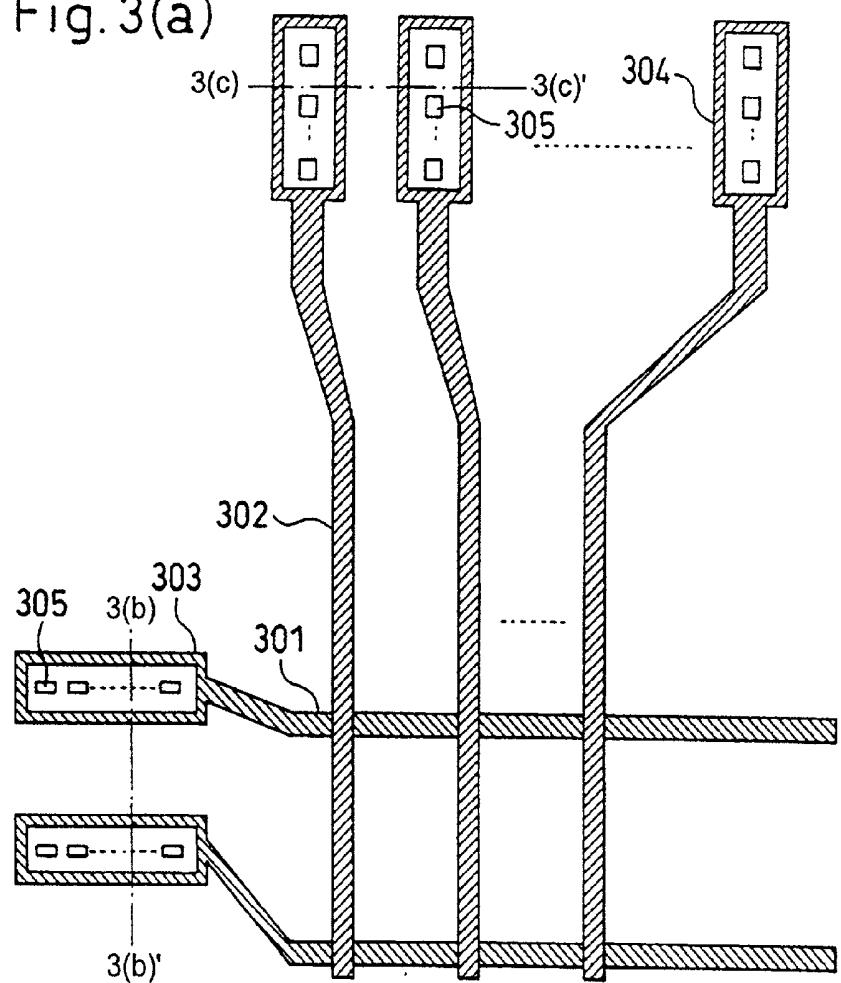


Fig. 3(b)

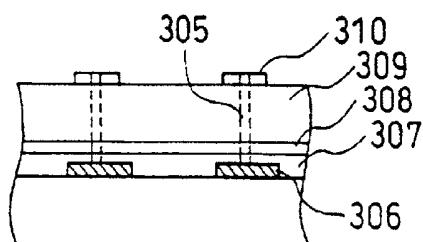


Fig. 3(c)

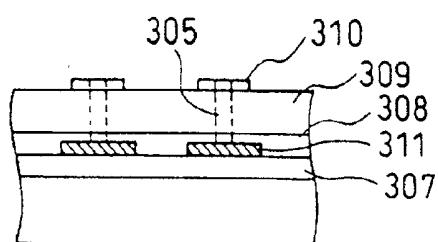


Fig.4(a)

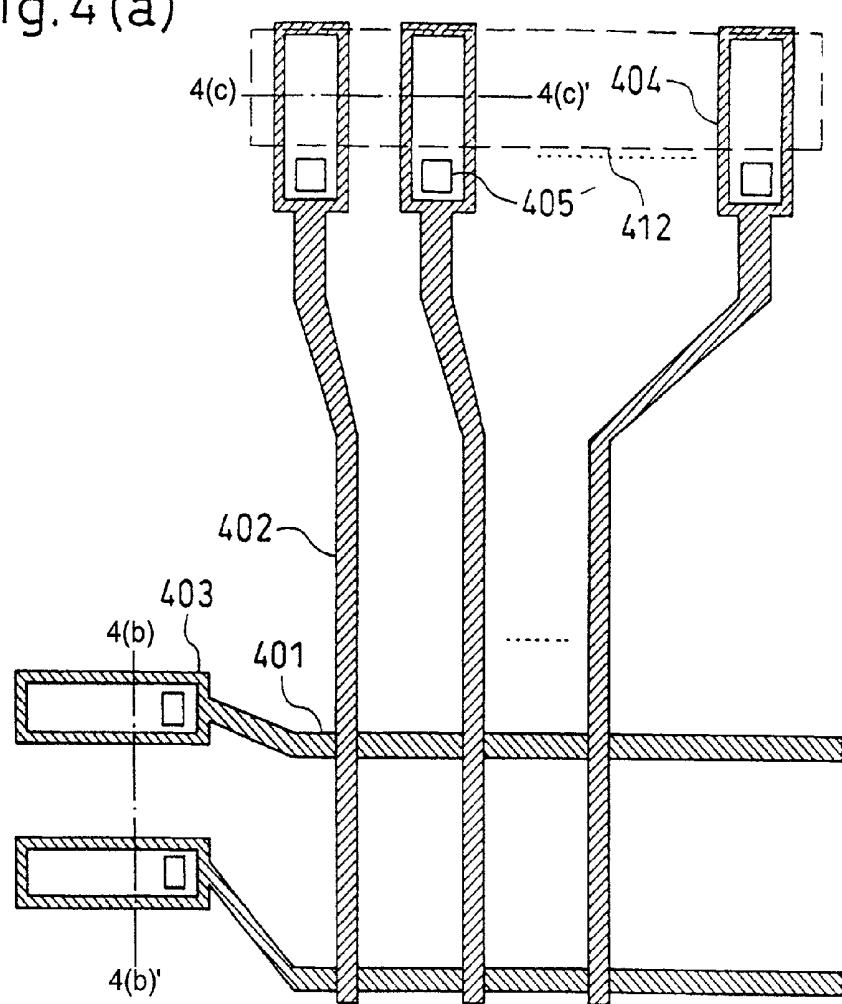


Fig.4(b)

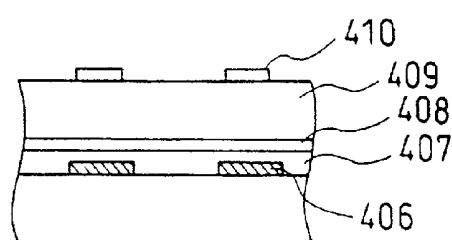


Fig.4(c)

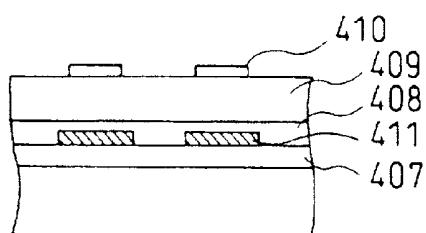


Fig. 5(a)

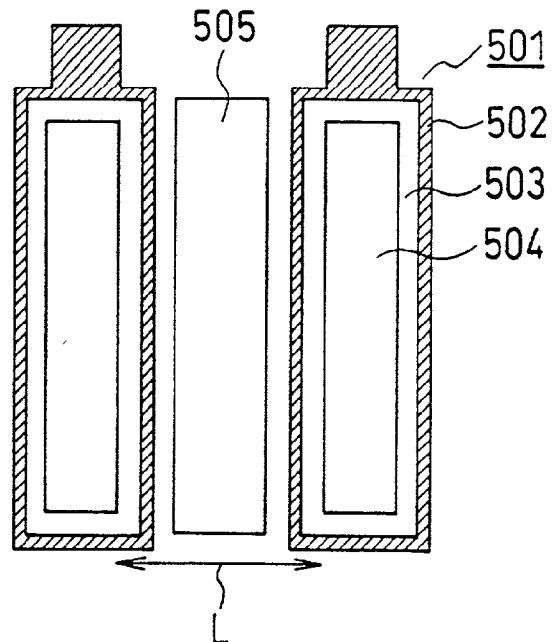


Fig. 5(b)

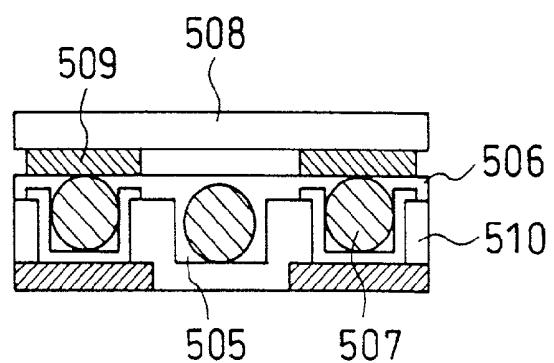


Fig. 6(a)

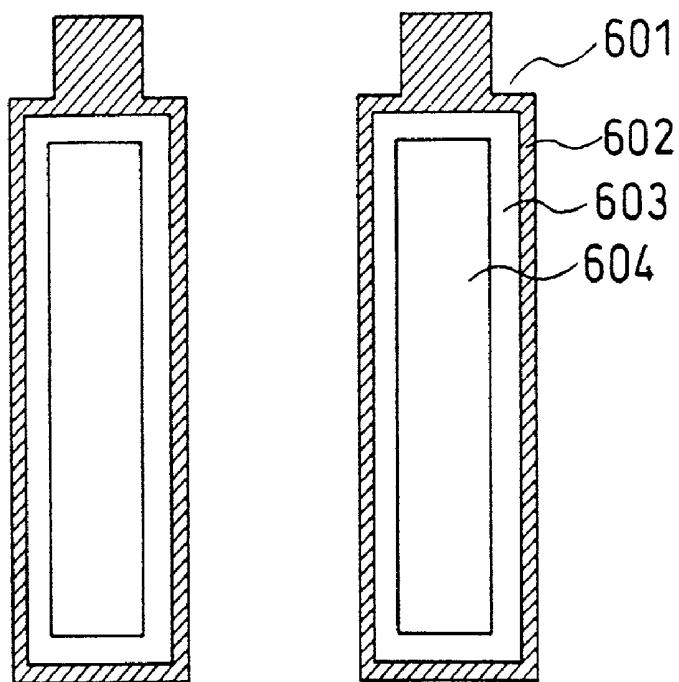


Fig. 6(b)

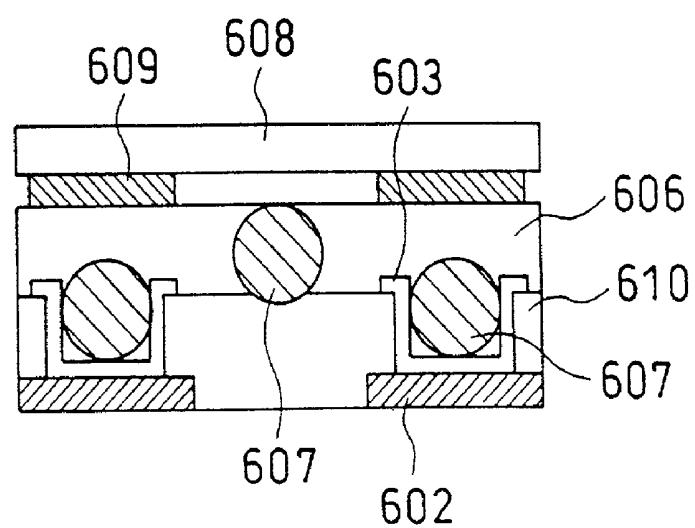


Fig. 7(a)

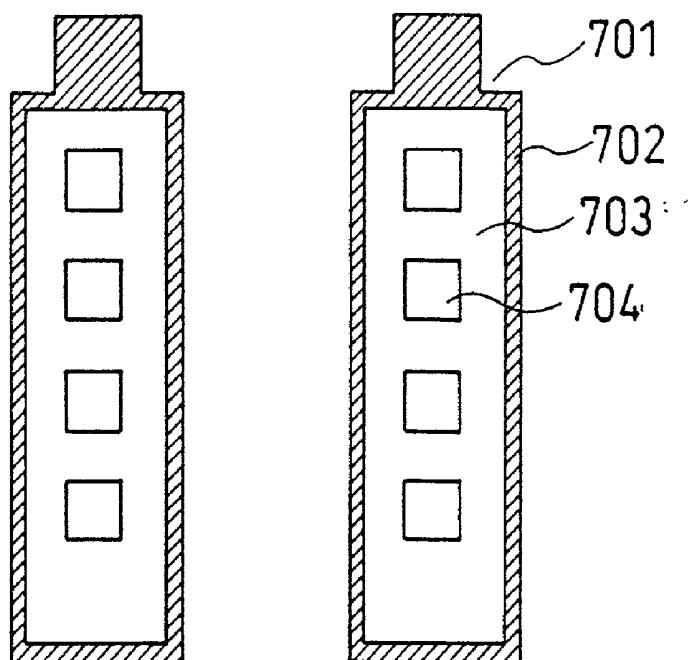


Fig. 7(b)

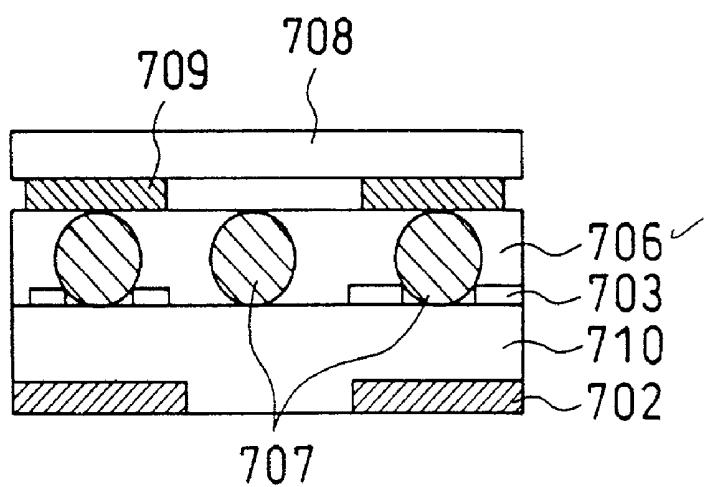


Fig. 8

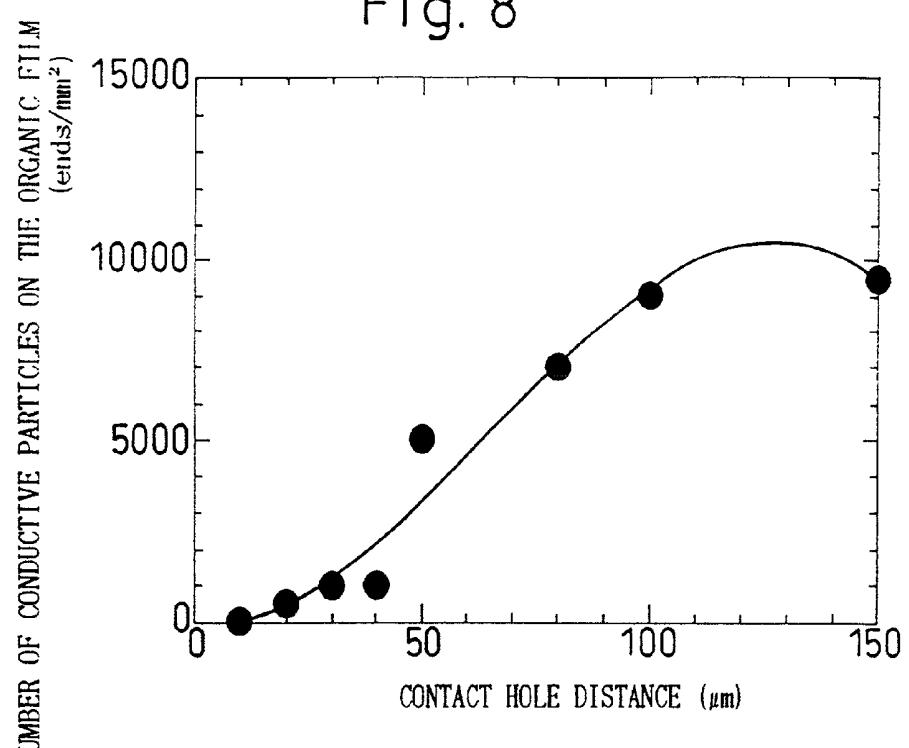


Fig. 9

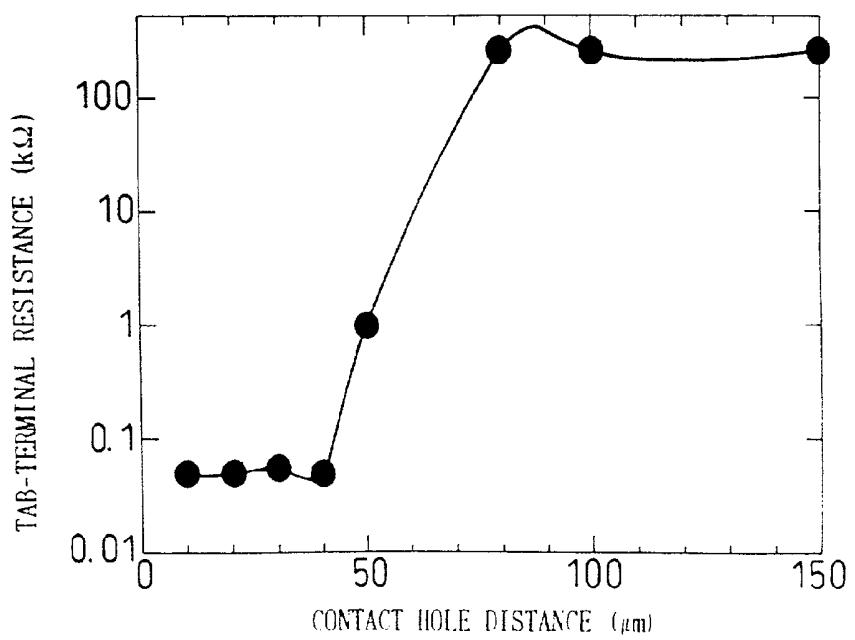


Fig. 10

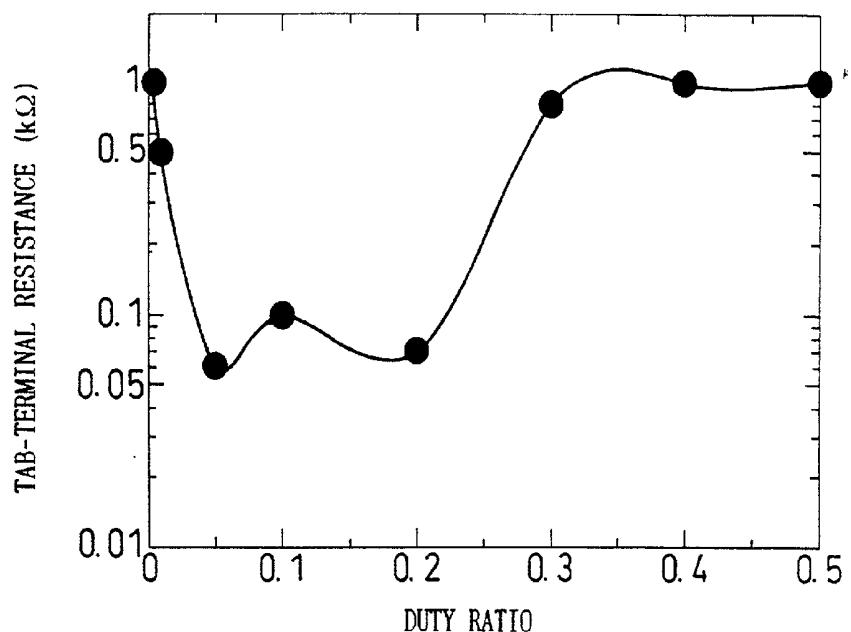


Fig. 11(a)
PRIOR ART

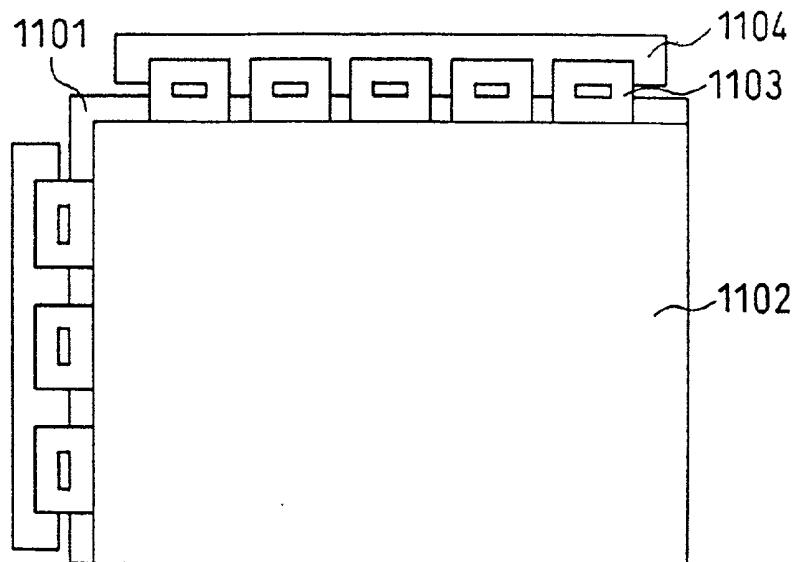


Fig. 11 (b)
PRIOR ART

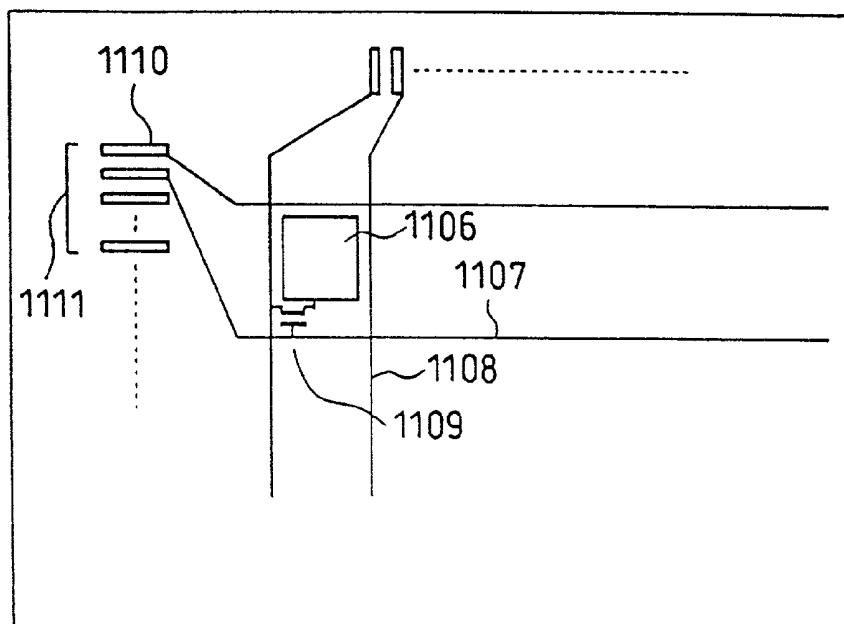


Fig. 12(a)
PRIOR ART

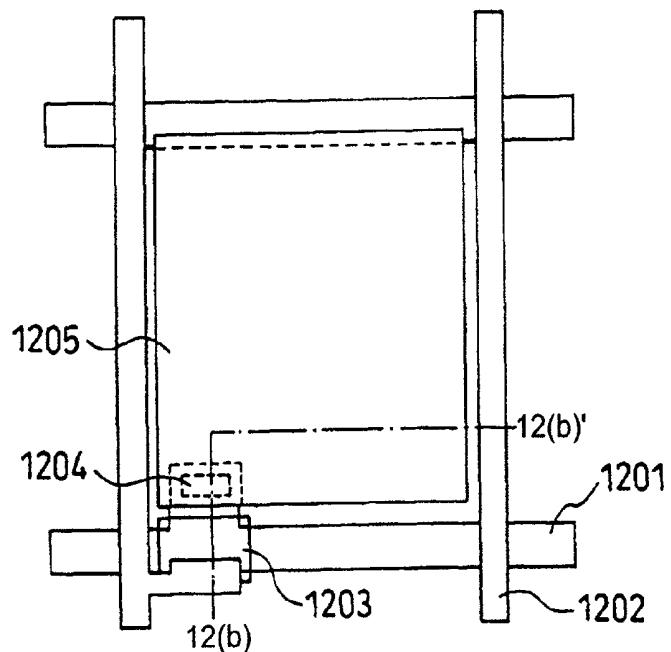


Fig. 12 (b)
PRIOR ART

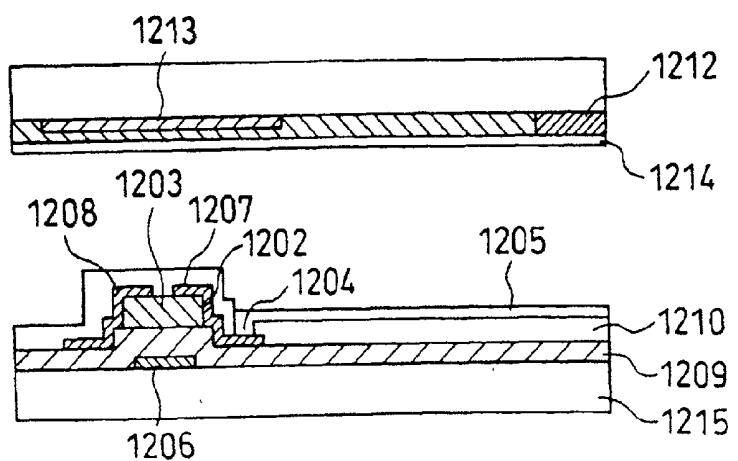


Fig. 13(a)
PRIOR ART

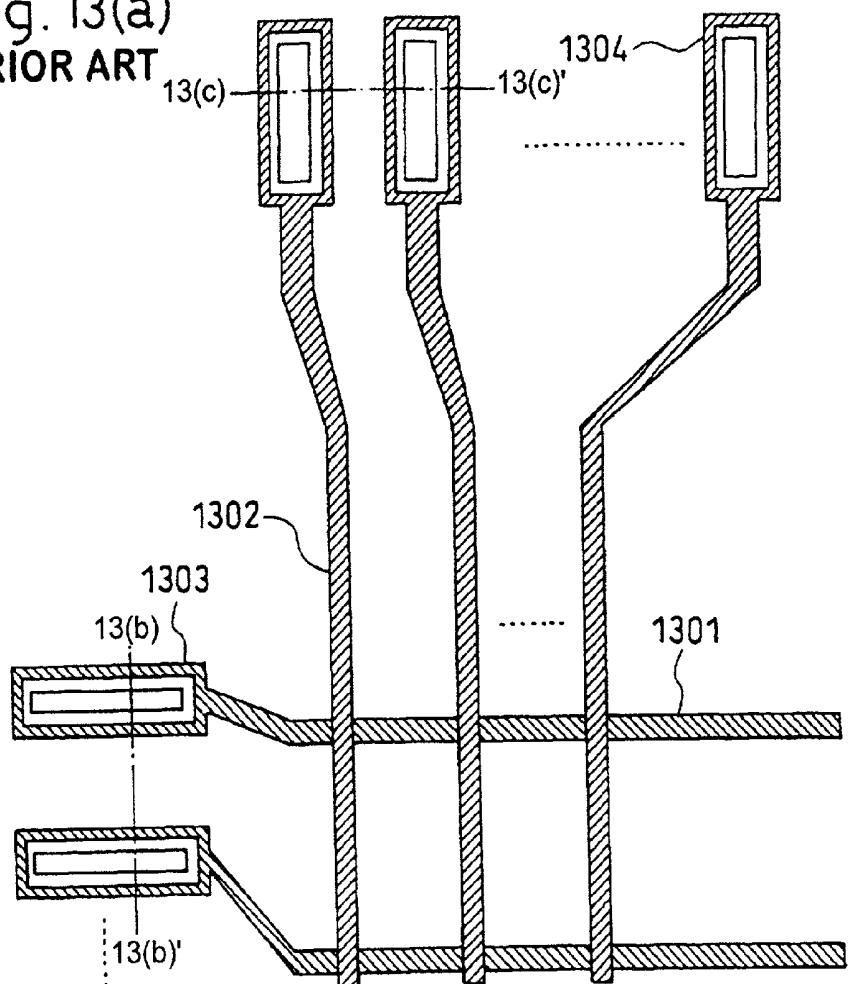


Fig. 13(b)
PRIOR ART

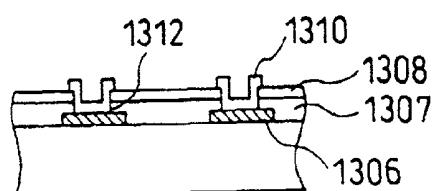


Fig. 13(c)
PRIOR ART

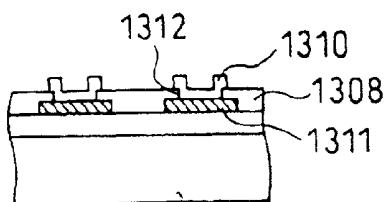


Fig. 14(a)
PRIOR ART

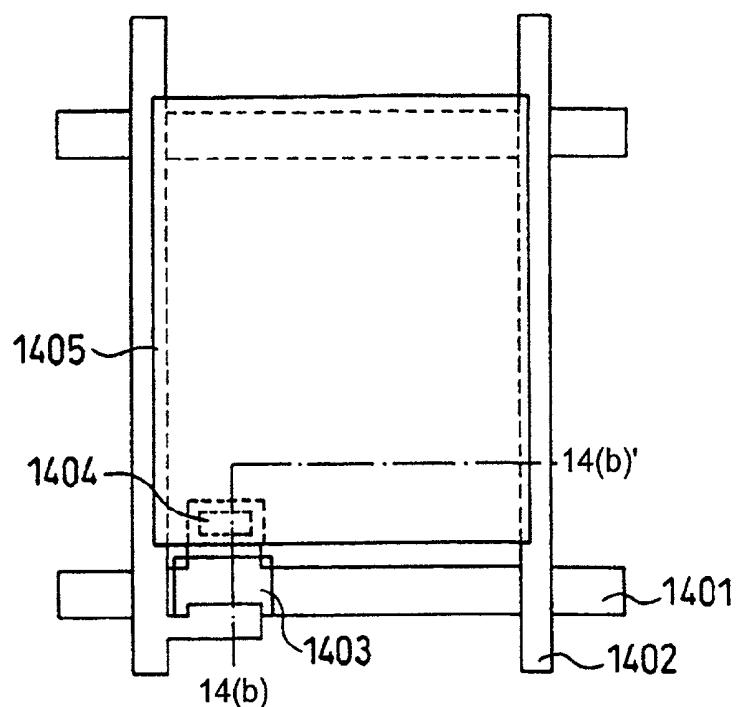


Fig. 14(b)
PRIOR ART

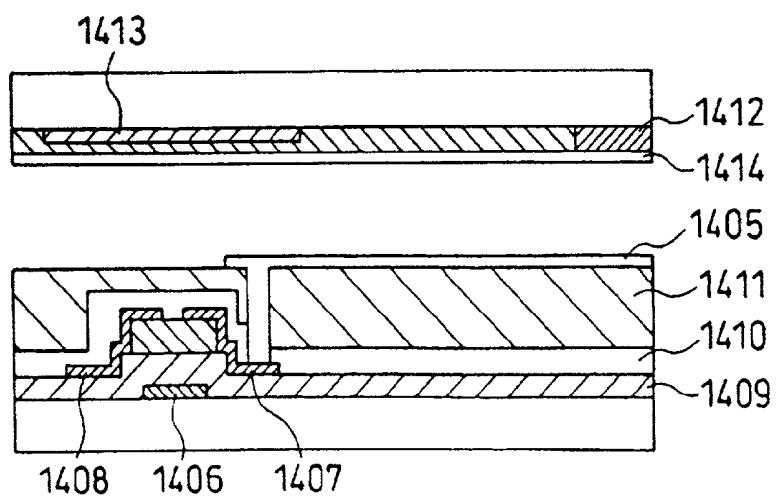


Fig. 15(a)
PRIOR ART

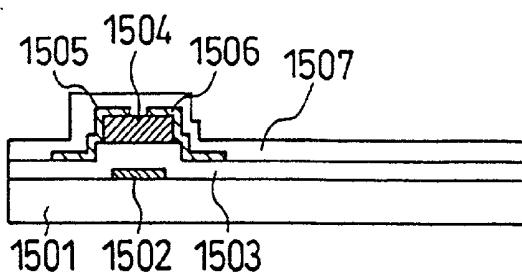


Fig. 15(b)
PRIOR ART

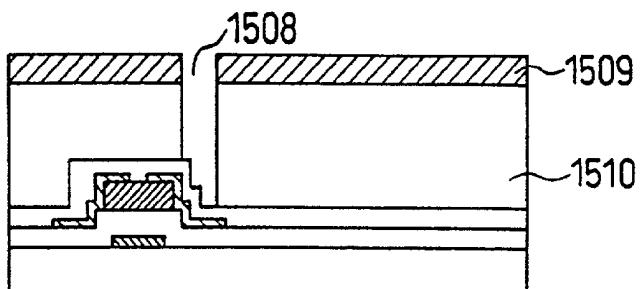


Fig. 15(c)
PRIOR ART

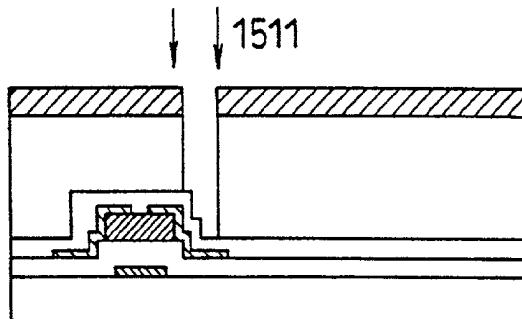
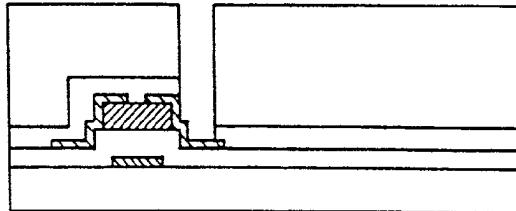


Fig. 15(d)
PRIOR ART



ACTIVE MATRIX LIQUID-CRYSTAL DISPLAY DEVICE

BACKGROUND OF THE INVENTION

[0001] 1. Field of the Invention

[0002] The present invention relates to an active matrix liquid-crystal display device, and more particularly to the configuration of a terminal for electrical connection to an external driving element.

[0003] 2. Background of the Invention

[0004] Active matrix liquid-crystal display devices are known as flat panel displays which save space and operate on a small amount of electrical power.

[0005] FIG. 11 illustrates the concept of an active matrix liquid-crystal display device of the past, and FIG. 11 (a) showing the configuration thereof, and FIG. 11 (b) showing an equivalent circuit of a TFT substrate.

[0006] This active matrix liquid-crystal display device has a thin-film transistor (TFT) substrate 1101 and a color filter substrate (hereinafter referred to as a CF substrate) 1102, in between which is sandwiched a twisted nematic (TN) liquid crystal.

[0007] The TFT substrate 1101 has a plurality of pixel electrodes 1106 on a matrix, these pixel electrodes 1106 being connected to thin-film transistors (TFTs) 1109, which act as switching transistors.

[0008] Scanning lines 1107 that supply a scanning signal are connected to the gate electrodes of the TFTs, and data lines 1108, which input a display signal, are connected to the drain electrodes, so as to drive the TFTs.

[0009] In the peripheral area around the TFT substrate 1101 are provided terminals 1110 for the purpose of inputting scanning and display signals, these being connected to a signal processing substrate (TAB: taped automated bonding) 1103.

[0010] Additionally, the TAB 1103 is connected to an external printed circuit board 1104. The CF substrate 1102 has a RGB color layers and a light-blocking layer for the purpose of blocking light, corresponding to each of the opposing electrodes and pixels.

[0011] FIG. 12 is a plan view and a cross-section view of a unit element in an active matrix liquid-crystal display device of the past.

[0012] In this display device, the display device is so configured in that the TFT 1203 is formed on a TFT glass substrate 1215, and the TFT 1203 is further comprising a gate electrode 1206 connected to a scanning line 1201, a gate insulation film 1209 formed so as to cover the gate electrode 1206, a drain electrode 1208 connected to a signal line 1202 formed on the gate insulation film 1209, a source electrode 1207 connected to a pixel electrode, a passivation film 1210 form so as to cover the above-noted elements, and a pixel electrode 1205 connected to the source electrode 1207 via a contact hole 1204 provided in the passivation film.

[0013] In the above-noted active matrix liquid-crystal display device of the past, terminals are provided around the

periphery of the display device for making connection between an external substrate TAB and each one of the wirings.

[0014] FIG. 13 shows the gate side terminal and data side terminal, FIG. 13 (a) being a plan view thereof, and FIG. 13 (b) and FIG. 13 (c) being cross-section views in the directions indicated by the lines A-A' and B-B'.

[0015] The gate side terminal 1303 is provided with a gate layer metal 1306 for forming a gate electrode or the like, onto one region of which is formed a contact hole 1312, a transparent electrode 1310 forming a pixel electrode, for example, being formed as the uppermost layer so as to cover the gate layer metal.

[0016] A data side terminal 1304 is provided with a data layer metal 1311 forming a drain electrode, for example, on one region of which is formed a contact hole 1312, a transparent electrode 1310 forming a pixel electrode, for example, being formed as the uppermost layer so as to cover the gate layer metal.

[0017] These terminals are connected to TAB lead wires by heating and applying pressure to an anisotropic conductive film (ACF) made of a thermally cured adhesive throughout which fine conductive particles are uniformly dispersed.

[0018] In a TFT substrate of the past, an inorganic film of a material such as SiN having a thickness of 200 to 400 nm is used as a passivation layer, and there was no overlap between pixel electrodes and wires.

[0019] Recently, however, there has been a disclosure, for example, in U.S. Pat. No. 5,641,974, of a technology for causing overlap between a pixel electrode and a wire and broadening the transparent region.

[0020] When this is done, in order to reduce the capacitance between the pixel electrode and the signal wires, further patterning is done of an organic film on the passivation film, this being formed to a thickness of 2 to 4 μm .

[0021] FIG. 14 (a) and FIG. 14 (b) are a plan view and a cross-section view of an active matrix substrate, respectively, in which the above-noted organic film is used in an interlayer insulation film.

[0022] The process up until the patterning of the passivation film 1410 is the same as in the past. a photosensitive organic film 1411 of acrylic resin or the like is spin-coated onto the passivation film 1410, and this is exposed and developed so as to form a pattern for contact holes 1404 or the like.

[0023] When this is done, the organic film in the terminal region is removed, and post-baking is done to thermally harden the organic film.

[0024] Finally, a pixel electrode 1405 is formed, and connection is made to the source electrode 1407 of the TFT.

[0025] In the above-noted technology disclosed in U.S. Pat. No. 5,641,974, the transparent region is larger and it is possible to obtain a liquid-crystal display device with brighter and better display performance than in the past.

[0026] However, because the passivation film patterning and organic film patterning are performed in different pro-

cess steps, the number of patterning steps increases, thereby complicating the process and increasing the manufacturing cost.

[0027] To solve the above-noted problems, a method was proposed in Japanese Patent Application No. 9-323423, whereby two layers of resist are used to performing organic film patterning and passivation film patterning simultaneously. **FIG. 15** shows the process flow using this method to form a contact hole.

[0028] Steps up until the formation of the passivation film are the same as in the prior art (**FIG. 15 (a)**). After continuous application of the organic film and the resist, exposure and developing are done to simultaneously pattern the resist and wet etch the organic film (**FIG. 15 (b)**).

[0029] Then, the patterning of the passivation film is dry-etched using the resist and organic film as a mask (**FIG. 15 (c)**).

[0030] Finally, the resist only is selectively melted with a specific removing liquid so as to remove the resist (**FIG. 15 (d)**).

[0031] When this is done, because one and the same mask is used to pattern the organic film and the passivation film, it is not possible to remove the organic film on the terminal part.

[0032] **FIG. 6** shows how the TAB 608 and the terminal 601 are connected. In order to distribute the conductive particles uniformly throughout the anisotropic conductive film, the diameter of the conductive particles is generally in the range 2 to 4 μm .

[0033] On the other hand, because the thickness of the organic film is 2 to 4 μm , if many conductive particles remain on the organic film, the distance between the lower metal layer of the terminal and the tape carrier package (TCP) is larger than the diameter of the conductive particles, making it difficult to obtain a good contact between the terminal and the TCP.

[0034] In U.S. Pat. No. 5,641,974, for the purpose of reducing the capacitance between the pixel electrodes and the signal wires, if when an active matrix substrate has a patterned photosensitive acrylic resin on a passivation film, the number of patterning steps increases by one in comparison with the process for producing an active matrix substrate of the prior art.

[0035] To solve this problem, there is disclosed in Japanese Patent Application No. 9-323423, technology whereby two layers of resist are used to performing organic film and passivation film patterning simultaneously, thereby reducing the number of patterning steps to the same as with the prior art.

[0036] In this case, because a thick organic film remains on the terminal part, if many conductive particles remain on the organic film, it is not possible to obtain a good contact between the terminal and the TAB.

[0037] Accordingly, it is an object of the present invention to provide a terminal structure which makes it possible to obtain a good contact between a terminal and a TAB in an active matrix substrate in which an organic film and a passivation film are patterned simultaneously.

SUMMARY OF THE INVENTION

[0038] In order to achieve the above-noted objects, the present invention has the following basic technical constitution.

[0039] Specifically, the present invention is an active matrix liquid-crystal display device comprising a substrate, a plurality of switching elements and a plurality of pixel electrodes formed in a matrix arrangement on the substrate, scanning lines controlling the switching elements, signal lines supplying data signal to the switching elements, terminals making electrical connections between the scanning and signal lines and external driving elements, and comprising a metal layer which forms the scanning line or the signal line, an interlayer film formed on the metal layer, and a transparent electrode forming the pixel electrode, contact holes formed in the interlayer film, through which the metal layer and the transparent electrode are connected to each other via a transparent electrode formed in the contact hole, and an anisotropic conductive film connecting the active matrix substrate to the external driving elements, the device is further characterized (1) by providing dummy contact holes in the passivation film between adjacent terminals, or by forming the contact holes as a plurality of via holes or contact holes having a diameter that is larger than the diameter of the conductive particles in the anisotropic conductive film or by providing these contact holes in a region outside the region of connection with the external drive elements, so that it is possible to prevent an increase in resistance caused by uneven distribution of conductive particles when an anisotropic conductive film is used for making connections, thereby achieving a good contact in the active matrix liquid-crystal display device.

BRIEF DESCRIPTION OF THE DRAWINGS

[0040] **FIG. 1** is a drawing illustrating the terminal part of the first embodiment of the present invention, (a) showing a plan view thereof, and (b) and (c) being cross-section views in the directions indicated by A-A' and B-B'.

[0041] **FIG. 2** is a drawing showing a liquid-crystal display device according to the first embodiment of the present invention, (a) being an overall view and (b) being a cross-section view in the direction indicated by A-A'.

[0042] **FIG. 3** is a drawing showing the terminal part of a liquid-crystal display device according to the second embodiment of the present invention, (a) being a plan view, and (b) and (c) being cross-section views in the directions indicated by A-A' and B-B'.

[0043] **FIG. 4** is a drawing showing the terminal part of a liquid-crystal display device according to the third embodiment of the present invention, (a) being a plan view, and (b) and (c) being cross-section views in the directions indicated by A-A' and B-B'.

[0044] **FIG. 5** is a drawing illustrating a terminal TEG experiment.

[0045] **FIG. 6** is a drawing illustrating an improper pressure application in which the organic film remains.

[0046] **FIG. 7** is a drawing illustrating a terminal TEG experiment.

[0047] **FIG. 8** is a drawing illustrating a terminal TEG experiment.

[0048] FIG. 9 is a drawing illustrating a terminal TEG experiment.

[0049] FIG. 10 is a drawing illustrating a terminal TEG experiment.

[0050] FIG. 11 is a drawing showing a liquid-crystal display device of the past.

[0051] FIG. 12 is a drawing showing a unit element of a liquid-crystal display device of the past.

[0052] FIG. 13 is a drawing showing a terminal in a liquid-crystal display device of the past.

[0053] FIG. 14 a drawing showing the unit terminal in U.S. Pat. No. 5,641,974.

[0054] FIG. 15 is a drawing showing the process flow in Japanese Patent Application No. 9-323423.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0055] Preferred embodiments of the present invention will be explained hereunder with reference to the attached drawings.

[0056] In order to overcome the above-mentioned problems shown in the conventional active matrix liquid-crystal display device, the inventors of the invention had conducted in several experiments so as to obtain information to specify and clarify the conditions to be changed from technical point of views, as follows;

[0057] As a first experiment, a contact hole 504 was formed in an organic film 510 formed on a metal film layer 502 as shown in FIG. 5, and at a terminal TEG 501 on which a transparent electrode 503 being formed, an anisotropic conductive film (ACF) 506 was applied thereto and the number of conductive particle remaining per unit area on the organic film 510 between terminals was counted.

[0058] The relationship between the distance L between contact holes and the number of conductive particles per unit area on the organic film is shown in FIG. 8.

[0059] From these results, it can be seen that, if the distance between terminals exceeds 40 μm , there is an increase in the number of conductive particles on the organic film, and it is thought that when the organic film is attached thereto, the conductive particle would probably be moved by approximately 15 μm , and thus if when a distance between contact holes is 40 μm or less, the conductive particles would fall into the contact hole.

[0060] FIG. 9 shows the contact resistance between the lower layer metal and the TAB via the anisotropic conductive film, and the distance between the contact holes.

[0061] From this, it can be seen that when the distance between contact holes exceeds about 40 μm , the contact resistance between the lower layer metal and the TAB starts to increase suddenly, and exceeds 100 k Ω at over 80 μm , making this impractical.

[0062] When the distance between the contact holes is short, there are almost no conductive particles on the organic film, so that a good contact is established between the metal layer and the TCP.

[0063] While when the distance between the contact holes is large, however, many conductive particles remain on the organic film, as shown in FIG. 6, the distance between the metal layer and the TCP being then the sum of the organic film thickness and the conductive particle diameter, thereby preventing the establishment of a good contact therebetween.

[0064] However, the usual distance between terminals is in the range from 50 to 100 μm . This is because positioning becomes extremely difficult when considering the connection to external terminals when the distance is shorter than 50 μm , and considering the contact formation margin, it is necessary to make the contact spacing even larger.

[0065] Therefore, in the present invention by establishing the distance between a dummy contact hole 505 and a contact hole 504 to be approximately 10 to 50 μm , which is 3 to 20 times the diameter of the conductive particles, it is possible to have the conductive particles fall into the contact hole 504 and dummy contact hole 505, so that a good contact is established, as shown in FIG. 5 (b).

[0066] As a second experiment, a contact hole was formed by a plurality of via holes 704 formed in the metal layer, as shown in FIG. 7, and over this a transparent electrode 703 was formed.

[0067] An anisotropic conductive film 706 was placed over this and pressed onto it to measure the resistance between the transparent electrode and the TAB.

[0068] With the overall surface area of the via holes as S1 and the surface area of the transparent electrode as S2, FIG. 10 shows the duty ratio of S1/S2 and the resistance.

[0069] With the range of $0.01 \leq$ duty ratio ≤ 0.3 , and particularly within the range $0.05 \leq$ duty ratio ≤ 0.3 , it was seen that the resistance is low. The reason the resistance increases with a duty ratio of larger than 0.3 is presumed to be because of insufficient conductive particles on the transparent electrode.

[0070] It is thought that the resistance increases at a duty ratio of less than 0.01 because of the excessively small size of the via holes 704, thereby causing an increase in the resistance of the contact between the transparent electrode and the lower layer metal through the via holes.

[0071] It is sufficient to make the size of the via holes larger than the maximum diameter of the conductive particles in the ACF. As a result, it is possible to establish a good contact, as shown in FIG. 7 (b).

[0072] Embodiments of the present invention are described in detail below, with reference made to relevant accompanying drawings.

[0073] It will be understood, however, that the present invention is not restricted to the embodiments described herein, and can take on other various forms, within the spirit of the present invention.

[0074] FIG. 2 shows a simplified view of the first embodiment of the present invention.

[0075] A liquid-crystal display device according to this embodiment has a liquid-crystal panel formed by a thin-film transistor substrate (TFT substrate) 201, a color filter substrate (CF substrate) 202, and a liquid crystal sandwiched

between these elements, and a large number of TABs 203 forming drivers for driving the liquid-crystal panel.

[0076] The TABs 203 are provided on two printed circuit boards 204 that are disposed along two sides of the liquid-crystal panel, which is rectangular, the base film of each TAB being connected by soldering to a signal processing printed circuit board.

[0077] The above-noted TABs have a bare chip 210 as an integrated circuit on a base film, the terminals of the bare chip being connected to TAB lead wires 209 which are wiring patterns formed on the base film.

[0078] These TABs 203 are electrically connected to the terminals 208 of the TFT substrate via an ACF 205.

[0079] The display part of the TFT substrate forming the liquid-crystal panel has the same configuration as disclosed in the Japanese Patent Application No. 9-323423.

[0080] The thickness of the organic film is 3 μm . Terminals leading from scanning lines and signal lines are provided in the area surrounding the TFT substrate, these being grouped into terminal blocks each having the same number of terminals as the TAB terminals.

[0081] FIG. 1 shows a plan view and a cross-section view of the terminals that make connections to external TABs.

[0082] The gate terminal 103 is formed by a gate metal layer 106, a gate insulation film 107, a passivation film 108, an organic film 109, and a transparent electrode 110, and the gate metal layer 106 and transparent electrode 110 are connected via a contact hole 112.

[0083] The size of the gate metal layer that forms the gate terminal 103 is 70 \times 200 μm , the distance between terminals is 100 μm , and the contact holes are 50 \times 150 μm .

[0084] From the results of the above-described basic experiments, a 80 \times 200 μm dummy contact hole 105 was provided on the organic film between terminals, so that not many conductive particles of the anisotropic conductive film remain on the organic film.

[0085] By doing this, the distance from the dummy contact hole from an end of the terminal becomes 10 μm , so that the conductive particles from the ACF fall into either a contact hole 112 or a dummy contact hole 105.

[0086] The data terminal 104 is formed by a data metal layer 111, a passivation film 108, an organic film 109, and a transparent electrode 110, the data metal layer 111 and transparent electrode 110 being connected via a contact hole 112.

[0087] The size of the data metal layer 111 that forms the data terminal 104 is 70 \times 200 μm , the distance between terminals is 50 μm , and the contact hole 112 is 50 \times 150 μm . A 30 \times 200 μm dummy contact hole 105 is provided on the organic film between terminals.

[0088] The terminal and TAB leads are mounted by hot pressing, with an intervening anisotropic conductive film (ACF).

[0089] As an anisotropic conductive film, a large number of conductive particles formed by nickel and gold plating the surface of fine spherical plastic beads, are dispersed in a binder of epoxy resin.

[0090] The outer diameter of the conductive particles in the anisotropic conductive film is 5 μm , and the density of the conductive particle is 10,000/mm². The anisotropic conductive film is connected by hot pressing to the TAB.

[0091] When this is done, by providing the dummy contact holes 105, there are almost no conductive particles remaining on the organic film.

[0092] The diameter of a crushed conductive particle is approximately 3 μm , and a good contact, with a contact resistance of no more than 100 Ω , is established between the terminals and the TCP.

[0093] FIG. 3 shows plan and cross-section views of the terminal making connection to an external TAB. The configuration of the display region of this liquid-crystal display device is the same as that of the first embodiment, and will not be described herein.

[0094] A gate electrode 303 is formed by a gate metal layer 306, a gate insulation film 307, a passivation film 308, an organic film 309, and a transparent electrode 310, the gate metal layer 306 and the transparent electrode 310 being connected via a contact hole.

[0095] The sizes of the gate metal layer 306 that forms the terminal and the transparent electrode 310 are the same as in the first embodiment.

[0096] Ten via holes 305 having dimensions of 10 \times 10 μm are provided as a contact hole. The data terminal 304 has the same configuration, with the exception of the gate metal layer 311 being used for the gate metal layer 306.

[0097] An anisotropic conductive film is hot pressed onto these terminals and connected to a TAB. When this is done, the duty ratio between the surface area of the contact hole and the surface area of the transparent electrode is 0.1, and the contact resistance between the terminal and TCP is less than 100 Ω , so that a good contact is established.

[0098] FIG. 4 shows plan and cross-section views of the terminal part making connection to an external TAB.

[0099] The configuration of this display region is the same as that of the first embodiment, and is not described herein. The gate terminal 403 is formed by a gate metal layer 406, a gate insulation film 407, a passivation film 408, an organic film 409, and a transparent electrode 410, the gate metal layer 406 and transparent electrode 410 being connected via a contact hole 405.

[0100] The sizes of the gate metal layer forming the terminal and the transparent electrode are the same as the first embodiment. The contact hole is formed by providing a 30 \times 20 μm via hole, avoiding the TAB pressing region. The data terminals have the same type of configuration.

[0101] An anisotropic conductive film is hot pressed onto these terminals and connected to a TAB.

[0102] When this is done, the duty ratio between the surface area of the contact hole and the surface area of the transparent electrode is 0.6, and the contact resistance between the terminal and TAB is less than 100 Ω , so that a good contact is established.

[0103] From these explanations about the embodiments of the present invention, it is apparent that the active matrix liquid-crystal display device of the present invention can be

produced by a method, for a first example, comprising a step of forming a dummy contact hole in the interlayer film and between the terminals being adjacently arranged to each other.

[0104] Further, the active matrix liquid-crystal display device of the present invention can be produced by a method, for a second example, comprising a step of forming at least one of the contact holes so as to have a diameter being greater than the maximum diameter of conductive particles in the anisotropic conductive film.

[0105] And as a third example of the method for producing the active matrix liquid-crystal display device of the present invention, the method comprises a step of forming the contact hole so as to be disposed in a location other than a location of connection to an external driving element.

[0106] In the method for producing an active matrix liquid-crystal display device according to the above-mentioned second and third examples, the method comprises a step of establishing the ratio of surface areas between the plurality of via holes or contact holes and the transparent electrode over the terminal to be at least 0.01 and no greater than 0.3.

[0107] Moreover, in the method for producing an active matrix liquid-crystal display device according to the above-mentioned first to third examples, the method comprises a step of forming the interlayer film with an organic film or a laminate formed by an organic film and a non-organic passivation film.

[0108] By adopting the configuration described in detail above, according to the present invention, in an active matrix substrate with an organic film in the terminal part, it is possible to achieve a stable, high-quality connection between the TCP and the terminal when mounting the TCP. This is achieved by providing dummy contact holes, so that the conductive particles of the anisotropic conductive film remaining on the organic film are sufficiently reduced. By establishing the surface area ratio between the transparent electrode forming the terminal and the contact hole in the range from 0.01 to 0.3, this can be achieved even if sufficient conductive particles of the anisotropic conductive film are left on the organic film.

What is claimed is:

1. An active matrix liquid-crystal display device comprising:

a substrate;

a plurality of switching elements and a plurality of pixel electrodes formed in a matrix arrangement on said substrate;

scanning lines controlling said switching elements;

signal lines supplying data signal to said switching elements;

terminals making electrical connections between said scanning and signal lines and external driving elements, and comprising a metal layer which forms said scanning line or said signal line, an interlayer film formed on said metal layer, and a transparent electrode forming said pixel electrode;

contact holes formed in said interlayer film, through which said metal layer and said transparent electrode are connected to each other via a transparent electrode formed in said contact hole; and

an anisotropic conductive film connecting said active matrix substrate to said external driving elements,

wherein said active matrix liquid-crystal display device comprises a dummy contact hole provided in said interlayer film formed between adjacent terminals.

2. An active matrix liquid-crystal display device comprising:

a substrate;

a plurality of switching elements and a plurality of pixel electrodes formed in a matrix arrangement on said substrate;

scanning lines controlling said switching elements;

signal lines supplying data signal to said switching elements;

terminals making electrical connections between said scanning and signal lines and external driving elements, and comprising a metal layer which forms said scanning line or said signal line, an interlayer film formed on said metal layer, and a transparent electrode forming said pixel electrode;

contact holes formed in said interlayer film, through which said metal layer and said transparent electrode are connected to each other via a transparent electrode formed in said contact hole; and

an anisotropic conductive film connecting said active matrix substrate to said external driving elements,

wherein at least one of said contact holes is formed so as to have a diameter being greater than the maximum diameter of conductive particles in said anisotropic conductive film.

3. An active matrix liquid-crystal display device comprising:

a substrate;

a plurality of switching elements and a plurality of pixel electrodes formed in a matrix arrangement on said substrate;

scanning lines controlling said switching elements;

signal lines supplying data signal to said switching elements;

terminals making electrical connections between said scanning and signal lines and external driving elements, and comprising a metal layer which forms said scanning line or said signal line, an interlayer film formed on said metal layer, and a transparent electrode forming said pixel electrode;

contact holes formed in said interlayer film, through which said metal layer and said transparent electrode are connected to each other via a transparent electrode formed in said contact hole; and

an anisotropic conductive film connecting said active matrix substrate to said external driving elements,

wherein said contact hole is disposed in a location other than a location of connection to an external driving element.

4. An active matrix liquid-crystal display device according to claim 2, wherein the ratio of surface areas between said plurality of via holes or contact holes and the transparent electrode over the terminal is at least **0.01** and no greater than 0.3.

5. An active matrix liquid-crystal display device according to claim 4, wherein said surface areas between of said plurality of via holes or contact holes is a total surface areas of summing up the respective surface areas of said plurality of said via holes or contact holes formed in said transparent electrode.

6. An active matrix liquid-crystal display device according to claim 1, wherein said interlayer film is an organic film or a laminate formed by an organic film and a non-organic passivation film.

7. An active matrix liquid-crystal display device according to claim 2, wherein said interlayer film is an organic film or a laminate formed by an organic film and a non-organic passivation film.

8. A method for producing an active matrix liquid-crystal display device comprising, a substrate, a plurality of switching elements and a plurality of pixel electrodes formed in a matrix arrangement on said substrate, scanning lines controlling said switching elements, signal lines supplying data signal to said switching elements, terminals making electrical connections between said scanning and signal lines and external driving elements, and comprising a metal layer which forms said scanning line or said signal line, an interlayer film formed on said metal layer, and a transparent electrode forming said pixel electrode, contact holes formed in said interlayer film, through which said metal layer and said transparent electrode are connected to each other via a transparent electrode formed in said contact hole, and an anisotropic conductive film connecting said active matrix substrate to said external driving elements, wherein said method comprising a step of forming a dummy contact hole in said interlayer film and between said terminals being adjacently arranged to each other.

9. A method for producing an active matrix liquid-crystal display device comprising, a substrate, a plurality of switching elements and a plurality of pixel electrodes formed in a matrix arrangement on said substrate, scanning lines controlling said switching elements, signal lines supplying data signal to said switching elements, terminals making electrical connections between said scanning and signal lines and external driving elements, and comprising a metal layer

which forms said scanning line or said signal line, an interlayer film formed on said metal layer, and a transparent electrode forming said pixel electrode, contact holes formed in said interlayer film, through which said metal layer and said transparent electrode are connected to each other via a transparent electrode formed in said contact hole, and an anisotropic conductive film connecting said active matrix substrate to said external driving elements, wherein said method comprising a step of forming at least one of said contact holes so as to have a diameter being greater than the maximum diameter of conductive particles in said anisotropic conductive film.

10. A method for producing an active matrix liquid-crystal display device comprising, a substrate, a plurality of switching elements and a plurality of pixel electrodes formed in a matrix arrangement on said substrate, scanning lines controlling said switching elements, signal lines supplying data signal to said switching elements, terminals making electrical connections between said scanning and signal lines and external driving elements, and comprising a metal layer which forms said scanning line or said signal line, an interlayer film formed on said metal layer, and a transparent electrode forming said pixel electrode, contact holes formed in said interlayer film, through which said metal layer and said transparent electrode are connected to each other via a transparent electrode formed in said contact hole, and an anisotropic conductive film connecting said active matrix substrate to said external driving elements, wherein said method comprising a step of forming said contact hole so as to be disposed in a location other than a location of connection to an external driving element.

11. A method for producing an active matrix liquid-crystal display device according to claim 9, wherein said method comprising a step of for establishing the ratio of surface areas between said plurality of via holes or contact holes and the transparent electrode over the terminal to be at least **0.01** and no greater than 0.3.

12. A method for producing an active matrix liquid-crystal display device according to claim 8, wherein said method comprising a step of forming said interlayer film with an organic film or a laminate formed by an organic film and a non-organic passivation film.

13. A method for producing an active matrix liquid-crystal display device according to claim 9, wherein said method comprising a step of forming said interlayer film with an organic film or a laminate formed by an organic film and a non-organic passivation film.

* * * * *

专利名称(译)	有源矩阵液晶显示器件		
公开(公告)号	US20020067452A1	公开(公告)日	2002-06-06
申请号	US10/056036	申请日	2002-01-28
申请(专利权)人(译)	NEC公司		
当前申请(专利权)人(译)	GOLD CHARM LIMITED		
[标]发明人	SAKAMOTO MICHIAKI SUGITANI CHOEI		
发明人	SAKAMOTO, MICHIAKI SUGITANI, CHOEI		
IPC分类号	G02F1/136 G02F1/13 G02F1/133 G02F1/1345 G02F1/1362 G02F1/1368 H05K1/11 H05K3/32 H05K3/36 G02F1/1343		
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外部链接	Espacenet USPTO		

摘要(译)

在有源矩阵液晶显示装置中，在相邻端子之间设置虚设接触孔，以防止端子的下金属层与由有机膜上残留的ACF的导电颗粒引起的TAB之间的连接不良，通过多个细通孔形成在端子的上层透明电极和下金属层之间形成连接的接触孔，使得ACF的足够的导电颗粒保留在上透明电极上并且形成良好的连接到TAB连接线。

