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(19) **United States**(12) **Patent Application Publication** (10) **Pub. No.: US 2001/0035852 A1**
Yoshihara et al. (43) **Pub. Date: Nov. 1, 2001**(54) **DISPLAY PANEL INCLUDING LIQUID
CRYSTAL MATERIAL HAVING
SPONTANEOUS POLARIZATION**(30) **Foreign Application Priority Data**

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300 South Wacker Dr.
Chicago, IL 60606 (US)****ABSTRACT**

There is provided a liquid crystal display including a panel using a liquid crystal material having spontaneous polarization, such as ferroelectric liquid crystal (FLC); having a faster response time suitable to display dynamic images. The FLC has the disadvantage caused by the incomplete memory effect at during driving for displaying "black" in several frames, where the light transmittance is preferably desired zero. The panel in the display are driven signals so that the driving signals are applied across the picture element, where the signals are positively or negatively offset to reference voltage of the panel.

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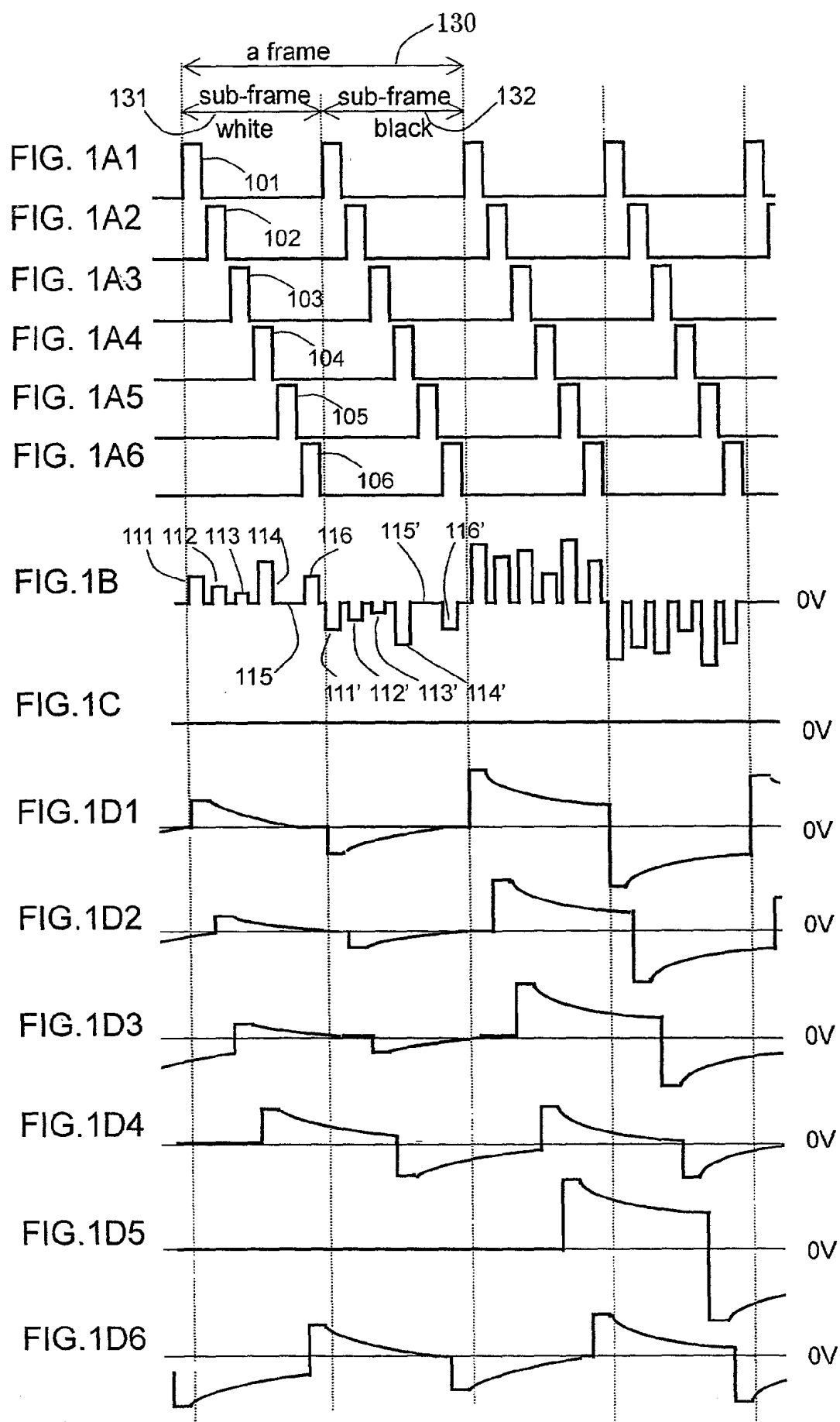


FIG. 2

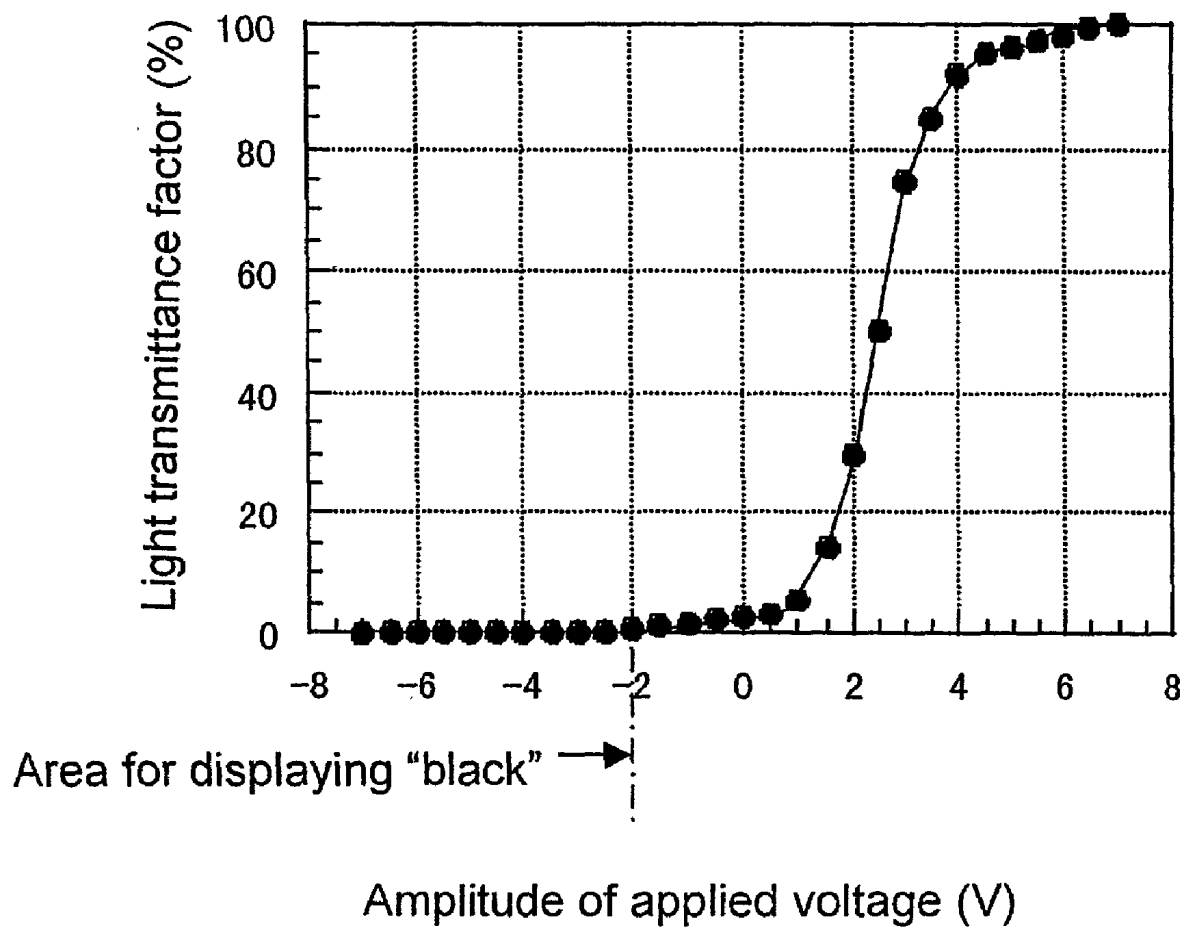


FIG. 3A

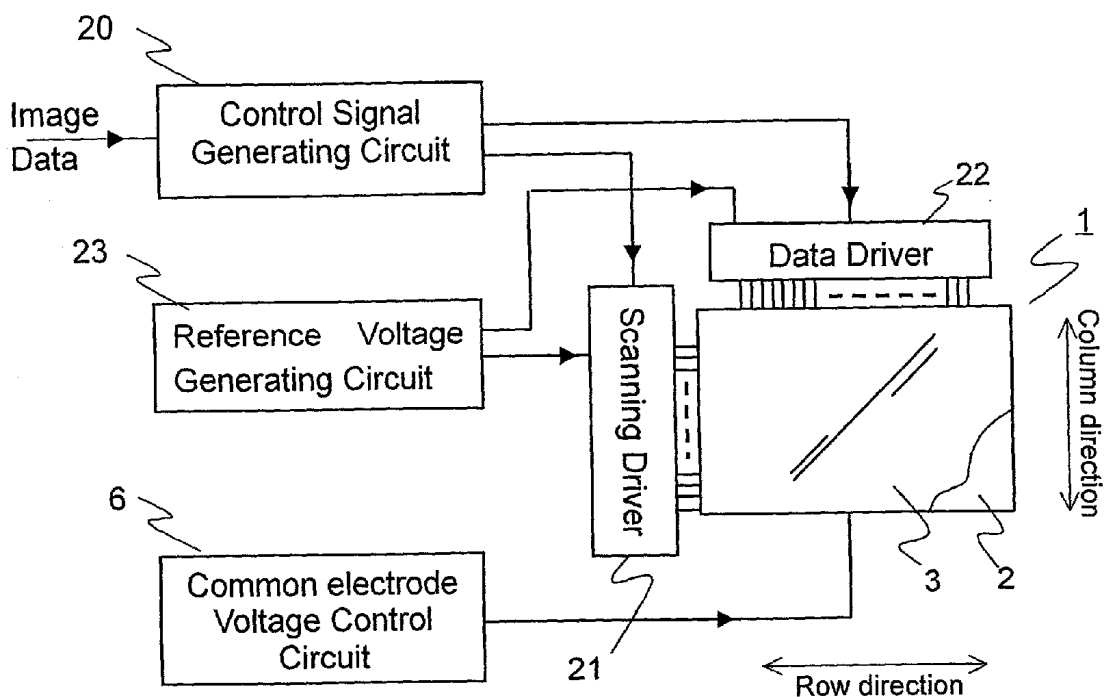


FIG. 3B

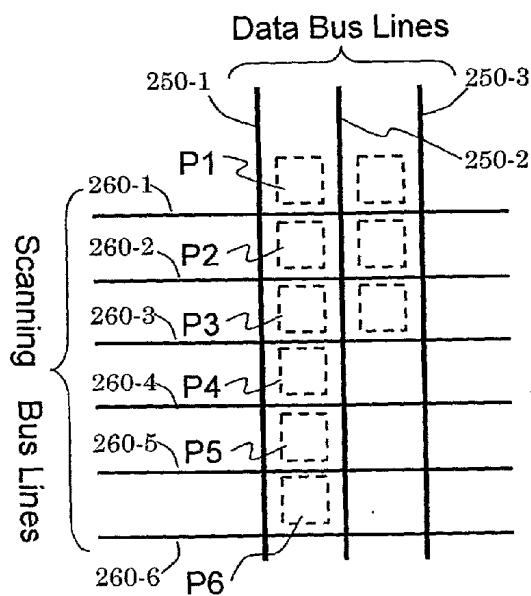
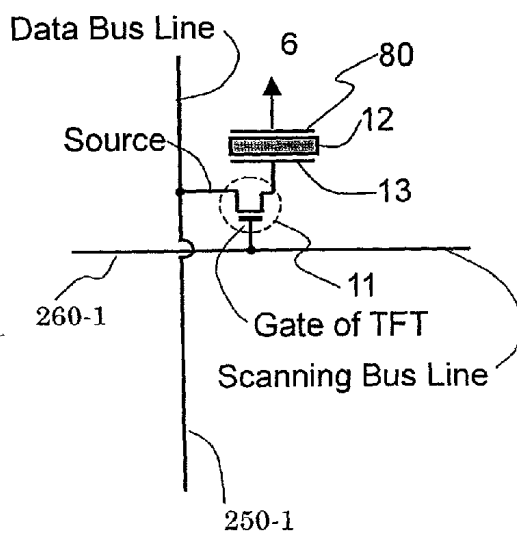


FIG. 3C



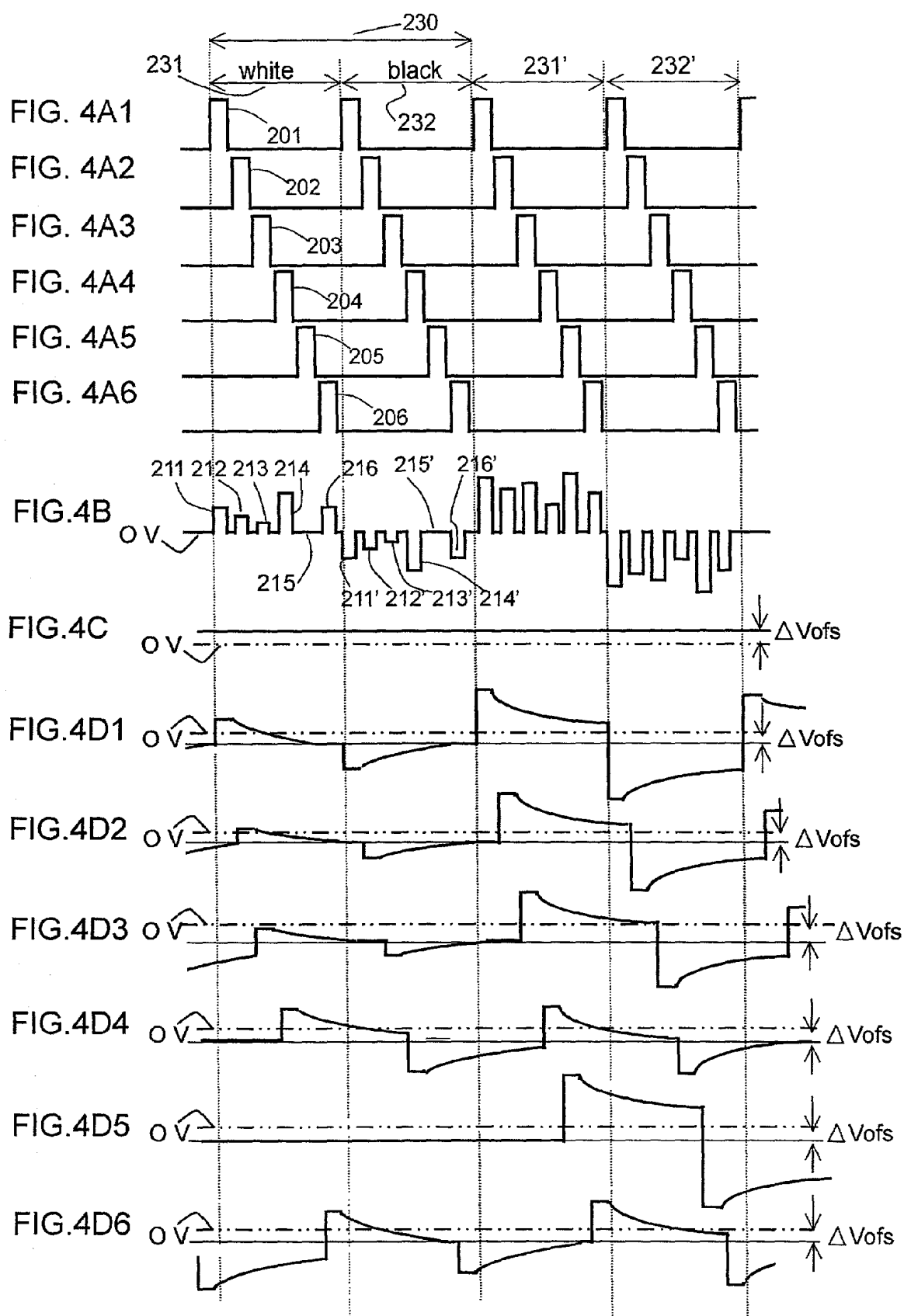


FIG. 5

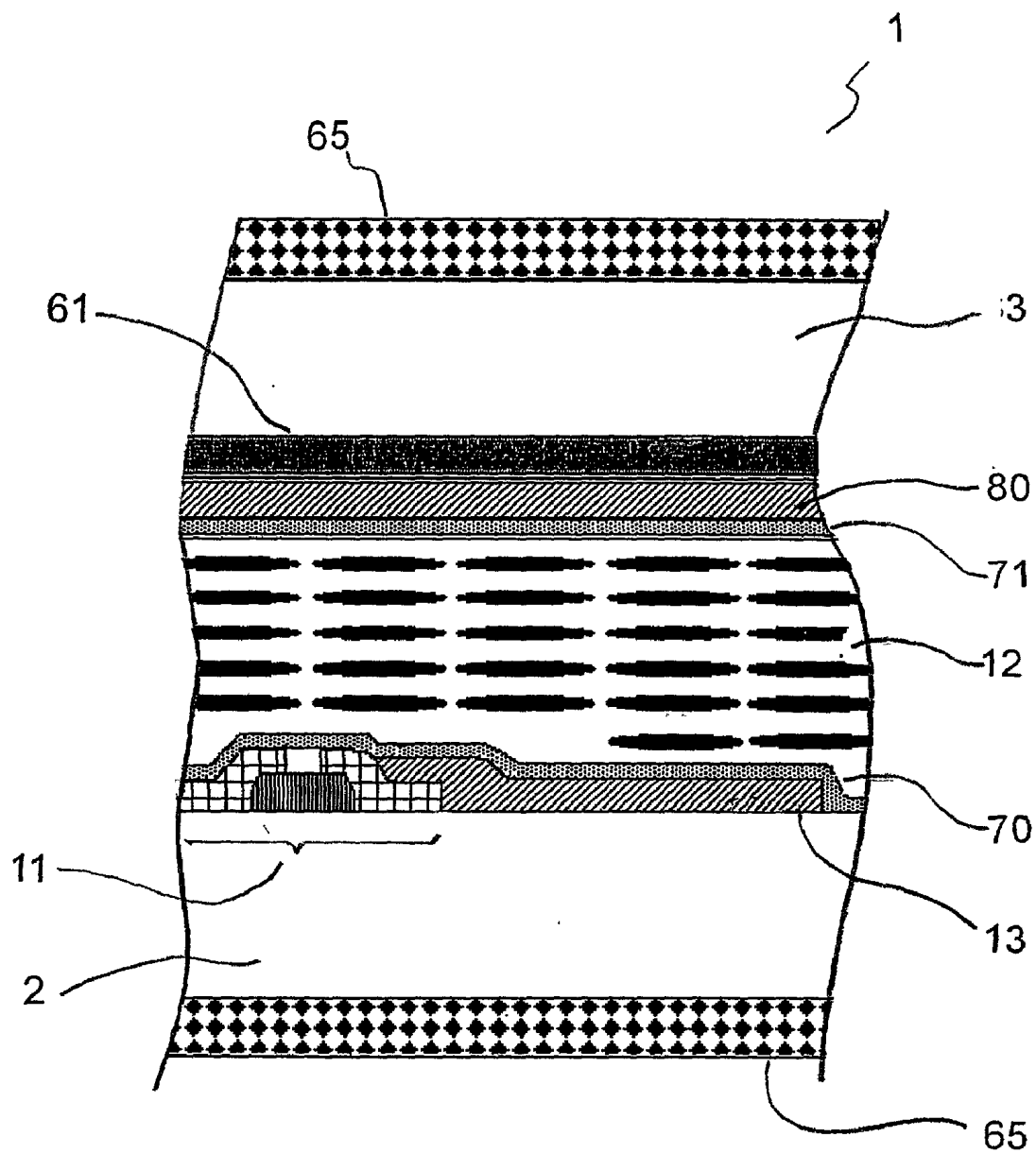


FIG. 6

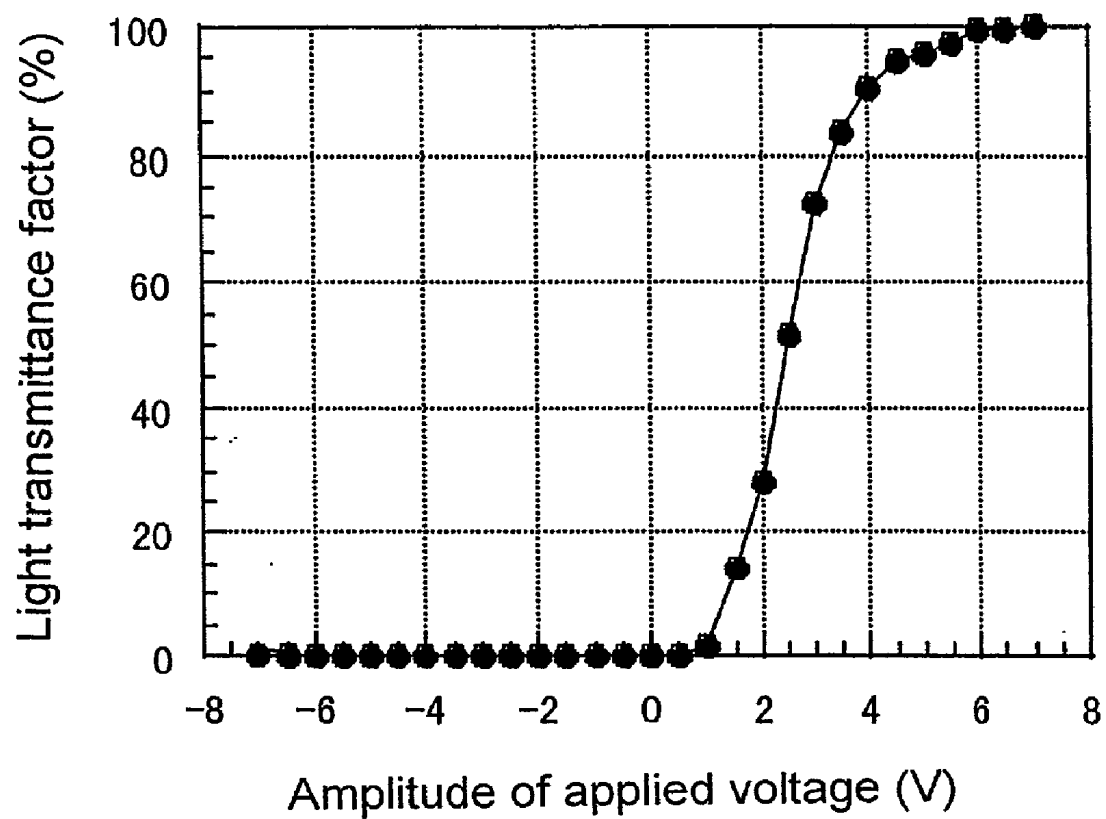


FIG. 7

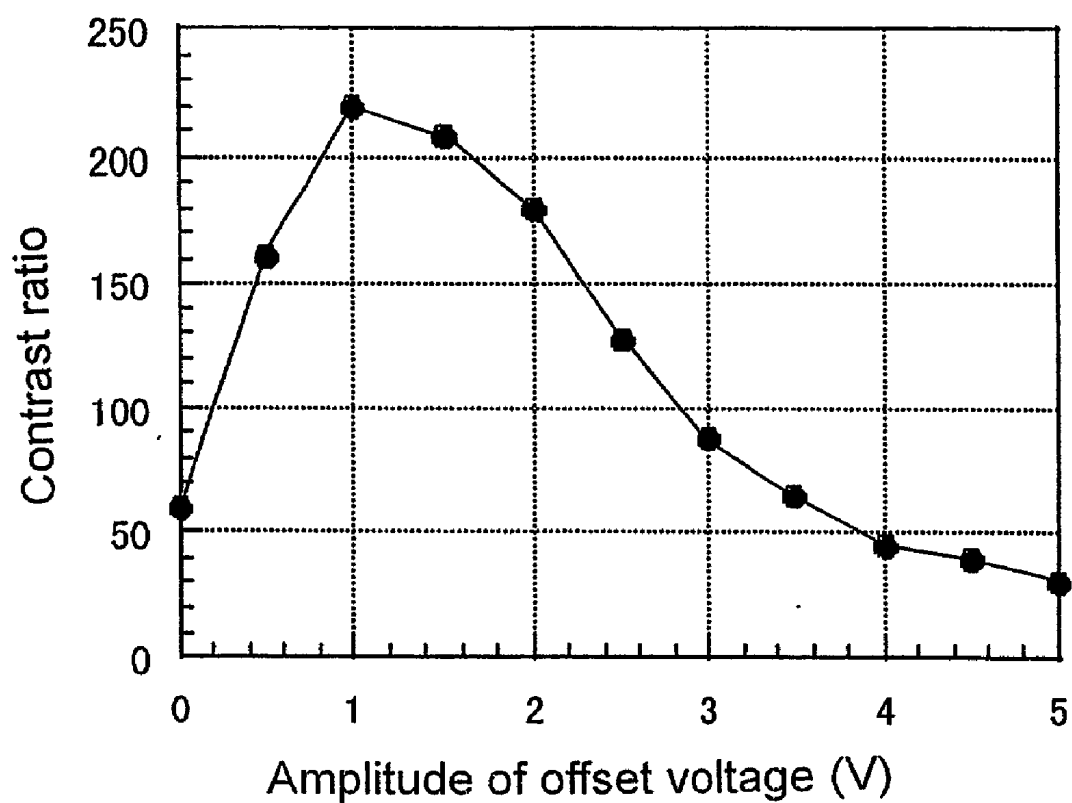


FIG. 8

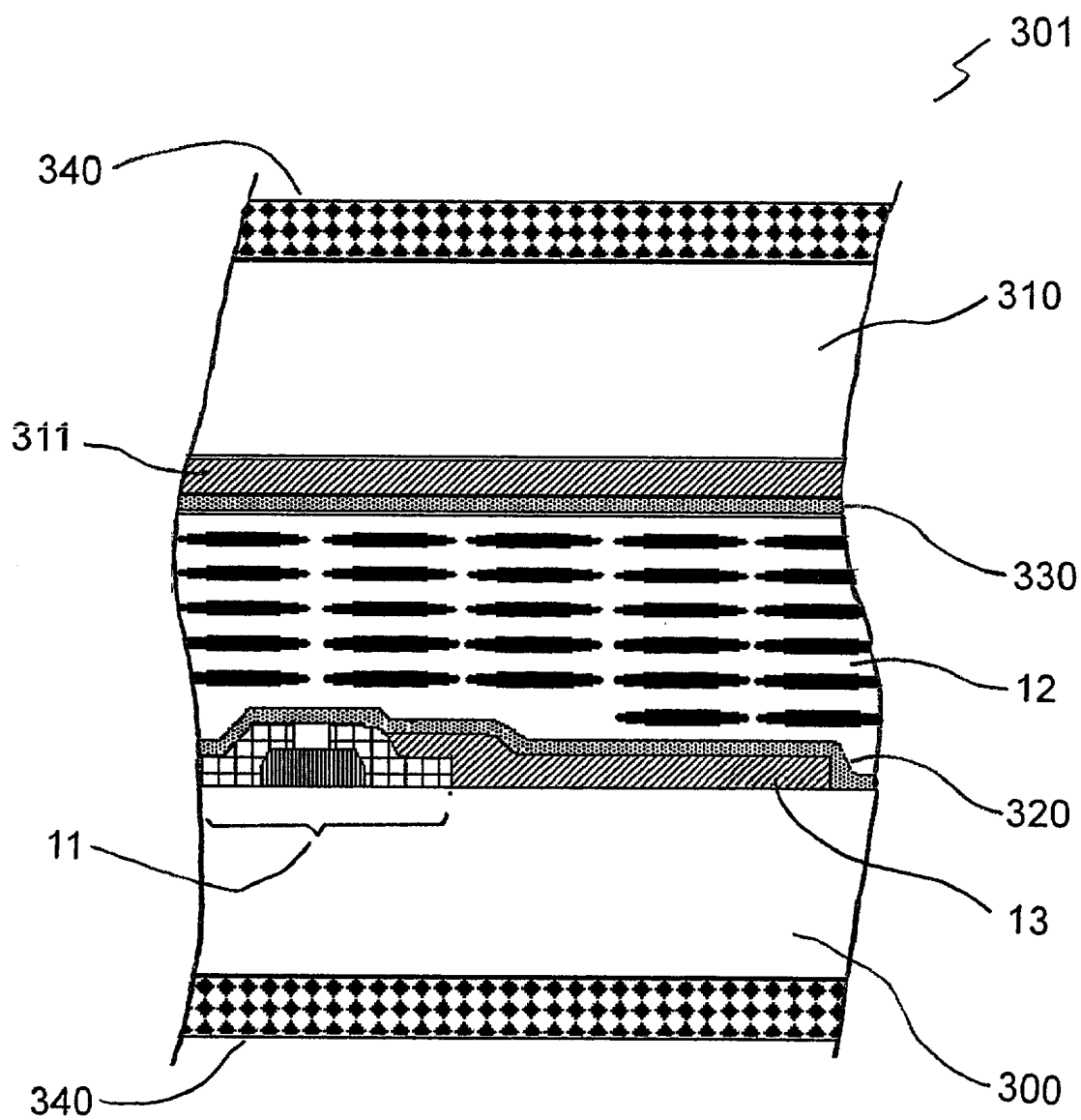


FIG. 9A

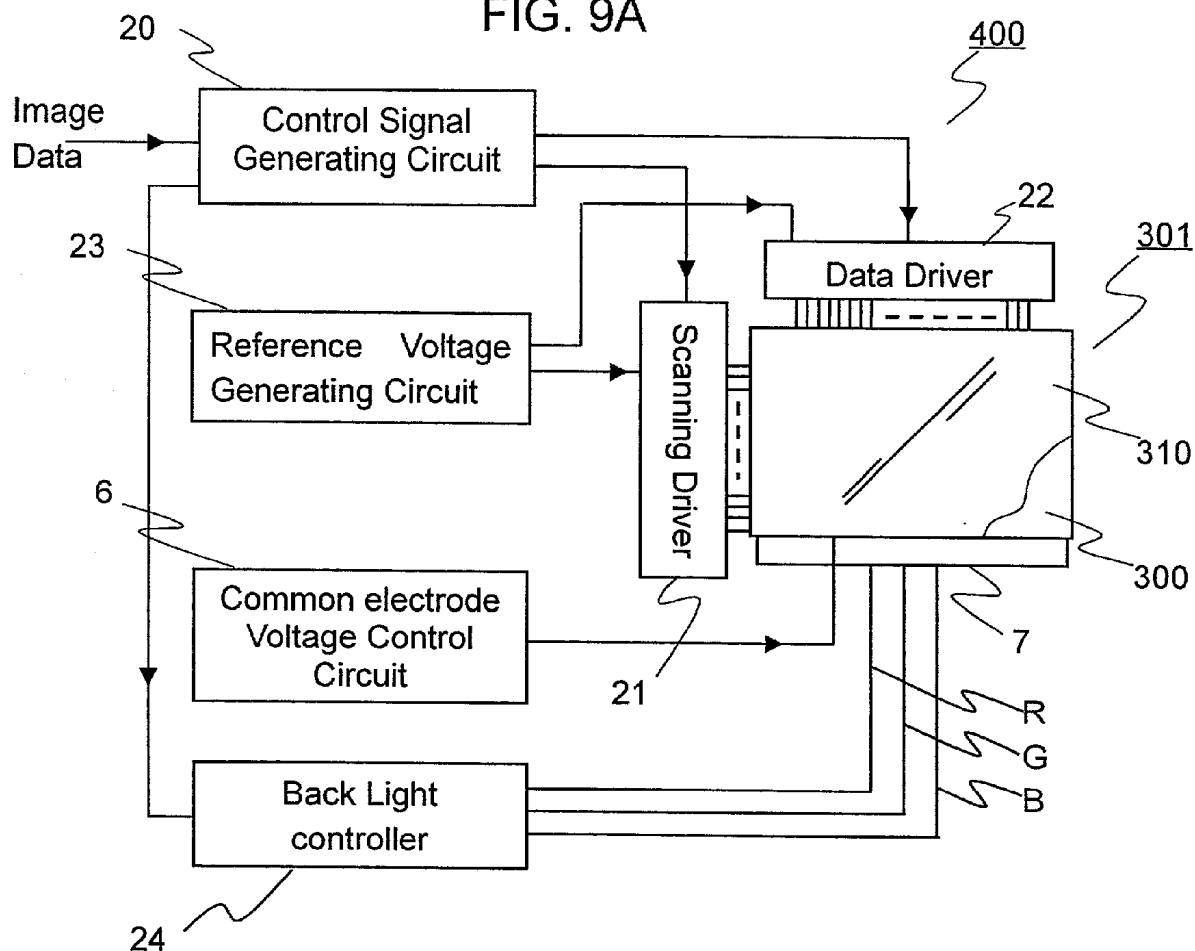


FIG. 9B

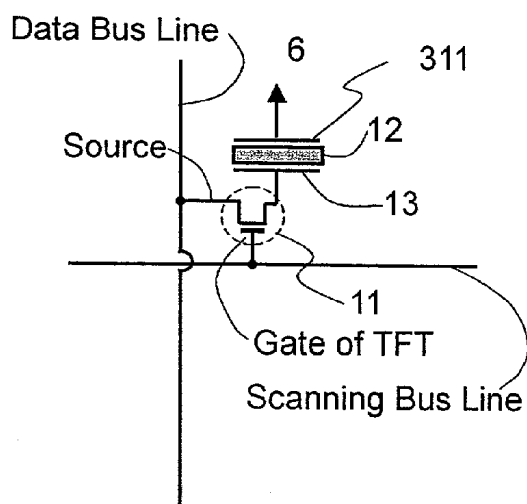


FIG. 10A1

FIG. 10A2

FIG. 10A3

FIG. 10A4

FIG. 10A5

FIG. 10A6

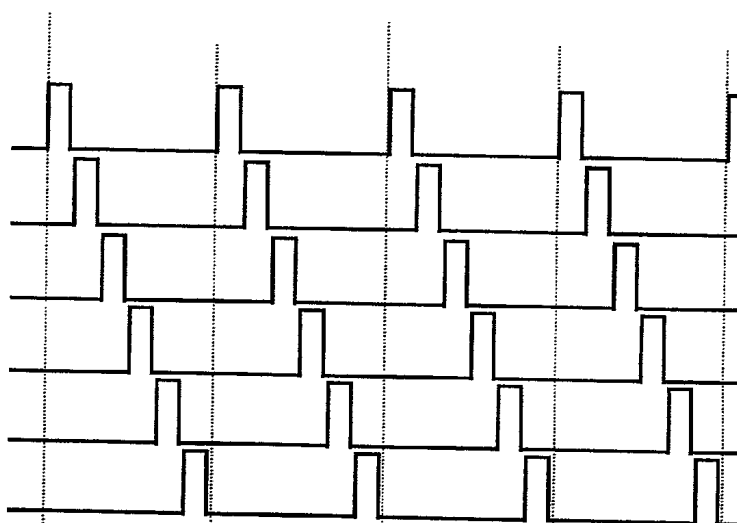


FIG. 10B

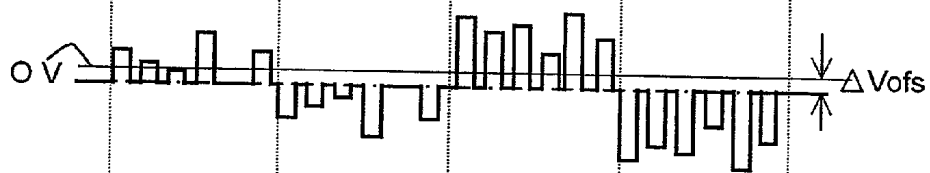


FIG. 10C



FIG. 10D1



FIG. 10D2



FIG. 10D3

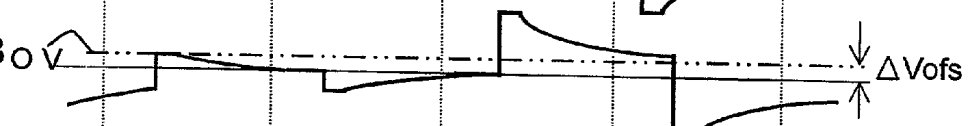


FIG. 10D4

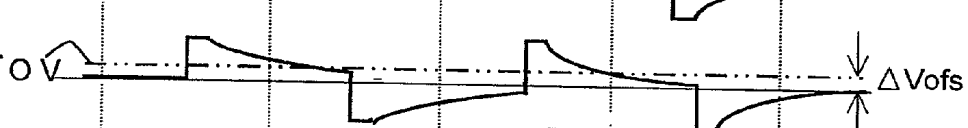


FIG. 10D5

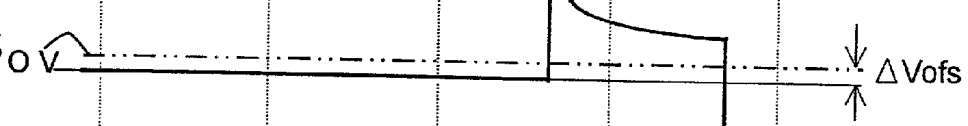
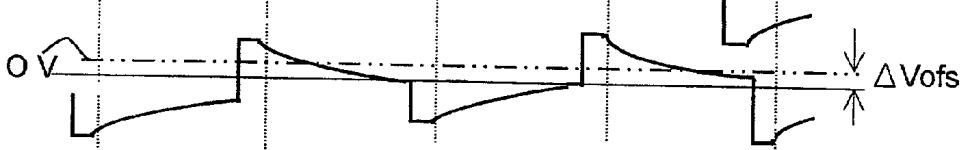


FIG. 10D6



DISPLAY PANEL INCLUDING LIQUID CRYSTAL MATERIAL HAVING SPONTANEOUS POLARIZATION

BACKGROUND OF THE INVENTION

[0001] 1. Field of the Invention

[0002] This invention relates to liquid crystal devices, and particularly to devices using liquid crystal having spontaneous polarization such as ferroelectric or antiferroelectric liquid crystal materials.

[0003] 2. Description of Related Art

[0004] In recent years, the research on liquid crystal has rapidly progressed, resulting in increase of its application to devices, such as a display panel, an optical modulator, and an optical shutter in printing machine and so on.

[0005] In particular, liquid crystal devices and liquid crystal panels are characterized by thin depth, a lightweight, and low consumption. Hence, the devices or the panels are used as a display unit in various kind of devices, such as mobile terminals, for example cellular phones and mobile computers, moreover desk-top computers or household television sets.

[0006] The configuration of a liquid crystal display panel generally has a pair of opposing substrates spaced appropriately apart, including electrodes on inner faces of these substrates for switching each picture element or pixel defined by the arrangement of electrodes. Liquid crystal material is filled in a space between these substrates which are sealed at their periphery, while the detailed configurations are described later.

[0007] The widely used liquid crystal materials in these days for the liquid crystal display devices are; super twisted nematic liquid crystal and twisted nematic liquid crystal, which are hereinafter referred to as STN and TN respectively. The liquid crystal display of STN driven through a simple-matrix-type electrode configuration, which is referred to as simple matrix, is liable to generate image degradation caused by electrical cross-talk between picture elements or pixels, while the relatively low manufacturing cost results from the use of the simple matrix. The liquid crystal display of STN driven through the simple matrix also has undesirable response time (slow response time) for displaying dynamic images such as moving pictures.

[0008] On the other hand, TN in devices may be driven through an active-matrix-type electrode configuration including thin film transistors as switching elements, where the configuration is referred to as active matrix.

[0009] The liquid crystal display device of TN driven through the active matrix generally has no problem of electrical cross-talk, hence its image quality is better than that produced by the liquid crystal display device of STN driven through the simple matrix. The liquid crystal display device of TN further has faster response time than that of the liquid crystal display of STN, however the response time is limited by the characteristics of material itself, which means the liquid crystal display of TN being unsuitable for a display panel required to respond at high speed for displaying dynamic images such as motion picture.

[0010] These two materials have a common problem of a narrow view angle, hence the preferable view direction to see images on the display using these materials is limited.

[0011] It is well known that a certain type of liquid crystal material has spontaneous polarization, and a ferroelectric liquid crystal, which is referred to as FLC hereinafter, is representative one of this type. This type of liquid crystal material is characterized by its fast response time ranging from several to several hundreds microseconds which is approximately one hundreds times faster as that of TN liquid crystal. Therefore, this type of material may solve the problem related to the response time.

[0012] The FLC also has a characteristic such that liquid crystal molecules of the FLC always maintains their axis parallel to an appropriately treated surface of a substrate contacting with the crystal. This characteristic leads to extremely smaller variation of index of refraction of the crystal according to view direction than that of TN or STN liquid crystals, resulting in a wider viewing angle of display panels using FLC material. Therefore the FLC material also has the advantage suitable for the liquid crystal as the material for a display panel.

[0013] However, the FLC material has a disadvantage such as a decrease of contrast ratio (or low contrast ratio) when used as the material for the display panel. The decrease comes from incompleteness of the memory effect during data being maintained in a picture element, where data corresponds with whether the light transmittance of the material in the element is low or high. That is, a little increase of the light transmittance in the picture element occurs when data pulses of zero amplitude for displaying "black" during several frames are applied to the element, and a little amount of light from a light source leaks through the element, resulting in decreasing the contrast ratio.

[0014] Therefore, the prevention of the increase of the light transmittance during displaying "black" is desired for improving the contrast ratio of the display panel using the FLC material.

SUMMARY OF THE INVENTION

[0015] There is provided a liquid crystal display including a panel using a liquid crystal material having spontaneous polarization, such as ferroelectric liquid crystal (FLC), having a faster response time suitable to display dynamic images. The FLC has the disadvantage caused by the incomplete memory effect during driving for displaying "black" in several frames, where the light transmittance is preferably desired zero. Due to preventing the decrease of contrast ratio caused by the incomplete memory effect, the panel in the display are driven signals so that the driving signals are applied across the picture element, where the signals are positively or negatively offset to reference voltage of the panel.

[0016] In one aspect of the present invention, a disadvantage such as decrease of contrast ratio (or low contrast ratio) when used as the material for the display panel is improved by use of an improved driving. The improved driving may shift the voltage appearing across a picture element to a positive or negative voltage from the reference potential of the panel.

[0017] In another aspect of the present invention, there is provided a crystal display panel such that a voltage applied to a common electrode provided on a face of an substrate is offset positively or negatively to improve the contrast ratio.

[0018] In further aspect of the present invention, there is provided a liquid crystal display panel such that data signal applied to data signal electrode is offset positively or negatively to improve the contrast ratio.

[0019] In still further aspect of the present invention, there is provided a liquid crystal display panel such that full color dynamic images are displayed, without color filter, by use of a light source which can emit each light of three primary colors.

BRIEF DESCRIPTION OF THE DRAWINGS

[0020] FIGS. 1A1 to 1A6 schematically show signal waveforms applied to scanning bus lines;

[0021] FIG. 1B schematically shows data signals applied to a data bus line.

[0022] FIG. 1C schematically shows a common voltage applied to a common electrode;

[0023] FIGS. 1D1 to 1D6 schematically show waveforms of voltage appearing across each picture element driven by corresponding data signal shown in FIG. 1B;

[0024] FIG. 2 schematically shows a diagram of light transmittance factor of the liquid crystal display panel driven by the signals shown in FIG. 1;

[0025] FIG. 3A schematically shows a block diagram of a liquid crystal display panel with circuits as the first preferred embodiment;

[0026] FIG. 3B schematically shows a relationship between six picture elements and data bus and scanning lines;

[0027] FIG. 3C schematically shows an equivalent circuit corresponding to a picture element;

[0028] FIGS. 4A1 to 4A6 schematically show signal waveforms applied to scanning bus lines;

[0029] FIG. 4B schematically shows data signals applied to a data bus line;

[0030] FIG. 4C schematically shows a common voltage applied to a common electrode;

[0031] FIGS. 4D1 to 4D6 schematically show waveforms of voltage appearing across each picture element driven by corresponding data signal shown in FIG. 4B;

[0032] FIG. 5 schematically shows a cross section of essential part of a liquid crystal display panel shown in FIG. 3;

[0033] FIG. 6 shows a performance of voltages applied to a picture element vs. light transmittance;

[0034] FIG. 7 shows a performance of voltages applied to a common electrode vs. contrast ratio;

[0035] FIG. 8 schematically shows a cross section of essential part of a liquid crystal display panel as the second preferred embodiment;

[0036] FIG. 9A schematically shows a block diagram of a liquid crystal display panel with circuits as the second preferred embodiment;

[0037] FIG. 9B schematically shows an equivalent circuit corresponding to a picture element;

[0038] FIGS. 10A1 to 10A6 schematically show signal waveforms applied to scanning bus lines;

[0039] FIG. 10B schematically shows data signals applied to a data bus line;

[0040] FIG. 10C schematically shows a common voltage applied to a common electrode; and

[0041] FIGS. 10D1 to 10D6 schematically show waveforms of voltage appearing across each picture element driven by corresponding data signal shown in FIG. 10B.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

[0042] Referring to FIG. 1, the schematic waveforms are shown in case of a liquid crystal display using FLC material which is driven through the active matrix, where six picture elements P1 to P6 arranged on a same column direction as shown in FIG. 3B are driven for example.

[0043] More in detail, each of FIGS. 1A1 to 1A6 schematically show gate pulses or scan pulses 101 to 106 applied to relevant scanning bus line respectively, each of the scanning bus lines is electrically connected to each gate electrode of thin film transistors (TFTs) as switching devices in the active matrix. During the application of the gate pulse 101, for example, to a scanning bus line the relevant TFTs turn on, and turn off if no application of the gate pulse. As shown in FIGS. 1A1 to 1A6, the gate pulses 101 to 106 are applied in sequence to each scanning bus line, hence these gate pulses 101 to 106 sequentially scan from the first scanning bus line to the last scanning bus line, while FIGS. 1A1 to 1A6 show only six gate pulses for six row lines for example.

[0044] FIG. 1B schematically shows the data signals 111 to 116 and 111' to 111' during a one frame, which is described in detail hereinafter, to applied to each of the six picture elements P1 to P6 for controlling the electrical potential occurring across the picture element P1 to P6 in synchronism with on or off state of the TFTs driven by the gate pulses 101 to 106 shown in FIGS. 1A1 to 1A6. The data signal 111 during a sub-frame 131, which is described in detail hereinafter, in synchronism with the gate pulse 101 shown in FIGS. 1A1 is applied to the relevant picture element P1.

[0045] FIG. 1C shows an electrical potential set to 0 V of a common electrode provided on an inner face of substrate opposing to the substrate having the active matrix, where a pair of these substrates are arranged so that the common electrode faces to the active matrix and the FLC material is provided between these substrates.

[0046] FIGS. 1D1 to 1D6 show each variation in time of electrical potentials occurring across liquid crystal in the picture elements P1 to P6 respectively. In FIG. 1B, the pulses 111 to 116, which are in the sub-frame 131, may set the FLC material in the picture element P1 to P6 except P5 in light transmissible mode in this case, which means the light may pass through the picture element P1 to P4 and P6, hence the sub-frame 131 is called as a white sub-frame or a white writing sub-frame. The pulses 111' to 116', which are in a sub-frame 132, may reset the FLC material in the picture element P1 to P6 into block mode in this case, which ideally means light cannot pass through the device, hence the

sub-frame **132** is called as a black sub-frame or a black writing sub-frame. A frame **130** comprises these sub-frames **131** and **132**.

[0047] On the contrary to this case, it is possible to arrange the display according to polarizer films provided on the outer faces of substrates so as to set the elements P1 to P6 being in the block mode, while the polarity of data signals **111** to **116** are same in FIG. 1B. Similarly, the elements P1 to P6 except P5 driven by the signal data **111'** to **114'** and **116'** respectively can be set as the light transmissible modes.

[0048] From the view point of driving a liquid crystal and a reliability of a pulse generator for generating data signals applied to the liquid crystal, it is preferable that each amplitude of data signal in the white sub-frame **131** preferably inversely equal to corresponding each amplitude of the data signal in the black sub-frame **132**, and the liquid crystal is driven in a order of white writing and black writing, as shown in FIG. 1B.

[0049] Therefore, a picture element desired to display "back" in the white writing sub-frame should be kept at 0 V during both sub-frames periods.

[0050] FIG. 2 shows the relationship between applied voltage across the picture element and the light transmittance factor of the picture element driven by the waveforms in FIGS. 1A to 1C. FIG. 2 shows that a little amount of light, such as the light emitted from a light source arranged behind the liquid crystal panel, passes through the picture element at 0 V which is a voltage of data signal, therefore -2 V should be applied across the picture element for light transmittance practically being zero.

[0051] In the period of the white sub-frame **131** the minimum amplitude of pulse is 0 V and setting the amplitude negative is not preferable according to above reason, that is, the reliability of pulse generator. Therefore in this case of setting the pulse amplitude to 0 V, the picture element in turn in the black sub-frame **132** can not display "black", because of the light transmittance being not zero.

[0052] The object of the present invention provides liquid crystal devices which have the improved contrast ratio by preventing a little light transmission through picture elements caused by the incompleteness of memory effect of the liquid crystal material having spontaneous polarization when data are written thereon.

[0053] The present invention provides liquid crystal devices characterized by improved contrast ratio which is attained by compensating the incompleteness of memory effect of the ferroelectric material when data is written thereon and maintaining the state of the light transmittance being almost zero. The compensation can be realized by offsetting the potential applied to one of electrodes which supplies a voltage to the picture element so that the devices display "black" or block mode.

[0054] FIG. 3A shows a block diagram of a liquid crystal display device including the improved driving system as the first preferred embodiment of the present invention, FIG. 3B shows the relationship between the six picture elements, the data bus lines, and the scanning bus lines. FIG. 3C shows an example of an equivalent circuit of one picture element which comprises a TFT **11** whose gate and source are electrically connected to a scanning bus line and a data bus

line respectively in this embodiment. The drain of the TFT **11** is electrically connected to a display electrode **13**. The FLC is provided between the display electrode **13** and a common electrode **80**, in this embodiment.

[0055] The liquid crystal display panel **1** shown in FIG. 3A comprises two substrates **2** and **3**, where the active matrix is formed on the inner face of the substrate **2** and the common electrode **80** is formed on the inner face, opposing to the active matrix, of the substrate **3**. A common electrode voltage control circuit **6** serves as a controlled offset voltage supplier which supplies a controlled voltage to the common electrode **80**. A reference voltage generating circuit **23** generates a reference voltage for defining the reference potential of the panel **1**.

[0056] Image data from an external device (not shown) are inputted into a control signal generating circuit **20** and stored in memory provided within the circuit **20**. The image data then are converted to respective pixel data corresponding to each picture element in the panel **1**. The pixel data in turn are sent to a data driver **22** in which the pixel data are converted to serial data for each lines and written to corresponding data bus line, while a synchronizing signal is sent from the circuit **20** to a scanning driver **21** for generating scanning pulses by which the gates of TFTs connected each of data bus lines are turn on. The scanning pulses are sequentially input to each scanning bus lines.

[0057] The each data signal inputted to data bus line can apply the voltage of data signal across each picture element during the gate of the TFT **11**, in the relevant picture element, being turned on.

[0058] With referring to FIGS. 4A1 to 4A6, these signals are applied to scanning bus lines relayed to six picture elements arranged on a same data bus line as similar as in the case of FIG. 1. The pulses **201** to **206** are scanning pulses which are applied to corresponding scanning bus lines. FIG. 4B shows a pulse train composing signals for six picture elements P1 to P6, for example, which are applied in synchronism with the relevant scanning pulses **201** to **206**. FIG. 4C shows a voltage offset for compensation of the incompleteness of memory effect of the FLC. And FIGS. 4D1 to 4D6 show each potential appeared across each picture elements P1 to P6 when the data signals **211** to **216**, **211'** to **216'**, **221** to **226**, **221'** to **226'** in FIG. 4B respectively during each sub-frame.

[0059] As shown in FIG. 4C, the common electrode voltage control circuit **6** supplies the common electrode **80** with the voltage ΔV_{ofs} which is offset from the reference level in the panel **1** so as to provide a stable "black" presentation, where in this embodiment ΔV_{ofs} has a positive polarity. The data signals **211** to **216**, **211'** to **216'**, **221** to **226**, and **221'** to **226'** as shown in FIG. 4B, are applied to data bus line for energizing each corresponding picture elements P1 to P6 during the relevant TFT being turned on by the corresponding gate scanning pulses **211** to **216**. As described above, as well in the first preferred embodiment each data signal for a picture element in sub-frame **231** for writing "white" and sub-frame **232** for writing "black" in a frame **230** respectively has opposite polarity and same amplitude.

[0060] FIG. 5 shows a cross section of an essential part in the panel **1** as the first preferred embodiment of the present

invention. The active matrix including TFT **11** and display electrode **13** are provided on the substrate **2** of glass, color filters **61** and common electrode **80**, which is transparent electrodes made of, for example, tin oxide, are provided on a substrate **3** of glass.

[0061] On one of faces of the substrate **2** there is provided the active matrix for the liquid crystal panel size of a 12.1-in. diagonal in which pixel pitches in row and column direction are 0.1025 and 0.3075 mm respectively, and the number of pixels is 800×3×600, where a pixel comprises three picture elements or sub-pixel which are arranged in row direction, therefor the pixel is of a square (0.1025×3 by 0.3075 mm). On one of faces of the substrate **3** there is provided a common electrode **80** deposited over a color filter **61** composed sub-filters for three colors of red, green, and blue which are formed at the same pitches (0.1025 mm) in row direction this embodiment.

[0062] A thin layer of polyimide is coated on a face with the active matrix of substrate **2** and on a face over the color filter **61** on the substrate **3**, after washing the substrates **2** and **3**. After appropriate treatments, such as cure or baking, each surface of the layers of 20 nm thick becomes an alignment layers **70** and **71** after buffered or rubbed in a single direction by a soft cloth, such as rayon.

[0063] Opposing each the alignment layers **70** and **71** each other, the substrates **2** and **3** spaced by distributed spacers made of silica of about 1.6 μm in average particle size are sealed along periphery thereof. And then, the ferroelectric liquid crystal material **12** including naphthalic liquid crystal as the chief ingredient (A. Mochizuki, et. al: Ferroelectrics, 133,353,(1991)) is filled in the space between the substrates **2** and **3**.

[0064] Each polarizer film **65** (Nitto-Denko: NPF-EG1225DU) is provided on each outer surface of the sealed substrates **2** and **3** so as to keep the relation of cross nicols condition each other, where back is presented when the longitudinal axis of molecule of the ferroelectric liquid crystal is tilted by the application of negative voltage to the data bus line.

[0065] The panel **1** formed in these steps above described is driven in a way as follow.

[0066] A voltage from the common electrode voltage control circuit **6**, which is positively offset by about $\Delta\text{ofs}=1$ V from the reference potential supplied from the reference voltage generating circuit **23**, is applied to the common electrode **80** for stabilizing the presentation of "black" as shown in FIG. 4C.

[0067] Each picture element is energized through the data electrode during the TFT **11** being on. And a pair of data signals of opposite polarity and same amplitude, for example **211** and **211'** in FIG. 4B, are applied to each picture element in the period of the sub-frames, such as **231** and **232**, **231'** and **232'**, for writing white and writing black respectively in each single frame **230**.

[0068] FIG. 6 shows performance of the liquid crystal display **1**, exhibiting light transmittance factor on basis of amplitude of data signal during application of data bus line. It shows that the light transmittance is almost nearly zero when the voltage applied to the data electrode is 0 V. This preferable performance comes from applying voltage posi-

tively offset to the common electrode **80**. Each electrical potential appearing across a picture element is shown in FIGS. 4D1 to 4D6. Both amplitude of data signal **215** and **215'** are 0 V for displaying black, while the effective potential applied across the pixel is negative during the sub-frames **231** and **232** in FIG. 4D5 so as to produce 0 of light transmittance factor. The measured contrast ratio, which is defined as the ratio between light transmittances in displaying white and black, is 220:1, where the amplitude of data signal applied to picture element is 0 V at black presentation and 7 V at white presentation.

[0069] These contrast ratio shows that the display panel **1** may be preferably used as a display device.

[0070] Furthermore, FIG. 7 shows a variation of contrast ratio of this display panel **1**, where the amplitude of offset voltage, Δofs applied to the common electrode **80** has been selected in the range 0 to 5 V. The ratio was calculated from the light transmittance factor of at black presentation (amplitude of data signal: 0 V) and at white presentation (amplitude of data signal: 7 V), while the amplitude of the offset voltage is selected in the range 0 to 5 V. If the desired contrast ratio is more than 100:1, the sufficient contrast ratio is performed in this display panel **1** in the range of 0.5 to 2 V of the offset voltage.

[0071] When the panel **1** was driven by the conventional way, where the common electrode was kept at 0 V, the contrast ratio became to 60:1.

[0072] Now referring to FIG. 8, the second preferred embodiment is shown. This cross section is of an essential part in a liquid crystal panel **301**.

[0073] On one of faces of the substrate **301** there is provided a active matrix for the liquid crystal panel size of a 12.1-in. diagonal in which pixel pitches in line and column direction are 0.3075 and 0.3075 mm respectively, and the number of picture element is 800×600. There is provided a transparent common electrode **311** deposited over one of faces of the substrate **310**. A thin layer of polyimide has been coated on each face with the active matrix and face with transparent common electrode of the washed substrates **300** and **310** respectively. After appropriate treatment, such as cure or baking, each surface of the cured layer of 20 nm thick becomes alignment layers **320** and **330** respectively after buffered or rubbed in a single direction by a soft cloth, such as rayon.

[0074] Opposing each of alignment layers **320** and **330** each other, the substrates **300** and **310** spaced by distributed spacers made of silica of about 1.6 μm in average particle size are sealed along periphery thereof. And then, the ferroelectric liquid crystal material **360** including naphthalic liquid crystal as the chief ingredient (A. Mochizuki, et. al: Ferroelectrics, 133,353, (1991)) is filled in the space between the substrates **300** and **310**.

[0075] Each of polarizer films **340** (Nitto-Denko: NPF-EG1225DU) is provided on each outer surface of the sealed substrates **300** and **310** so as to keep the relation of cross nicols condition each other, where back is presented by tilting of the longitudinal axis of molecule of the ferroelectric liquid crystal.

[0076] FIG. 9A shows a block diagram of the liquid crystal display **400** in the second preferred embodiment,

where a part having same reference number as one in FIG. 3A has a similar function. FIG. 9B shows an equivalent circuit for a picture element. A liquid crystal display 400 has a back light source 7 which comprises light emitted diodes and can emit each monochromatic light of red, blue, and green time divisionally and is located behind the panel 301, that is, behind the second substrate 300. The source 7 is driven for emitting each color by driving signals from a back light controller 24 on basis of the synchronizing signal from the circuit 20, resulting in emitting each color in synchronism with panel operation, such as scanning operation.

[0077] A voltage, which is positively offset by about 1 V from the reference potential supplied from the reference voltage generating circuit 23, is applied to the common electrode 311 for stabilizing the displaying "black" as shown in FIG. 4C. Each of data signals shown in FIG. 4B is applied to the display electrode 13 through the data bus line during the TFTs 11 being turned on. And data signals of opposite polarity and same amplitude are applied alternately to a pixel by 180 Hz in the period of each pair of sub-frame for white-writing and black-writing in a single frame.

[0078] In this embodiment, a full color presentation is composed of three frames, each of which is used for presentation of a chromatic color. In synchronism with of each frame, the source 7 is energized to emit a corresponding color alternately, where the lighting method is well known as a field sequential method. The desirable full color images are dynamically and clearly presented.

[0079] Comparing the panel 301 with the panel 1 in the first embodiment, the number of picture element of the panel 301 in the second embodiment becomes $\frac{1}{3}$ of the number of picture element of the panel 1 in the first embodiment, while the panel sizes are same. Resultant increase of each aperture area of the picture element together with no use of color filters gives effect to present bright images.

[0080] The second preferred embodiment uses the field sequential lighting method, hence it is necessary to drive each picture three times faster than the panel 1 using three color filter. However the use of the ferroelectric liquid crystal material characterized with fast response can realize to present images at 180 Hz of frame frequency, while it is difficult to use the conventional TN material at such high rate.

[0081] With referenced to FIG. 10, the third preferred embodiment is shown. The panel 1 in the first preferred embodiment is driven by the signals shown in FIGS. 10A1 to 10A6, 10B, and 10C. That is, a voltage of 0 V is applied to the common electrode as shown in FIG. 10C, and each of data signals offset negatively by 1 V is applied to the data bus line as shown in FIG. 10B. In this case, each voltage appearing across the picture element is shown in FIGS. 10D1 to 10D6, which are similar to each in FIGS. 4D1 to 4D6 respectively. This leads to a similar performance according to the contrast ratio. The driving method in the third preferred embodiment is applicable to a liquid crystal display panel which has no common electrode, such as a panel driven through a simple matrix.

[0082] From the first to third preferred embodiments, each of the display panels includes the active matrix and ferroelectric liquid crystal material. However, the present invention may be applicable to the display panel which includes a simple matrix, and to devices such as optical modulators or optical shutter therein.

[0083] As shown above, the present invention provides the display panel which has the improved contrast ratio resulting from preventing the increase of the light transmittance after data writing into the panel, where the increase is caused by the incompleteness of memory effect of ferroelectric liquid crystal panel at data writing.

[0084] While various embodiments of the present invention have been shown and described, it should be understood that other modifications, substitutions and alternatives may be apparent to one of ordinary skill in the art. Such modifications, substitutions and alternatives can be made without departing from the spirit and scope of the invention, which should be determined from the appended claims.

[0085] Various features of the invention are set forth in the appended claims.

What is claimed is:

1. A liquid crystal device comprising:

a liquid crystal material characterized by spontaneous polarization, being applied signal for controlling a light transmittance of said material, wherein a voltage of said signal for writing data to said material is offset positively or negatively from 0 V at said material except during applying said signal.

2. The liquid crystal device claim 1 wherein said signal is offset positively or negatively so that a light transmission through said liquid crystal material being driven by said signal becomes to be blocked.

3. A liquid crystal device comprising:

a first substrate including a first electrode on a first face thereof;

a second substrate including a second electrode on a second face thereof, wherein said second substrate and said first substrate are sealed spaced apart so that said first and second face each other;

a liquid crystal material having spontaneous polarization filled in a space between said first and second substrates;

a first voltage generating circuit for supplying a voltage to said first electrode; and

a data signal circuit for supplying a data pulse to said second electrode,

wherein a voltage across said liquid crystal between said first and second electrodes is kept positively or negatively to a reference voltage of said device except during said data pulse being applied.

4. The liquid crystal device in claim 3 wherein said data pulse is offset positively or negatively so that a light transmission through said liquid crystal material being driven by said pulse becomes to be blocked.

5. The liquid crystal device claim 3 or 4 wherein said second substrate having an active element electrically connected to said second electrode so as to electrically control a picture element.

6. The liquid crystal device claim 5 wherein said voltage supplied by said first voltage generating circuit is offset so that a voltage across said liquid crystal material between said first and second electrodes is kept positively or negatively to said reference voltage of said device except during said data pulse being applied.

7. A liquid crystal panel comprising:

- a first substrate including a first electrode on a first face thereof;
- a second substrate including a second electrode on a second face thereof, wherein said second substrate and said first substrate are sealed spaced apart so that said first and second face each other;
- a liquid crystal material having spontaneous polarization filled in a space between said first and second substrates;
- a first voltage generating circuit for supplying a voltage to said first electrode;
- a data signal circuit for supplying a data pulse to said second electrode; and
- a light source for emitting more than monochromatic lights, each of said monochromatic lights being emitted time divisionally toward said first or second substrates,

wherein a voltage across said liquid crystal material between said first and second electrodes is kept positively or negatively to a reference voltage of said device during except said data pulse being applied.

8. A liquid crystal panel comprising:

- a first substrate including a first electrode on a first face thereof;
- a second substrate including a second electrode on a second face thereof, wherein said second substrate and said first substrate are sealed spaced apart so that said first and second face each other;
- a liquid crystal material having spontaneous polarization filled in a space between said first and second substrates;
- a first voltage generating circuit for supplying a voltage to said first electrode;
- a data signal circuit for supplying a data pulse to said second electrode; and

polarizer films provided on each outer face of said first and second substrates,

wherein a voltage across said liquid crystal material between said first and second electrodes is kept positively or negatively to a reference voltage of said panel except during said data pulse being applied so that said liquid crystal material blocks a light transmission through said liquid crystal material.

9. A liquid crystal display panel comprising:

- a first substrate including a common electrode on a first face thereof;
- a second substrate including data signal electrodes, scanning electrodes, and switching elements which are connected to one of said data signal electrodes and one of said scanning electrodes on a second face thereof,

wherein said second substrate and said first substrate are sealed spaced apart so that said first and second faces face each other;

- a liquid crystal material having spontaneous polarization filled in a space between said first and second substrates;
- a common reference voltage generating circuit for defining a reference voltage of said data signal electrode; and
- a common electrode voltage generating circuit for supplying a voltage to said common electrode, wherein said common voltage is offset to positive or negative voltages.

10. The liquid crystal display panel of claim 9 wherein said liquid crystal material having spontaneous polarization is ferroelectric liquid crystal material.

11. The liquid crystal display panel of claim 9 wherein said first substrate has a color filter.

12. A liquid crystal display panel comprising:

- a first substrate including a common electrode on a first face thereof;
- a second substrate including data bus lines, scanning bus lines, and switching elements which are connected to one of said data bus lines and one of said scanning bus lines on a second face thereof, wherein said second substrate and said first substrate are sealed spaced apart so that said first and second faces face each other;
- a liquid crystal material having spontaneous polarization filled in a space between said first and second substrates; and
- a common electrode voltage generating circuit for supplying a voltage to said common electrode; and
- a common reference voltage generating circuit for defining a reference voltage of said data bus lines, wherein said reference voltage is offset to positive or negative voltages.

13. The liquid crystal display panel of claim 12 wherein said liquid crystal material having spontaneous polarization is ferroelectric liquid crystal material.

14. The liquid crystal display panel of claim 12 wherein said first substrate has a color filter.

15. The liquid crystal display panel claim 12 further comprising: polarizer films provided on each outer faces of said first and second substrate, wherein said common voltage is offset so as that a light transmission of said liquid crystal material becomes to be block.

16. The liquid crystal display panel claim 12 further comprising: a light source emitting a plurality of monochromatic colors, wherein each monochromatic color is emitted by said light source time divisionally in synchronism with a operation of said liquid crystal display panel.

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专利名称(译)	显示面板包括具有自发极化的液晶材料		
公开(公告)号	US20010035852A1	公开(公告)日	2001-11-01
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[标]申请(专利权)人(译)	富士通株式会社		
申请(专利权)人(译)	FUJITSU LIMITED		
当前申请(专利权)人(译)	FUJITSU LIMITED		
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发明人	YOSHIHARA, TOSHIAKI MAKINO, TETSUYA SHIROTO, HIRONORI KIYOTA, YOSHINORI		
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其他公开文献	US7233306		
外部链接	Espacenet USPTO		

摘要(译)

提供一种液晶显示器，包括使用具有自发极化的液晶材料的面板，例如铁电液晶（FLC）；具有适合于显示动态图像的更快响应时间。FLC具有由在几个帧中显示“黑色”的驱动期间的不完全记忆效应引起的缺点，其中光透射率优选地期望为零。显示器中的面板是驱动信号，使得驱动信号施加在像素上，其中信号正向或负向偏移 to 面板的参考电压。

