



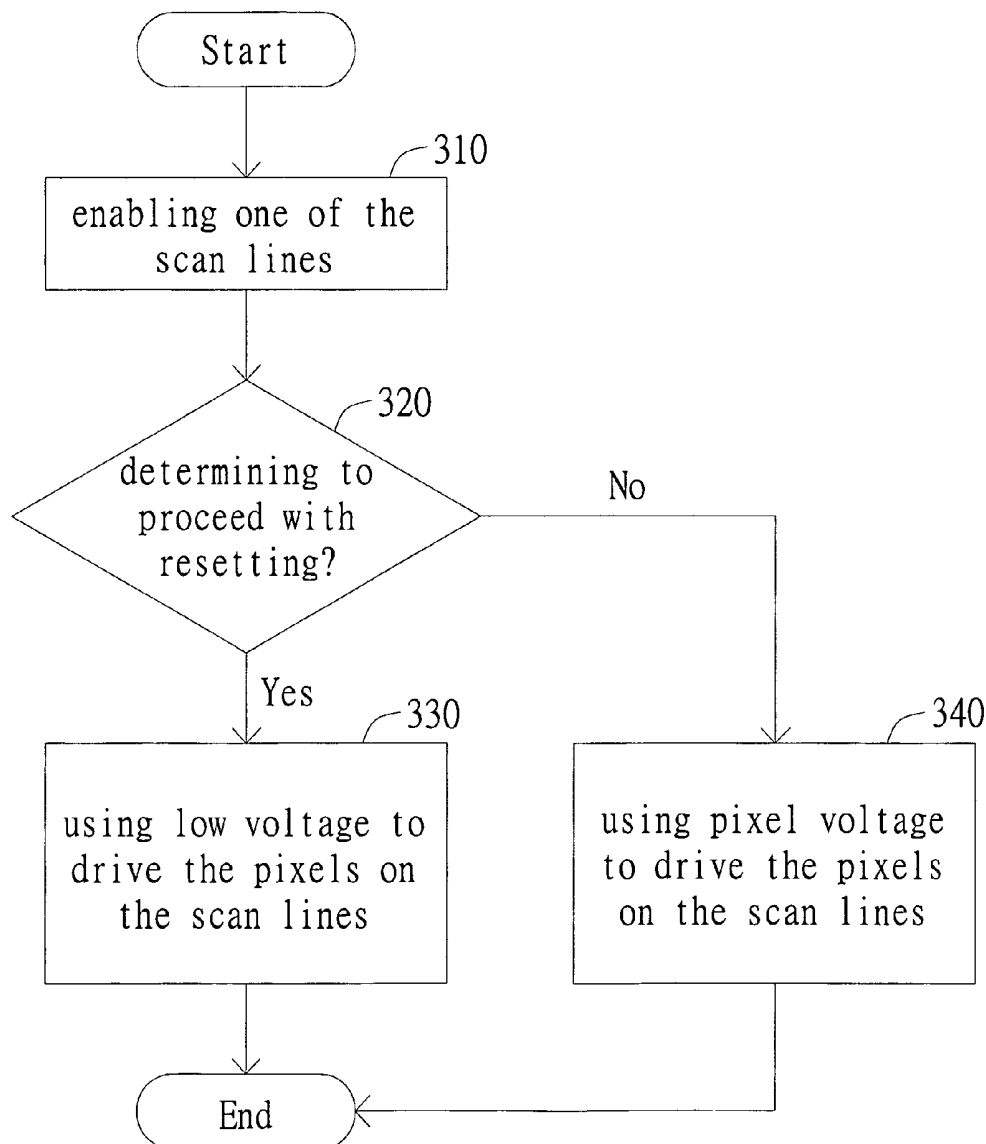
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(19) **United States**(12) **Patent Application Publication** (10) **Pub. No.: US 2005/0168434 A1**
Wang-Yang (43) **Pub. Date: Aug. 4, 2005**(54) **DRIVING METHOD OF A MULTI-DOMAIN
VERTICAL ALIGNMENT LIQUID CRYSTAL
DISPLAY**

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ATLANTA, GA 30339-5948 (US)(57) **ABSTRACT**

A driving method of a multi-domain vertical alignment (MVA) liquid crystal display (LCD). The LCD, which receives an image signal and displays a frame according to the image signal received, includes a plurality of scan lines. The driving method according to the invention initially enables one of the scan lines, then determines whether to proceed with resetting the enabled scan line or not. If the enabled scan line is to be reset, a low voltage is applied to the pixels on the enabled scan line. If the enabled scan line is not to be reset, the image signal is applied to the pixels on the enabled scan line.

(73) Assignee: **Chi Mei Optoelectronics Corp.**(21) Appl. No.: **11/032,426**(22) Filed: **Jan. 10, 2005**(30) **Foreign Application Priority Data**

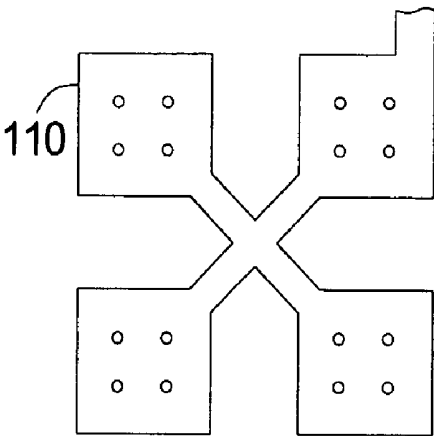


FIG 1A

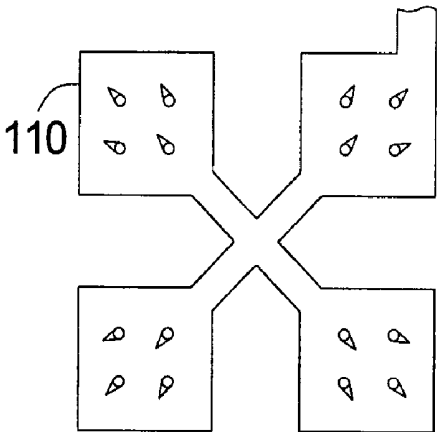


FIG 1B

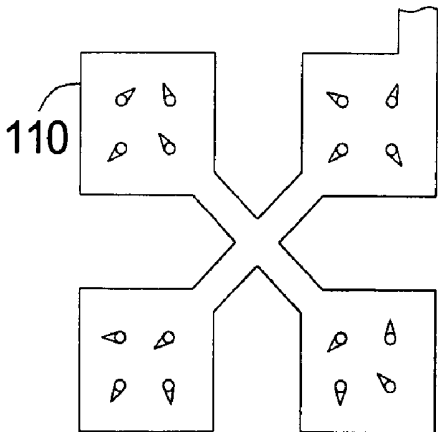


FIG 1C

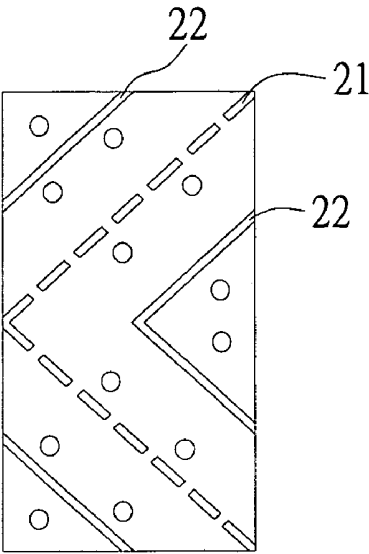


FIG. 2A

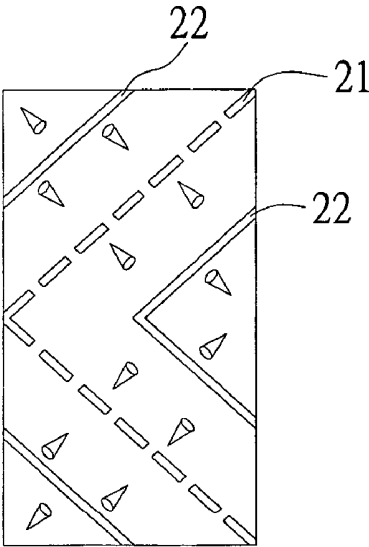


FIG. 2B

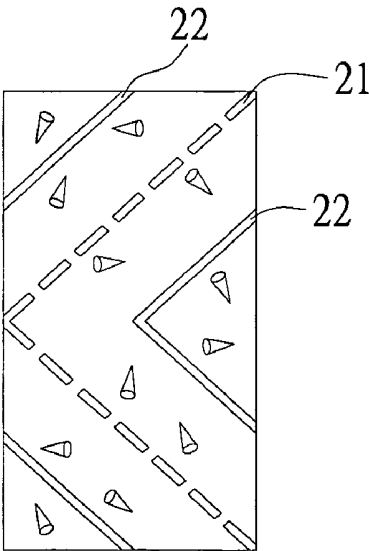


FIG. 2C

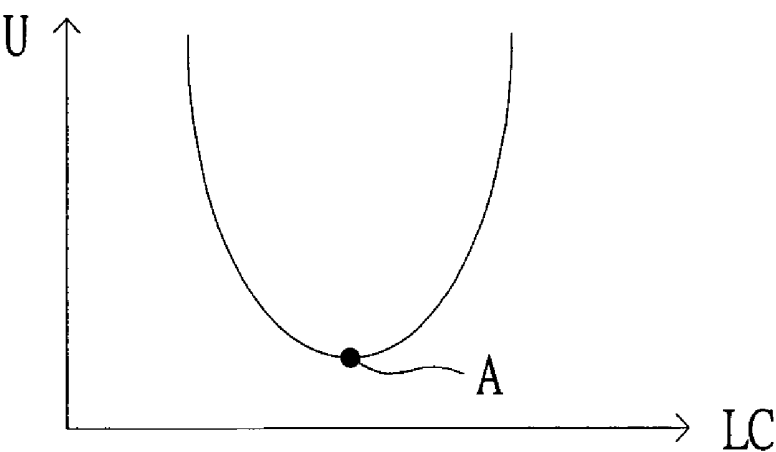


FIG. 3A

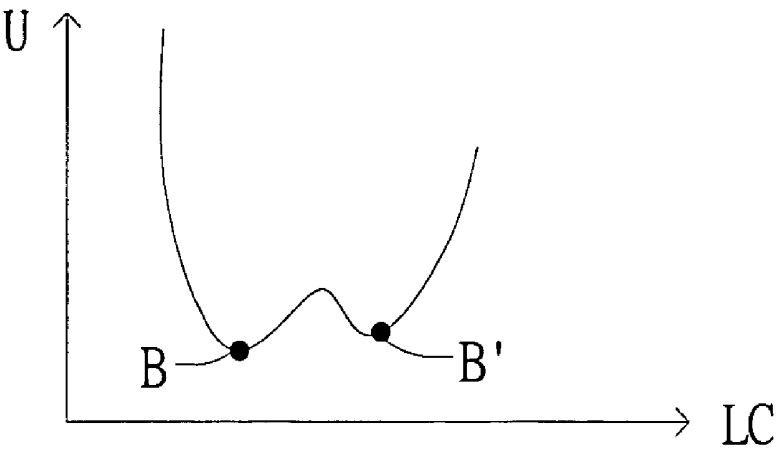


FIG. 3B

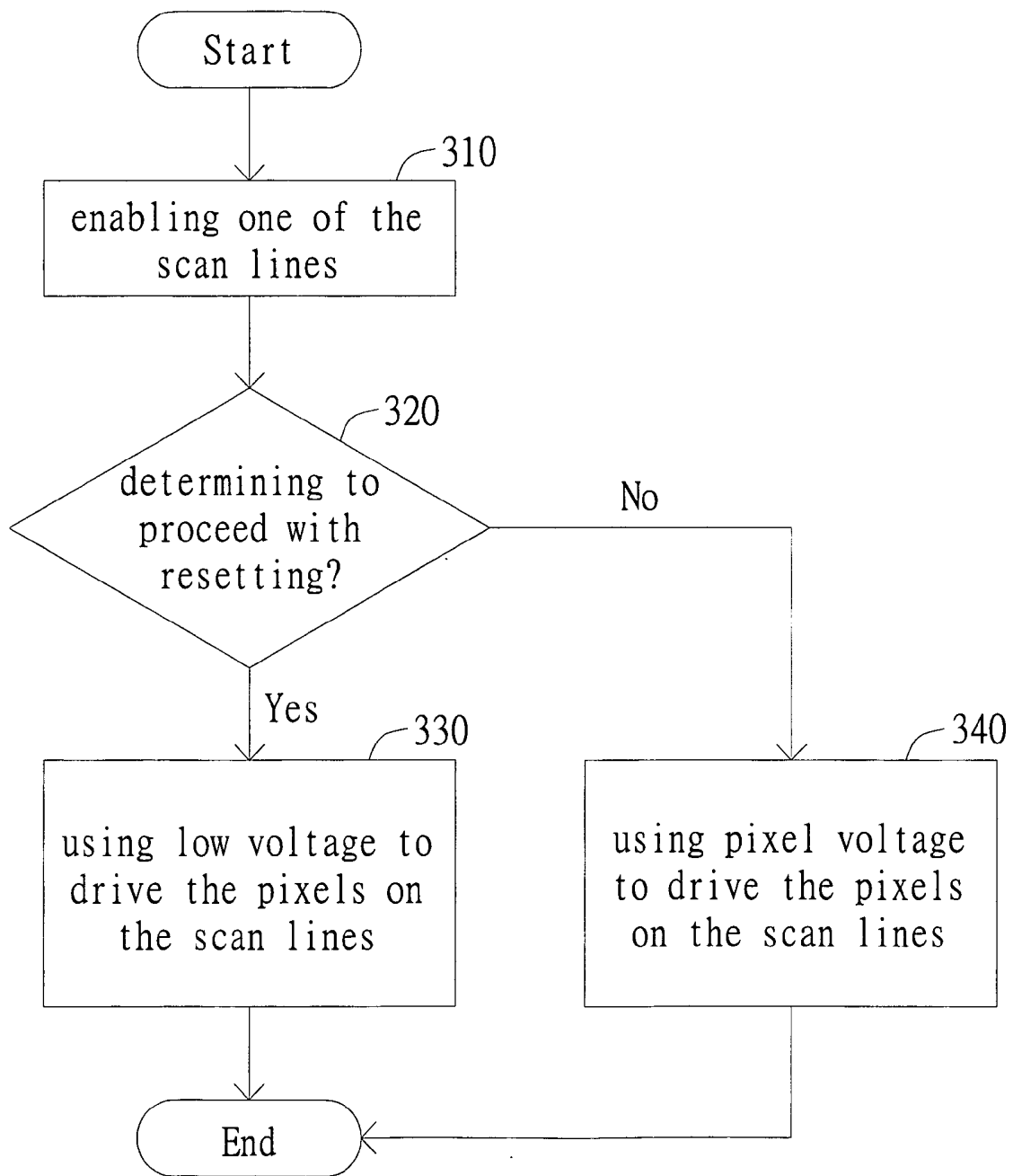


FIG. 4

DRIVING METHOD OF A MULTI-DOMAIN VERTICAL ALIGNMENT LIQUID CRYSTAL DISPLAY

CROSS REFERENCE TO RELATED APPLICATION

[0001] This application claims the benefit of Taiwan application Serial No. 93102247, filed Jan. 30, 2004, the subject matter of which is incorporated herein by reference.

TECHNICAL FIELD

[0002] The invention relates in general to a driving method of a multi-domain vertical alignment (MVA) liquid crystal display (LCD), and more particularly to a driving method for resolving the frame retention problem of an MVA LCD.

BACKGROUND

[0003] Compared with a conventional cathode-ray tube (CRT) display, a flat-panel display has gradually become the main-stream in the display market for lighter and slimmer, high quality frame displays. Of the flat-panel displays, the thin-film-transistor liquid crystal display (TFT LCD) plays an essential role. However, ordinary TFT LCDs have restricted applications due to narrow visual angles.

[0004] With the feature of broader visual angles, the multi-domain vertical alignment (MVA) LCD has become a target for the display industry to achieve.

[0005] FIGS. 1A-1C are top views of an MVA LCD pixel. Transparent electrode 110 of the pixel includes four domains, wherein the four domains are interlinked. FIG. 1A shows a schematic diagram of the pixel before a voltage is applied. The liquid crystal molecules are perpendicular to the plane of the transparent electrode 110, so only one end point of the liquid crystal molecules can be seen in the top view, wherein the end point is denoted by a circular point. FIG. 1B shows a schematic diagram of the pixel after a voltage is applied. When a voltage is applied to the pixel, the liquid crystal molecules tilt toward the center of the four domains, enabling the user to view the screen frame at a broader angle of view.

[0006] FIGS. 2A-2C are top views of another MVA LCD pixel. FIG. 2A is a schematic diagram of the pixel before a voltage is applied, wherein slit 21 is disposed at the lower panel of the LCD while protrusion 22 is disposed at the upper panel of the LCD. FIG. 2B is a schematic diagram of the pixel after a voltage has been applied. When a voltage is applied to the pixel, the liquid crystal molecules tilt according to respective electric field direction and split into multiple domains.

[0007] However, when an external driving force, such as an electric field or the user's touch, is applied to the MVA LCD, mura appears on the display screen. The liquid crystal molecules, having received the external driving force, would not be aligned in accordance with original designed directions. FIG. 1C and FIG. 2C are schematic diagrams showing the pixel having received the external driving force. After receiving the external driving force, the alignment directions of the liquid crystal molecules is disordered, causing the penetration rate of the liquid crystal to change.

When the external force is removed, mura still remains on the screen, leading to the quality defect of frame retention.

SUMMARY

[0008] It is therefore an object of the invention to provide a driving method of multi-domain vertical alignment (MVA) liquid crystal display (LCD) preventing the occurrence of mura effect or frame retention.

[0009] One embodiment of the present invention is directed to a driving method of a MVA LCD. The LCD, which receives an image signal and displays a frame accordingly, includes a plurality of scan lines. The driving method first enables one of the scan lines, then determines whether to proceed with resetting the scan line. If the scan line is reset, a low voltage is applied to the pixels on the scan line. If the scan line is not reset, the image signal is applied to the pixels on the scan line.

[0010] Other objects, features, and advantages of the invention will become apparent from the following detailed description of the preferred but non-limiting embodiments. The following description is made with reference to the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

[0011] FIGS. 1A-1C are top views of an MVA LCD pixel;

[0012] FIGS. 2A-2C are top views of another MVA LCD pixel;

[0013] FIG. 3A shows the relation between free energy U and alignment direction LC before a voltage is applied on the liquid crystal molecules of the pixel;

[0014] FIG. 3B shows the relation between free energy U and alignment direction LC after a voltage is applied on the liquid crystal molecules of the pixel; and

[0015] FIG. 4 is a flowchart for a driving method of an MVA LCD according to a preferred embodiment of the invention.

DETAILED DESCRIPTION OF THE INVENTION

[0016] When a multi-domain vertical alignment (MVA) liquid crystal display (LCD) receives an external force, for example, touched by a user, mura appears on the screen. FIG. 3A shows the relation between free energy U and alignment direction LC before a voltage is applied on the liquid crystal molecules of the pixel. When in a steady state, the liquid crystal molecules are at the lowest point of free energy as shown in point A. The alignment directions of the liquid crystal molecules are then perpendicular to the plane of the transparent electrode. FIG. 3B shows the relation between free energy U and alignment direction LC after a voltage, 6 volts for instance, is applied on the liquid crystal molecules of the pixel. The liquid crystal molecules now have a plurality of steady states, which are exemplified by two steady states points, B and B'. After receiving a voltage, the liquid crystal molecules are at the steady state point B. After receiving an external force with the voltage applied, the steady state of the liquid crystal molecules probably would shift to point B' such that the alignment directions are changed. When the external force is removed, the steady state of the liquid crystal molecules remains at point B'. This

would change the penetration rate of the pixel and mura would appear on the force-applying part of the screen.

[0017] If a low voltage, 0 volts for instance, is applied to the liquid crystal molecules which are at the steady state point B' and followed by a pixel voltage, 6 volts for instance, then the liquid crystal molecules will return to the steady state of point B. The mura effect caused by the external force would be eliminated, and the screen would return to the normal status. The driving method of the embodiment eliminates the mura effect from the screen according to the above mentioned reset principle.

[0018] A conventional liquid crystal screen receives an image signal and displays a frame according to the image signal received. The driving method thereof is to enable a scan line of the liquid crystal screen, and then apply the pixel voltages, generated in response to the image signals, onto the pixels on the enabled scan line via data lines. Each of the scan lines is enabled once sequentially to complete a frame.

[0019] Referring to FIG. 4, a flowchart for a driving method of an MVA LCD according to a preferred embodiment of the invention is shown. First the driving method enables one scan line on the liquid crystal screen (step 310). Next, it determines whether to proceed to a resetting process (step 320). If the scan line is to be reset, a low voltage is applied to the pixels on the enabled scan line (step 330). If the scan line is not to be reset, pixel voltages are applied to the pixels on the enabled scan line (step 340).

[0020] In resetting step 330, after a low voltage, 0 volts for instance, is applied onto the pixel, the liquid crystal molecules return to the initial steady state illustrated in the steady state of point A in FIG. 3A. When the scan line is enabled again at the next frame, the liquid crystal molecules are able to achieve the proper steady state with the pixel voltage applied. Any mura caused by a previously exerted but now removed external force would be removed after the resetting process. The time required for the resetting process is very short and is not easily perceived by the human eye so the image quality is not affected.

[0021] The mura can be eliminated in 2 seconds if each of the scan lines is reset within 2 seconds. For a liquid crystal screen with a 60 Hz refresh rate, 60 frames are displayed per second. One way to reset all the scan lines in 2 seconds is to insert a resetting frame formed by the low voltage into any of the 120 frames within the 2 seconds.

[0022] Another way to reset all the scan lines in 2 seconds is to reset some of the scan lines each frame with all the scan lines being reset after 120 frames are displayed. For example, the scan lines are divided into a normal group and a reset group, wherein the scan lines of the normal group are driven by the original image signal, while the scan lines of the reset group are driven by the low voltage. With a liquid crystal screen having 1024 scan lines, by resetting 9 different scan lines ($1024/(60 \times 2) \approx 9$) each frame, the mura can be eliminated in 2 seconds. For each frame, 9 scan lines belong to the reset group while the other 1015 scan lines belong to the normal group.

[0023] The above embodiment uses 0 volts as the low voltage to reset the scan lines. But in practice the low voltage may not need to be as low as 0 volts to reset the scan lines. The maximum resetting low voltage required is different depending on the grey value of the pixel. A reference table

can be created showing the maximum resetting low voltage for each grey value by experimentation. During the driving process, the value of the low voltage required for resetting can be determined by the reference table and the image signal.

[0024] The driving method of MVA LCD disclosed in the above preferred embodiment eliminates the mura formed due to an external force to provide a satisfying high quality LCD.

[0025] While the invention has been described by way of example and in terms of a preferred embodiment, it is to be understood that the invention is not limited thereto. On the contrary, it is intended to cover various modifications and similar arrangements and procedures, and the scope of the appended claims therefore should be accorded the broadest interpretation so as to encompass all such modifications and similar arrangements and procedures.

What is claimed is:

1. A driving method of multi-domain vertical alignment (MVA) liquid crystal display (LCD), the LCD including a plurality of scan lines and receiving an image signal so as to display a frame, the driving method comprising:

enabling one of the scan lines;

determining whether to proceed with resetting the enabled scan line;

driving the pixels on the enabled scan line by a low voltage, if the enabled scan line is to be reset; and

driving the pixels on the enabled scan line in response to the image signal, if the enabled scan line is not to be reset.

2. The driving method according to claim 1, wherein the LCD displays a plurality of frames and one of the frames is driven by the low voltage.

3. The driving method according to claim 1, wherein the LCD receives a plurality of frames and the pixels on a part of the scan lines on each frame are driven by the low voltage.

4. The driving method according to claim 1, wherein the low voltage is generated in response to the image signal.

5. A driving method of an MVA LCD, the LCD including a plurality of scan lines and receiving an image signal so as to display a frame, the driving method comprising:

determining whether to reset the frame or not;

displaying the frame by a low voltage, if the frame is to be reset; and

displaying the frame in response to the image signal, if the frame is not be reset.

6. A driving method of an MVA LCD, the LCD including a plurality of scan lines and receiving an image signal so as to display a frame driving method comprising:

dividing the scan lines into a normal group and a reset group;

sequentially enabling the scan lines;

driving the pixels on the enabled scan line in response to the image signal, if the enabled scan line belongs to the normal group; and

driving the pixels on the enabled scan line by a low voltage, if the enabled scan line belongs to the reset group.

7. The driving method according to claim 6, wherein the low voltage is generated in response to the image signal.

* * * * *

专利名称(译)	多域垂直配向液晶显示器的驱动方法		
公开(公告)号	US20050168434A1	公开(公告)日	2005-08-04
申请号	US11/032426	申请日	2005-01-10
[标]申请(专利权)人(译)	群创光电股份有限公司		
申请(专利权)人(译)	奇美电子股份有限公司.		
当前申请(专利权)人(译)	群创光电		
[标]发明人	WANG YANG LI		
发明人	WANG-YANG, LI		
IPC分类号	G02F1/1337 G02F1/133 G09G3/20 G09G3/36		
CPC分类号	G09G3/207 G09G3/3637 G09G2310/062 G09G3/3681 G09G3/3644		
优先权	093102247 2004-01-30 TW		
其他公开文献	US7298353		
外部链接	Espacenet USPTO		

摘要(译)

一种多域垂直配向 (MVA) 液晶显示器 (LCD) 的驱动方法。接收图像信号并根据接收的图像信号显示帧的LCD包括多条扫描线。根据本发明的驱动方法最初启用扫描线之一，然后确定是否继续重置启用的扫描线。如果要复位启用的扫描线，则将低电压施加到启用的扫描线上的像素。如果不重置启用的扫描线，则将图像信号施加到启用的扫描线上的像素。

