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(19) **United States**(12) **Patent Application Publication** (10) **Pub. No.: US 2008/0036721 A1****Kim et al.**(43) **Pub. Date: Feb. 14, 2008**(54) **LIQUID CRYSTAL DISPLAY DEVICE AND DRIVING METHOD THEREOF**(30) **Foreign Application Priority Data**

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(76) Inventors: **Binn Kim**, Seoul (KR); **Nam Wook Cho**, Gunpo-si (KR); **Sung Hak Jo**, Anyang-si (KR)**Publication Classification**(51) **Int. Cl.**
G09G 3/36 (2006.01)(52) **U.S. Cl.** **345/96; 345/87**(57) **ABSTRACT**

A liquid crystal display (LCD) device includes a data drive circuit that inverts polarities of data every $2k$ horizontal periods (k is an integer not less than 2); and an LCD panel including a plurality of data lines and a plurality of gate lines crossing each other to define a plurality of pixels, each pixel including a switching device, wherein source electrodes of the switching devices in the pixels arranged in a vertical direction are connected to two different data lines.

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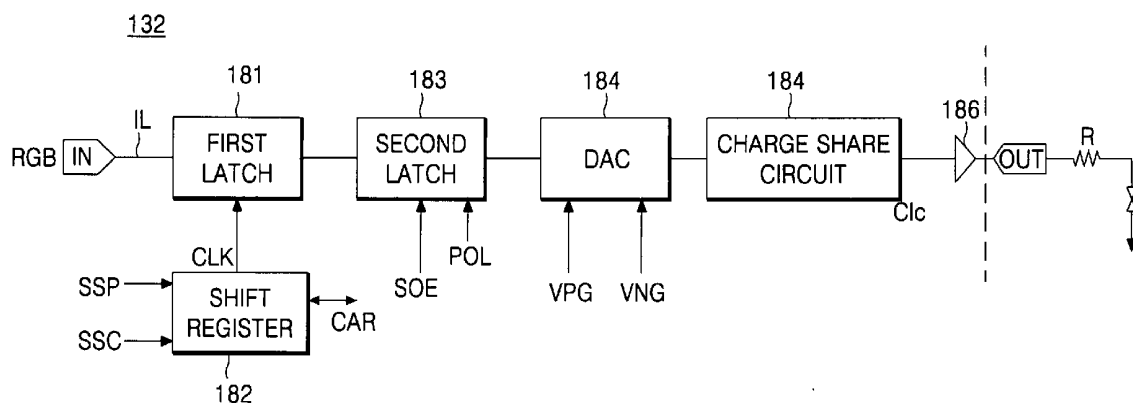
(21) Appl. No.: **11/635,541**(22) Filed: **Dec. 8, 2006**

FIG. 1
RELATED ART

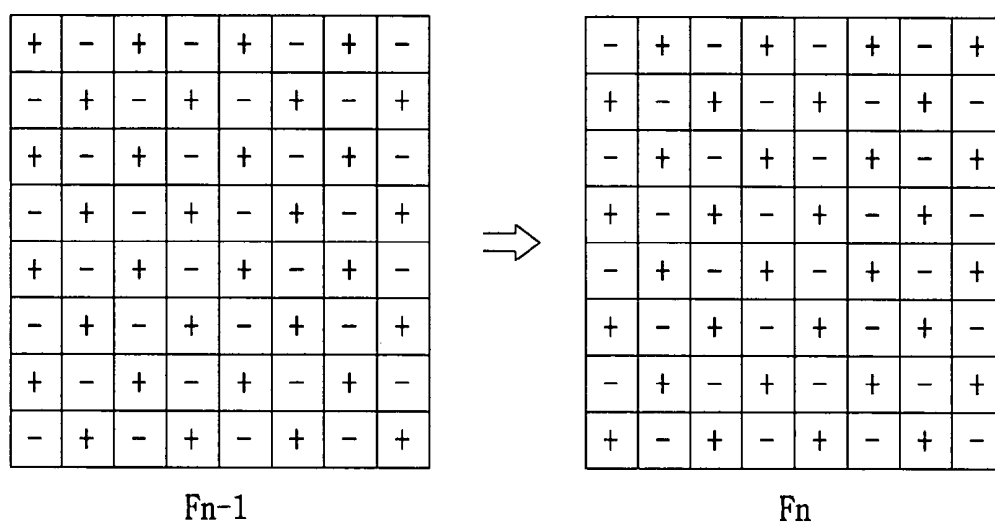


FIG. 2
RELATED ART

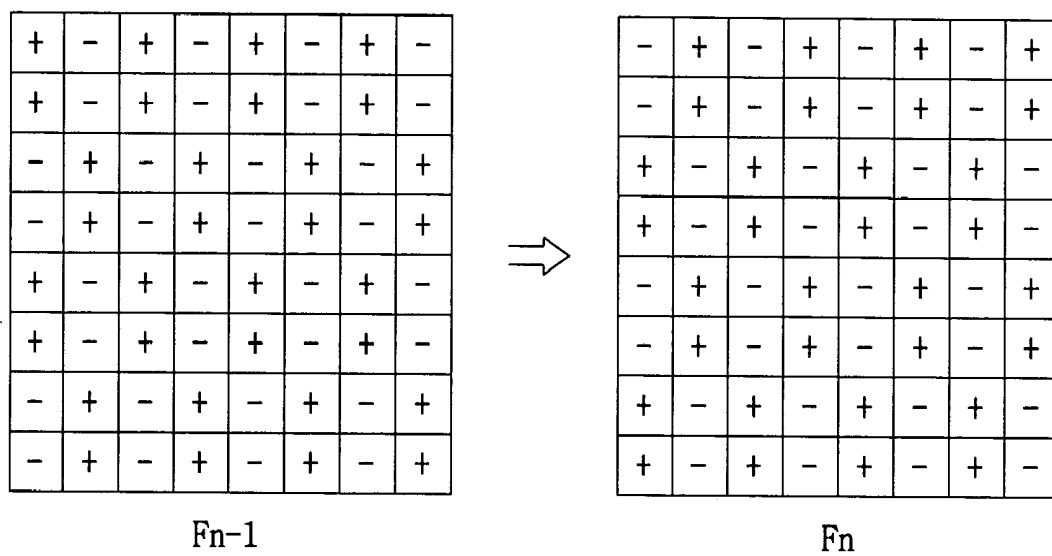


FIG. 3
RELATED ART

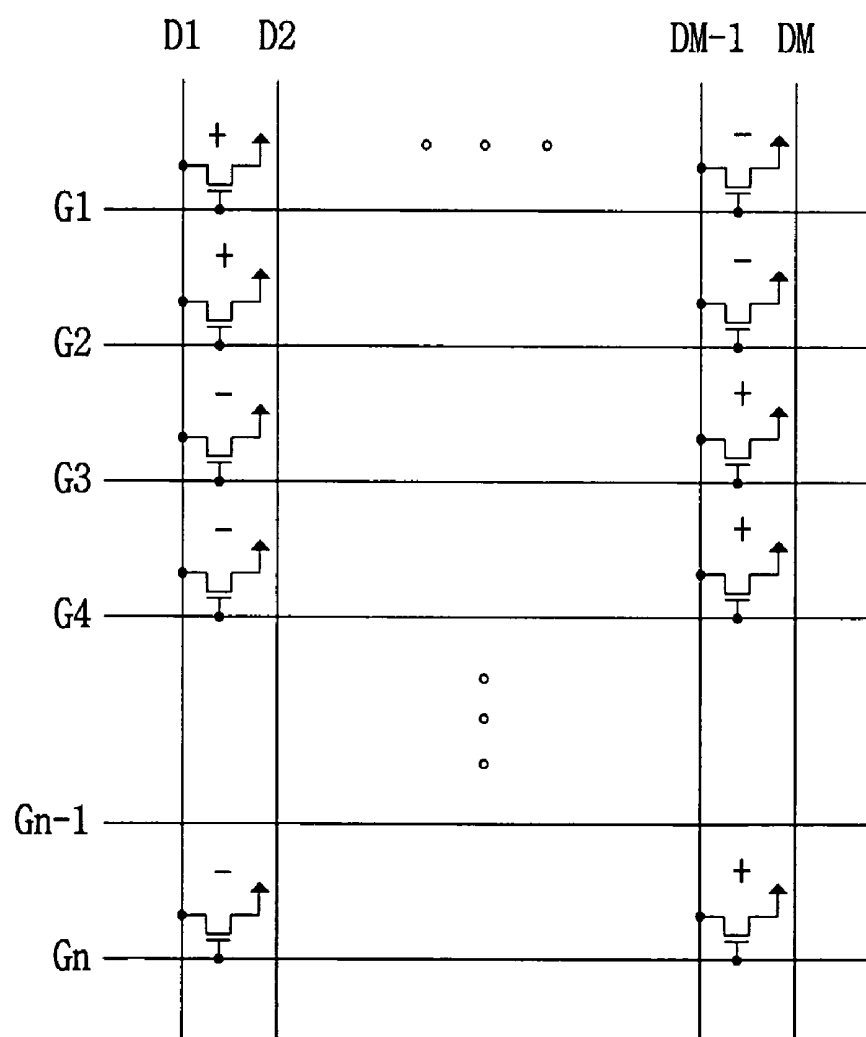


FIG. 4
RELATED ART

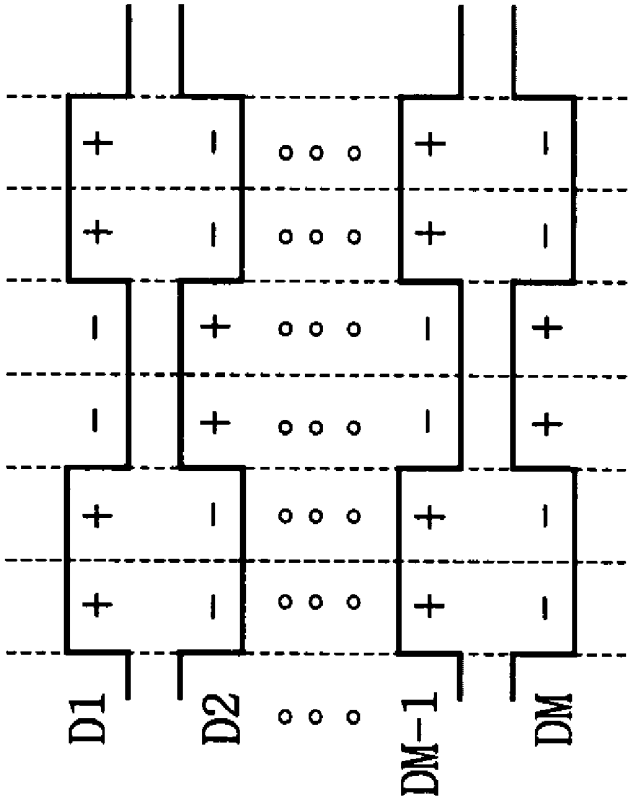


FIG. 5
RELATED ART

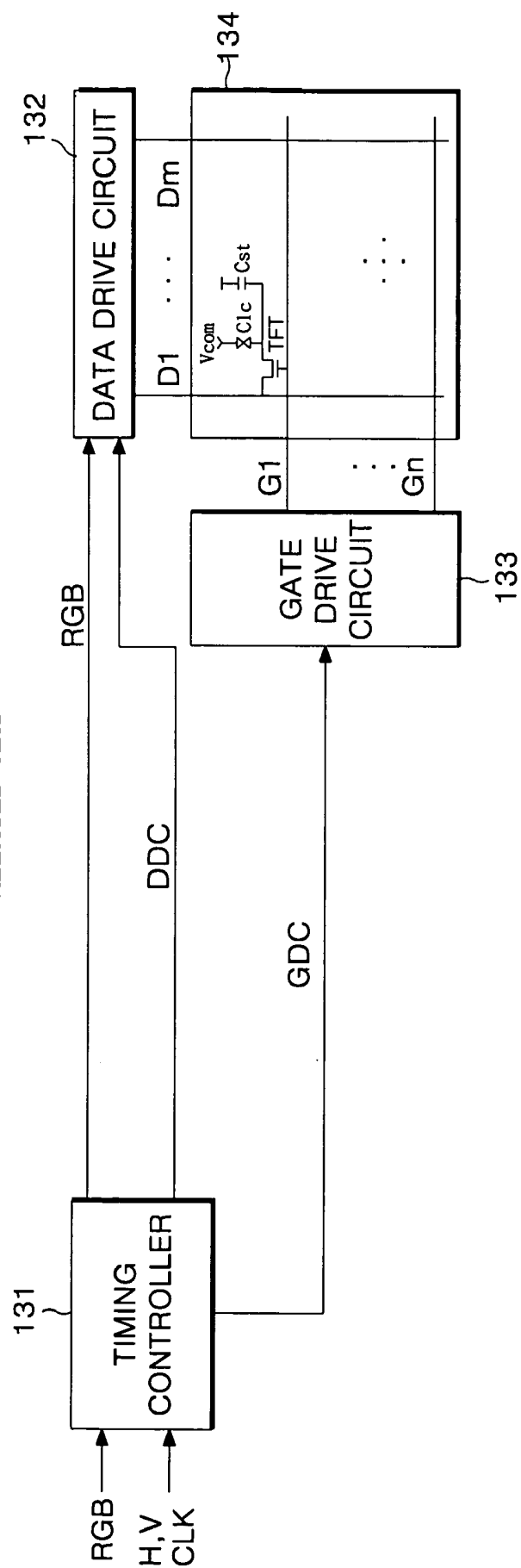


FIG. 6

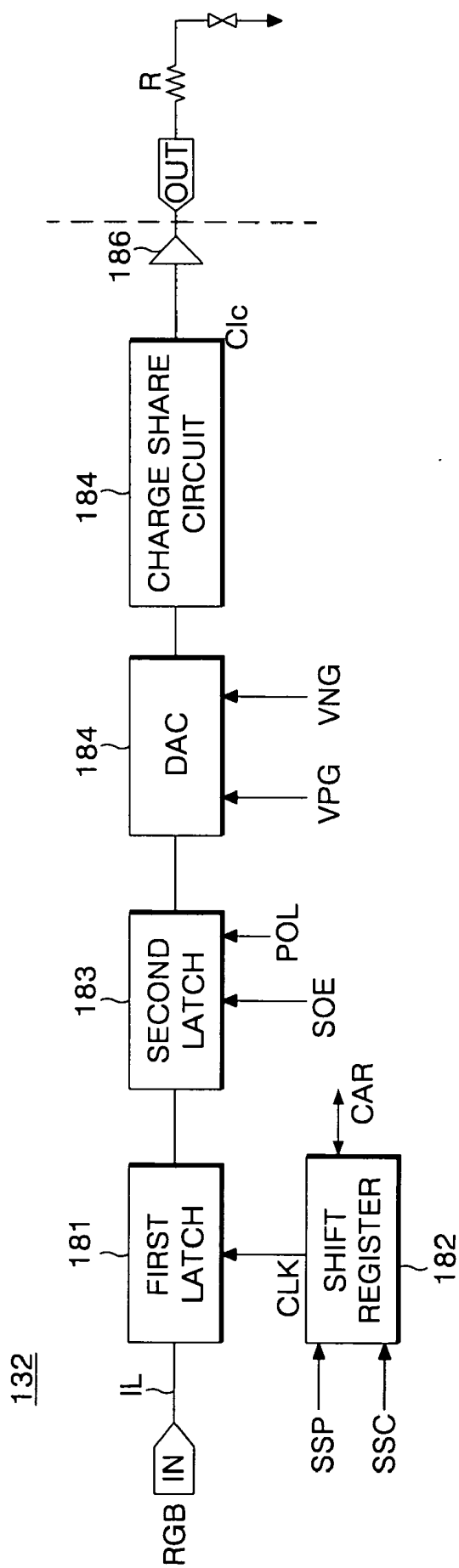


FIG. 7

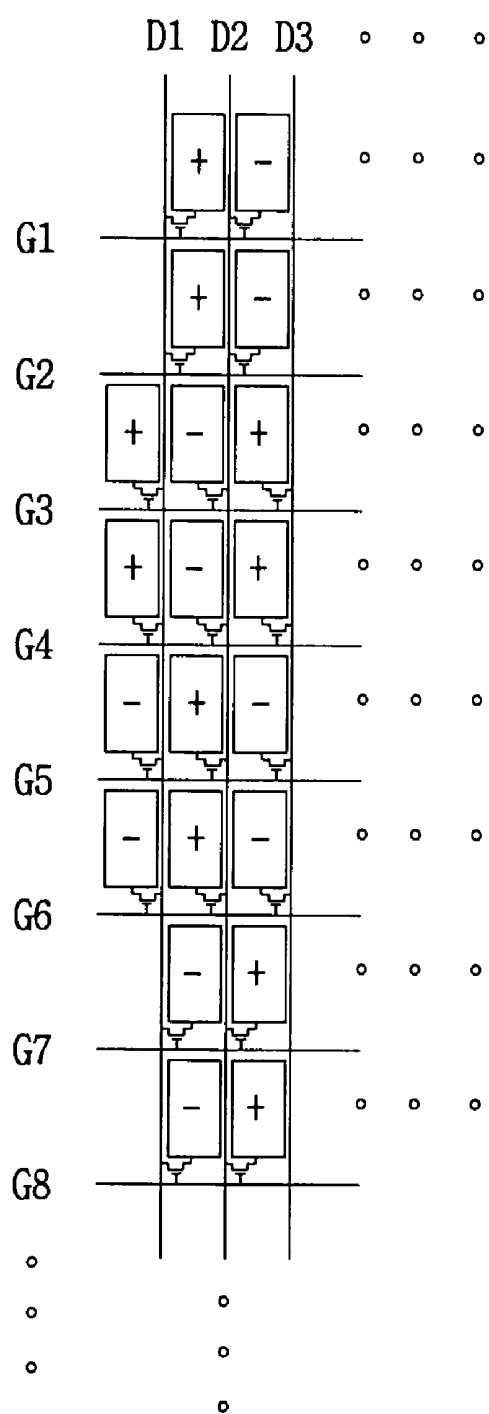


FIG. 8

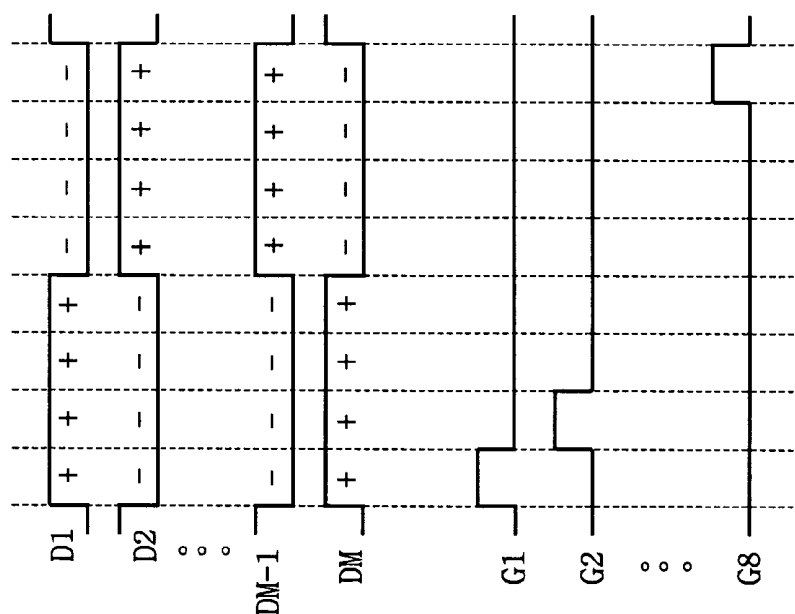


FIG. 9

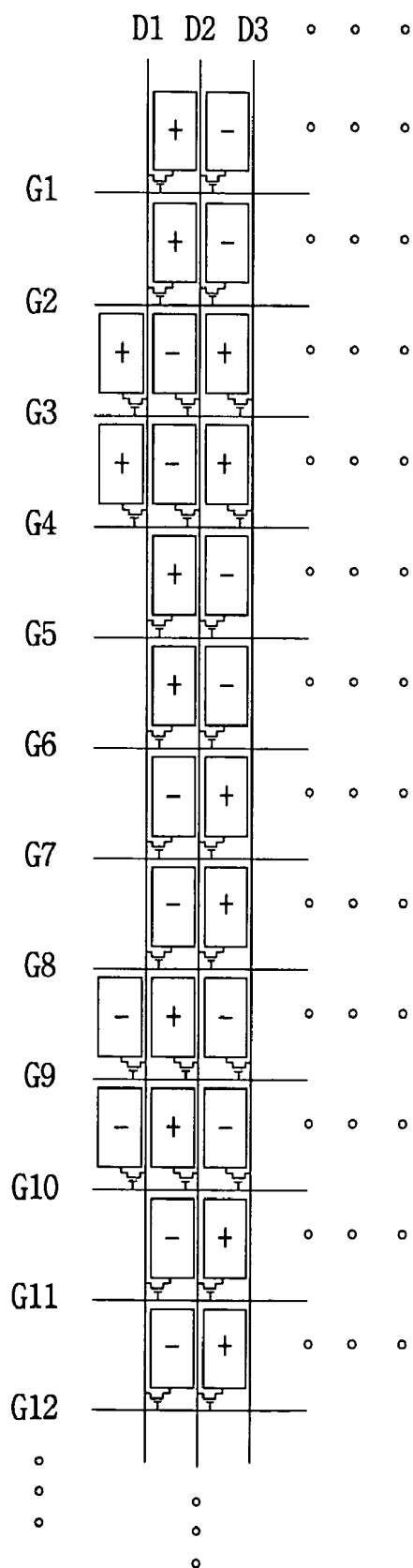


FIG. 10

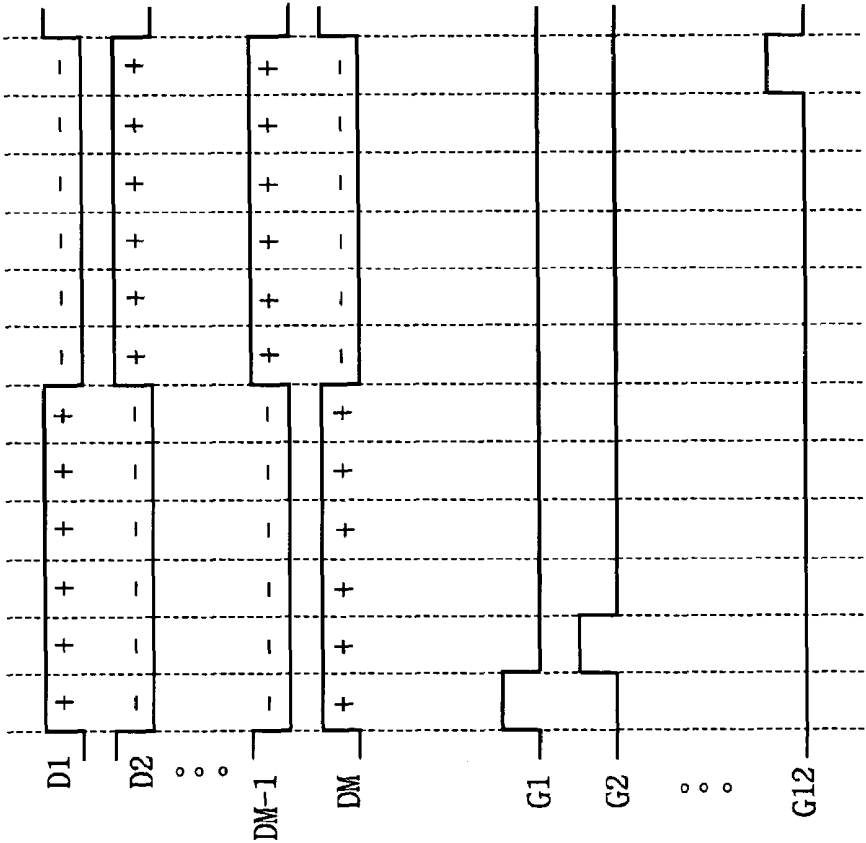


FIG. 11

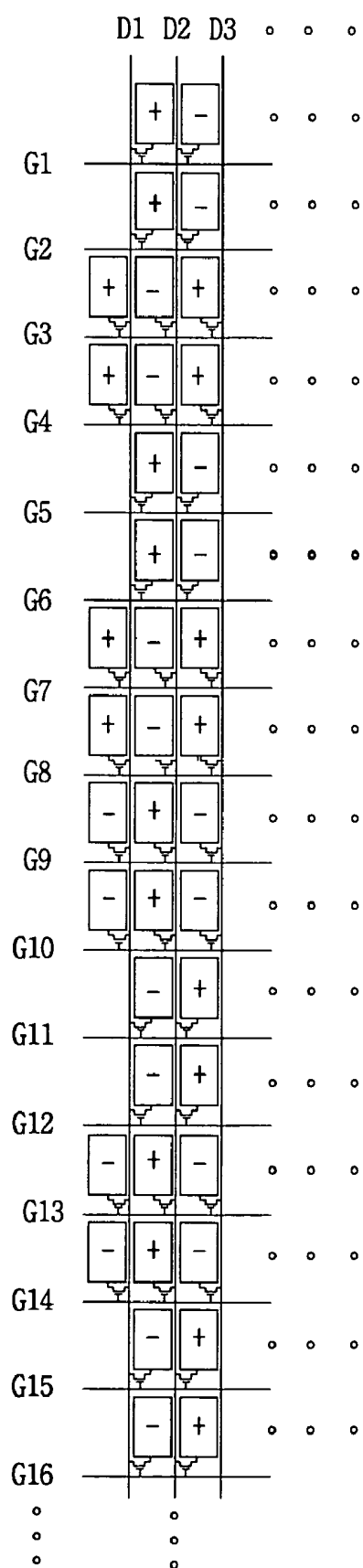
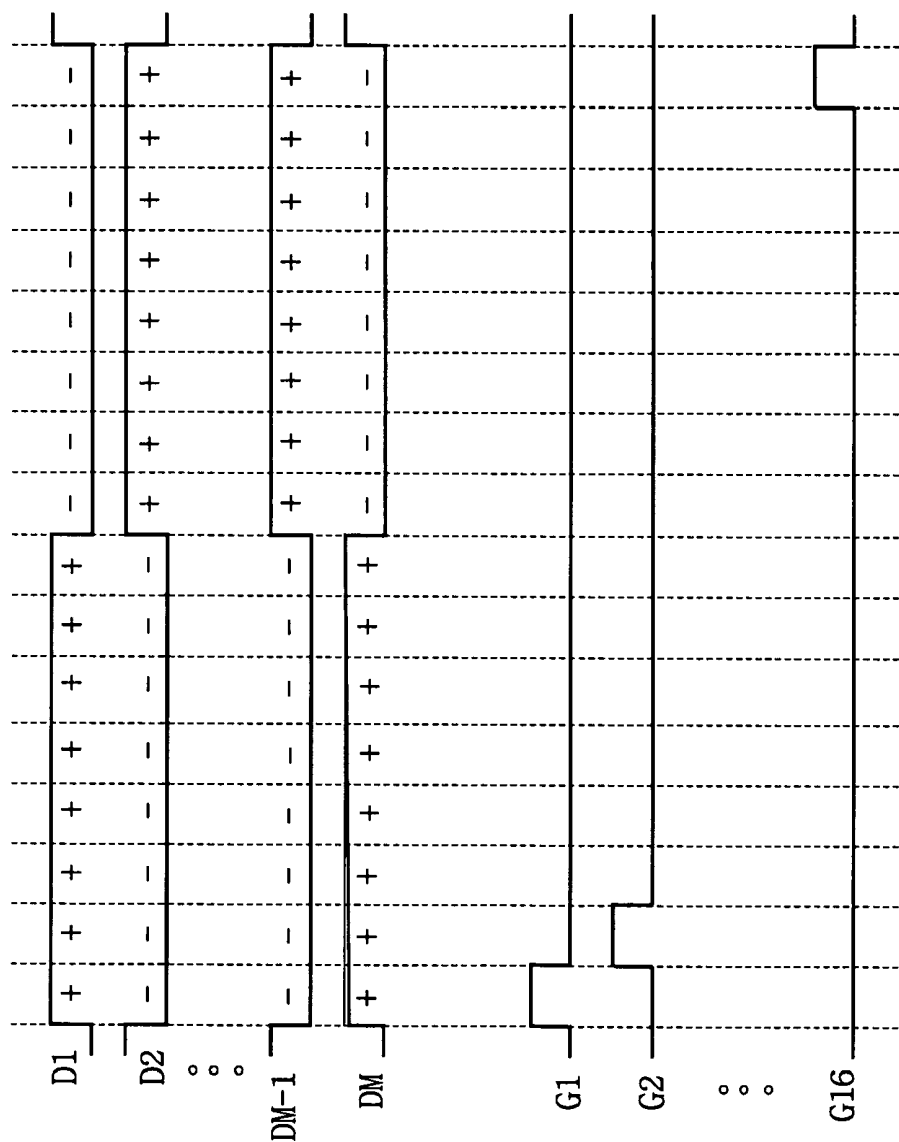


FIG. 12



LIQUID CRYSTAL DISPLAY DEVICE AND DRIVING METHOD THEREOF

[0001] This application claims the benefit of Korean Patent Application No. P2006-0039330, filed on May 1, 2006, which is hereby incorporated by reference for all purposes as if fully set forth herein.

BACKGROUND OF THE INVENTION

[0002] 1. Field of the Invention

[0003] The present invention relates to a liquid crystal display device, and more particularly to a liquid crystal display device and method for driving the same that can reduce power consumption.

[0004] 2. Discussion of the Related Art

[0005] Liquid crystal display (LCD) devices control the light transmittance of pixels in accordance with video signals to display images. Active matrix type LCD devices are advantageous in displaying moving images because of a switching device in each pixel. Thin film transistors (hereinafter, referred to as "TFTs") are mainly used for the switching device.

[0006] LCD devices periodically invert the polarity of data charged in pixels to reduce flickers and residual image defects. The inversion method is classified into a line inversion method in which the polarity of one horizontal line of pixels is opposite to the polarity of neighboring line of pixels, a column inversion method in which the polarity of one vertical line of pixels is opposite to the polarity of neighboring line of pixels and a dot inversion method in which the polarity of a pixel is opposite to the polarity of neighboring pixels in horizontal and vertical line directions.

[0007] FIG. 1 is a schematic view of a plurality of pixels illustrating the dot inversion method according to the related art.

[0008] Referring to FIG. 1, in the dot inversion method, the polarity of data charged in a pixel is opposite to the polarity of data charged in neighboring pixels in horizontal and vertical line directions. Also, the polarities of the data of the pixels are inverted for each frame F_n-1 , F_n .

[0009] In order to reduce flickers in both the vertical and horizontal directions and to reduce power consumption, a two dot inversion method is recently suggested and widely used for LCD devices. FIGS. 2 and 3 are schematic views illustrating the two dot inversion method according to the related art. FIG. 4 shows the polarities of data applied to the data lines in the two dot inversion method according to the related art.

[0010] Referring to FIGS. 2 to 4, in the two dot inversion method, two pixels neighboring in the vertical direction is treated as a unit of pixels so that the polarity of data charged in two pixels that are neighboring in the vertical direction is opposite to the polarity of data charged in neighboring units of pixels in horizontal and vertical line directions. As in the dot inversion method, the polarities of the data charged in the pixels are also inverted for each frame F_n-1 , F_n .

[0011] Recently, as the resolution of LCD devices becomes higher, the number of pixels connected to a one horizontal line is increasing. As a result, even with the two

dot inversion method, the power consumption of the LCD device is high due to the high driving frequency.

SUMMARY OF THE INVENTION

[0012] Accordingly, the present invention is directed to an LCD device and method for driving the same that substantially obviates one or more problems due to limitations and disadvantages of the related art.

[0013] An advantage of the present invention is to provide an LCD device and method for driving the same that reduces power consumption

[0014] Additional features and advantages of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the invention. These and other advantages of the invention will be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

[0015] To achieve these and other advantages and in accordance with the purpose of the invention, as embodied and broadly described herein, a liquid crystal display (LCD) device includes a data drive circuit that inverts polarities of data every $2k$ horizontal periods (k is an integer not less than 2); and an LCD panel including a plurality of data lines and a plurality of gate lines crossing each other to define a plurality of pixels, each pixel including a switching device, wherein source electrodes of the switching devices in the pixels arranged in a vertical direction are connected to two different data lines.

[0016] In another aspect of the present application, a driving method of a liquid crystal display (LCD) device includes providing an LCD panel wherein a plurality of data lines and a plurality of gate lines cross each other to define a plurality of pixels, each pixel includes a switching device, and source electrodes of the switching devices in the pixels arranged in a vertical direction are connected to two different data lines; and inverting polarities of data every $2k$ horizontal periods (k is an integer not less than 2) and applying the inverted data to one of the data lines.

[0017] In yet another aspect of the present application, A driving method of a display device includes providing a display panel wherein a plurality of data lines and a plurality of gate lines cross each other to define a plurality of pixels, each pixel includes a switching device, and source electrodes of the switching devices in the pixels arranged in a vertical direction are connected to more than two different data lines; and inverting polarities of data corresponding to at least four horizontal periods in each frame and applying the inverted data to one of the data lines.

[0018] It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE DRAWINGS

[0019] The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate embodiments of the invention and together with the description serve to explain the principles of the invention.

[0020] In the drawings:

[0021] FIG. 1 is a schematic view of a plurality of pixels illustrating the dot inversion method according to the related art;

[0022] FIGS. 2 and 3 are schematic views illustrating the two dot inversion method according to the related art;

[0023] FIG. 4 shows the polarities of data applied to the data lines in the two dot inversion method according to the related art;

[0024] FIG. 5 is a block diagram illustrating an LCD device according to the present invention;

[0025] FIG. 6 is a block diagram illustrating the data drive circuit 132 in FIG. 5;

[0026] FIG. 7 is a schematic view illustrating the polarities of data applied to an LCD device according to the first embodiment of the present invention;

[0027] FIG. 8 shows the polarities of data applied to the data lines from a data drive circuit according to the first embodiment of the present invention;

[0028] FIG. 9 is a schematic view illustrating the polarities of data applied to an LCD device according to the second embodiment of the present invention;

[0029] FIG. 10 shows the polarities of data applied to the data lines from a data drive circuit according to the second embodiment of the present invention;

[0030] FIG. 11 is a schematic view illustrating the polarities of data applied to an LCD device according to the third embodiment of the present invention; and

[0031] FIG. 12 shows the polarities of data applied to the data lines from a data drive circuit according to the third embodiment of the present invention.

DETAILED DESCRIPTION OF THE ILLUSTRATED EMBODIMENT

[0032] Reference will now be made in detail to embodiments of the present invention, examples of which are illustrated in the accompanying drawings.

[0033] FIG. 5 is a block diagram illustrating a liquid crystal display (LCD) device according to the present invention.

[0034] Referring to FIG. 5, the LCD device includes an LCD panel 134 where data lines D1 to Dm and gate lines G1 to Gn cross each other and a TFT is formed adjacent to each crossing of the gate and data lines for driving a pixel Clc, a data drive circuit 132 for supplying data to the data lines D1 to Dm, a gate drive circuit 133 for supplying scan pulses to the gate lines G1 to Gn, and a timing controller 131 for controlling the data drive circuit 132 and the gate drive circuit 133.

[0035] The LCD panel 134 has a liquid crystal between two glass substrates and the data lines D1 to Dm and the gate lines G1 to Gn cross each other on the lower glass substrate. The TFT located adjacent to each crossing of the data line D1 to Dm and the gate line G1 to Gn supplies data of the data line D1 to Dm to the pixel Clc in response to the scan pulse from the gate line G1 to Gn.

[0036] To this end, a gate electrode of the TFT is connected to the gate line G1 to Gn and a source electrode of the TFT is connected to the data line D1 to Dm. A drain electrode of the TFT is connected to a pixel electrode of the pixel Clc. A common voltage Vcom is supplied to a common electrode that forms an electric field together with the pixel electrode to control the light transmittance of the pixel Clc.

[0037] In the related art LCD device, the source electrodes of the TFTs in a vertical line are all connected to a single data line. In the present invention, a predetermined number of the source electrodes in a vertical line direction are divided to be selectively connected to two adjacent data lines. A construction of an LCD device and method for driving the same according to the present invention will now be described in more detail.

[0038] The timing controller 131 generates a gate control signal GDC for controlling the gate drive circuit 133 and a data control signal DDC for controlling the data drive circuit 132 using vertical/horizontal synchronization signals V, H and a clock CLK. The data control signal DDC includes a source start pulse SSP, a source shift clock SSC, a source output signal SOE, a polarity signal POL, etc. The gate control signal GDC includes a gate shift clock GSC, a gate output signal GOE, a gate start pulse GSP, etc.

[0039] In the LCD device according to the present invention, the alignment and polarities of data RGB supplied to the data lines D1 to Dm are modulated by the timing controller 131 and the data drive circuit 132. The timing controller 131 aligns data RGB inputted from an image source in accordance with the configuration between the data lines D1 to Dm and the source electrodes of the TFTs using a data alignment circuit (not shown) and supplies the data RGB to the data drive circuit 132. The data driver circuit modulates the data RGB in accordance with the polarity control signal POL and the control data DDC outputted from the timing controller 131 and supplies the modulated data RGB to the data lines D1 to Dm.

[0040] The gate drive circuit 133 sequentially supplies the scan pulse to the gate lines G1 to Gn in response to the gate control signal GDC outputted from the timing controller 131.

[0041] FIG. 6 is a block diagram illustrating the data drive circuit 132 in FIG. 5.

[0042] Referring to FIG. 6, the data drive circuit 132 includes a plurality of integrated circuits IC and each IC includes a shift register 182, a first latch 181, a second latch 183, a digital to analog converter 184 (hereinafter, referred to as "DAC"), a charge share circuit 185 and a buffer 186.

[0043] The shift register 182 generates a sampling signal by shifting the source start pulse SSP generated from the timing controller 131 in accordance with the source shift clock signal SSC. Further, the shift register 182 shifts the source start pulse SSP and transmits a carry signal CAR to the shift register 182 of the next stage.

[0044] The first latch 181 samples and stores the digital data RGB in accordance with the sampling signal inputted from the shift register 182. The second latch 183 latches the digital data RGB inputted from the first latch 181 and then simultaneously outputs the digital data RGB corresponding to one horizontal line to the DAC 184 in response to the source output signal SOE.

[0045] The DAC 184 converts the digital data RGB outputted from the second latch 183 into analog data RGB using a positive analog gamma voltage VPG or a negative analog gamma voltage VNG in accordance with the polarity signal POL. According to the present invention, the polarities of the analog data RGB applied to the data line D1 to Dm from the data drive circuit 132 are inverted every 2k horizontal periods (k is an integer not less than 2). Also, the polarities

of the analog data RGB applied to the data line D1 to Dm that are adjacent each other in a horizontal direction are inverted.

[0046] The charge share circuit 185 supplies a charge share voltage to the data line D1 to Dm through a buffer 186 for a high logic period of the source output signal SOE generated from the timing controller 131. The buffer 186 acts to output the analog data RGB inputted from the DAC 184 to the data line D1 to Dm without the analog data RGB signal being attenuated. The reference numeral 'R' in FIG. 6 is a line resistance between the data line D1 to Dm and the output terminal of the data drive circuit 132.

[0047] FIG. 7 is a schematic view illustrating the polarities of data an LCD device according to the first embodiment of the present invention, and FIG. 8 shows the polarities of data applied to the data lines from a data drive circuit according to the first embodiment of the present invention.

[0048] Referring to FIG. 7, the LCD device according to the first embodiment of the present invention has an effective pixel group formed in an effective display area and a dummy pixel group formed outside the effective display area.

[0049] Dummy pixels outside the effective display area do not contribute to displaying images even though data are supplied. The source electrodes of the dummy pixels outside the effective display area corresponding to $(8n-5)^{th}$, $(8n-4)^{th}$, $(8n-3)^{th}$ and $(8n-2)^{th}$ (n is an integer) horizontal lines are connected to the first data line D1 and the source electrodes of the effective pixels corresponding to $(8n-5)^{th}$, $(8n-4)^{th}$, $(8n-3)^{th}$ and $(8n-2)^{th}$ (n is an integer) horizontal lines are connected to the data lines starting from the data line D2.

[0050] For example, the source electrodes of the dummy pixels outside the effective display area corresponding to the third to sixth horizontal lines HL3 to HL6 are connected to the first data line D1 and the source electrodes of the effective pixels corresponding to the third to sixth horizontal lines HL3 to HL6 are connected to the data lines starting from the data line D2.

[0051] Referring to FIG. 8, the polarities of data applied to the data line D1 to Dm from the data drive circuit 132 are inverted every four horizontal periods and the polarities of data applied to the odd number data lines are opposite to the polarities of data applied to the even number data lines. However, because of the configuration between the source electrodes of the TFTs and the data lines D1 to Dm, the polarities of the effective pixels are inverted every two horizontal periods.

[0052] The data on the first data line D1 are supplied to the dummy pixels corresponding to the third to sixth horizontal lines HL3 to HL6 and the data on the second data line D2 are supplied to the first effective pixels corresponding to the third to sixth horizontal lines HL3 to HL6. The data on the first data line D1 are also supplied to the first effective pixels corresponding to the first, second, seventh and eighth horizontal lines HL1, HL2, HL7, HL8. Because the dummy pixels are not formed in outside the effective display area of the first, second, seventh and eighth horizontal lines HL1, HL2, HL7, HL8, the data on the first data line D1 are supplied to the first effective pixels corresponding to the first, second, seventh and eighth horizontal lines HL1, HL2, HL7, HL8 through the first data line D1.

[0053] As a result, the power consumption of the LCD device can be reduced by decreasing the driving frequency of the data drive circuit 132 to $\frac{1}{2}$ without compromising display quality.

[0054] FIG. 9 is a schematic view illustrating the polarities of data applied to an LCD device according to the second embodiment of the present invention. FIG. 10 shows the polarities of data applied to the data lines from a data drive circuit according to the second embodiment of the present invention.

[0055] Referring to FIG. 9, the LCD device according to the second embodiment of the present invention has an effective pixel group formed in an effective display area and a dummy pixel group formed outside the effective display area.

[0056] The source electrodes of dummy pixels outside the effective display area corresponding to $(6n-3)^{th}$ and $(6n-2)^{th}$ (n is an integer) horizontal lines are connected to the first data line D1 and the source electrodes of the effective pixels corresponding to $(6n-3)^{th}$ and $(6n-2)^{th}$ (n is an integer) horizontal lines are connected to the data lines starting from the data line D2.

[0057] For example, the source electrodes of the dummy pixels outside the effective display area corresponding to the third, fourth, ninth and tenth horizontal lines HL3, HL4, HL9, HL10 are connected to the first data line D1 and the source electrodes of the effective pixels corresponding to the third, fourth, ninth and tenth horizontal lines HL3, HL4, HL9, HL10 are connected to the data lines starting from the data line D2.

[0058] Referring to FIG. 10, the polarities of data applied to the data line D1 to Dm from the data drive circuit 132 are inverted every six horizontal periods and the polarities of data applied to the odd number data lines are opposite to the polarities of data applied to the even number data lines. However, because of the configuration between the source electrodes of the TFTs and the data lines D1 to Dm, the polarities of the effective pixels are inverted every two horizontal periods.

[0059] The data on the first data line D1 are supplied to the dummy pixels corresponding to the third, fourth, ninth and tenth horizontal lines HL3, HL4, HL9, HL10, and the data on the second data line D2 are supplied to the first effective pixels corresponding to the third, fourth, ninth and tenth horizontal lines HL3, HL4, HL9, HL10. The data on the first data line D1 are also supplied to the first effective pixels corresponding to the first, second, fifth to eighth, eleventh and twelfth horizontal lines HL1, HL2, HL5 to HL8, HL11, HL12. Because the dummy pixels are not formed in outside the effective display area of the first, second, fifth to eighth, eleventh and twelfth horizontal lines HL1, HL2, HL5 to HL8, HL11, HL12, the data on the first data line D1 are supplied to the first effective pixels corresponding to the first, second, fifth to eighth, eleventh and twelfth horizontal lines HL1, HL2, HL5 to HL8, HL11, HL12.

[0060] As a result, the power consumption of the LCD device can be reduced by decreasing the driving frequency of the data drive circuit 132 to $\frac{1}{3}$ without compromising display quality.

[0061] FIG. 11 is a schematic view illustrating the polarities of data applied to an LCD device according to the third embodiment of the present invention. FIG. 12 shows the polarities of data applied to the data lines from a data drive circuit according to the third embodiment of the present invention.

[0062] Referring to FIG. 11, the LCD device according to the second third embodiment of the present invention has an

effective pixel group formed in an effective display area and a dummy pixel group formed outside the effective display area.

[0063] The source electrodes of dummy pixels outside the effective display area corresponding to $(16n-13)^{th}$, $(16n-12)^{th}$, $(16n-9)^{th}$, $(16n-8)^{th}$, $(16n-7)^{th}$, $(16n-6)^{th}$, $(16n-3)^{th}$ and $(16n-2)^{th}$ (n is an integer) horizontal lines are connected to the first data line D1 and the source electrodes of the effective pixels corresponding to $(16n-13)^{th}$, $(16n-12)^{th}$, $(16n-9)^{th}$, $(16n-8)^{th}$, $(16n-7)^{th}$, $(16n-6)^{th}$, $(16n-3)^{th}$ and $(16n-2)^{th}$ (n is an integer) horizontal lines are connected to the data lines starting from the data line D2.

[0064] For example, the source electrodes of the dummy pixels outside the effective display area corresponding to the third, fourth, seventh to tenth, thirteenth, fourteenth, nineteenth and twentieth horizontal lines HL3, HL4, HL7 to HL10, HL13, HL14, HL19, HL20 are connected to the first data line D1 and the source electrodes of the effective pixels corresponding to the third, fourth, seventh to tenth, thirteenth, fourteenth, nineteenth and twentieth horizontal lines HL3, HL4, HL7 to HL10, HL13, HL14, HL19 and HL20 are connected to the data lines starting from the data line D2.

[0065] Referring to FIG. 12, the polarities of data applied to the data line D1 to Dm from the data drive circuit 132 are inverted every eight horizontal periods and the polarities of data applied to the odd number data lines are opposite to the polarities of data applied to the even number data lines. However, because of the configuration between the source electrodes of the TFTs and the data lines D1 to Dm, the polarities of the effective pixels are inverted every two horizontal periods.

[0066] The data on the first data line D1 are supplied to the dummy pixels corresponding to the third, fourth, seventh to tenth, thirteenth and fourteenth horizontal lines HL3, HL4, HL7 to HL10, HL13, HL14, and the data on the second data line D2 are supplied to the first effective pixels corresponding to the third, fourth, seventh to tenth, thirteenth and fourteenth horizontal lines HL3, HL4, HL7 to HL10, HL13, HL14. The data on the first data line D1 are also supplied to the first effective pixels corresponding to the first, second, fifth, sixth, eleventh, twelfth, fifteenth and sixteenth horizontal lines HL1, HL2, HL5 to HL8, HL11, HL12. Because the dummy pixels are not formed in outside the effective display area of the first, second, fifth, sixth, eleventh, twelfth, fifteenth and sixteenth horizontal lines HL1, HL2, HL5 to HL8, HL11, HL12, the data on the first data line D1 are supplied to the first effective pixels corresponding to the first, second, fifth, sixth, eleventh, twelfth, fifteenth and sixteenth horizontal lines HL1, HL2, HL5 to HL8, HL11, HL12.

[0067] As a result, the power consumption of the LCD device can be further reduced by decreasing the driving frequency of the data drive circuit 132 to $\frac{1}{4}$ without compromising display quality.

[0068] In the first to third embodiments, the two dot inversion method with a smaller driving frequency is realized by modulating data applied to the data lines and configuration of the data lines, TFTs and dummy pixels. However, it should be understood that the principles of the present invention can be applicable to an LCD device without dummy pixels by further modulating data applied to the data lines. It should be also understood that the principles of the present invention can be applicable to an LCD device driven by a more than two dot inversion method such as a

three dot inversion method by modulating data applied to the data lines and configuration of the data lines and TFTs.

[0069] As described above, an LCD device according to the present invention is driven by the two dot inversion method in which the polarities of data applied to the data line D1 to Dm from the data drive circuit 132 are inverted every 2k horizontal periods (k is an integer not less than 2). As a result, the driving frequency of the data drive circuit and the power consumption of the LCD device are reduced.

[0070] It will be apparent to those skilled in the art that various modifications and variation can be made in the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention cover the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

What is claimed is:

1. A liquid crystal display (LCD) device, comprising: a data drive circuit that inverts polarities of data every 2k horizontal periods (k is an integer not less than 2); and an LCD panel including a plurality of data lines and a plurality of gate lines crossing each other to define a plurality of pixels, each pixel including a switching device, wherein source electrodes of the switching devices in the pixels arranged in a vertical direction are connected to two different data lines.
2. The LCD device according to claim 1, wherein the polarities of the data charged in the pixels arranged in the vertical direction are inverted every two pixels.
3. The LCD device according to claim 2, wherein the data drive circuit inverts the polarities of the data every 4 horizontal periods.
4. The LCD device according to claim 3, wherein the source electrodes of the switching devices connected to $(8n-5)^{th}$, $(8n-4)^{th}$, $(8n-3)^{th}$ and $(8n-2)^{th}$ (n is an integer) gate lines are connected to the same data line.
5. The LCD device according to claim 4, further comprising a plurality of dummy pixels connected to the $(8n-5)^{th}$, $(8n-4)^{th}$, $(8n-3)^{th}$ and $(8n-2)^{th}$ (n is an integer) gate lines.
6. The LCD device according to claim 5, wherein the dummy pixels are connected to the same data line.
7. The LCD device according to claim 2, wherein the data drive circuit inverts the polarities of the data every 6 horizontal periods.
8. The LCD device according to claim 7, wherein the source electrodes of the switching devices connected to $(6n-3)^{th}$ and $(6n-2)^{th}$ (n is an integer) gate lines are connected to the same data line.
9. The LCD device according to claim 8, further comprising a plurality of dummy pixels connected to the $(6n-3)^{th}$ and $(6n-2)^{th}$ (n is an integer) gate lines.
10. The LCD device according to claim 9, wherein the dummy pixels are connected to the same data line.
11. The LCD device according to claim 2, wherein the data drive circuit inverts the polarity of the data every 8 horizontal periods.
12. The LCD device according to claim 11, wherein the source electrodes of the switching devices connected to $(16n-13)^{th}$, $(16n-12)^{th}$, $(16n-9)^{th}$ to $(16n-6)^{th}$, $(16n-3)^{th}$ and $(16n-2)^{th}$ (n is an integer) gate lines are connected to the same data line.
13. The LCD device according to claim 12, further comprising a plurality of dummy pixels connected to the

$(16n-13)^{th}$, $(16n-12)^{th}$, $(16n-9)^{th}$ to $(16n-6)^{th}$, $(16n-3)^{th}$ and $(16n-2)^{th}$ (n is an integer) gate lines.

14. The LCD device according to claim **13**, wherein the dummy pixels are connected to the same data line.

15. A driving method of a liquid crystal display (LCD) device, comprising:

providing an LCD panel wherein a plurality of data lines and a plurality of gate lines cross each other to define a plurality of pixels, each pixel includes a switching device, and source electrodes of the switching devices in the pixels arranged in a vertical direction are connected to two different data lines; and

inverting polarities of data every $2k$ horizontal periods (k is an integer not less than 2) and applying the inverted data to one of the data lines.

16. The driving method according to claim **15**, wherein the polarities of the data charged in the pixels arranged in the vertical direction are inverted every two pixels.

17. The driving method according to claim **16**, wherein the polarities of the data is inverted every 4 horizontal periods.

18. The driving method according to claim **16**, wherein the polarities of the data is inverted every 6 horizontal periods.

19. The driving method according to claim **16**, wherein the polarities of the data is inverted every 8 horizontal periods.

20. A driving method of a display device, comprising:

providing a display panel wherein a plurality of data lines and a plurality of gate lines cross each other to define a plurality of pixels, each pixel includes a switching device, and source electrodes of the switching devices in the pixels arranged in a vertical direction are connected to more than two different data lines; and
inverting polarities of data corresponding to at least four horizontal periods in each frame and applying the inverted data to one of the data lines.

* * * * *

专利名称(译)	液晶显示装置及其驱动方法		
公开(公告)号	US20080036721A1	公开(公告)日	2008-02-14
申请号	US11/635541	申请日	2006-12-08
[标]申请(专利权)人(译)	KIM BINN 赵南旭 曹诚HAK		
申请(专利权)人(译)	KIM BINN 赵南旭 曹诚HAK		
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摘要(译)

液晶显示 (LCD) 装置包括数据驱动电路，该电路每2k个水平周期反转数据的极性 (k是不小于2的整数) ；LCD面板，包括多条数据线和多条栅极线，彼此交叉以形成多个像素，每个像素包括开关器件，其中沿垂直方向排列的像素中的开关器件的源极连接两条不同的数据线。

