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(54) **LIQUID CRYSTAL DISPLAY APPARATUS**

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ABSTRACT

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To provide a liquid crystal display apparatus including: a substrate; another substrate arranged opposite the substrate; a liquid crystal layer sandwiched between the two substrates, and a plurality of pixels forming a display section, in which each of the pixels is provided with a first and a second pixel electrodes corresponding to the pixel, and a common electrode corresponding to the first and second pixel electrodes.

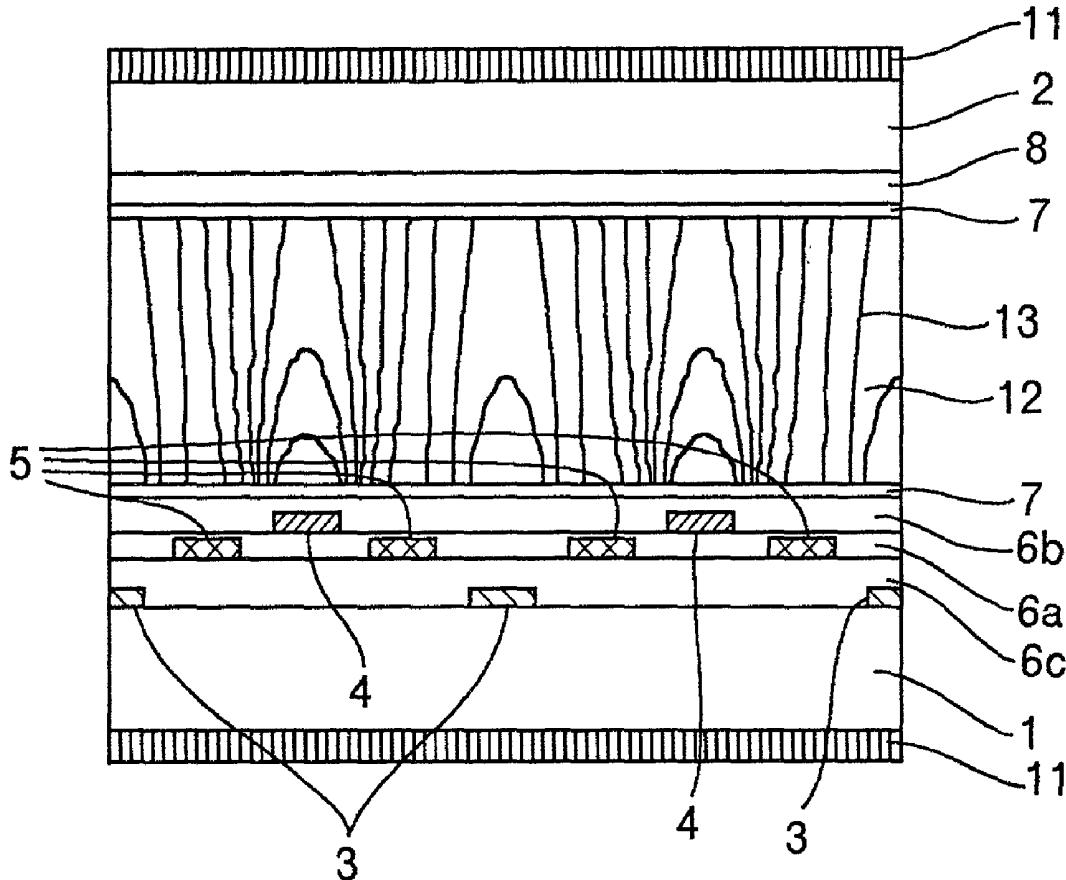


FIG.1

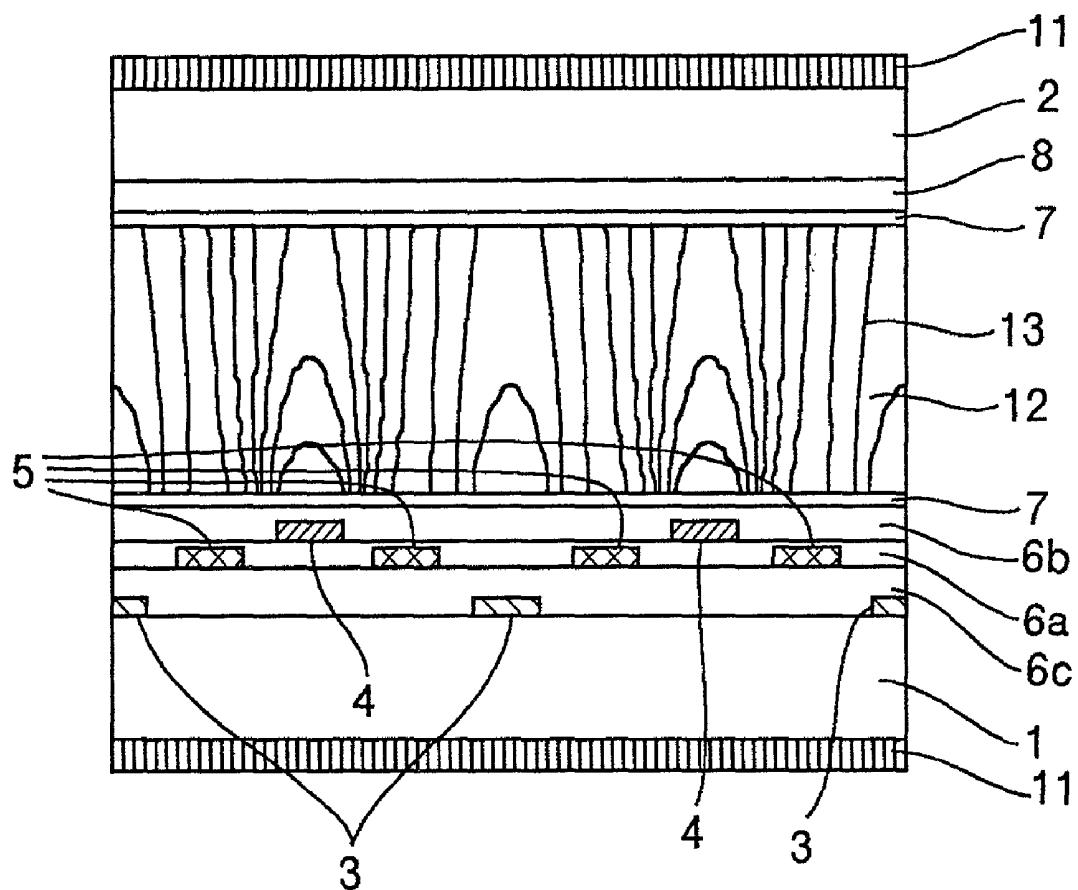


FIG.2

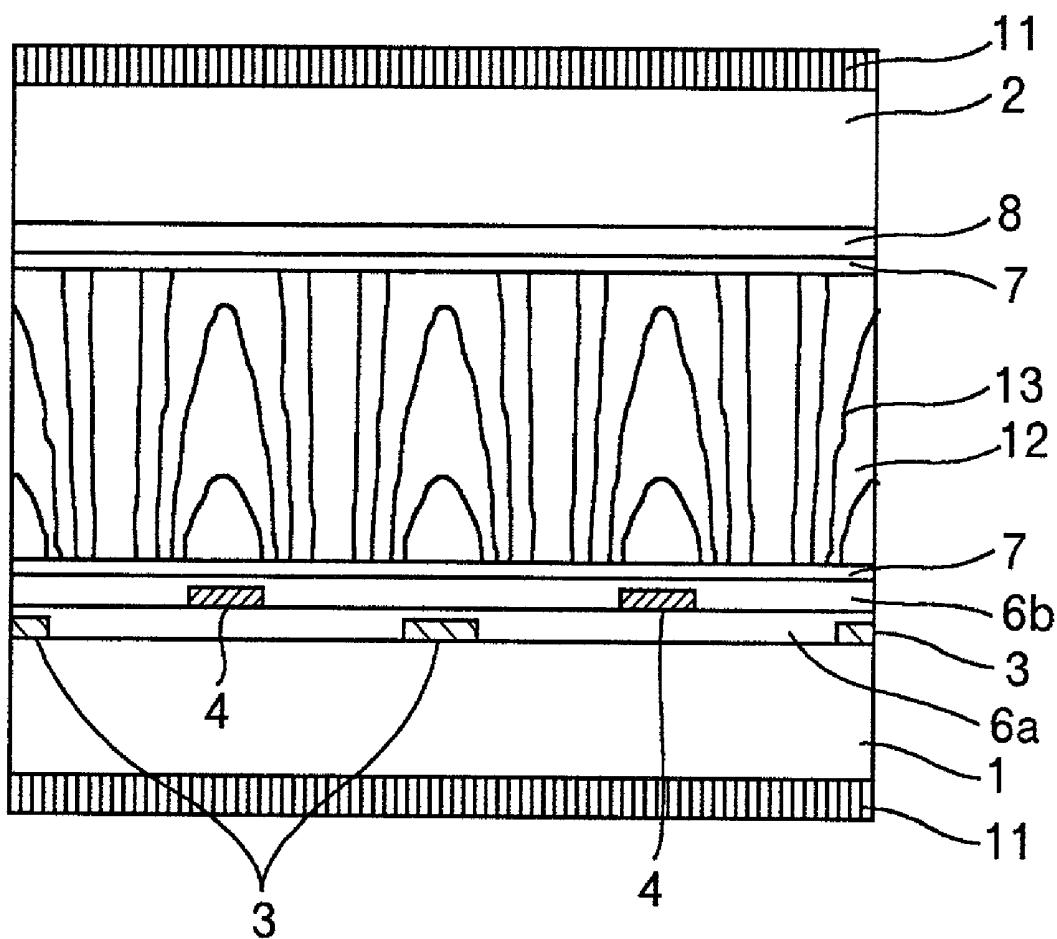


FIG.3A

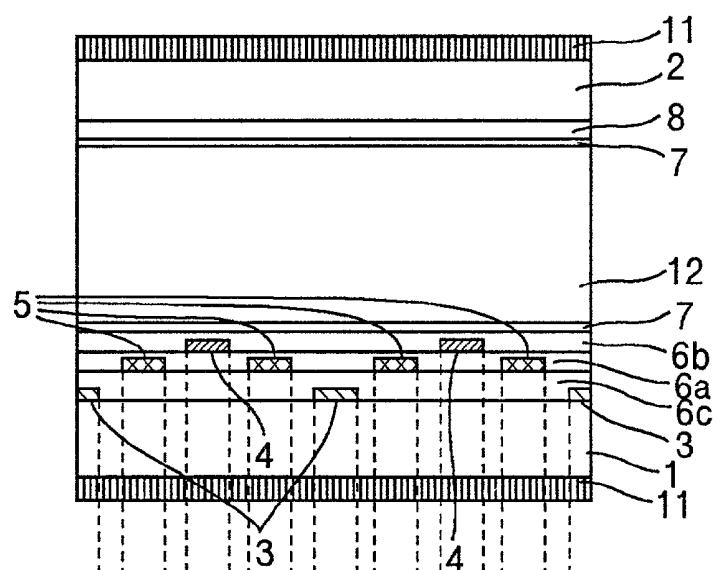


FIG.3B

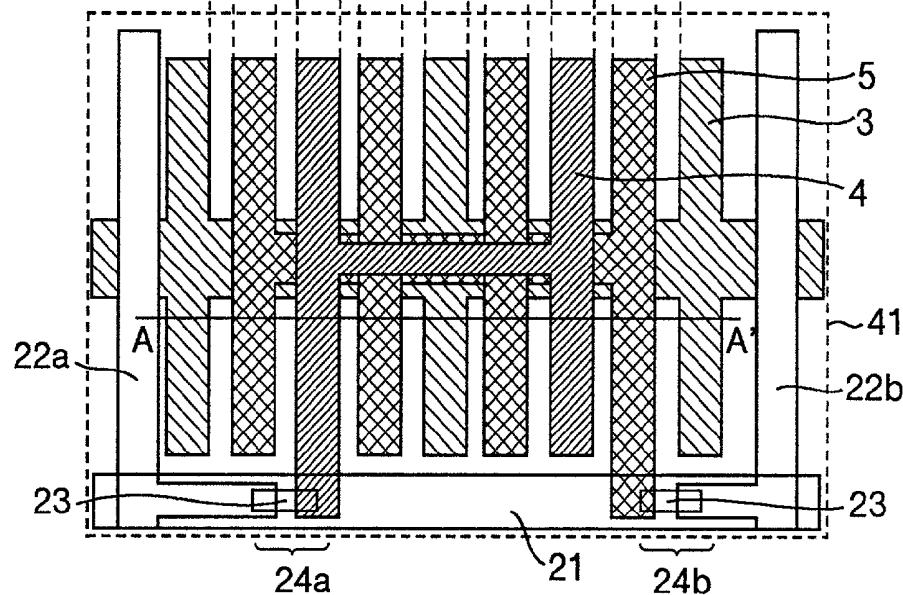


FIG.4

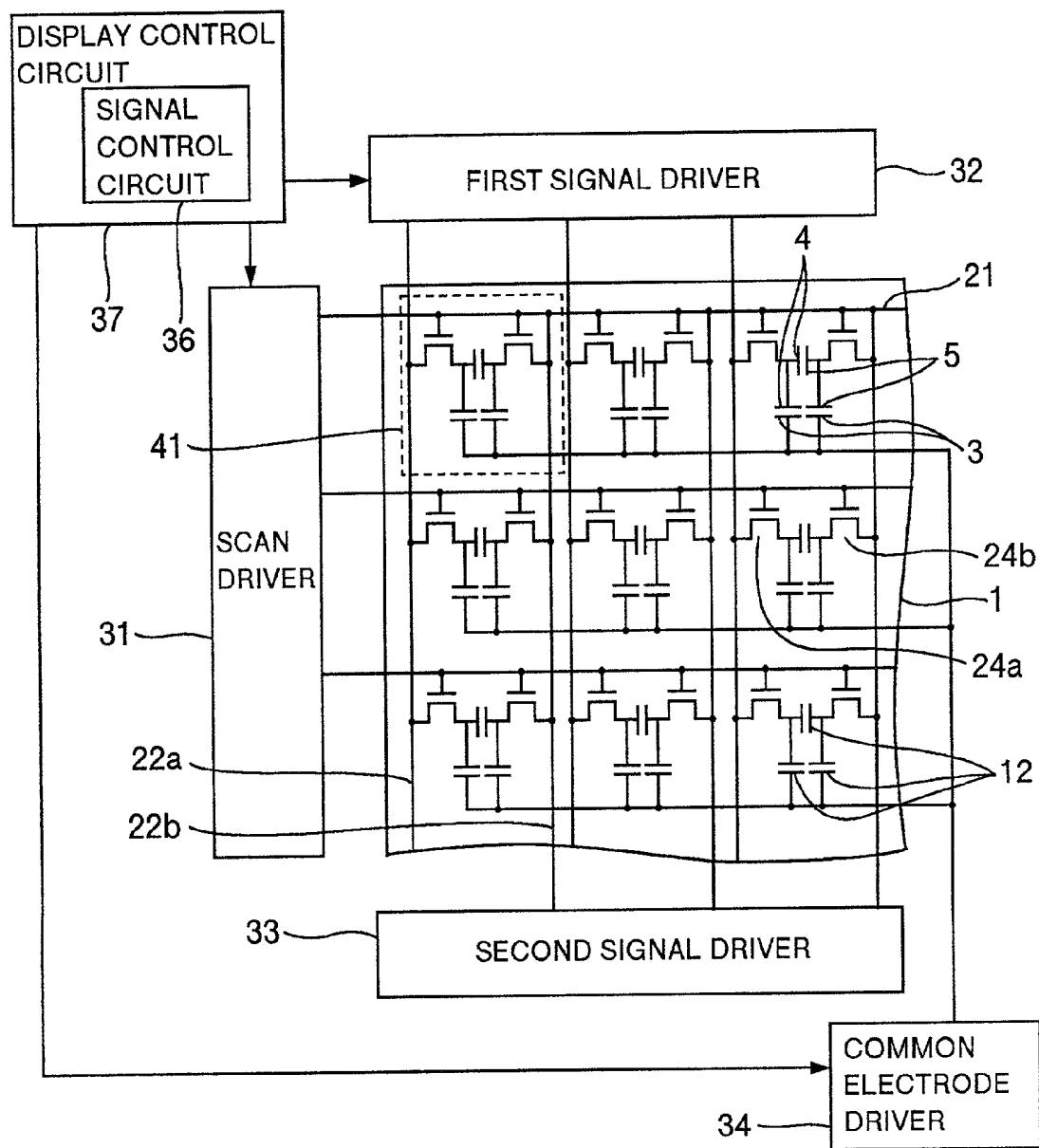


FIG.5A

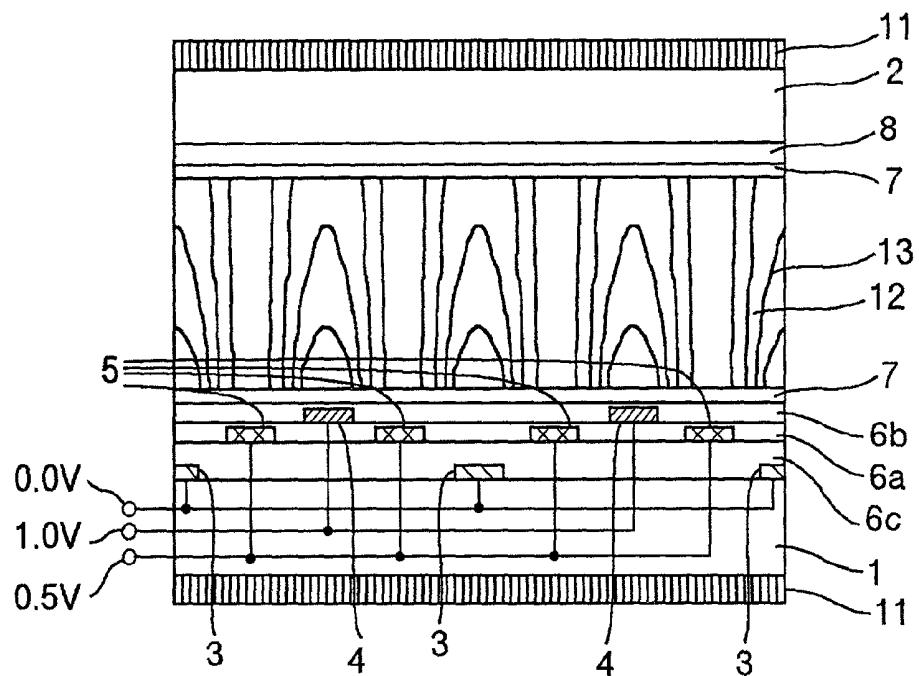


FIG.5B

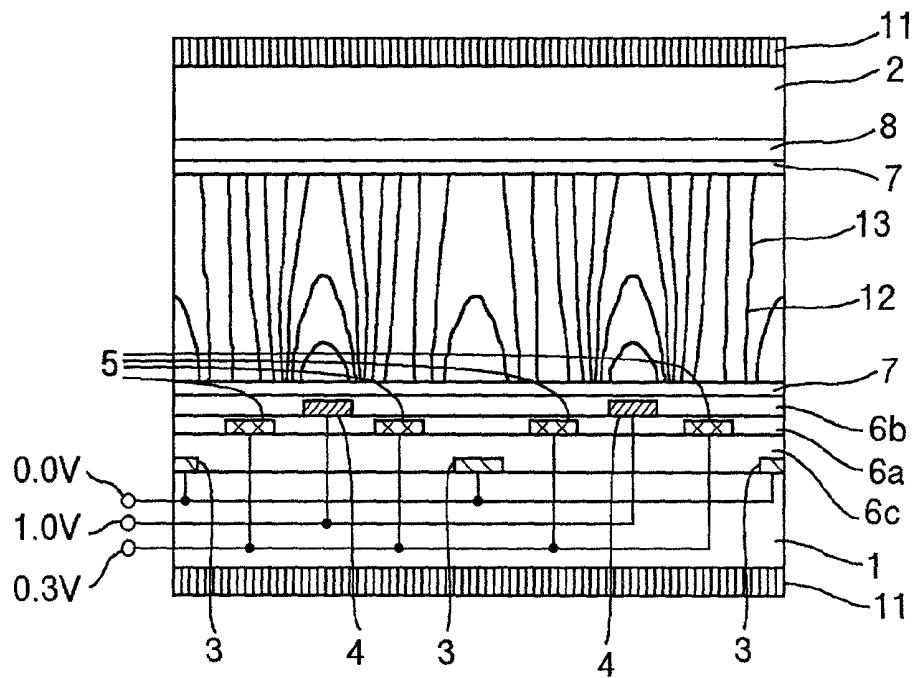


FIG.6

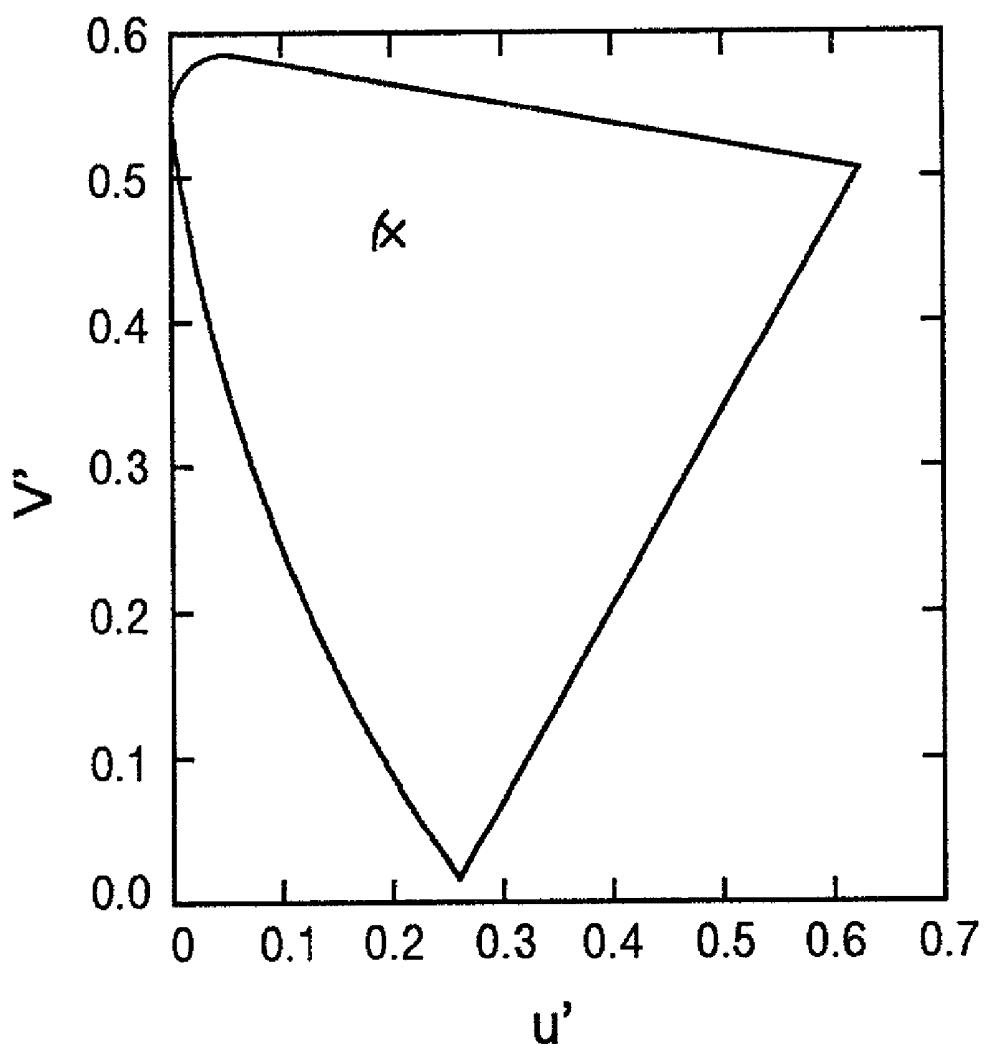


FIG.7A

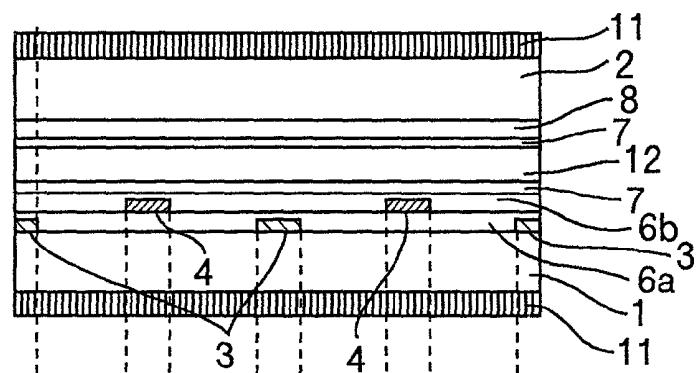


FIG.7B

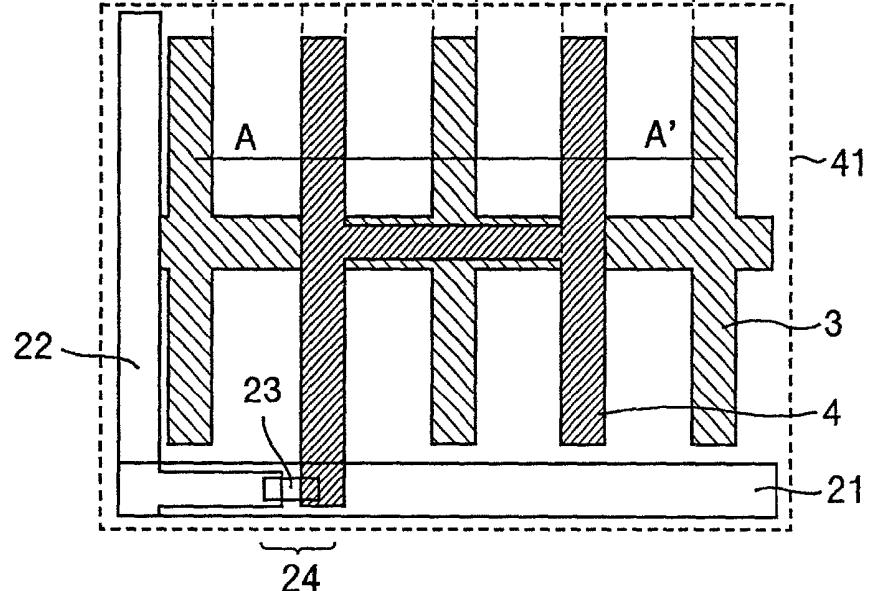


FIG.8

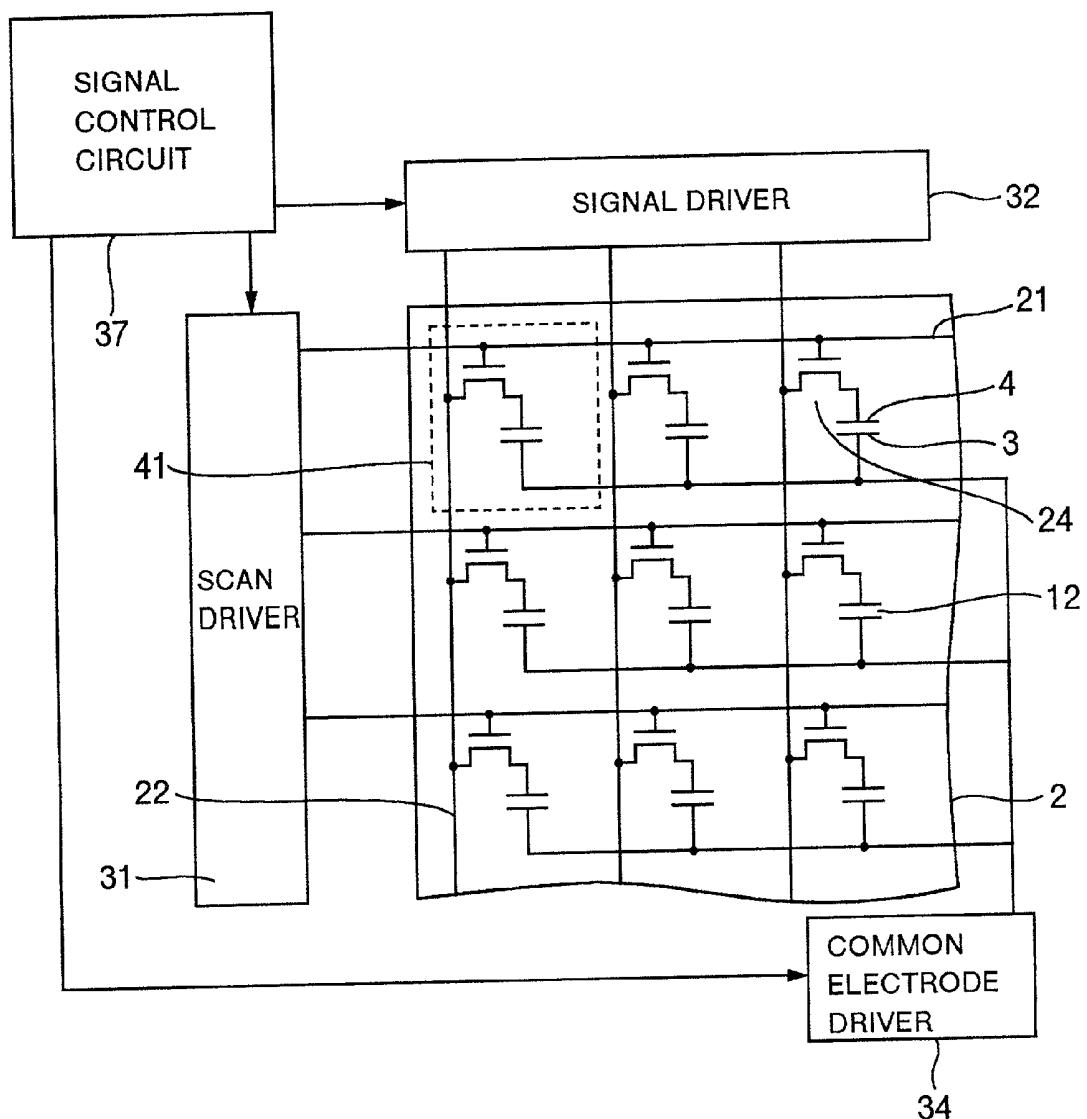


FIG.9

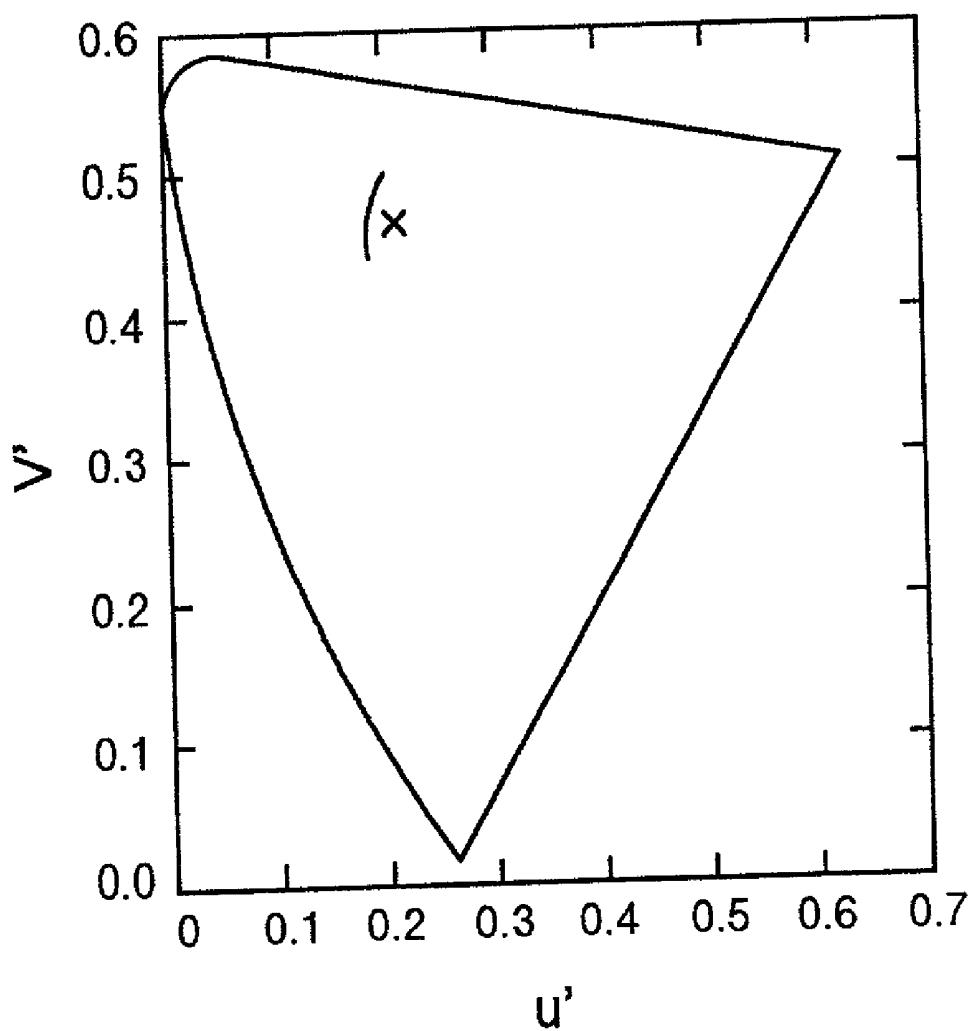


FIG.10

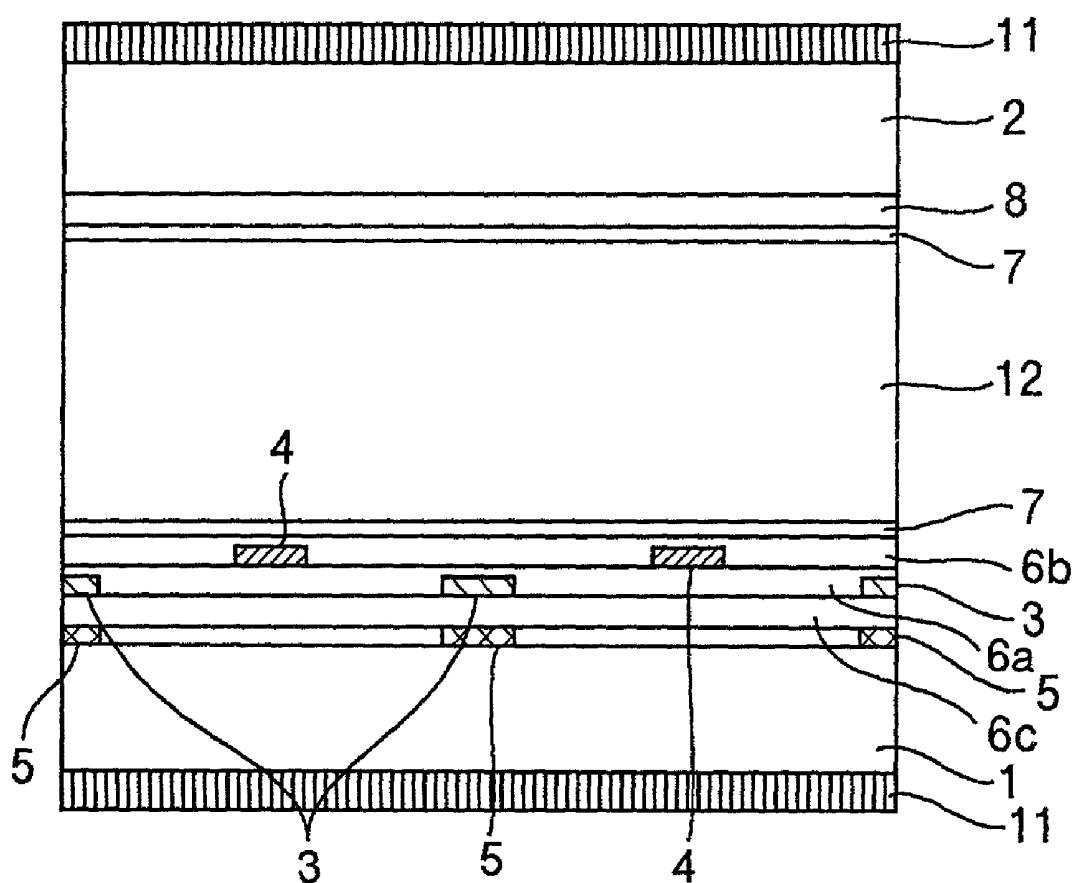


FIG.11

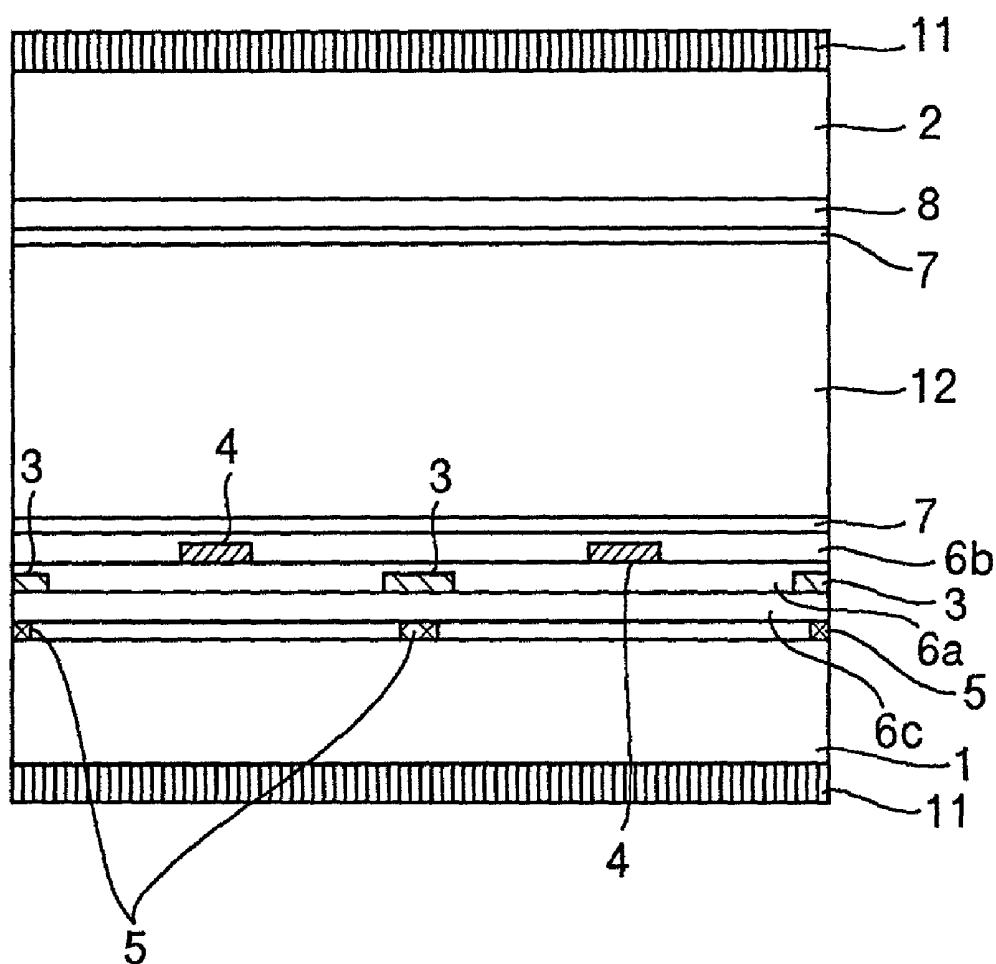


FIG.12

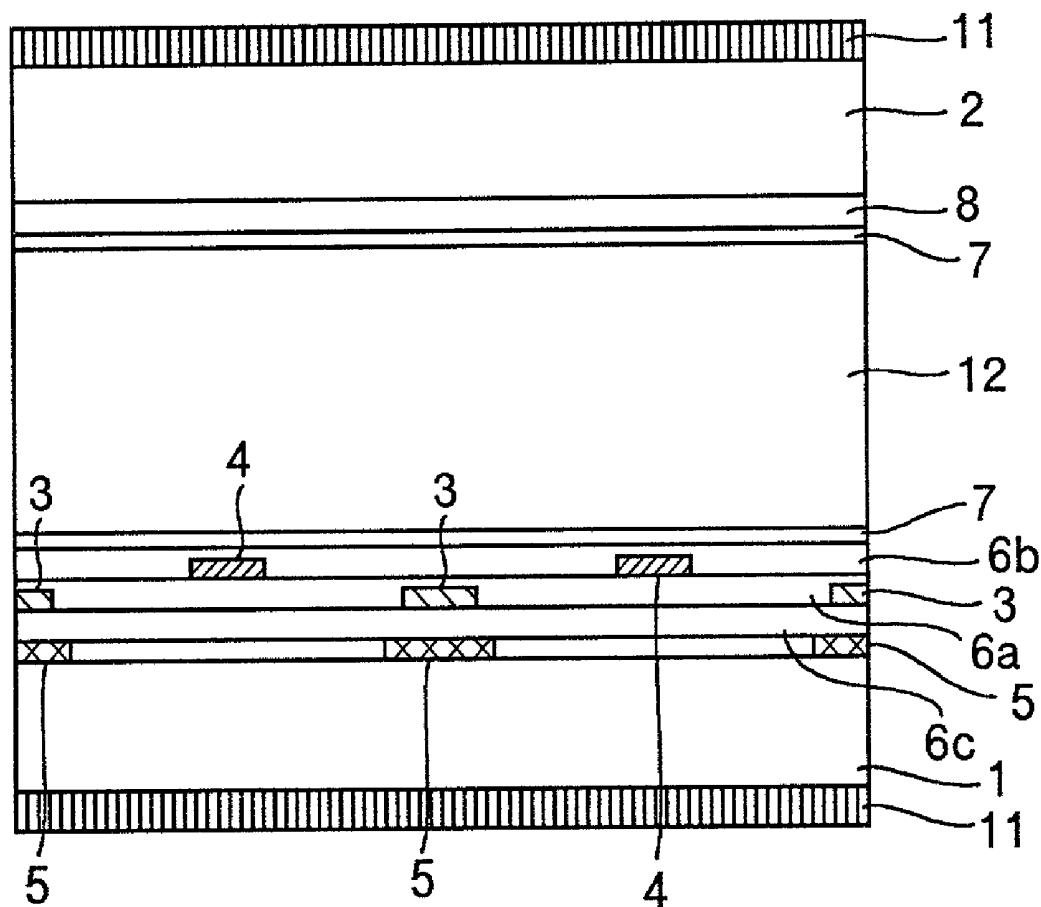


FIG.13A

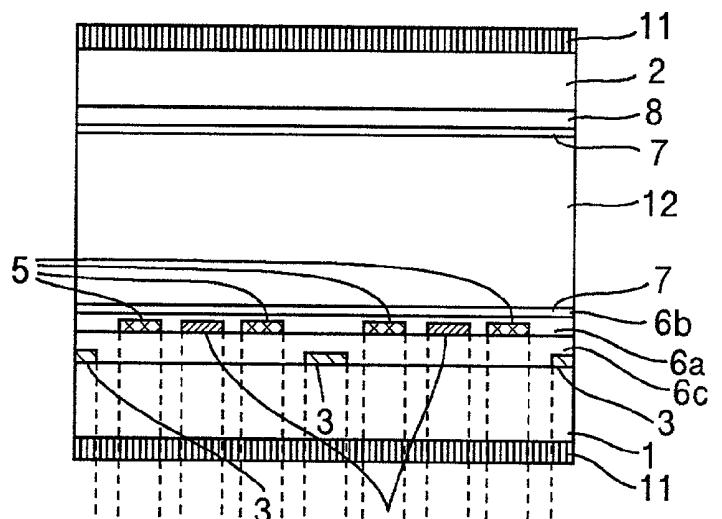


FIG.13B

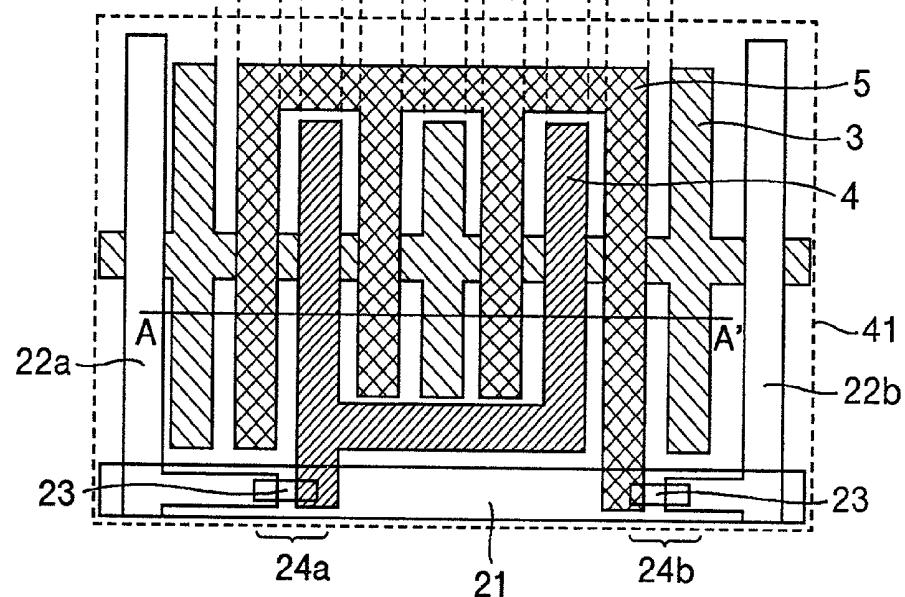


FIG.14A

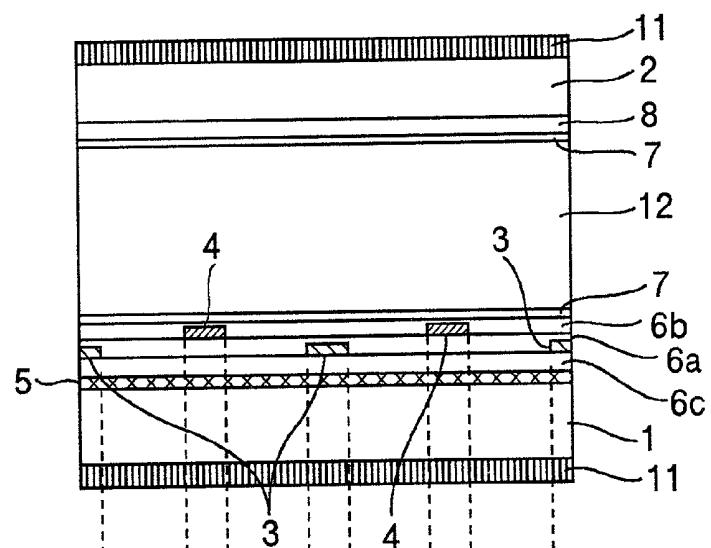


FIG.14B

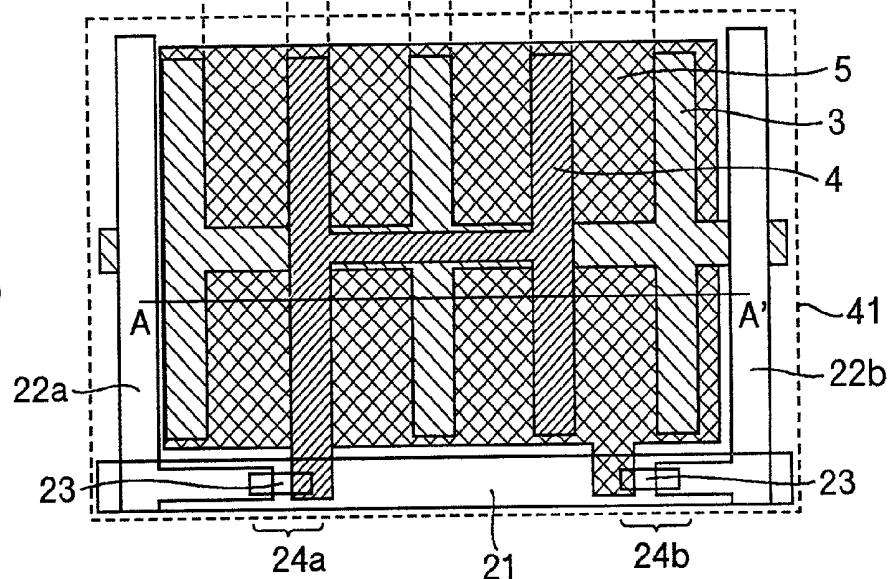


FIG.15A

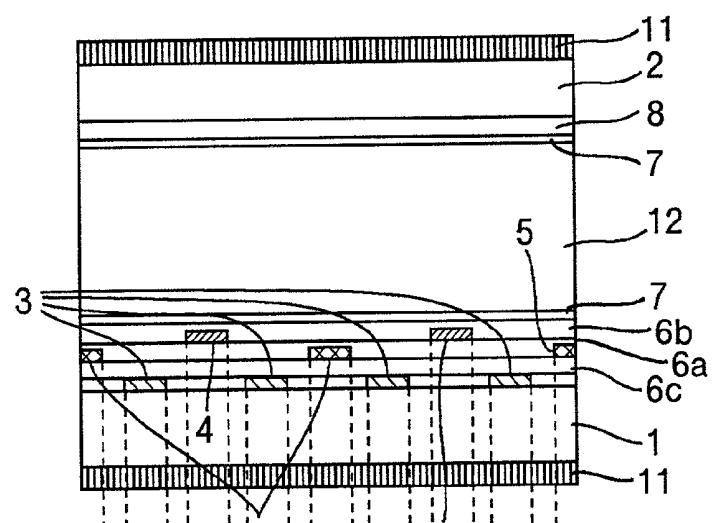


FIG.15B

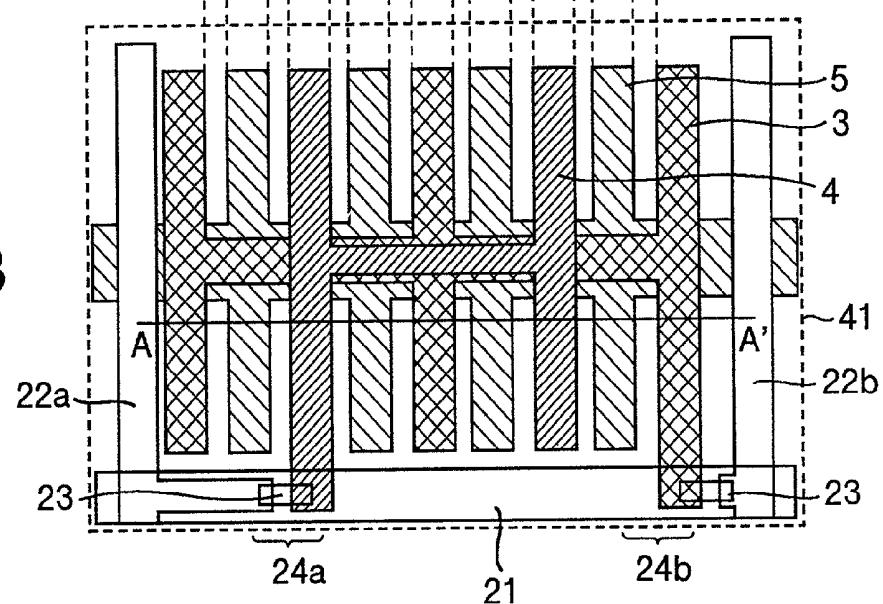


FIG.16A

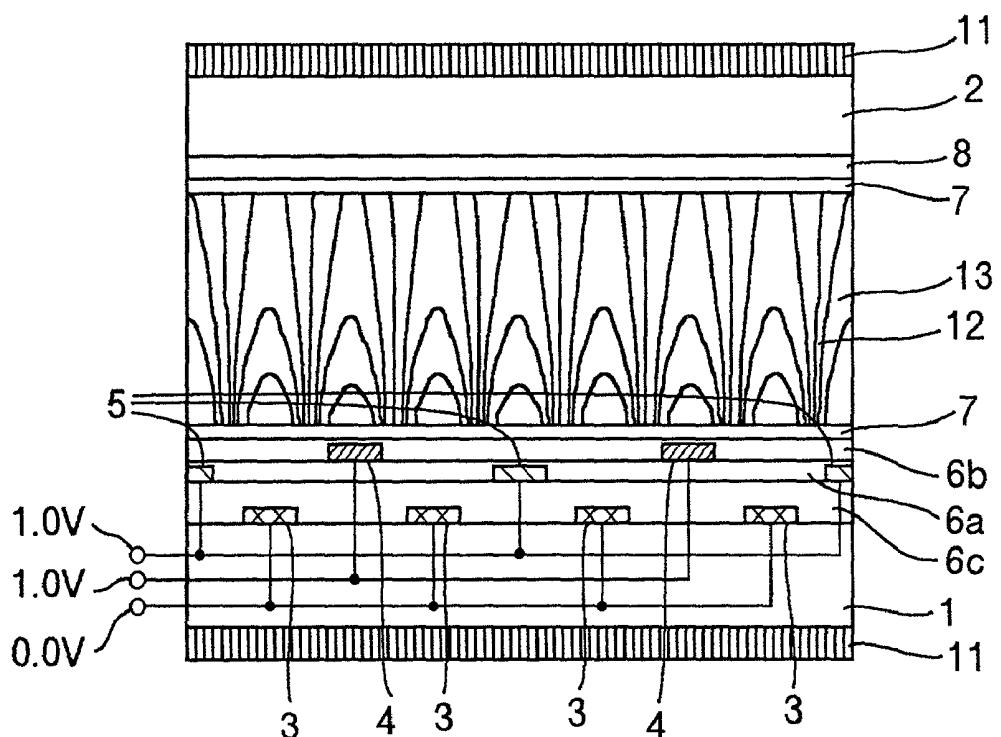


FIG.16B

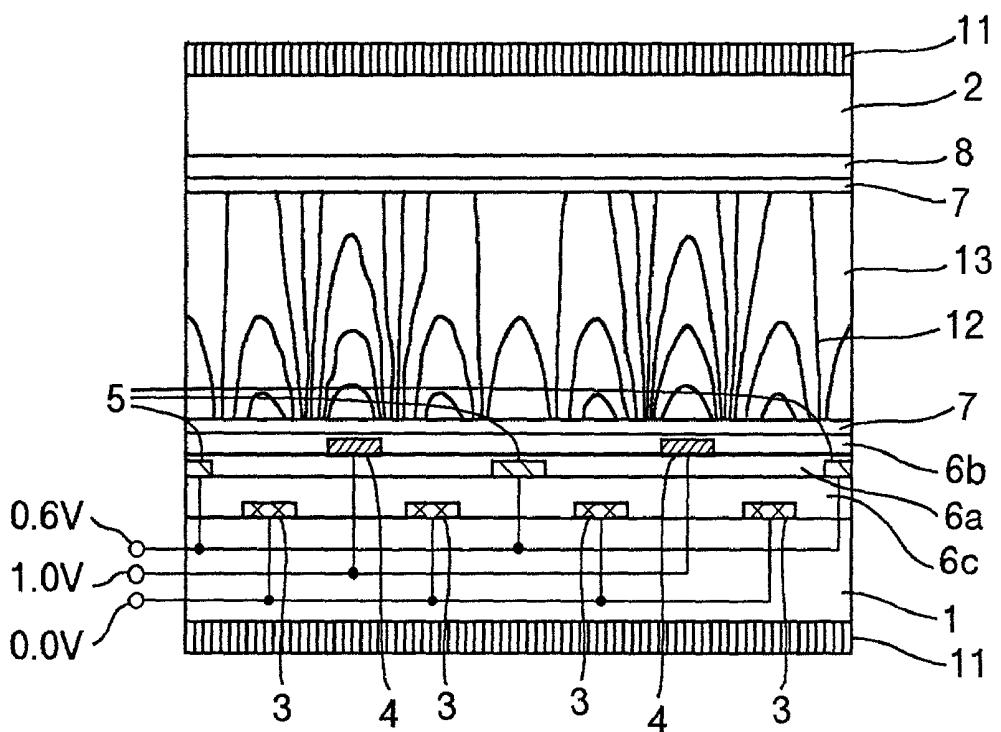


FIG.17A

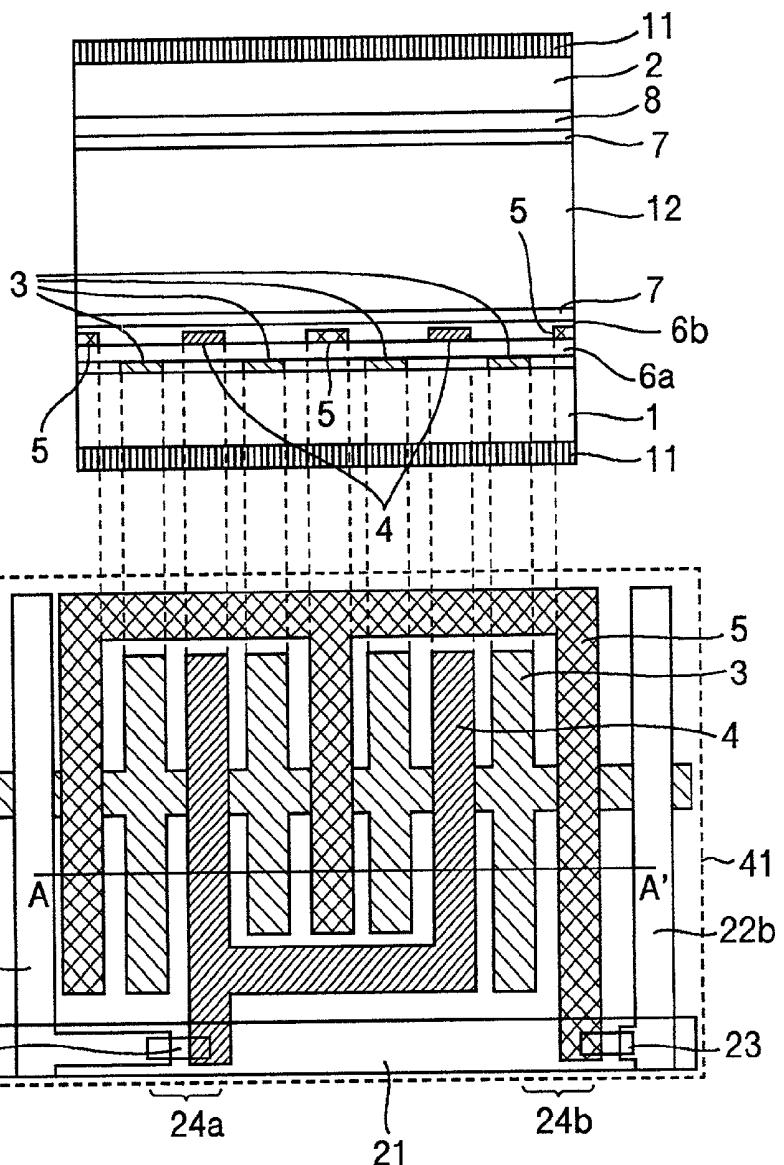


FIG.17B

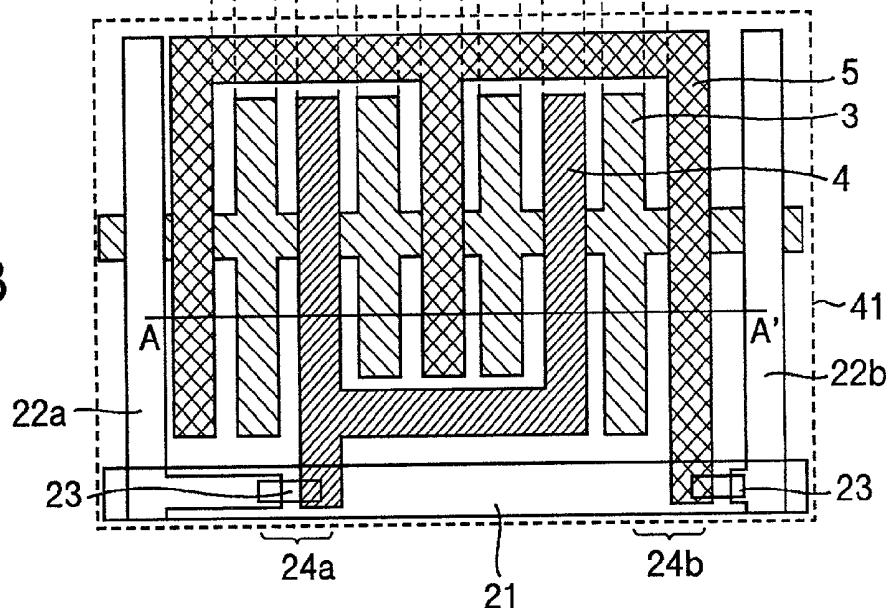


FIG.18A

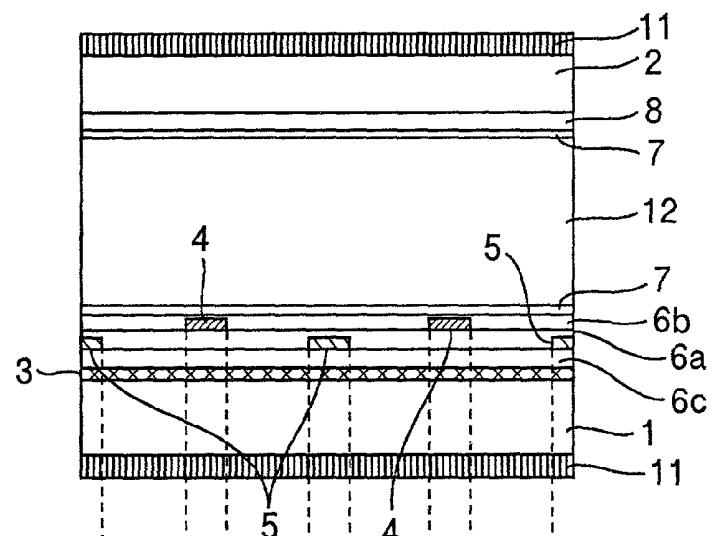


FIG.18B

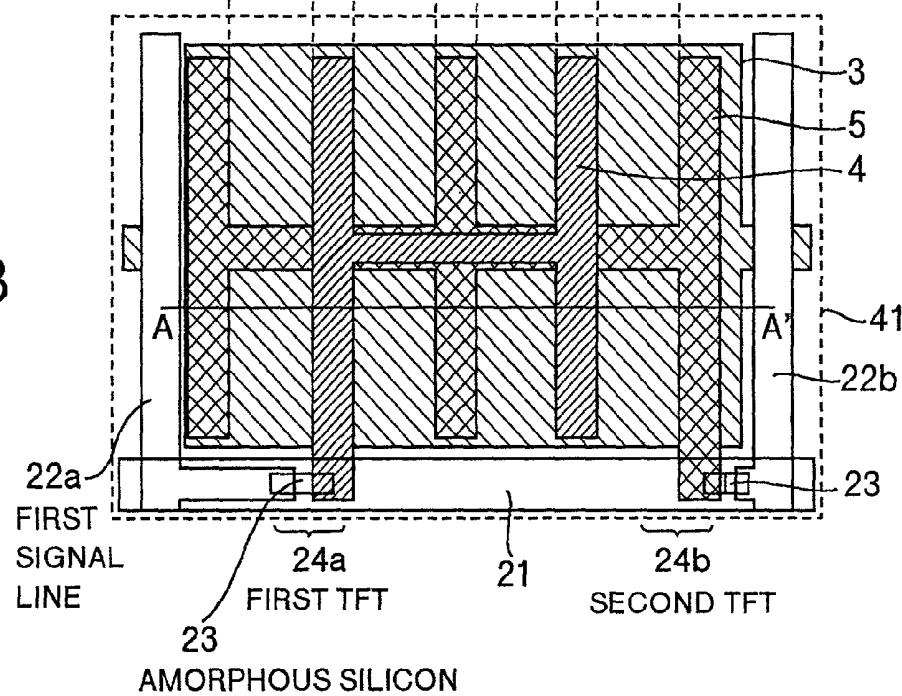


FIG.19A

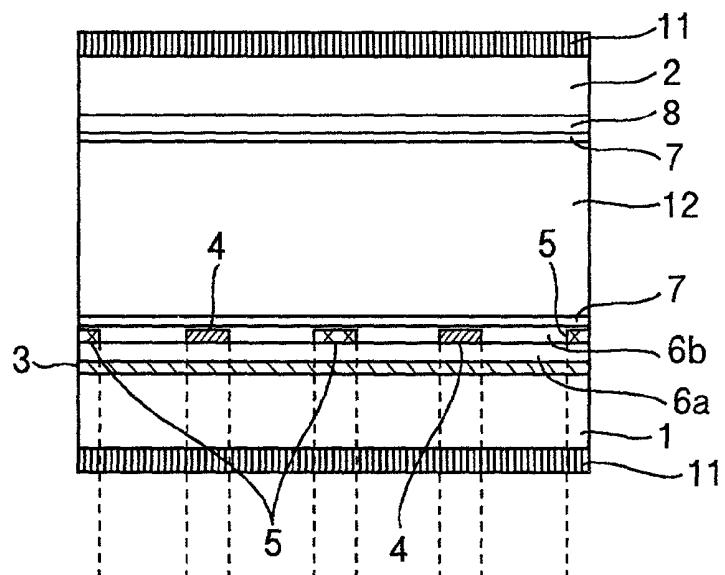


FIG.19B

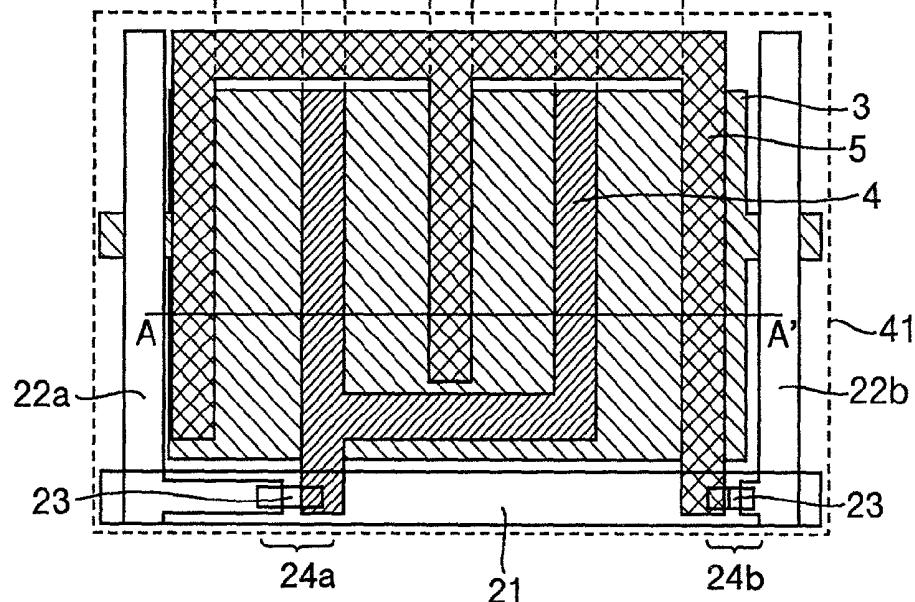


FIG.20

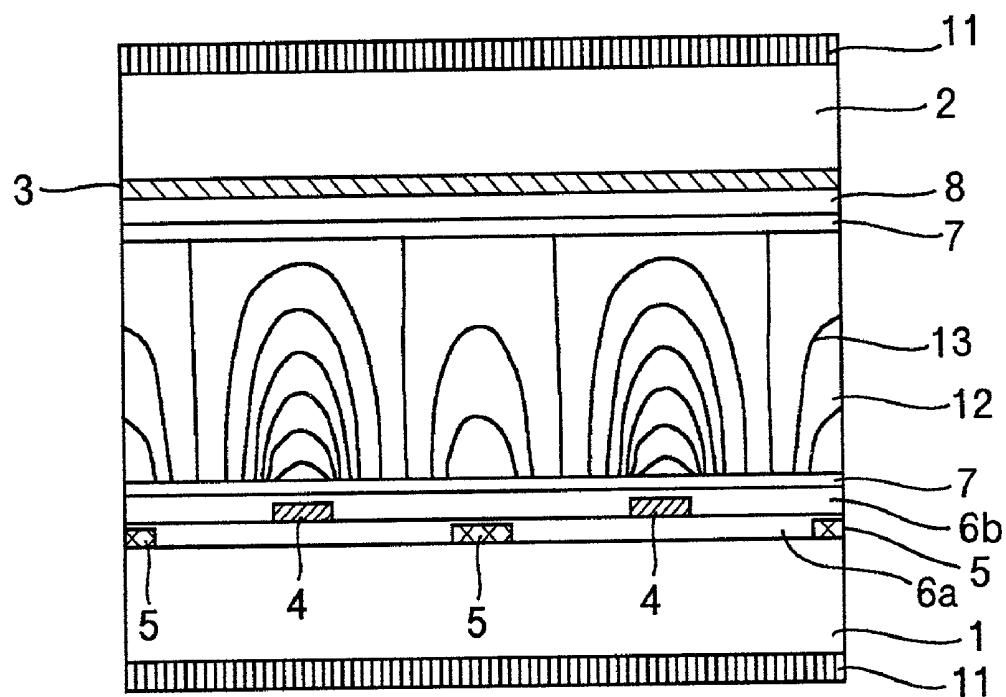


FIG.21

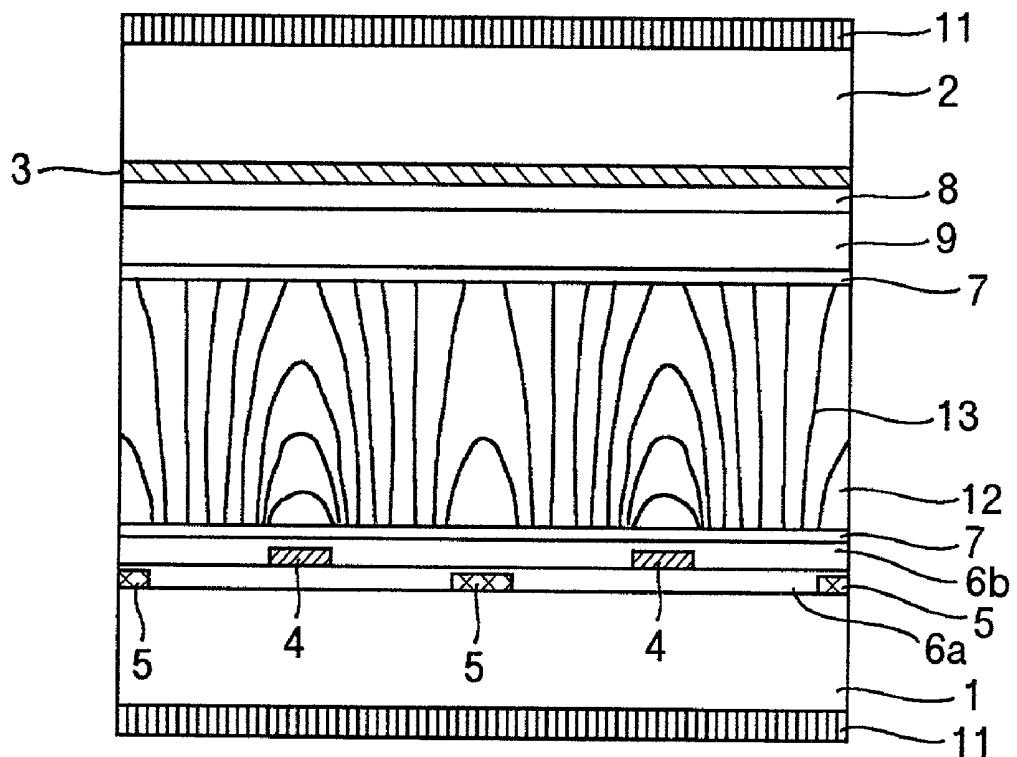
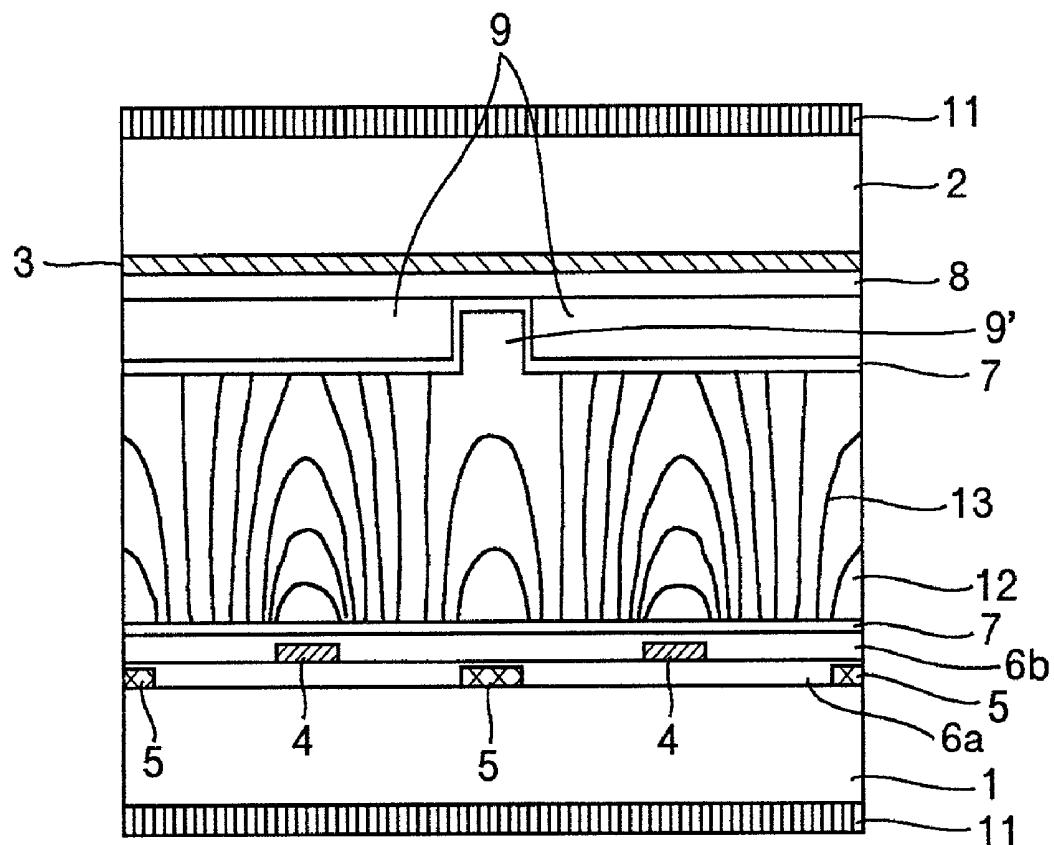


FIG.22



LIQUID CRYSTAL DISPLAY APPARATUS

BACKGROUND OF THE INVENTION

[0001] The present invention relates to a liquid crystal display apparatus having a new configuration.

[0002] Conventional liquid crystal display apparatuses employ a display mode represented by a Twisted Nematic (TN) display mode and in which electric fields are applied substantially perpendicularly to a substrate surface (these electric fields are hereinafter referred to as "vertical fields"). The TN display mode, however, has the disadvantage of using a small angle of visibility.

[0003] On the other hand, an In-Plane Switching (IPS) display mode has been proposed in JP-B-63-21907, U.S. Pat. No. 4,345,249, WO No. 91/10,936, and JP-A-6-160878 and etc. specifications.

[0004] In this IPS display mode, an electrode for driving liquid crystals is formed on one of a pair of substrates that sandwiches the liquid crystals therebetween, and electric fields substantially parallel with a surface of the substrate (these electric fields are hereinafter referred to as "horizontal fields") are applied to the liquid crystals. This display mode provides a larger angle of visibility than the TN display mode.

[0005] FIG. 2 is a schematic sectional view showing an example of a configuration of a pixel portion of a liquid crystal display apparatus using the IPS display mode. The liquid crystal display apparatus has a substrate 1, a substrate 2 arranged opposite the substrate 1, a liquid crystal layer 12 sandwiched between the substrates 1 and 2, common electrodes 3 and pixel electrodes 4 for applying horizontal fields, insulated films 6a and 6b disposed on the substrate 1, a liquid-crystal orientation control layer 7 (hereinafter referred to as an "orientation film") disposed on the insulated film 6b, a color filter 8 and an orientation film 7 disposed on the substrate 2, and polarizing plates 11 disposed on surfaces of the substrates 1 and 2 which do not face the liquid crystals, the polarizing plates having their own optical characteristics varied according to an orientation state of the liquid crystals. The common electrodes 3 and 4 are linear and are arranged substantially in parallel.

[0006] In the IPS display mode, the common electrodes 3 and the pixel electrodes 4 generate horizontal fields as shown by equipotential lines in FIG. 2. Images are displayed when these fields cause the liquid crystals to rotate within a plane substantially parallel with the substrate 1.

SUMMARY OF THE INVENTION

[0007] There are now needs for a display monitor that can reliably reproduce colors, but the above described IPS display mode is disadvantageous in that color tones vary depending on a driving voltage.

[0008] Means for solving this problem has been reported in JP-A-9-297299 specification. The means reported in this publication, however, solves the problem with a combination of a light source and a liquid crystal panel. Thus, to widen a range of light sources any of which are to be selected, a means is desirable which restrains a variation in color tones using only the liquid crystal panel.

[0009] Further, the means reported in JP-A-9-297299 specification desirably sets a retardation $d_{\text{eff}} \Delta n_{\text{eff}}$ at a small value of 250 nm or less (where d_{eff} denotes an effective thickness of the liquid crystal layer and Δn_{eff} denotes an effective refractive-index anisotropy of the liquid crystals). This setting requires a gap and the refractive-index anisotropy to be reduced. A reduction in the gap may lower productivity, while a reduction in refractive-index anisotropy narrows the range of liquid crystals any of which are to be selected. Thus, solving these problems requires a means that does not substantially restrict the retardation.

[0010] It is an object of the present invention to provide a liquid crystal display apparatus having a new configuration and which restrains a variation in color tones on a display panel depending on a driving voltage without any unreasonable restrictions on retardation, using only the liquid crystal panel.

[0011] The present invention, adapted to attain the above object, will be described below in brief.

[0012] [1] A liquid crystal display apparatus having a first substrate, a second substrate arranged opposite the first substrate, a liquid crystal layer sandwiched between the first substrate and the second substrate, and a plurality of pixels forming a display section,

[0013] in which each of the pixels is provided with a first pixel electrode and a second pixel electrode both corresponding to the pixel, and a common electrode corresponding to the first and second pixel electrodes.

[0014] [2] A liquid crystal display apparatus having a first substrate, a second substrate arranged opposite the first substrate, a liquid crystal layer sandwiched between the first substrate and the second substrate, and a plurality of pixels forming a display section,

[0015] in which each of the pixels has a first and a second pixel electrodes each corresponding to the pixel and disposed on the first substrate, and a common electrode corresponding to the first and second pixel elements and disposed on the first substrate.

[0016] The first and second pixel electrodes can desirably provide each of the pixels with a corresponding potential.

[0017] The apparatus desirably includes a first signal driver for supplying a potential to the first pixel electrodes, a second signal driver for supplying a potential to the second pixel electrodes, and a signal control circuit for controlling signals transmitted to the first and second signal drivers.

[0018] [3] A liquid crystal display apparatus in which the first pixel electrodes, the common electrodes, and the second pixel electrodes are disposed on the first substrate.

[0019] In each pixel, the first pixel electrode and the common electrode may be linear and may be arranged substantially in parallel, and the second pixel electrode may be located between the first pixel electrode and the common electrode.

[0020] In this case, when a difference between the potentials provided to the first pixel electrode and to the common electrode is largest or smallest, the potential provided to the

second pixel electrode is desirably substantially equal to an average of the potentials provided to the first pixel electrode and to the common electrode.

[0021] Furthermore, desirably, plural pieces of the first pixel electrode arranged in each pixel are connected together via a first junction, plural pieces of the second pixel electrode arranged in each pixel are connected together via a second junction, the plural pieces of the first pixel electrode and the first junction do not overlap the plural pieces of the second pixel electrode and the second junction, the first pixel electrode, the second pixel electrode, the first signal line, and the second signal line are arranged in the same layer, and the common electrode and scan line are arranged in the same layer.

[0022] Further, at least part of the second pixel electrode may overlap the first pixel electrode or the common electrode, the second pixel electrode may be linear, the second pixel electrode may be as wide as or narrower than the first pixel electrode or common electrode, which is overlapped by the part of the second pixel electrodes, the second pixel electrode may be linear, and the second pixel electrode may be wider than the first pixel electrode or common electrode, which is overlapped by the part of the second pixel electrode.

[0023] In this case, when the difference between the potentials provided to the first pixel electrode and to the common electrode is largest or smallest, the potential provided to the second pixel electrode desirably substantially equals the potential provided to either the first pixel electrode or the common electrode.

[0024] Furthermore, desirably, the plural pieces of the first pixel electrode arranged in each pixel are connected together via a first junction, the plural pieces of the second pixel electrode arranged in each pixel are connected together via a second junction, the plural pieces of the first pixel electrode and the first junction do not overlap the plural pieces of the second pixel electrode and the second junction, the first pixel electrode, the second pixel electrode, the first signal line, and the second signal line are arranged in the same layer, and the common electrode and scan line are arranged in the same layer.

[0025] Further, the first pixel electrode and the common electrode may be linear and may be arranged substantially in parallel, the second pixel electrode may be located below the first pixel electrode and the common electrode, the second pixel electrode overlaps the first pixel electrode and the common electrode, and insulated films may be disposed between the second pixel electrode and the first pixel electrode and between the second pixel electrode and the common electrode.

[0026] In this case, when the difference between the potentials provided to the first pixel electrode and to the common electrode is largest or smallest, the potential provided to the second pixel electrode is desirably substantially equal to the average of the potentials provided to the first pixel electrode and to the common electrode.

[0027] Further, the first and second pixel electrodes may be linear and may be arranged substantially in parallel, the common electrode may be located between the first pixel electrode and the second pixel electrode, at least part of the common electrode may overlap the first pixel electrode or

the second pixel electrode, the first and second pixel electrodes may be linear and may be arranged substantially in parallel, the common electrode may be located below the first and second pixel electrodes, the common electrode overlaps the first and second pixel electrodes, and insulated films may be disposed between the common electrode and the first pixel electrode and between the common electrode and the second pixel electrode.

[0028] In this case, when the difference between potentials provided to the first pixel electrode and to the common electrode is largest or smallest, the potential provided to the second pixel electrode desirably substantially equals the potential provided to the first pixel electrode.

[0029] Furthermore, desirably, the plural pieces of the first pixel electrode arranged in each pixel are connected together via a first junction, the plural pieces of the second pixel electrode arranged in each pixel are connected together via a second junction, the plural pieces of the first pixel electrode and the first junction do not overlap the plural pieces of the second pixel electrode and the second junction, the first pixel electrode, the second pixel electrode, the first signal line, and the second signal line are arranged in the same layer, and the common electrode and scan line are arranged in the same layer.

[0030] [4] A liquid crystal display apparatus in which the first and second pixel electrodes are disposed on the first substrate, and the common electrodes are disposed on the second substrate.

[0031] Desirably, in each pixel, the first and second pixel electrodes may be linear and may be arranged substantially in parallel, and the common electrode overlaps the first and second pixel electrodes.

[0032] Further, a dielectric of 1.5 μm thickness may be arranged on the common electrode, and a portion of the dielectric which overlaps the common electrode includes a recess penetrating the dielectric or having a depth amounting to 50% or more of the thickness.

[0033] In this case, when the difference between the potentials provided to the first pixel electrode and to the common electrode is largest or smallest, the potential provided to the common electrode is desirably substantially equal to the average of the potentials provided to the first pixel electrode and to the second pixel electrode.

[0034] [5] Desirably, the liquid crystal apparatus includes a scan driver, a plurality of first scan lines connected to the scan driver, a plurality of first signal lines connected to the first signal driver and disposed so as to cross the plurality of first scan lines, and second signal lines connected to the second signal driver, the plurality of pixels each correspond to an area enclosed by a corresponding one of the plurality of first scan lines and a corresponding one of the plurality of first signal lines, the first pixel electrodes correspond to the first signal lines, and the second pixel electrodes correspond to the second signal lines.

[0035] Desirably, the first substrate is provided with the first scan lines, the first signal lines, and first switch elements each arranged in a neighborhood of an intersection between the corresponding first scan line and first signal line, and the

second substrate is provided with second scan lines connected to the scan driver and arranged so as to cross the second signal lines, the second signal lines, and second switch elements each arranged in a neighborhood of an intersection between the corresponding second scan line and second signal line.

[0036] Alternatively, the first substrate is provided with the first scan lines, the first signal lines, the second signal lines, first switch elements each arranged in a neighborhood of an intersection between the corresponding first scan line and first signal line, and second switch elements each arranged in a neighborhood of an intersection between the corresponding first scan lines and second signal line.

BRIEF DESCRIPTION OF THE DRAWINGS

[0037] **FIG. 1** is a view showing a sectional configuration of a pixel according to Embodiment 1 of a liquid crystal display apparatus of the present invention;

[0038] **FIG. 2** is a view showing a sectional configuration of a pixel according to a conventional liquid crystal display apparatus;

[0039] **FIGS. 3A and 3B** are views showing a configuration of the pixel according to Embodiment 1 of the liquid crystal display apparatus of the present invention;

[0040] **FIG. 4** is a diagram showing a configuration of Embodiment 1 of the liquid crystal display apparatus of the present invention;

[0041] **FIGS. 5A and 5B** are views useful in explaining control of a distribution of electric fields in the pixel portion according to Embodiment 1 of the liquid crystal display apparatus of the present invention;

[0042] **FIG. 6** is a chart showing a dependence of color tones on a driving voltage according to Embodiment 1 of the liquid crystal display apparatus of the present invention;

[0043] **FIG. 7A and 7B** are views showing a configuration of the pixel according to the conventional liquid crystal display apparatus;

[0044] **FIG. 8** is a diagram showing a configuration of the conventional liquid crystal display apparatus;

[0045] **FIG. 9** is a chart showing a dependence of color tones on a driving voltage according to the conventional liquid crystal display apparatus;

[0046] **FIG. 10** is a view showing a sectional configuration of a pixel according to Embodiment 3 of the liquid crystal display apparatus of the present invention;

[0047] **FIG. 11** is a view showing a sectional configuration of a pixel according to Embodiment 4 of the liquid crystal display apparatus of the present invention;

[0048] **FIG. 12** is a view showing a sectional configuration of a pixel according to Embodiment 5 of the liquid crystal display apparatus of the present invention;

[0049] **FIGS. 13A and 13B** are views showing a sectional configuration of a pixel according to Embodiment 6 of the liquid crystal display apparatus of the present invention;

[0050] **FIGS. 14A and 14B** are views showing a sectional configuration of a pixel according to Embodiment 7 of the liquid crystal display apparatus of the present invention;

[0051] **FIGS. 15A and 15B** are views showing a sectional configuration of a pixel according to Embodiment 8 of the liquid crystal display apparatus of the present invention;

[0052] **FIGS. 16A and 16B** are views useful in explaining control of the distribution of electric fields in the pixel portion according to Embodiment 8 of the liquid crystal display apparatus of the present invention;

[0053] **FIGS. 17A and 17B** are views showing a sectional configuration of a pixel according to Embodiment 9 of the liquid crystal display apparatus of the present invention;

[0054] **FIGS. 18A and 18B** are views showing a sectional configuration of a pixel according to Embodiment 10 of the liquid crystal display apparatus of the present invention;

[0055] **FIGS. 19A and 19B** are views showing a sectional configuration of a pixel according to Embodiment 11 of the liquid crystal display apparatus of the present invention;

[0056] **FIG. 20** is a view showing a sectional configuration of a pixel according to Embodiment 12 of the liquid crystal display apparatus of the present invention;

[0057] **FIG. 21** is a view showing a sectional configuration of a pixel according to Embodiment 13 of the liquid crystal display apparatus of the present invention; and

[0058] **FIG. 22** is a view showing a sectional configuration of a pixel according to Embodiment 14 of the liquid crystal display apparatus of the present invention.

DETAILED DESCRIPTION OF THE EMBODIMENTS

[0059] The causes of the variation in the color tones in the IPS display mode will be described below.

[0060] The transmissivity (T) in the IPS display mode is expressed by Equation [1]:

$$T = T_0 \cdot \sin^2(2\phi) \cdot \sin^2((\pi \cdot d_{\text{eff}} \cdot \Delta n_{\text{eff}}) / \lambda) \quad [1]$$

[0061] where T_0 denotes a correction factor, ϕ denotes the angle between an effective optical axis of a liquid crystal and a polarization direction of incident light, and ϕ denotes the wavelength of the incident light.

[0062] Accordingly, when the angle between the effective orientation direction of the liquid crystal and the polarization direction of the incident light is $\pi/4$ radian (45°), light of the wavelength λ , which is double the effective retardation $d_{\text{eff}} \cdot \Delta n_{\text{eff}}$ exhibits the highest transmissivity. That is, the variation in the effective retardation $d_{\text{eff}} \cdot \Delta n_{\text{eff}}$ varies the wavelength having the highest transmissivity, thereby varying the color tones.

[0063] Thus, the reason why the color tones, which have been bluish, become yellowish as the driving voltage increases is shown below. With a low driving voltage, the effective thickness d_{eff} is small due to the movement of part of the liquid crystal layer. With a high driving voltage, the effective thickness d_{eff} is large due to the movement of the entire liquid crystal layer.

[0064] The problems with the variations in color tones can be solved as described below.

[0065] Equation [1] indicates that the wavelength λ having the highest transmissivity is double the wavelength of the effective retardation $d_{\text{eff}} \cdot \Delta n_{\text{eff}}$. Accordingly, by adjusting the

Δn_{eff} so as to offset the variation in the effective thickness d_{eff} depending on the driving voltage, the variation in effective retardation $d_{\text{eff}} \Delta n_{\text{eff}}$ as well as the variation in the wavelength λ having the highest transmissivity can be restrained.

[0066] The effective refractive-index anisotropy Δn_{eff} can be adjusted in the following manner: When all the liquid crystals rise at a rising angle θ from the substrate 1, the effective refractive-index anisotropy $\Delta n_{\text{eff}} = \cos\theta \cdot \Delta n$, where Δn denotes the refractive-index anisotropy of the liquid crystal. Accordingly, the effective refractive-index anisotropy Δn_{eff} can be adjusted by regulating the start-up of the liquid crystals. In the above described conventional IPS display mode, since the liquid crystals are moved by the horizontal fields, the liquid crystals will not rise, and the refractive-index anisotropy Δn_{eff} of the liquid crystals substantially equals the refractive-index anisotropy Δn thereof. When, however, electric fields having vertical field components are applied to the liquid crystals, the latter, having a positive dielectric anisotropy, rise to reduce the effective refractive-index anisotropy Δn_{eff} of the liquid crystals.

[0067] When the liquid crystals rise to reduce the effective refractive-index anisotropy Δn_{eff} , the transmissivity of blue light, having a short wavelength, increases as shown by Equation [1].

[0068] In the conventional IPS display mode, the color tones become yellowish as the driving voltage increase. This problem can be solved by the present invention in the following manner: With a low driving voltage, no vertical field components are applied, thus preventing the liquid crystals from rising. At this time, a backlight or the gap is regulated so as to obtain a desired white. With a high driving voltage, vertical field components are applied to raise the liquid crystals, thereby reducing the effective refractive-index anisotropy Δn_{eff} . At this time, the blue, having a short wavelength, is emphasized to prevent the color tones from becoming yellowish. In this manner, the variation in the color tones depending on the driving voltage can be restrained.

[0069] As described above, if the color tones become yellowish as the driving voltage increases, the liquid crystals may be prevented from rising when the driving voltage is lowest, and may be raised when it is highest. On contrary, if the color tones become bluish as the driving voltage increases, the liquid crystals may be raised when the driving voltage is lowest, and may be prevented from rising when it is highest. On the other hand, if, instead of varying monotonously, the color tones become bluish and yellowish as the driving voltage varies, the liquid crystals may be prevented from rising when the color tones become bluest, and may be raised when they become yellowest.

[0070] According to the present invention, the liquid crystal panel alone can restrain the variation in the color tones depending on the driving voltage, with no particular restrictions on the light source. Further, the variation in the color tones depending on the driving voltage can be restrained without any restrictions on the retardation. Consequently, the refractive index or anisotropy of the liquid crystals is not restricted, and those crystals are available which have a high refractive-index anisotropy but are quick to respond and which have a wide range of operative temperatures.

[0071] It has been reported that when the face color of people in an image become bluish, viewers get the impres-

sion that the image is more degraded (Video Information Media Society Magazine, Vol. 54, No. 1, p. 93 to 100 (2,000)). Thus, in an animated image with a large number of people displayed, the face color can be restrained from becoming bluish by restraining the liquid crystals from rising, while making the display yellowish. That is, natural animated images can be displayed. On the contrary, for a display for a word processor program or the like, images can be clearly displayed by raising the liquid crystals and making the display bluish.

[0072] Equation [1] represents a transmissivity obtained in a normally black mode in which the apparatus enters a dark state when the driving voltage is low. The present invention, however, restrains the variation in the color tones by adjusting the retardation, and is thus applicable even to a normally white mode in which the apparatus enters a bright state when the driving voltage is low.

[0073] JP-A-9-244046 specification reports means for forming vertical field components to modulate the color tones. The means reported in this forms vertical field components on the basis of the difference between the average potential pair of comb teeth electrodes disposed on one of the substrates, and the potential at an opposite electrode disposed on the other substrate substantially halfway between the comb teeth electrode. In a manufacturing process, however, the upper and lower substrates may be misaligned, and the resulting locational relationship between the pair of comb teeth electrodes and the opposite electrode may not be as designed. In this case, the distribution of electric fields applied to the liquid crystals significantly differs from the design. Thus, to obtain desired chromaticity and brightness, the relationship between the potential at the comb teeth electrodes and the potential at the opposite electrode must be adjusted for each liquid crystal display apparatus.

[0074] The present invention is adapted to solve the above problems, and it is an object thereof to provide a configuration that prevents the distribution of electric fields applied to the liquid crystals even if the upper and lower substrates are misaligned as well as a new method for controlling vertical and horizontal field components.

[0075] Next, the present invention will be more specifically described on the basis of embodiments.

Embodiment 1

[0076] The configuration of Embodiment 1 of the present invention will be described with reference to FIGS. 3A, 3B, and 4. FIGS. 3A and 3B are views useful in explaining the configuration of a pixel portion, and FIG. 4 is a view useful in explaining a driving system for a liquid crystal display apparatus. More specifically, FIG. 3B is a bird's eye view useful in explaining the configuration of a pixel 41 on a substrate 1, FIG. 3A is a sectional view taken along line A-A' in FIG. 3B, and FIG. 4 is a view useful in explaining a configuration for wiring on the substrate 1.

[0077] A liquid crystal display apparatus of this embodiment has a configuration described below.

[0078] As shown in FIGS. 3A and 3B, the apparatus has a substrate 1, a substrate 2, a liquid crystal layer 12 sandwiched between the substrates 1 and 2 and having a positive dielectric anisotropy, a plurality of pixels 41 forming a

display section, a first pixel electrode **4** and a second pixel electrode **5** corresponding to the pixel **41**, and a common electrode **3** corresponding to the first pixel electrode **4** and the second pixel electrode **5**.

[0079] Further, as shown in **FIG. 4**, the apparatus comprises a first signal driver **32** for supplying a potential to the first pixel electrodes **4**, a second signal driver **33** for supplying a potential to the second pixel electrodes **5**, a signal control circuit **36** for controlling signals transmitted to the first signal driver **32** and the second signal driver **33**, a common electrode driver **34** for supplying a potential to the common electrode **3**, a scan driver **31** for selecting pixels, a signal control circuit **36**, and a display control circuit **37** for controlling the first and second signal drivers **32** and **33**, the common electrode driver **34**, and the scan driver **31**. The substrate **1** comprises a plurality of scan lines **21** connected to the scan driver **31**, a plurality of first signal lines **22a** connected to the first signal driver **32**, crossing the scan lines **21**, and corresponding to the first pixel electrodes **4**, a plurality of second signal lines **22b** connected to the second signal driver **33**, crossing the scan lines **21**, and corresponding to the second pixel electrodes **5**, pixels **41** each formed so as to correspond to an area enclosed by the corresponding scan line **21** and first signal line **22a**, first TFTs **24a** that are each a switch element arranged near the intersection between the corresponding scan line **21** and first signal line **22a** and electrically connected to these scan line **21** and second signal line **22a**, second TFTs **24b** that are each a switch element arranged near the intersection between the corresponding scan line **21** and first signal line **22b** and electrically connected to these scan line **21** and second signal line **22b**, the first pixel electrodes **4** each electrically connected to the first TFT **24a**, second pixel electrodes **5** each electrically connected to the second TFT **24b**, and common electrodes **3** each electrically connected to the common electrode driver **34**.

[0080] Further, as shown in **FIGS. 3A and 3B**, the substrate **1** has the common electrode **3**, the first pixel electrode **4**, the second pixel electrode **5**, and an insulated film **6** disposed thereon. The insulated film **6** has an alignment layer **7** disposed thereon. The substrate **2** has a color filter **8** and an alignment layer **7** disposed thereon. Surfaces of the substrates **1** and **2** which do not face the liquid crystal each have a polarizer **11** disposed thereon. Here, the order in which the electrodes and the insulated film are arranged is not limited in order to obtain the effects of the present invention.

[0081] The first pixel electrode **4**, the common electrode **3**, and the second pixel electrode **5** disposed on the substrate **1** are linear, each have a width of $4 \mu\text{m}$, and are arranged substantially in parallel. The material of these electrodes is chromium molybdenum. The second pixel electrode **5** is located between the common electrode **3** and the first pixel electrode **4**, and the material thereof is ITO (Indium-Tin-Oxide). The distance between the first pixel electrode **4** and the second pixel electrode **5** is $10 \mu\text{m}$. Then, the material or width of these electrodes is not particularly limited in order to obtain the effects of the present invention. However, for an increased aperture ratio, the second pixel electrode is desirably a transparent conductor such as ITO.

[0082] In this embodiment, since all the electrodes are thus disposed on the substrate **1**, the distribution of electric

fields does not vary even if the substrates **1** and **2** are misaligned. Thus, the potentials provided to the electrodes need not be adjusted for each liquid crystal display apparatus as in the above described JPA-9-244046 specification.

[0083] The substrates **1** and **2** are made of glass of 0.7 mm thickness. The first TFT **24a** and the second TFT **24b** are each made of amorphous silicon **23**. In the present invention, the two switch elements are thus disposed, so that arbitrary potentials can be provided to two of the three electrodes to form an arbitrary distribution of electric fields. As a result, movements such as the twisting or rising of liquid crystals can be controlled.

[0084] The first pixel electrode **4** and the common electrode **3** are each made of chromium molybdenum. Insulated films **6a**, **6b**, and **6c** are each composed of silicon nitride, and have a film thickness of 0.2 , 0.8 , and $0.8 \mu\text{m}$, respectively. The alignment layer **7** has a film thickness of 80 nm , and is subjected to a rubbing process for orienting a liquid crystal. A rubbing direction is inclined through 15° from the longitudinal direction of the first pixel electrode **4**.

[0085] Polymeric beads of $4 \mu\text{m}$ diameter are distributed between the substrates **1** and **2** to maintain a constant gap in a liquid crystal layer. The liquid crystal layer has a refractive-index anisotropy of 0.0947 and a dielectric anisotropy of 10.5 .

[0086] The polarizers **11** are arranged in a crossed Nicols manner so as to establish the normally black mode. A transmission axis of one of the polarizers aligns with the rubbing direction.

[0087] A back light is not restricted, and an under light type or a side light type may be used; a back light having color tones that provide a desired white is employed in the present invention.

[0088] Active matrix driving is used in this embodiment.

[0089] The distribution of electric fields according to this embodiment will be described with reference to **FIG. 1**. **FIG. 1** corresponds to **FIG. 3A**, to which equipotential lines have been added. As is apparent from equipotential lines **13**, the distribution of electric fields applied to the liquid crystal according to this embodiment is different from that obtained in the conventional IPS display mode as shown in **FIG. 2**. Further, in the IPS display mode, since only two electrodes are provided, the distribution of electric fields cannot be arbitrarily controlled. According to the present invention, however, since the two electrodes and the two switch elements are provided, the distribution of electric fields can be controlled.

[0090] The distribution of electric fields can be controlled by adjusting the ratio of the potentials provided to the common electrode **3**, the first pixel electrode **4**, and the second pixel electrode **5**. Distributions of electric fields obtained by adjusting the ratio of the potentials provided to the common electrode **3**, the first pixel electrode **4**, and the second pixel electrode **5** will be described with reference to **FIG. 5**. **FIG. 5A** shows a distribution of electric fields obtained when potentials are provided to the common electrode **3**, the first pixel electrode **4**, and the second pixel electrode **5** at a ratio of $0.0:1.0:0.5$. Likewise, **FIG. 5B** shows a distribution of electric fields obtained when potentials are provided to the common electrode **3**, the first pixel

electrode 4, and the second pixel electrode 5 at a ratio of 0.0:1.0:0.3. In this case, the rate of vertical field components is higher in **FIG. 5B** than in **FIG. 5A**. That is, in **FIG. 5B**, the liquid crystals rise more sharply than in **FIG. 5A**, thus emphasizing the blue. The distribution of electric fields can be adjusted in this manner.

[0091] As described above, in this embodiment, the vertical and horizontal field components are regulated by providing a potential to the second pixel electrode 5 so as to disturb electric fields formed by the first pixel electrode 4 and the common electrode 3.

[0092] The ratio of the potentials provided to the common electrode 3, the first pixel electrode 4, and the second pixel electrode 5 is determined in the following manner: The driving voltage, that is, a potential difference between the common electrode 3 and the first pixel electrode 4, is varied while keeping the potential provided to the second pixel electrode 5 equal to the average of the potentials provided to the common electrode 3 and the first pixel electrode 4. Then, a driving voltage with which the image becomes most bluish is determined. At this time, the refractive-index anisotropy of the liquid crystal and the gap in the liquid crystal layer are regulated so as to obtain a desired white. Then, while varying the driving voltage, the potential provided to the second pixel electrode is adjusted by the signal control circuit 36 so as to obtain the desired white. In this manner, the desired white is obtained in all gradations.

[0093] In this case, the distribution of electric fields depends on the sequence of the electrode layers, the shapes and sizes of the electrodes, the thicknesses and material of the insulated films, the material of the liquid crystal, the gap of the liquid crystal layers, the substrates, or the like. However, by correspondingly adjusting the ratio of the potentials provided to the common electrode 3, the first pixel electrode 4, and the second pixel electrode 5, the effects of the present invention can be obtained. Furthermore, the effects of the present invention can of course be obtained even if the refractive index of the liquid crystal layer, the gap in the liquid crystal layer, the back light, or the like is different from that of this embodiment.

[0094] In this embodiment, since all the electrodes are disposed on the substrate 1, the variation of the distribution of electric fields is very small even if the substrates 1 and 2 are misaligned. Consequently, this embodiment is unlikely to be affected by misalignment.

[0095] In this embodiment, the chromaticity can be controlled by adjusting the ratio of the potentials provided to the common electrode 3, the first pixel electrode 4, and the second pixel electrode 5 so as to change the manner of raising the liquid crystal. As a result, the dependence of color tones on the gradation is as shown in the chromaticity chart (CIE : Comission International de l'éclairage (1976) in **FIG. 6**. The symbol x in this figure denotes the chromaticity of "white". The amount of variation is below the detection limits of human beings; this embodiment provides a liquid crystal display apparatus in which the dependence of the color tones on the gradation is very low.

Comparative Example

[0096] The configuration of a comparative example of the present invention will be described with reference to **FIGS.**

7A, 7B and 8. FIGS. 7A and 7B are views useful in explaining the configuration of a pixel portion, and **FIG. 8** is a view useful in explaining a driving system for a liquid crystal display apparatus. More specifically, **FIG. 7B** is a bird's eye view useful in explaining the configuration of the pixel 41 on the substrate 1, **FIG. 7A** is a sectional view taken along line A-A' in **FIG. 7B**, and **FIG. 8** is a view useful in explaining a configuration for wiring on the substrate 1.

[0097] A liquid crystal display apparatus of this embodiment has a configuration described below.

[0098] As shown in **FIGS. 7A and 7B**, the apparatus has the substrate 1, the substrate 2, the liquid crystal layer 12 sandwiched between the substrates 1 and 2 and having a positive dielectric anisotropy, the plurality of pixels 41 forming the display section, the first pixel electrode 4 corresponding to the pixel 41, and the common electrode 3 corresponding to the pixel electrode 4.

[0099] Further, as shown in **FIG. 8**, the apparatus comprises the signal driver 32 for supplying a potential to the pixel electrodes 4, the common electrode driver 34 for supplying a potential to the common electrodes 3, the scan driver 31 for selecting pixels, and the display controller 37 for controlling the signal drivers 32, the common electrode driver 34, and the scan driver 31. The substrate 1 comprises the plurality of scan lines 21 connected to the scan driver 31, the plurality of signal lines 22 connected to the signal driver 32, crossing the scan lines 21, and corresponding to the pixel electrodes 4, the pixels 41 each formed so as to correspond to the area enclosed by the corresponding scan line 21 and signal line 22, the TFTs 24 that are each a switch element arranged near the intersection between the corresponding scan line 21 and signal line 22 and electrically connected to these scan line 21 and signal line 22, the pixel electrodes 4 each electrically connected to the TFT 24, and the common electrodes 3 each electrically connected to the common electrode driver 34.

[0100] Further, as shown in **FIGS. 7A and 7B**, the substrate 1 has the common electrode 3 and the pixel electrode 4 disposed thereon. The insulated film 6 has the alignment layer 7 disposed thereon. The substrate 2 has the color filter 8 and the alignment layer 7 disposed thereon. The surfaces of the substrates 1 and 2 which do not face the liquid crystal each have the polarizer 11 disposed thereon.

[0101] The pixel electrode 4 and the common electrode 3, both disposed on the substrate 1, are linear, each have a width of 4 μm , and are arranged substantially in parallel. The distance between the pixel electrode 4 and the common electrode 3 is 10 μm .

[0102] The substrates 1 and 2 are made of glass of 0.7 mm thickness. The TFT 24 is made of the amorphous silicon 23. The pixel electrode 4 and the common electrode 3 are each made of chromium molybdenum. The insulated films 6a and 6b are each composed of silicon nitride, and have a film thickness of 0.2 and 0.8 μm , respectively. The alignment layer 7 has a film thickness of 80 nm, and is subjected to a rubbing process for orienting the liquid crystal. The rubbing direction is inclined through 15° from the longitudinal direction of the first pixel electrode 4.

[0103] The polymeric beads of 4 μm diameter are distributed between the substrates 1 and 2 to maintain a constant

gap in a liquid crystal layer. The liquid crystal layer has a refractive-index anisotropy of 0.0947 and a dielectric anisotropy of 10.5.

[0104] The polarizers 11 are arranged in a crossed Nicols manner so as to establish the normally black mode. The transmission axis of one of the polarizers aligns with the rubbing direction.

[0105] The backlight is the same as in Embodiment 1.

[0106] The active matrix driving is used in this example.

[0107] With respect to the sectional configuration of the pixel portion in this comparative example, a comparison between FIGS. 7A and 3A indicates that the configuration on the substrate 2 in this comparative example is the same as in Embodiment 1 and that the configuration on the substrate 1 therein is also the same as in Embodiment 1 except that the substrate 1 does not have the second pixel electrode 5 nor the insulated film 6c disposed thereon.

[0108] Accordingly, in this comparative example, the lack of the second pixel electrode 5 prevents the distribution of electric fields from being adjusted. Further, the chromaticity varies depending on the driving voltage as shown in FIG. 9, and the amount of the variation is beyond the detection limits of human beings. FIG. 9 shows the chromaticity chart (CIE: Commission International de l'éclairage (1976). The symbol x in this figure denotes the chromaticity of "white".

Embodiment 2

[0109] Embodiment 2 of the liquid crystal display apparatus according to the present invention is the same as the liquid crystal display apparatus of Embodiment 1 except that the common electrode 3, the first pixel electrode 4, and the second pixel electrode 5 are made of ITO, a transparent conductive material.

[0110] In Embodiment 2, the ratio of the potentials provided to the common electrode 3, the first pixel electrode 4, and the second pixel electrode 5 is different from that in Embodiment 1. This is because the area through which light is transmitted changes to vary the average of E_z/E_x in the area through which light is transmitted. This will be described with reference to FIG. 1. As is apparent from the equipotential lines 13 in FIG. 1, the area on the common electrode 3 and the first pixel electrode 4 has a larger E_z/E_x than the area between the common electrode 3 and the first pixel electrode 4. Thus, if the common electrode 3 and the first pixel electrode 4 are opaque, light is transmitted through only areas having a small E_z/E_x . On the other hand, if the above three electrodes are transparent, light is transmitted through both areas having a small E_z/E_x and those having a large E_z/E_x . As a result, the use of transparent electrodes increases the average E_z/E_x of the areas through which light is transmitted, that is, the effective E_z/E_x thereof, compared to the use of opaque electrodes.

[0111] Thus, the ratio of the potentials provided to the common electrode 3, the first pixel electrode 4, and the second pixel electrode 5 differs from that in Embodiment 1, but the variation in the color tone depending on the driving voltage can be restrained as in Embodiment 1 by adjusting the ratio of the potentials in the same manner as in Embodiment 1.

Embodiment 3

[0112] Embodiment 3 of the liquid crystal display apparatus according to the present invention is the same as the liquid crystal display apparatus of Embodiment 1 except that the second pixel electrode 5 overlaps the common electrode 3 and is made of chromium molybdenum like the common electrode 3 and the first pixel electrode 4, as shown in FIG. 10.

[0113] In this embodiment, even if the second pixel electrode 5 is not transparent, the aperture ratio does not decrease. Accordingly, the material of the electrodes can be selected more flexibly.

[0114] Similar effects are also obtained if the second pixel electrode 5 overlaps the first pixel electrode 4.

Embodiment 4

[0115] Embodiment 4 of the liquid crystal display apparatus according to the present invention is the same as the liquid crystal display apparatus of Embodiment 3 except that the second pixel electrode 5 is as wide as or narrower than the common electrode 3, overlapped thereby, as shown in FIG. 11.

[0116] According to this embodiment, compared to Embodiment 3, the aperture ratio is restrained from decreasing even if the misalignment of a mask during a manufacturing process causes the second pixel electrode 5 and the common electrode 3 to be offset from each other.

Embodiment 5

[0117] Embodiment 5 of the liquid crystal display apparatus according to the present invention is the same as the liquid crystal display apparatus of Embodiment 3 except that the second pixel electrode 5 is wider than the common electrode 3, overlapped thereby and that the second pixel electrode 5 is composed of ITO, as shown in FIG. 12.

[0118] According to this embodiment, compared to Embodiment 3, vertical field components are obtained more easily, thus widening a range over which the color tones can be adjusted.

Embodiment 6

[0119] The configuration of Embodiment 6 of the present invention will be described with reference to FIGS. 13A and 13B. FIG. 13B is a bird's eye view useful in explaining the configuration of the pixel 41 on the substrate 1, and FIG. 13A is a sectional view taken along line A-A' in FIG. 13B.

[0120] Embodiment 6 of the liquid crystal display apparatus according to the present invention is the same as the liquid crystal display apparatus of Embodiment 1 except for the pixel configuration on the substrate 1, as shown in FIGS. 13A and 13B.

[0121] As shown in FIG. 13B, the plurality of first pixel electrodes 4 are connected together via a first junction, the plurality of second pixel electrodes 5 are connected together via a second junction, the plurality of first pixel electrodes 4 and the first junction do not overlap the plurality of second pixel electrodes 5 and the second junction.

[0122] As shown in FIG. 13A, the first pixel electrodes 4 and the second pixel electrodes 5 can be arranged in the same layer.

[0123] Furthermore, the first pixel electrodes 4, the second pixel electrodes 5, the first signal line 22a, and the second signal line 22b are arranged in the same layer, and the common electrodes 3 and a scanning line 21 are arranged in the same layer.

[0124] According to this embodiment, compared to Embodiment 1, the number of insulated layers and thus the number of photolithography steps in the manufacturing process can be reduced.

Embodiment 7

[0125] The configuration of Embodiment 7 of the present invention will be described with reference to FIGS. 14A and 14B. FIG. 14B is a bird's eye view useful in explaining the configuration of the pixel 41 on the substrate 1, and FIG. 14A is a sectional view taken along line A-A' in FIG. 14B.

[0126] Embodiment 7 of the liquid crystal display apparatus according to the present invention is the same as the liquid crystal display apparatus of Embodiment 1 except that the second pixel electrode 5 is arranged below the first pixel electrode 4 and the common electrode 3 via the insulated film 6c and that the second pixel electrode 5 overlaps the common electrode 3 and the first pixel electrode 4, as shown in FIGS. 14A and 14B.

[0127] According to this embodiment, as compared with Embodiment 1, the variation of the distribution of electric fields is small even if the mask is misaligned during the manufacturing process. Consequently, this embodiment is unlikely to be affected by the misalignment of the mask.

[0128] It should be appreciated that the present invention is applicable to the case where the common electrode 3, the first pixel electrode 4, and the second pixel electrode 5 are all formed of a transparent conductive material such as ITO.

Embodiment 8

[0129] The configuration of Embodiment 8 of the present invention will be described with reference to FIGS. 15A and 15B. FIG. 15B is a bird's eye view useful in explaining the configuration of the pixel 41 on the substrate 1, and FIG. 15A is a sectional view taken along line A-A' in FIG. 15B.

[0130] In Embodiment 8 of the liquid crystal display apparatus according to the present invention, the common electrode 3 is located between the first pixel electrode 4 and the second pixel electrode 5 as shown in FIGS. 15A and 15B and is composed of ITO. The first pixel electrode 4 and the second pixel electrode 5 are composed of chromium molybdenum. This liquid crystal display apparatus is the same as that of Embodiment 1 in the other points.

[0131] The distribution of electric fields can be controlled by adjusting the ratio of the potentials provided to the common electrode 3, the first pixel electrode 4, and the second pixel electrode 5. Distributions of electric fields obtained by adjusting the ratio of the potentials provided to the common electrode 3, the first pixel electrode 4, and the second pixel electrode 5 will be described with reference to FIGS. 16A and 16B. FIG. 16A shows a distribution of electric fields obtained when potentials are provided to the common electrode 3, the first pixel electrode 4, and the second pixel electrode 5 at a ratio of 0.0:1.0:1.0. Likewise, FIG. 16B shows a distribution of electric fields obtained

when potentials are provided to the common electrode 3, the first pixel electrode 4, and the second pixel electrode 5 at a ratio of 0.0:1.0:0.6. In this case, the rate of vertical field components is higher in FIG. 16B than in FIG. 16A. That is, the distribution of electric fields can be adjusted.

[0132] As described above, in this embodiment, the vertical and horizontal field components are regulated by providing different potentials to the first and second pixel electrodes 4 and 5 so as to disrupt the symmetry of the distribution of electric fields with respect to a centerline of the common electrode 3 in the longitudinal direction.

[0133] In this embodiment, the ratio of the potentials provided to the common electrode 3, the first pixel electrode 4, and the second pixel electrode 5 is determined in the following manner: The driving voltage is varied while keeping the potential at the second pixel electrode 5 equal to the potential at the first pixel electrode 4. Then, a driving voltage with which the image becomes most bluish is determined. At this time, the refractive-index anisotropy of the liquid crystal and the gap in the liquid crystal layer are regulated so as to obtain a desired white. Then, while varying the driving voltage, the potential provided to the second pixel electrode 5 is adjusted by the signal control circuit 36 so as to obtain the desired white. At this time, different potentials are provided to the first and second pixel electrodes 4 and 5 to adjust the ratio of the vertical field components to the horizontal field components. In this manner, the dependence of the chromaticity on the gradation can be restrained.

Embodiment 9

[0134] The configuration of Embodiment 9 of the present invention will be described with reference to FIGS. 17A and 17B. FIG. 17B is a bird's eye view useful in explaining the configuration of the pixel 41 on the substrate 1, and FIG. 17A is a sectional view taken along line A-A' in FIG. 17B.

[0135] Embodiment 9 of the liquid crystal display apparatus according to the present invention is the same as the liquid crystal display apparatus of Embodiment 1 except for the pixel configuration on the substrate 1, as shown in FIGS. 17A and 17B.

[0136] As shown in FIG. 17B, plural pieces of the first pixel electrode 4 are connected together via a first junction, plural pieces of the second pixel electrode 5 are connected together via a second junction, the plural pieces of the first pixel electrode 4 and the first junction do not overlap the plural pieces of the second pixel electrodes 5 and the second junction.

[0137] Thus, as shown in FIG. 17A, the first pixel electrode 4 and the second pixel electrode 5 can be arranged in the same layer.

[0138] Furthermore, the first pixel electrode 4, the second pixel electrode 5, the first signal line 22a, and the second signal line 22b are arranged in the same layer, and the common electrode 3 and the scanning line 21 are arranged in the same layer.

[0139] According to this embodiment, compared to Embodiment 8, the number of insulated layers and thus the number of photolithography steps in the manufacturing process can be reduced.

Embodiment 10

[0140] The configuration of Embodiment 10 of the present invention will be described with reference to FIGS. 18A and 18B. FIG. 18B is a bird's eye view useful in explaining the configuration of the pixel 41 on the substrate 1, and FIG. 18A is a sectional view taken along line A-A' in FIG. 18B.

[0141] Embodiment 10 of the liquid crystal display apparatus according to the present invention is the same as the liquid crystal display apparatus of Embodiment 8 except that the common electrode 3 is arranged below the first pixel electrode 4 and the second pixel electrode 5 via the insulated film 6c and that the common electrode 3 overlaps the common electrode 3 and the second pixel electrode 5, as shown in FIGS. 18A and 18B.

[0142] According to this embodiment, compared to Embodiment 8, the variation of the distribution of electric fields is small even if the mask is misaligned during the manufacturing process. Consequently, this embodiment is unlikely to be affected by the misalignment of the mask.

[0143] Further, since the common electrode 3 is arranged below the first and second pixel electrodes 4 and 5, if TFTs of a bottom gate structure are to be produced, the common electrode 3 and the scan line 21 can be formed in the same layer. Consequently, the configuration on the substrate 1 can be produced more easily than in Embodiment 7.

[0144] It should be appreciated that the present invention is applicable to the case where the common electrode 3, the first pixel electrode 4, and the second pixel electrode 5 are all formed of a transparent conductive material such as ITO.

Embodiment 11

[0145] The configuration of Embodiment 11 of the present invention will be described with reference to FIGS. 19A and 19B. FIG. 19B is a bird's eye view useful in explaining the configuration of the pixel 41 on the substrate 1, and FIG. 19A is a sectional view taken along line A-A' in FIG. 19B.

[0146] Embodiment 11 of the liquid crystal display apparatus according to the present invention is the same as the liquid crystal display apparatus of Embodiment 10 except for the pixel configuration on the substrate 1, as shown in FIGS. 19A and 19B.

[0147] As shown in FIG. 19B, the plural pieces of the first pixel electrode 4 are connected together via a first junction, the plural pieces of the second pixel electrode 5 are connected together via a second junction, the plural pieces of the first pixel electrode 4 and the first junction do not overlap the plural pieces of the second pixel electrode 5 and the second junction.

[0148] Thus, as shown in FIG. 19A, the first pixel electrode 4 and the second pixel electrode 5 can be arranged in the same layer.

[0149] Furthermore, the first pixel electrode 4, the second pixel electrode 5, the first signal line 22a, and the second signal line 22b are arranged in the same layer, and the common electrode 3 and the scanning line 21 are arranged in the same layer.

[0150] According to this embodiment, compared to Embodiment 10, the number of insulated layers and thus the number of photolithography steps in the manufacturing process can be reduced.

Embodiment 12

[0151] Embodiment 12 of the liquid crystal display apparatus according to the present invention is the same as the liquid crystal display apparatus of Embodiment 7 except that the common electrode 3 is disposed on the substrate 2 and overlaps the first and second pixel electrodes 4 and 5.

[0152] As in this embodiment, even if the electrodes are located on both the substrates 1 and 2, the distribution of electric fields applied to the liquid crystal is prevented from varying by arranging the electrodes on the substrate 2 so as to overlap all the corresponding electrodes on the substrate 1, even if the substrates 1 and 2 are misaligned. Thus, a desired white can be obtained without the need to adjust the potentials provided to the electrodes for each liquid crystal display apparatus.

[0153] In this embodiment, the ratio of the potentials provided to the electrodes is adjusted in the following manner: A potential difference $(x_1 - x_2)$ between the first pixel electrode 4 and the second pixel electrode 5 is increased while maintaining the ratio of the potentials at the common electrode 3, the first pixel electrode 4, and the second pixel electrode 5 at $(x_1 + x_2)/2 : x_1 : x_2$, where x_1 denotes the potential of the first pixel element 4, and x_2 denotes the potential of the second pixel element 5. Then, a potential difference between the first pixel electrode 4 and the second pixel electrode 5 with which the image becomes most bluish is determined. At this time, the refractive-index anisotropy of the liquid crystal and the gap in the liquid crystal layer are regulated so as to obtain the desired white. Then, while varying the potential difference between the first pixel electrode 4 and the second pixel electrode 5, the potential ratio between the common electrode 3 and the first pixel electrode 4 and the second pixel electrode 5 is adjusted by the signal control circuit 36 so as to obtain the desired white. For example, the vertical field components increase when the potential ratio is at $x_1 : x_1 : x_2$. In this manner, the dependence of the color tones on the gradation can be restrained.

Embodiment 13

[0154] Embodiment 13 of the liquid crystal display apparatus according to the present invention is the same as the liquid crystal display apparatus of Embodiment 12 except that a dielectric 9 of 2 μm thickness is arranged on the second pixel electrode 5 as shown in FIG. 21.

[0155] According to this embodiment, compared to Embodiment 12, the horizontal field components in the liquid crystal layer increase as shown by the equipotential lines 13, thereby improving the transmissivity.

[0156] Further, the present invention provides the same effect even if the dielectric 9 is a color filter or a flattened film.

Embodiment 14

[0157] Embodiment 14 of the liquid crystal display apparatus according to the present invention is the same as the liquid crystal display apparatus of Embodiment 13 except that a recess 9' penetrating the dielectric 9 is formed in a portion of the dielectric 9 which overlaps the second pixel electrode 5 as shown in FIG. 22.

[0158] In the dielectric 9, an electrode is considered to be present only in the recess 9', so that by processing the recess

into an arbitrary shape, a distribution of electric fields similar to the one obtained if an electrode of an arbitrary shape is produced.

[0159] In this embodiment, the recess 9' is formed in the dielectric 9 near the center of the pixel. This is equivalent to the arrangement of the common electrode 3 only in the neighborhood of the center of the pixel; the liquid crystal rises toward the location of this recess. Consequently, the liquid crystal rises in all directions to improve the angle of visibility characteristic.

[0160] The manner of providing the potentials to the electrodes will be described below in brief.

[0161] In the conventional IPS display mode, the color tones monotonously become yellowish as the driving voltage increases. Thus, the liquid crystal need not be raised when the driving voltage is highest. Consequently, with the liquid crystal display apparatus configured as in Embodiment 1, when the driving voltage is lowest, the potential provided to the second pixel electrode 5 may be set equal to the average of the potentials provided to the first pixel electrode 4 and the common electrode 3 so that a distribution of electric fields similar to that obtained in the conventional IPS display mode can be obtained. On the contrary, if the color tones monotonously become bluish as the driving voltage increases, the potential provided to the second pixel electrode 5 may be set equal to the average of the potentials provided to the first pixel electrode 4 and the common electrode 3 when the driving voltage is highest.

[0162] Also with the liquid crystal display apparatus configured as described in Embodiment 2 or 7, a distribution of electric fields similar to that obtained in the conventional IPS display mode is obtained when the potential provided to the second pixel electrode 5 is the average of the potentials provided to the first pixel electrode 4 and the common electrode 3. Accordingly, similarly, the potential provided to the second pixel electrode 5 may be set equal to the average of the potentials provided to the first pixel electrode 4 and the common electrode 3 when the driving voltage is highest or lowest.

[0163] Likewise, with the liquid crystal display apparatus configured as described in any of Embodiments 3 to 5, when the driving voltage is highest or lowest, the potential provided to the second pixel electrode 5 may be set substantially equal to the potential provided to any one of the first pixel electrode 4 or the common electrode 3, which is overlapped by the second pixel electrode 5, so that a distribution of electric fields similar to that obtained in the conventional IPS display mode can be obtained.

[0164] Similarly, with the liquid crystal display apparatus configured as described in Embodiment 8 or 11, when the driving voltage is highest or lowest, the potential provided to the second pixel electrode 5 may be set equal to the potential provided to the first pixel electrode so that a distribution of electric fields similar to that obtained in the conventional IPS display mode can be obtained.

[0165] Likewise, with the liquid crystal display apparatus configured as described in any of Embodiments 12 to 14, when the difference between the potentials provided to the first pixel element 4 and to the second pixel element 5 is largest or smallest, the potential provided to the common electrode 3 may be set substantially equal to the average of

the potentials provided to the first and second pixel electrodes 4 and 5 so that a distribution of electric fields similar to that obtained in the conventional IPS display mode can be obtained.

[0166] According to the present invention, the common electrode, the first pixel electrode, and the second pixel electrode can adjust the distribution of electric fields applied to the liquid crystal, thus providing a liquid crystal display apparatus which can adjust the color tones and which can restrain the variation in the color tones depending on the driving voltage.

1. A liquid crystal display apparatus, comprising:
a first substrate;
a second substrate arranged opposite the first substrate;
a liquid crystal layer sandwiched between said first substrate and said second substrate; and
a plurality of pixels which are sandwiched between said first substrate and said second substrate and form a display section,

wherein each of said pixels is provided with a first pixel electrode and a second pixel electrode both corresponding to said pixel, and a common electrode corresponding to said first and second pixel electrodes.

2. The liquid crystal display apparatus according to claim 1, wherein said first and second pixel electrodes can provide each of said pixels with a corresponding potential.

3. The liquid crystal display apparatus according to claim 1, further comprising:

- a first signal driver for supplying a potential to said first pixel electrodes;
- a second signal driver for supplying a potential to said second pixel electrodes; and
- a signal control circuit for controlling signals transmitted to said first and second signal drivers.

4. The liquid crystal display apparatus according to claim 1, wherein said first pixel electrode, said common electrodes, and said second pixel electrodes are disposed on said first substrate.

5. The liquid crystal display apparatus according to claim 4,

wherein said first pixel electrode and said common electrode are linear and are arranged substantially in parallel, and

said second pixel electrode is located between said first pixel electrode and said common electrode.

6. The liquid crystal display apparatus according to claim 4,

wherein in each pixel, said first pixel electrode and said common electrode are linear and are arranged substantially in parallel, and

at least part of said second pixel electrode overlaps said first pixel electrode or said common electrode.

7. The liquid crystal display apparatus according to claim 6,

wherein said second pixel electrode is linear, and said second pixel electrode is as wide as or narrower than the first pixel electrode or common electrode, which is overlapped by the part of the second pixel electrodes.

8. The liquid crystal display apparatus according to claim 6,

wherein said second pixel electrode is linear, and said second pixel electrode is wider than said first pixel electrodes or common electrode, which is overlapped by the part of the second pixel electrode.

9. The liquid crystal display apparatus according to claim 4,

wherein said first pixel electrode and said common electrode are linear and are arranged substantially in parallel, and

said second pixel electrode is located below said first pixel electrode and said common electrode, said second pixel electrode overlaps said first pixel electrode and said common electrode, and insulated films are disposed between said second pixel electrode and said first pixel electrode and between said second pixel electrode and said common electrode.

10. The liquid crystal display apparatus according to claim 4,

wherein said first pixel electrode and said second pixel electrode are linear and are arranged substantially in parallel, and

said common electrode is located between said first pixel electrode and said second pixel electrode.

11. The liquid crystal display apparatus according to claim 4,

wherein said first and said second pixel electrodes are linear and are arranged substantially in parallel, and

at least part of said common electrode overlaps said first or said second pixel electrode.

12. The liquid crystal display apparatus according to claim 4,

wherein said first and said second pixel electrodes are linear and are arranged substantially in parallel, and

said common electrode is located below said first and second pixel electrodes, said common electrode overlaps said first and said second pixel electrodes, and insulated films are disposed between said common electrode and said first pixel electrode and between said common electrode and said second pixel electrode.

13. The liquid crystal display apparatus according to claim 5,

wherein plural pieces of said first pixel electrode arranged for said corresponding pixels are connected together via a first junction, plural pieces of said second pixel electrode arranged for said corresponding pixels are connected together via a second junction, and said plural pieces of said first pixel electrode and said first junction do not overlap said plural pieces of second pixel electrodes and said second junction.

14. The liquid crystal display apparatus according to claim 13,

wherein said first pixel electrode, said second pixel electrode, said first signal line, and said second signal line are arranged in the same layer, and said common electrode and said scan line are arranged in the same layer.

15. The liquid crystal display apparatus according to claim 1,

wherein said first and said second pixel electrodes are disposed on said first substrate, and said common electrodes are disposed on said second substrate.

16. The liquid crystal display apparatus according to claim 15,

wherein said first and said second pixel electrodes are linear and are arranged substantially in parallel, and said common electrode overlaps said first and said second pixel electrodes.

17. The liquid crystal display apparatus according to claim 15,

wherein a dielectric having a thickness of $1.5 \mu\text{m}$ or more is arranged on said common electrode.

18. The liquid crystal display apparatus according to claim 17,

wherein a portion of the dielectric which overlaps said common electrode includes a recess penetrating said dielectric or having a depth amounting to 50% or more of the thickness.

19. The liquid crystal display apparatus according to claim 5,

wherein when a difference between potentials provided to said first pixel electrode and to said common electrode is largest or smallest, a potential provided to said second pixel electrode is substantially equal to an average of the potentials provided to said first pixel electrode and to said common electrode.

20. The liquid crystal display apparatus according to claim 6,

wherein when the difference between the potentials provided to said first pixel electrode and to said common electrode is largest or smallest, the potential provided to said second pixel electrode is substantially equal to the potential provided to said first pixel electrode or common electrode, which is overlapped by said second pixel electrode.

21. The liquid crystal display apparatus according to claim 10,

wherein when the difference between the potentials provided to said first pixel electrode and to said common electrode is largest or smallest, the potential provided to said second pixel electrode is substantially equal to the potential provided to said first pixel electrode.

22. The liquid crystal display apparatus according to claim 16,

wherein when a difference between the potentials provided to said first pixel electrode and to said second pixel electrode is largest or smallest, the potential provided to said common electrode is substantially

equal to an average of the potentials provided to said first pixel electrode and to said second pixel electrode.

23. The liquid crystal display apparatus according to claim 1,

wherein said liquid crystal layer has a positive dielectric anisotropy.

24. The liquid crystal display apparatus according to claim 1, further comprising:

a scan driver;

a plurality of first scan lines connected to the scan driver;

a plurality of first signal lines connected to said first signal driver and disposed so as to cross said plurality of first scan lines; and

second signal lines connected to said second signal driver, and

wherein said plurality of pixels each correspond to an area enclosed by a corresponding one of said plurality of first scan lines and a corresponding one of said plurality of first signal lines, said first pixel electrodes correspond to said first signal lines, and said second pixel electrodes corresponds to said second signal lines.

25. The liquid crystal display apparatus according to claim 24,

wherein said first substrate is provided with:

said first scan lines;

said first signal lines; and

first switch elements each arranged in a neighborhood of an intersection between said corresponding first scan line and first signal line, and

the second substrate is provided with second scan lines connected to said scan driver and arranged so as to cross said second signal lines, said second signal lines,

and second switch elements each arranged in a neighborhood of an intersection between said corresponding said second scan line and said second signal line.

26. The liquid crystal display apparatus according to claim 24,

wherein said first substrate is provided with:

said first scan lines;

said first signal lines;

said second signal lines;

first switch elements each arranged in a neighborhood of an intersection between said corresponding first scan line and said first signal line; and

second switch elements each arranged in a neighborhood of an intersection between said corresponding first scan line and said second signal line.

27. A liquid crystal display apparatus comprising:

a first substrate;

a second substrate arranged opposite said first substrate;

a liquid crystal layer sandwiched between said first substrate and said second substrate; and

a plurality of pixels which are sandwiched between said first substrate and said second substrate and form a display section,

wherein each of said pixels has a first and a second pixel electrodes each corresponding to said pixel and disposed on said first substrate, and a common electrode corresponding to said first and said second pixel elements and disposed on either said first or said second substrate.

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摘要(译)

提供一种液晶显示装置，包括：基板；与基板相对设置的另一基板；夹在两个基板之间的液晶层和形成显示部分的多个像素，其中每个像素设置有对应于像素的第一和第二像素电极，以及对应于第一和第二像素的公共电极。第二像素电极。

