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(54) **MULTIDOMAIN LIQUID CRYSTAL DISPLAY DEVICE**

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(57) **ABSTRACT**

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The present invention relates to a multidomain liquid crystal display device that is capable of appropriately adjusting the angle of the boundary line of a slit formed on one substrate and the pitch of liquid crystal to thereby obtain multidomain for a main viewing angle on each of domains, thereby allowing a simplified manufacturing process and improving the viewing angle and transmissivity. The multidomain liquid crystal display device includes first and second substrates, the first substrate having a plurality of pixel areas, a transparent conductive layer on each pixel area of the first substrate and having at least one or more slits inclined at a prescribed angle with respect to a boundary of the pixel area, and a liquid crystal layer between the first and second substrates.

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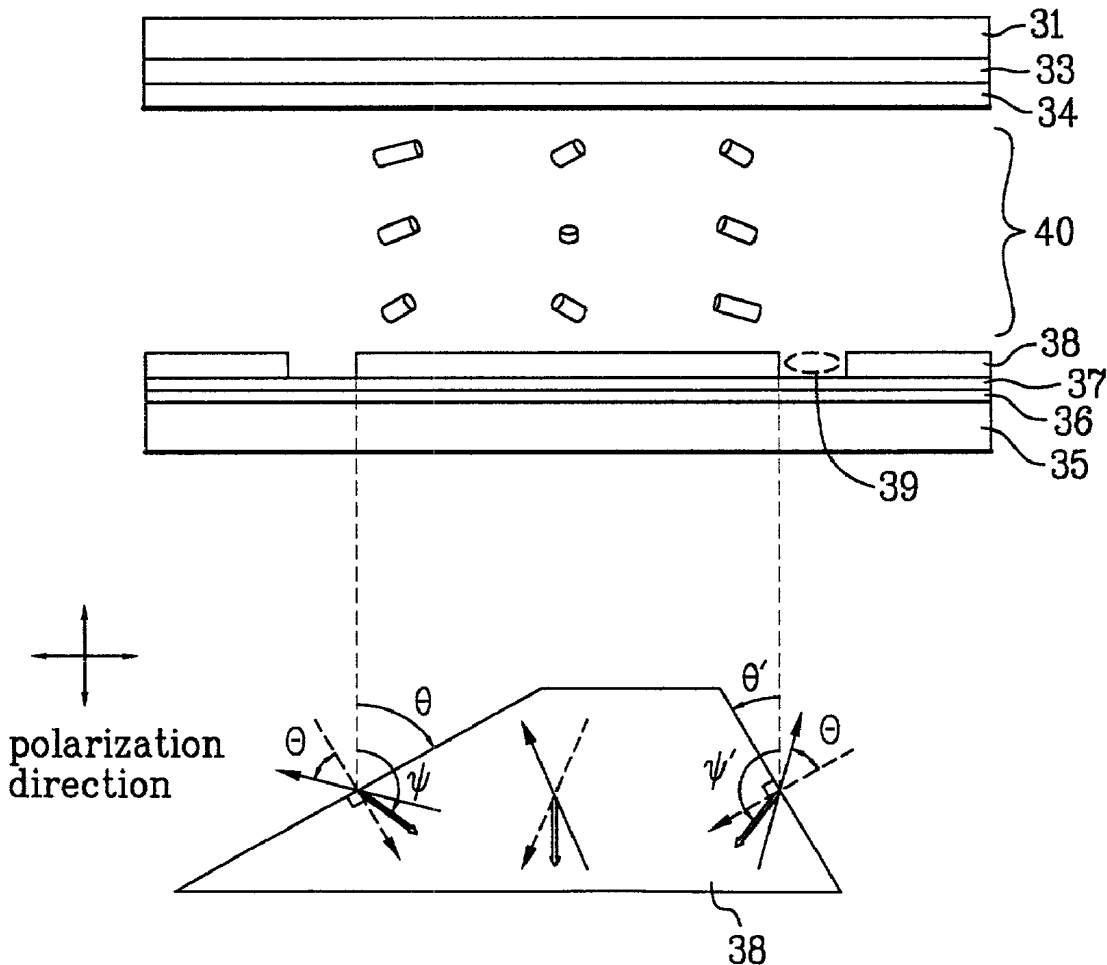


FIG. 1

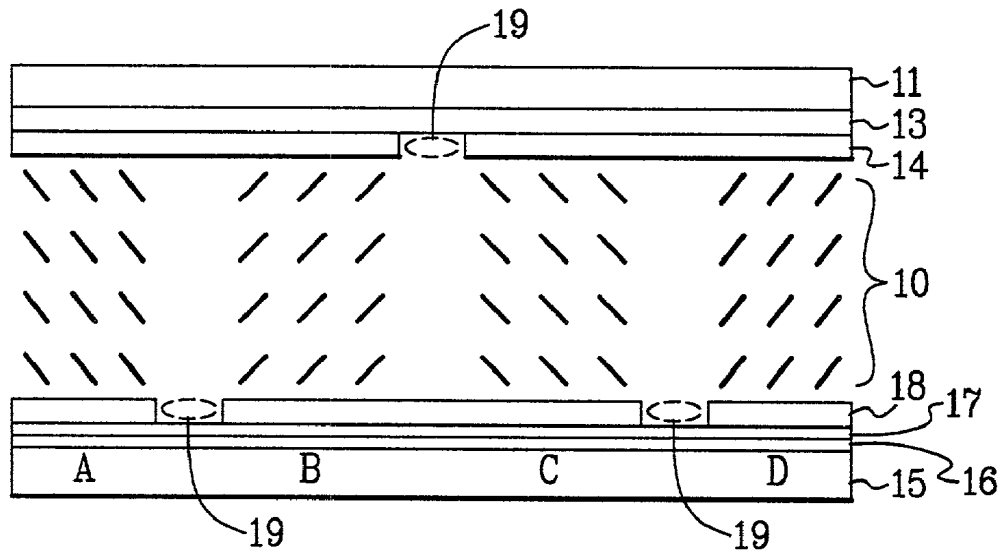


FIG. 2

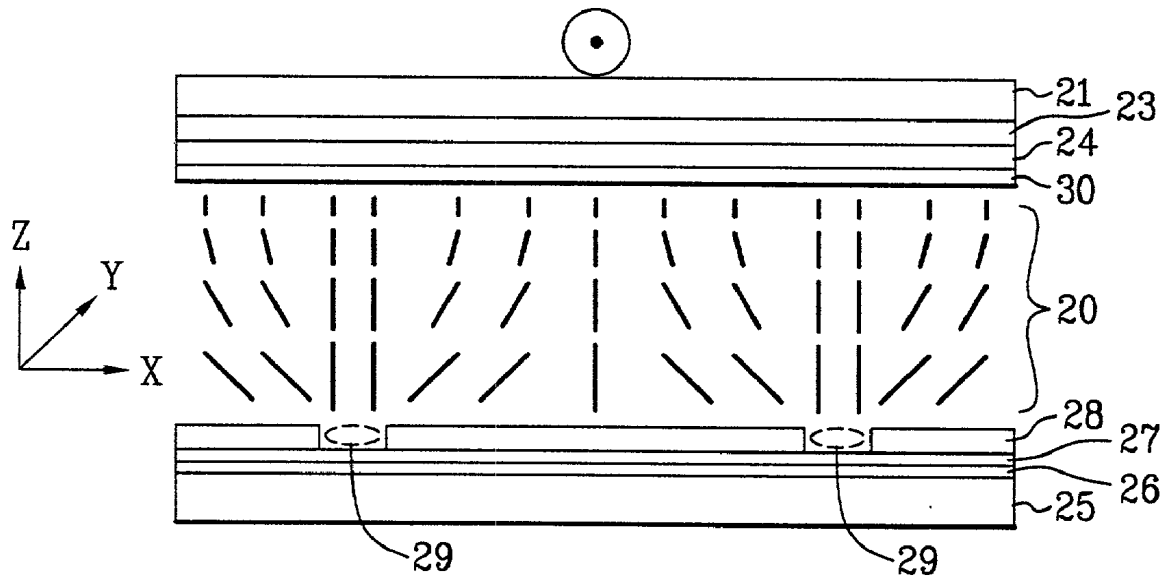


FIG. 3A

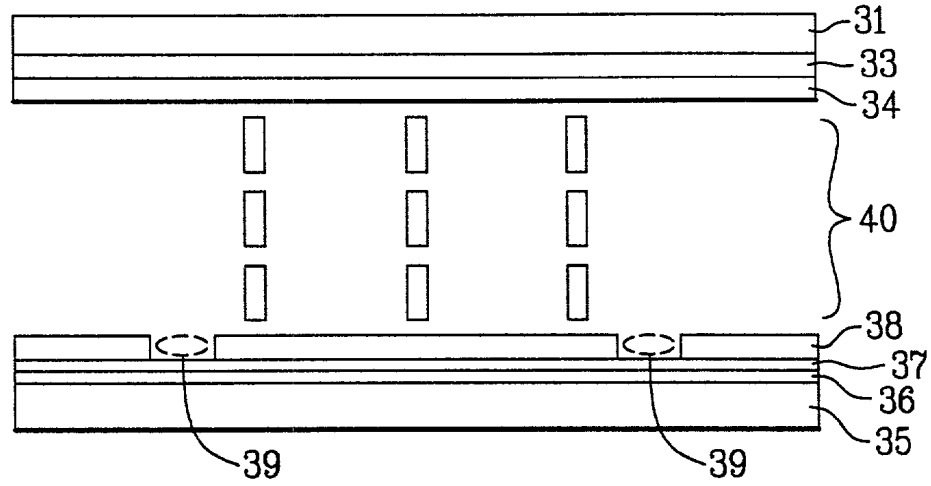


FIG. 3B

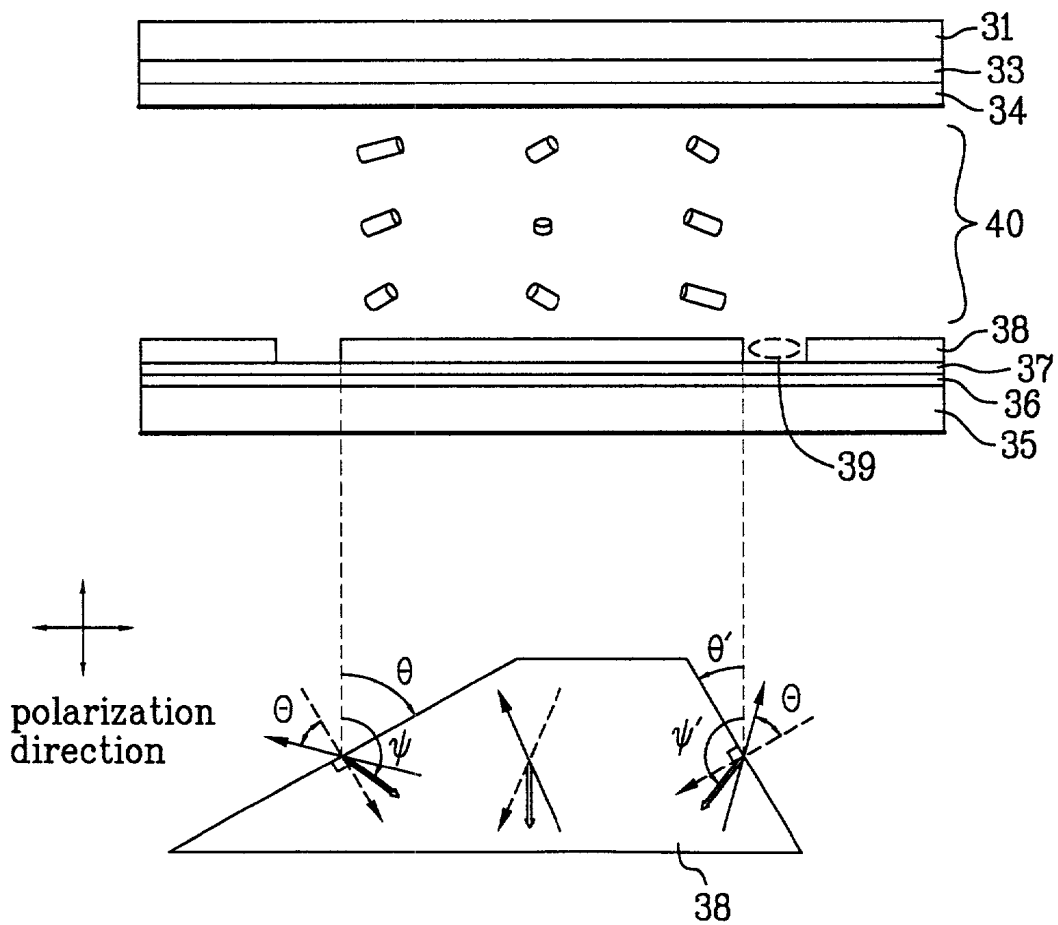


FIG. 4

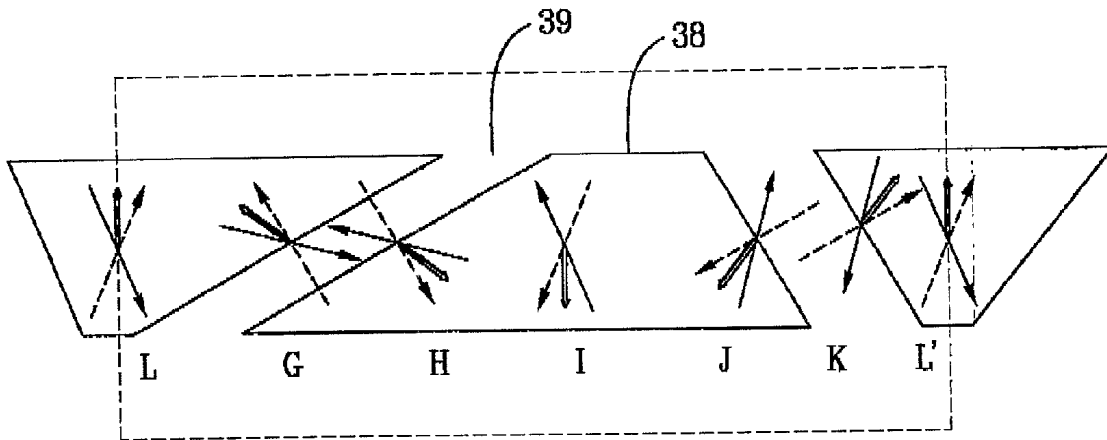
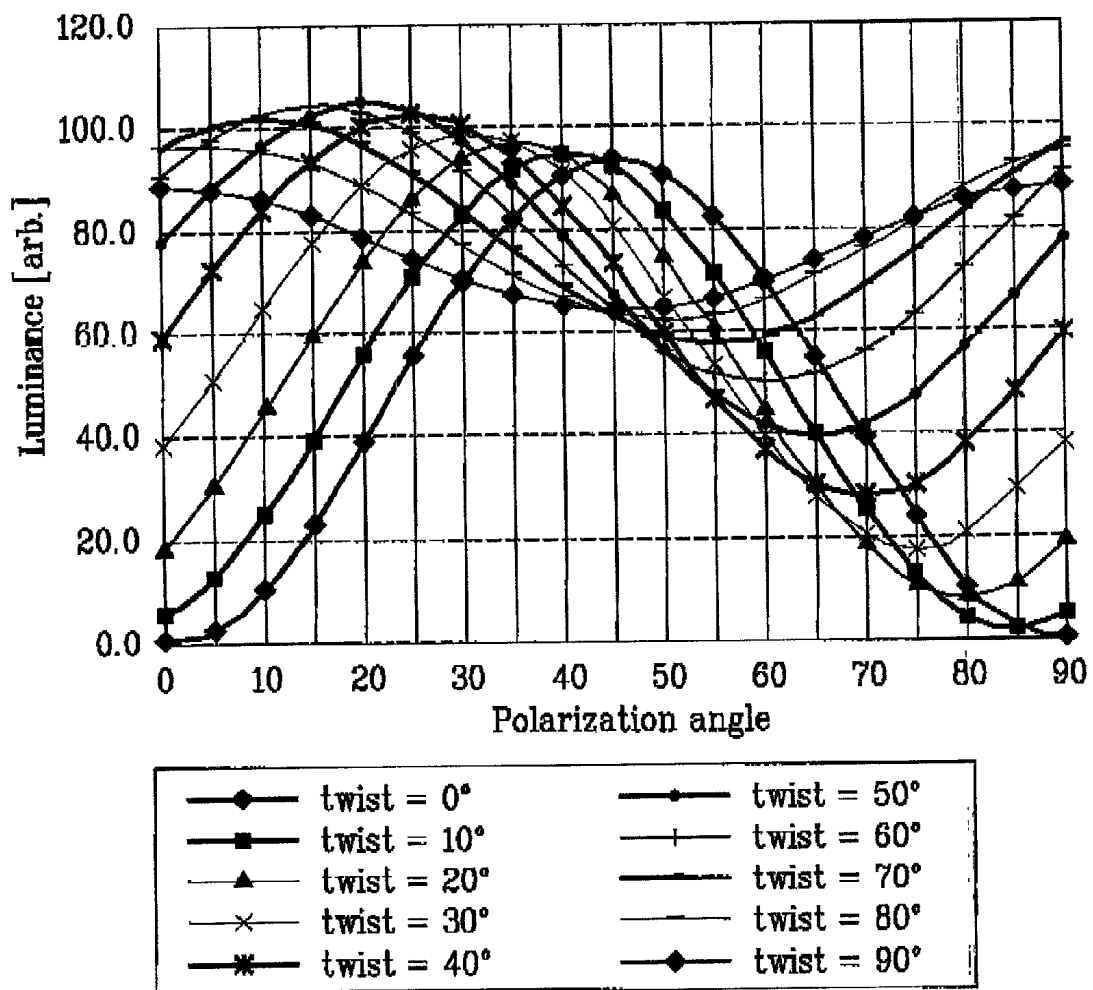


FIG. 5



MULTIDOMAIN LIQUID CRYSTAL DISPLAY DEVICE

[0001] The present invention claims the benefit of Korean Patent Application No. P 2000-45924 filed in Korea on Aug. 8, 2000, which is hereby incorporated by reference.

BACKGROUND OF THE INVENTION

FIELD OF THE INVENTION

[0002] The present invention relates to a liquid crystal display device (hereinafter, referred to as LCD), and relates more particularly to a multidomain LCD having a simplified manufacturing process, an improved viewing angle and increased transmissivity.

DESCRIPTION OF RELATED ART

[0003] When manufacturing LCD devices, the aim is to provide a vivid viewing screen that does not fatigue a user's eyes.

[0004] A LCD is comprised of two substrates that face each other with liquid crystal injected between the two substrates. A LCD generally uses a liquid crystal having a twisted nematic (hereinafter, referred to as TN) mode. The liquid crystal has different refractive anisotropy between the light propagating in a direction of the longitudinal axes of liquid crystal molecules(n_{11}) and the light propagating in a direction perpendicular to the direction of the longitudinal axes(n_{\perp}). This difference results in a viewing angle that is substantially narrow.

[0005] Many solutions for addressing the problem of a narrow viewing angle in a LCD have been proposed, examples of which are as follows: a film compensated mode in which a compensating film is provided to compensate for the viewing angle; a multidomain mode in which a pixel is divided into a plurality of domains, so that the main viewing angle on the respective domains is different from each other, thereby increasing the effective viewing angle; a vertical alignment mode in which the alignment of the liquid crystal is in a vertical direction when a voltage is not applied; and an in-plane switching mode in which the liquid crystal molecules are rotated horizontally by using a parallel electric field formed by the two electrodes arranged on a single substrate.

[0006] The liquid crystal cells in which the multidomain mode is applied are a domain divided twisted nematic (DDTN) liquid crystal cell, a two-domain twisted nematic (TDTN) liquid crystal cell, a complementary twisted nematic (CTN) liquid crystal cell and a four-domain twisted nematic (FDTN) liquid crystal cell. In the case of a FDTN, the four domains are divided on the unit pixel such that the gray inversion in four directions compensate for each other, thereby providing a broader viewing angle than that of a two-domain liquid crystal cell.

[0007] The above-mentioned multidomain modes are achieved by mechanical rubbing or irradiating light on the alignment layers of the two substrates, respectively, to control both a pretilt angle and a pretilt direction so as to control the direction of the liquid crystal. In the case of mechanical rubbing, a photolithography is carried out several times. Whereas in the case of irradiating the light, the light irradiating process has to be carried out several times

for each domain using a mask. Both of these process require a complicated manufacturing process.

[0008] However, the alignment treatment has not been widely used in recent years. Another method has been developed in which a slit in the electrode formed on the substrate distorts the electric field applied to the liquid crystal layer, such that the direction of the liquid crystal molecules are positioned in a desired direction. Examples of the above method are patterned vertical alignment (hereinafter, referred to as PVA) and lateral field induced vertical alignment (hereinafter, referred to as LFIVA).

[0009] The PVA is carried out with a plurality of slits formed by etching the transparent electrodes on both the upper and lower substrates, and thus, the azimuth angle of the alignment of the liquid crystal is determined by a lateral electric field generated at the time when a voltage is applied to pixels. The LFIVA is carried out with a plurality of slits formed in the pixel electrode and with the common electrode rubbed in the longitudinal directions of the slits, such that the electric field formed has horizontal components as well as vertical components.

[0010] The formation of the slits requires a patterning process comprising the steps of: forming an electrode film on the whole surface of the substrate; forming a photoresist film on the electrode film; exposing the photoresist film using a mask; etching the exposed photoresist film to form a pattern; and patterning the electrode film by using the patterned photoresist as a mask.

[0011] Vertical alignment is made in the LIFVA so that the directions of the longitudinal axes of the liquid crystal molecules are aligned vertically to the substrate surface. In more detail, the liquid crystal having a negative dielectric anisotropy is injected, so that the longitudinal axes of the liquid crystal molecules are disposed vertically on the plane of the alignment layer when no voltage is applied. On the other hand, the liquid crystal molecules move from being vertically disposed on the plane of the alignment layer to being horizontally disposed on the plane of the alignment layer when a voltage over a threshold voltage is applied.

[0012] FIG. 1 is a sectional view of related art PVA, and FIG. 2 is a sectional view of related art LFIVA.

[0013] As shown in FIG. 1, the conventional PVA comprises first and second substrates 11 and 15 facing each other with a liquid crystal layer 10 formed between the first substrate 11 and second substrate 5. The first substrate 11 has a black matrix (not shown) for preventing light-leakage, a color filter layer 13 between the black matrixes and a common electrode 14 having a plurality of slits on the color filter layer 13. The second substrate 15 has a plurality of data lines and gate lines (not shown) arranged perpendicularly to one another that define a plurality of pixel areas. Each of the pixel areas has a thin film transistor (not shown) with a gate electrode, a gate insulation film 16, a semiconductor layer, a source electrode and a drain electrode. At the cross point of the data lines and gate lines, a protective film 17 is formed on the whole surface of the thin film transistor, and a pixel electrode 18 is connected to the drain electrode of the thin film transistor on the protective film 17. The pixel electrode 18 has a plurality of slits 19.

[0014] Specifically, the plurality of slits in the common electrode 14 and the pixel electrode 18, each respectively require a patterning process.

[0015] In more detail, the first substrate **11** and second substrate **15** are first prepared. The gate lines and the gate electrode (not shown) are formed on the second substrate **15**. Then, the gate insulation film **16** is formed on the gate electrode of the second substrate **15**. Thereafter, a semiconductor layer (not shown) is formed on the gate insulation film **16** and subsequently the data lines are formed perpendicularly to the gate lines. At the same time, the source/drain electrodes (not shown) are formed on the semiconductor layer.

[0016] The gate lines, gate electrode, data lines and source/drain electrodes are formed of a metal having a low resistance, such as Cu, Al and Mo or an Al alloy, by sputtering and patterning. The gate insulation film **16** is formed of an inorganic material having an excellent adhesion with the above metal and a high insulation internal pressure, such as SiNx, SiOx, etc., by plasma enhanced chemical vapor deposition (PECVD).

[0017] Next, the protective film **17** of SiNx, SiOx or Benzocyclobutene (BCB) having a low dielectric constant is formed on the whole surface of the laminated layer. Then, the protective film **17** is selectively removed to expose a predetermined portion of the drain electrode to form a contact hole. Subsequently, the pixel electrode **18** made of a transparent conductive material, is formed on the protective film **17** and is electrically connected to the drain electrode through the contact hole.

[0018] Thereafter, the photoresist (not shown) is applied on the pixel electrode **18** and is patterned using photolithography. Then, the pixel electrode **18** is selectively etched using the photoresist pattern as a mask to form a plurality of slits **19** in the pixel electrode **18**.

[0019] The black matrix and the color filter layer **13** are formed on the first substrate **11**. Then, a transparent conductive material is deposited to thereby form the common electrode **14**. In the same manner as described above, photolithography is used to form slits **19** in the common electrode **14**.

[0020] The first substrate **11** and second substrate **15** are attached facing each other. Then, the liquid crystal **10** having a negative dielectric anisotropy and containing a chiral dopant is injected and the space between the first substrate **11** and second substrate **15** is sealed, thereby completing the LCD.

[0021] The common electrode **14** and the pixel electrode **18**, which apply a voltage to the liquid crystal **10**, are formed of indium tin oxide (hereinafter, referred to as ITO) in which 5% tin oxide is mixed. The ITO can be spray deposited on a glass substrate, deposited by vacuum deposition when the material is formed on the substrate in a vacuum vessel, or deposited by high-frequency sputtering when the material is discharged in a gas at a low pressure.

[0022] The angle (between the slit edge and the surface of the adjacent substrate edge) of the slit **19** is 90°, as shown in FIG. 1. One of the plurality of slits **19** is formed in the common electrode **14**, and two are formed in the pixel electrode **18**, within the unit pixel area. Since the alignment direction of the liquid crystal molecules is determined based upon the application of the electric field from the slits **19**, the vertical alignment layer is applied without a rubbing process.

[0023] If a voltage over a threshold voltage is applied to the PVA-structured LCD formed as discussed above, the slit **19** cause the liquid crystal molecules initially aligned in a vertical direction to become parallel to the planar direction of the pixel electrode **18** centered around the slits **19**. As a result, the directions of the main viewing angle are the same in domains A and C, and the directions of the main viewing angle are the same in the domains B and D. In other words, the unit pixel area is divided into two domains, thereby embodying a two-domain LCD.

[0024] On the other hand, as shown in FIG. 2, the LFIVA is structured in such a fashion that a plurality of slits **29** are formed only in a pixel electrode **28** on a second substrate **25**. A color filter layer **23** and common electrode **24** are formed on the first substrate **21**. An alignment layer **30** is applied on the common electrode **24** and are rubbed in a direction of the Y axis, i.e., the longitudinal axis of the slit **29**, thereby determining an alignment direction for the liquid crystal molecules **20**.

[0025] In the LFIVA structure, when no voltage is applied, the longitudinal axes of the liquid crystal molecules **20** are arranged perpendicular to the plane of the alignment layer **30**. When the voltage over a threshold voltage is applied, the longitudinal axes of the liquid crystal molecules over the second substrate **25** are disposed toward the plane of the alignment layer, and the liquid crystal molecules over the first substrate **21** are aligned parallel to the rubbing direction as a result of the induced lateral electric field between the slits **29** in the pixel electrode **28**. In other words, the LFIVA structure creates both the vertical electric field between electrodes **24** and **28** and the lateral electric field caused by the slits **29** in the second substrate **25** are in vertical relation with each other, thereby forming two domains centered around respective slits **29** within a unit pixel.

[0026] Therefore, the liquid crystal alignment structure that is twisted in a clockwise direction and the liquid crystal alignment structure that is twisted in a counterclockwise direction are formed with respect to the slit **29** between the two structures, thereby exhibiting the two-directional main viewing angle within the unit pixel. As a result, the gray inversion in the two directions compensates for each other, thereby exhibiting excellent transmissivity, response time and viewing angle compared to a single domain.

[0027] However, the conventionally developed multidomain LCDs have several problems.

[0028] First, a separate patterning process is required to form the slits in each of the transparent conductive layers on the first and second substrates in PVA to obtain a multidomain LCD. Therefore the manufacturing process becomes more complicated.

[0029] Secondly, the LFIVA requires mechanical rubbing of the alignment layer on the first substrate, which may generate dust or static electricity and thus degrade the quality of the device.

[0030] Third, the LFIVA does not sufficiently twist the liquid crystal molecules toward the direction of the slits, such that the transmissivity decreases in accordance with the angle of the polarization axis.

[0031] Finally, the LFIVA is a two-domain structure dependent upon the viewing direction, such that the color and contrast ratio vary depending upon the viewing direction.

SUMMARY OF THE INVENTION

[0032] The present invention is directed to a multidomain liquid crystal display device that substantially obviates one or more of the problems due to limitations and disadvantages of the related art.

[0033] It is an object of the present invention to provide a multidomain liquid crystal display device that is capable of appropriately adjusting the angle of the boundary line of a slit formed on one substrate and the pitch of liquid crystal to obtain multidomain for a main viewing angle on each of domains, resulting in a simplified manufacturing process, and improved viewing angle and transmissivity.

[0034] Additional features and advantages of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the invention. The objectives and other advantages of the invention will be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

[0035] To achieve these and other advantages and in accordance with the purpose of the present invention, as embodied and broadly described, a multidomain LCD includes: first and second substrates, the first substrate having a plurality of pixel areas, a transparent conductive layer on each pixel area of the first substrate and having at least one or more slits inclined at a prescribed angle with respect to a boundary of the pixel area, and a liquid crystal layer between the first and second substrates.

[0036] In another aspect, multidomain LCD includes first and second substrates, a plurality of data lines and a plurality of gate lines arranged substantially perpendicular to one another on the first substrate to define a plurality of pixel areas, a pixel electrode on each pixel area on the first substrate and having at least one or more slits inclined at a prescribed angle with respect to a boundary of a pixel area, first and second vertical alignment layers on the first substrate respectively, and the second substrate, and a liquid crystal layer between the first and second substrates.

[0037] It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE DRAWINGS

[0038] The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate embodiments of the invention and together with the description serve to explain the principles of the invention. In the drawings:

[0039] FIG. 1 is a sectional view of related art PVA;

[0040] FIG. 2 is a sectional view of related art LFVA;

[0041] FIG. 3A is a sectional view of the LCD of the present invention when no voltage is applied;

[0042] FIG. 3B is a sectional view of the LCD of the present invention when a voltage is applied and a corresponding plan view of the pixel electrode;

[0043] FIG. 4 is a plan view of a pixel electrode including the inclined slits in the LCD of the present invention; and

[0044] FIG. 5 is a graph of the relationship between the polarization angle and luminance in the LCD of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

[0045] The preferred embodiment of the present invention will be discussed in detail with reference to the accompanying drawings.

[0046] FIG. 3A is a sectional view of the LCD of the present invention to which no voltage is applied, and FIG. 3B is a sectional view of the LCD of the present invention when a voltage is applied and a corresponding plan view of the pixel electrode. FIG. 4 is a plan view of a pixel electrode including inclined slits in the LCD of the present invention. The solid line arrow marks shown in FIGS. 3B and 4 represent the alignment direction of the liquid crystal on the first substrate when an electric field is applied to the upper and lower electrodes. The dotted line arrow marks represents the alignment direction of the liquid crystal on the second substrate. The thick solid line arrow marks represent the direction of the main viewing angle. When an electric field is applied to the upper and lower electrodes.

[0047] As shown in FIGS. 3A and 3B, the LCD according to the present invention comprises first and second substrates 31 and 35 facing each other, and a liquid crystal layer 30 between the first and second substrates 31 and 35. The first substrate 31 has a black matrix (not shown) on prescribed portions thereof, a color filter layer 33 within the black matrix for embodying colors R, G and B, and a common electrode 34 on the color filter layer 33. The second substrate 35 has a plurality of data lines and gate lines (not shown) arranged perpendicularly to one another that define a plurality of pixel areas. Each pixel area has a thin film transistor (not shown) having a gate electrode, a gate insulation film 36, a semiconductor layer, source electrode and drain electrode. A protective film 37 covers the whole surface of the thin film transistor. A pixel electrode 38 of a trapezoid shape connected to the drain electrode of the thin film transistor is formed with a plurality of slits 39 on the protective film 37.

[0048] The liquid crystal 40 has a negative dielectric anisotropy and the liquid crystal molecules are twisted by adding a chiral dopant. Hence, such the liquid crystal molecules obtain an alignment state similar to that of the TN liquid crystal cell when no voltage is applied.

[0049] Vertical alignment layers (not shown) are applied on the upper surfaces of the pixel electrode and the common electrode, without rubbing. In the event the liquid crystal having the negative dielectric anisotropy is used, if the vertical alignment layers are not formed, the azimuth angle on which the liquid crystal molecules lie is not predetermined. As a consequence, the liquid crystal molecules are arranged irregularly in every direction. Therefore, it is preferable that the vertical alignment layers are formed on the upper surfaces of the pixel electrode and the common electrode.

[0050] In order to manufacture the LCD according to the present invention, the first and second substrates 31 and 35

are prepared, and the black matrix is formed of a metal having an excellent light shielding property on the first substrate **31**. The resists of colors of R, G and B are applied in the named order within the black matrix, thereby forming the color filter layer **33**. Thereafter, ITO as a transparent conductive material is deposited on the whole surface of the color filter layer **33**, thereby forming the common electrode **34**.

[0051] Next, a metal layer is deposited by sputtering on the second substrate **35** and patterned to form the gate lines and the gate electrode. SiNx or SiOx is applied on the whole surface of the gate lines to form the gate insulation film **36**. Subsequently, an amorphous silicon layer is formed on the gate insulation film **36** over the gate electrode and patterned to form the semiconductor layer.

[0052] Thereafter, a metal layer is vapor-deposited on the gate insulation film **36** and patterned to form the data lines and the source/drain electrodes. SiNx, SiOx or Benzocyclobutene is applied on the whole surface including the data lines to form the protective film **37**.

[0053] Next, the protective film **37** is etched to form a contact hole, and ITO as a transparent conductive material is deposited in such a manner as to be connected to the drain electrode of each pixel through the contact hole. A photoresist is then applied on the ITO and patterned using a photolithography process. The ITO is selectively etched using a mask formed by the patterned photoresist to form the pixel electrode **38** of a trapezoid shape as a result of the plurality of slits **39** formed therein.

[0054] The slit patterning process is carried out only on the pixel electrode **38**, and the slit is inclined when viewed in a plan view.

[0055] Subsequent to the formation of the pixel electrode **38**, the vertical alignment layer are applied on the upper surfaces of the common electrode **34** and the pixel electrode **38**, respectively. Finally, the first substrate **31** and the second substrate **35** are attached facing each other, and the liquid crystal **40** having a negative dielectric anisotropy and containing a chiral dopant is injected into the space between the first substrate **31** and second substrate **35**, which is then sealed to complete the LCD.

[0056] When no voltage is applied to the LCD formed as described above in accordance with the present invention, the longitudinal axes of the liquid crystal molecules are aligned vertically to each substrate surface, as shown in **FIG. 3A**. When a voltage is applied, the liquid crystal molecules are aligned in such a manner as shown in **FIG. 3B**. In other words, the lateral electric field induced by the slits **39** causes the liquid crystal molecules on the second substrate **35** to aligned vertically to the slit boundary. The alignment of the liquid crystal molecules on the first substrate **31** is dependent upon the amount of the chiral dopant. As shown in **FIG. 4**, four domains (that is, the domains G, H, J and K) are formed centering around the slits **39**. In that case, intermediate domains I, L and L' are formed between the four domains, thereby embodying the multidomain where one or more domains in addition to the four are formed.

[0057] By using the boundary condition of the slits **39**, the relationships among the inclination angles θ and θ' of the slit with respect to the boundary of the pixel area, the total twist

angle θ of the liquid crystal, and the main viewing angles Ψ and Ψ' (see **FIG. 3B**) are given by the following expressions:

[0058] i) left slit boundary line

$$\Psi = \theta + 90^\circ - \theta/2$$

[0059] In case where $\Psi = 135^\circ$,

$$\theta' + \theta/2 = 45^\circ$$

(1)

[0060] ii) right slit boundary line

$$\Psi' = \theta' + 90^\circ - \theta'/2$$

[0061] In case where $\Psi' = 135^\circ$,

$$\theta' + \theta/2 = 45^\circ$$

(2)

[0062] From the expressions (1) and (2),

$$\theta + \theta' = 90^\circ$$

$$\theta' = 90^\circ - \theta > 0^\circ$$

(3)

[0063] From the expressions (2) and (3),

$$\theta = 2\theta - 90^\circ \geq 0^\circ$$

$$\theta \geq 45^\circ$$

(4)

[0064] From the expressions (3) and (4),

$$45^\circ \leq \theta < 90^\circ \quad 0^\circ \leq \theta' < 90^\circ$$

[0065] Within the above range, most efficient four-domain vertical alignment operates. The reason why the main viewing angle Ψ is fixed at the angle of 135° is that the main viewing angle Ψ has to have the angle of 45° to the polarization axis in order to obtain a maximum transmissivity because the polarization axes are at the angles of 0° and 90° . For example, if θ is 30° , θ' is 90° and the maximum transmissivity can be achieved when the total twist angle θ is 30° . The twist angle is preferably in a range of 0° to 90° , more preferably in a range of 30° to 80° .

[0066] In other words, the pitch of the liquid crystal is adjusted by an appropriate amount of the chiral dopant. Upon application of the electric field, the alignment direction of the liquid crystal molecules on the first substrate can be adjusted, wherein the desired twist angle can be obtained with a slit at an angle for the desired twist angle alignment.

[0067] The amount of chiral dopant is preferably in a range of 0.1 wt % to 1.0 wt %, more preferably in a range of 0.3 wt % to 0.8 wt %.

[0068] If the inclination angles of the slits are made as shown in **FIG. 4**, the main viewing angle within the unit pixel areas, where the pixel electrodes of the trapezoidal shaped, are continuously arranged in different directions in the four domains, thereby achieving the multidomain LCD.

[0069] Therefore, the adjustment of the inclination angle of the slit, the amount of the chiral dopant and the twist angle of the liquid crystal enables the directions of the main viewing angle within the unit pixel areas to be adjusted, thereby embodying the LCD having a multidomain.

[0070] **FIG. 5** shows the graph illustrating the luminance according to the angle of the polarization axis, when a cell gap is $4.0 \mu\text{m}$. As shown, when the twist angle is 90° , the variation of the luminance according to the angle of the polarization axis is relatively small.

[0071] It will be apparent to those skilled in the art that various modifications and variations can be made in the

multidomain liquid crystal display of the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention covers the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

What is claimed is:

1. A multidomain liquid crystal display device comprising:

first and second substrates;

the first substrate having a plurality of pixel areas;

a transparent conductive layer on each pixel area of the first substrate and having at least one or more slits inclined at a prescribed angle with respect to a boundary of the pixel area; and

a liquid crystal layer between the first and second substrates.

2. The device of claim 1, wherein the liquid crystal layer has a negative dielectric anisotropy.

3. The device of claim 1, wherein the liquid crystal layer has a chiral dopant.

4. The device of claim 3, wherein the chiral dopant is in a range of 0.1 wt % to 1.0 wt %.

5. The device of claim 1, further comprising a vertical alignment layer on the transparent conductive layer.

6. The device of claim 1, wherein the slit has an inclination angle in a range of 45° to 90° with respect to the boundary of the pixel area.

7. The device of claim 1, further comprising a plurality of data lines and gate lines crossing each other and defining a plurality of pixel areas; and

thin film transistors at the intersections of the data lines and gate lines on the first substrate.

8. The device of claim 1, wherein a twist angle of the liquid crystal layer is in a range of 0° to 90°.

9. The device of claim 1, wherein a twist angle of the liquid crystal layer is in a range of 30° to 80°.

10. A multidomain liquid crystal display device comprising:

first and second substrates;

a plurality of data lines and a plurality of gate lines arranged substantially perpendicular to one another on the first substrate to define a plurality of pixel areas;

a pixel electrode on each pixel area on the first substrate and having at least one or more slits inclined at a prescribed angle with respect to a boundary of a pixel area;

first and second vertical alignment layers on the first and second substrates; and

a liquid crystal layer between the first and second substrates.

11. The device of claim 10, wherein the liquid crystal layer has a negative dielectric anisotropy.

12. The device of claim 10, wherein the liquid crystal layer has a chiral dopant.

13. The device of claim 12, wherein the chiral dopant is in a range of 0.1 wt % to 1.0 wt %.

14. The device of claim 10, wherein the slit has an inclination angle in a range of 45° to 90° with respect to the boundary of the pixel area.

15. The device of claim 10, wherein no alignment treatment for the first and second vertical alignment layers is carried out.

16. The device of claim 10, wherein the pixel electrode is of a trapezoidal shape.

17. The device of claim 10, further comprising a color filter layer on the second substrate; and

a common electrode on the color filter layer.

18. The device of claim 10, wherein a twist angle of the liquid crystal layer is in a range of 0° to 90°.

19. The device of claim 10, wherein a twist angle of the liquid crystal layer is in a range of 30° to 80°.

20. The device of claim 10, wherein the slits have an inclination angle with respect to the boundary of the pixel area.

* * * * *

专利名称(译)	多畴液晶显示装置		
公开(公告)号	US20020063833A1	公开(公告)日	2002-05-30
申请号	US09/916246	申请日	2001-07-30
[标]申请(专利权)人(译)	乐金显示有限公司		
申请(专利权)人(译)	L.G.飞利浦液晶CO., LTD.		
当前申请(专利权)人(译)	LG DISPLAY CO., LTD.		
[标]发明人	YOO JANG JIN YOON KI HYUK		
发明人	YOO, JANG JIN YOON, KI HYUK		
IPC分类号	G02F1/136 G02F1/1333 G02F1/1337 G02F1/1343		
CPC分类号	G02F1/133707 G02F2001/133742 G02F1/134336 G02F1/133753		
优先权	1020000045924 2000-08-08 KR		
其他公开文献	US6587173		
外部链接	Espacenet USPTO		

摘要(译)

多畴液晶显示装置本发明涉及一种多畴液晶显示装置，它能够适当地调节在一个基板上形成的狭缝的边界线的角度和液晶的间距，从而在每个畴上获得主视域的多视野，从而允许简化的制造工艺并改善视角和透射率。多畴液晶显示装置包括第一和第二基板，第一基板具有多个像素区域，在第一基板的每个像素区域上具有透明导电层，并且具有相对于第一基板以预定角度倾斜的至少一个或多个狭缝。像素区域的边界，以及第一和第二基板之间的液晶层。

