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(19) **United States**(12) **Patent Application Publication**
Kim(10) **Pub. No.: US 2005/0134781 A1**(43) **Pub. Date: Jun. 23, 2005**(54) **LIQUID CRYSTAL DISPLAY DEVICE AND
DRIVING METHOD THEREOF**(52) **U.S. Cl. 349/139**(76) **Inventor: Woo Hyun Kim, Seoul (KR)**(57) **ABSTRACT**

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Publication Classification(51) **Int. Cl.⁷ G02F 1/1343**

A liquid crystal display device and a driving method thereof for reducing the number of data lines and the number of data drive integrated circuits corresponding thereto are disclosed. In the device, a plurality of gate lines is provided in a direction crossing a plurality of data lines. First and second control lines are provided in a direction being parallel to the gate lines. First liquid crystal cells are provided at one side on a basis of the data lines. Second liquid crystal cells are provided at other side on a basis of the data lines. A first switching part is provided for each first liquid crystal cell to apply video signals supplied from the data lines to the first liquid crystal cells under control of the first control line and the gate line. A second switching part is provided for each second liquid crystal cell to apply video signals supplied from the data lines to the second liquid crystal cells under control of the second control line and the gate line.

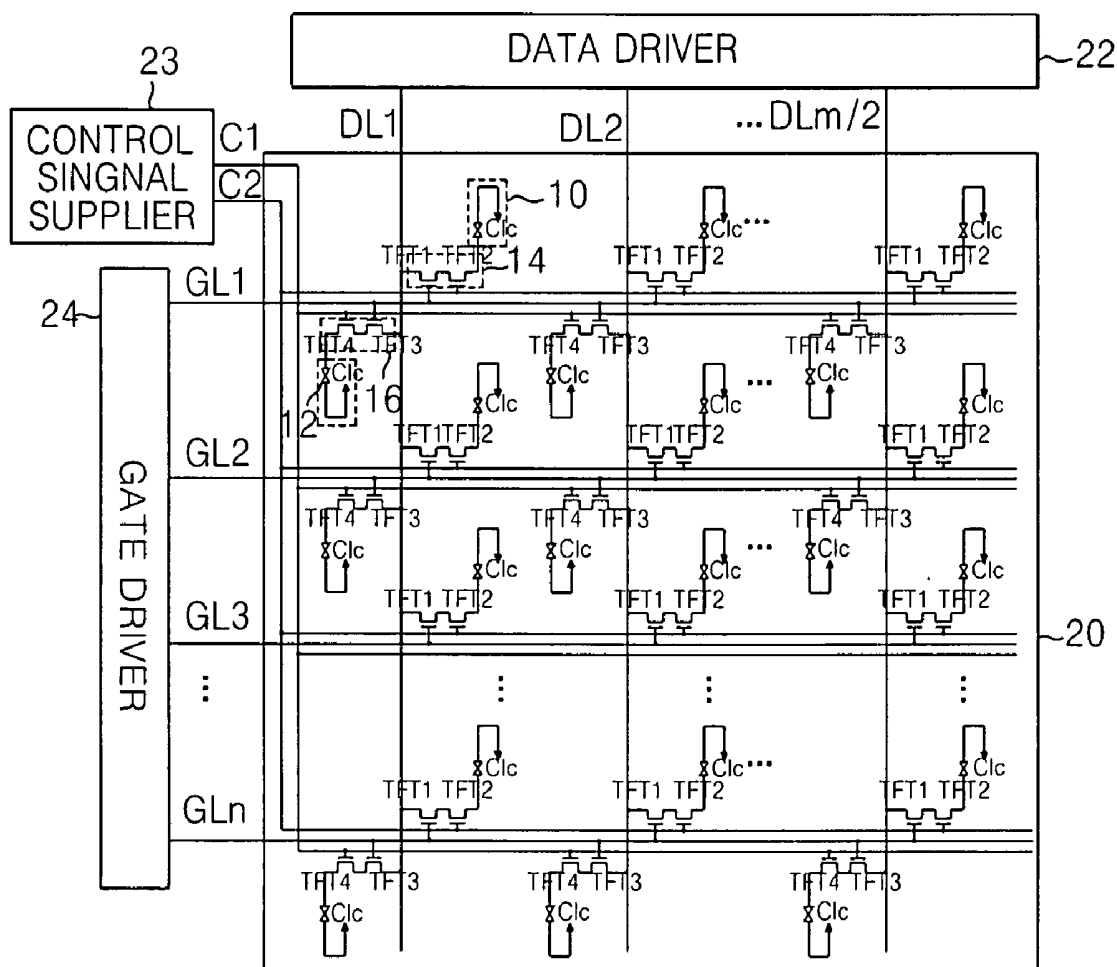


FIG. 1
RELATED ART

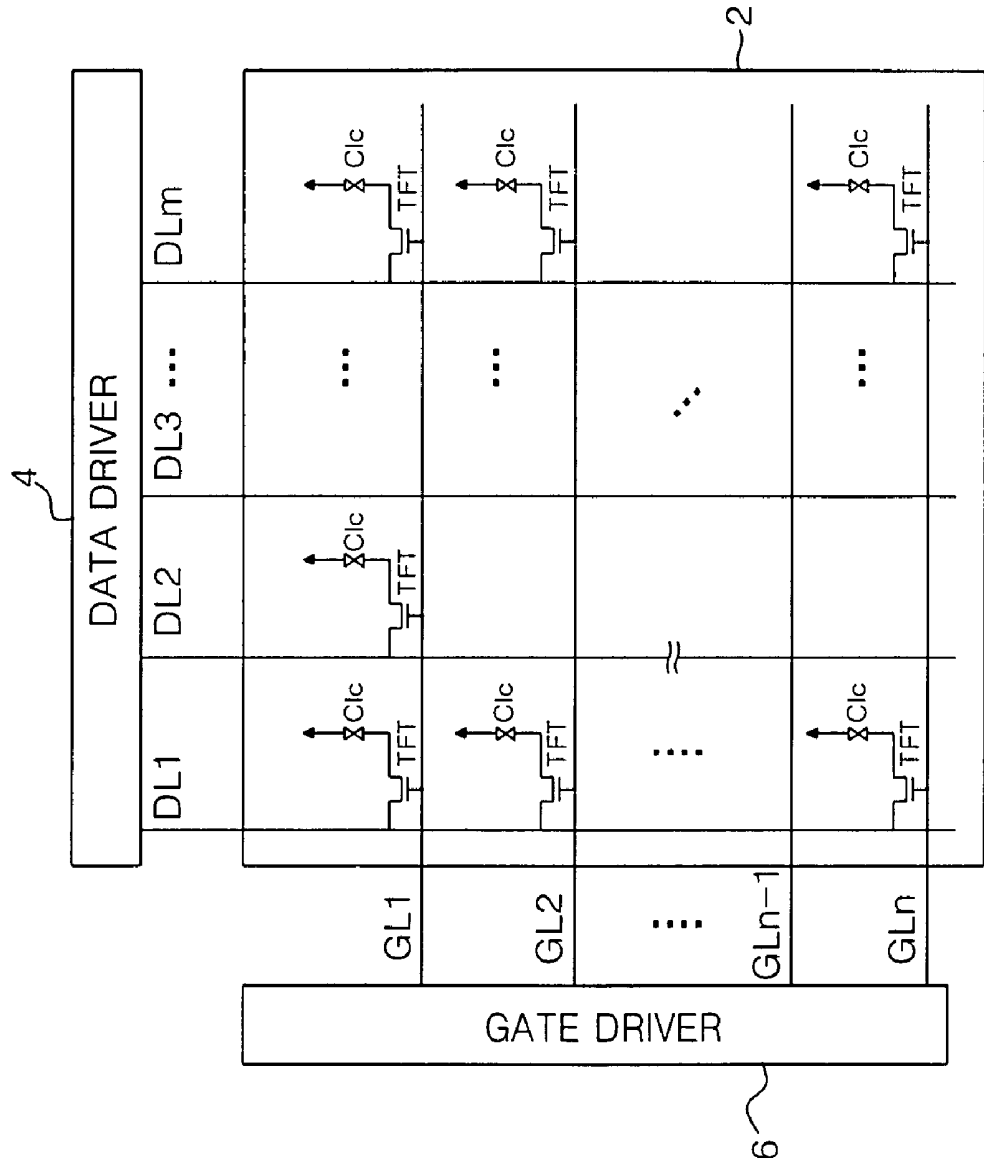


FIG. 2

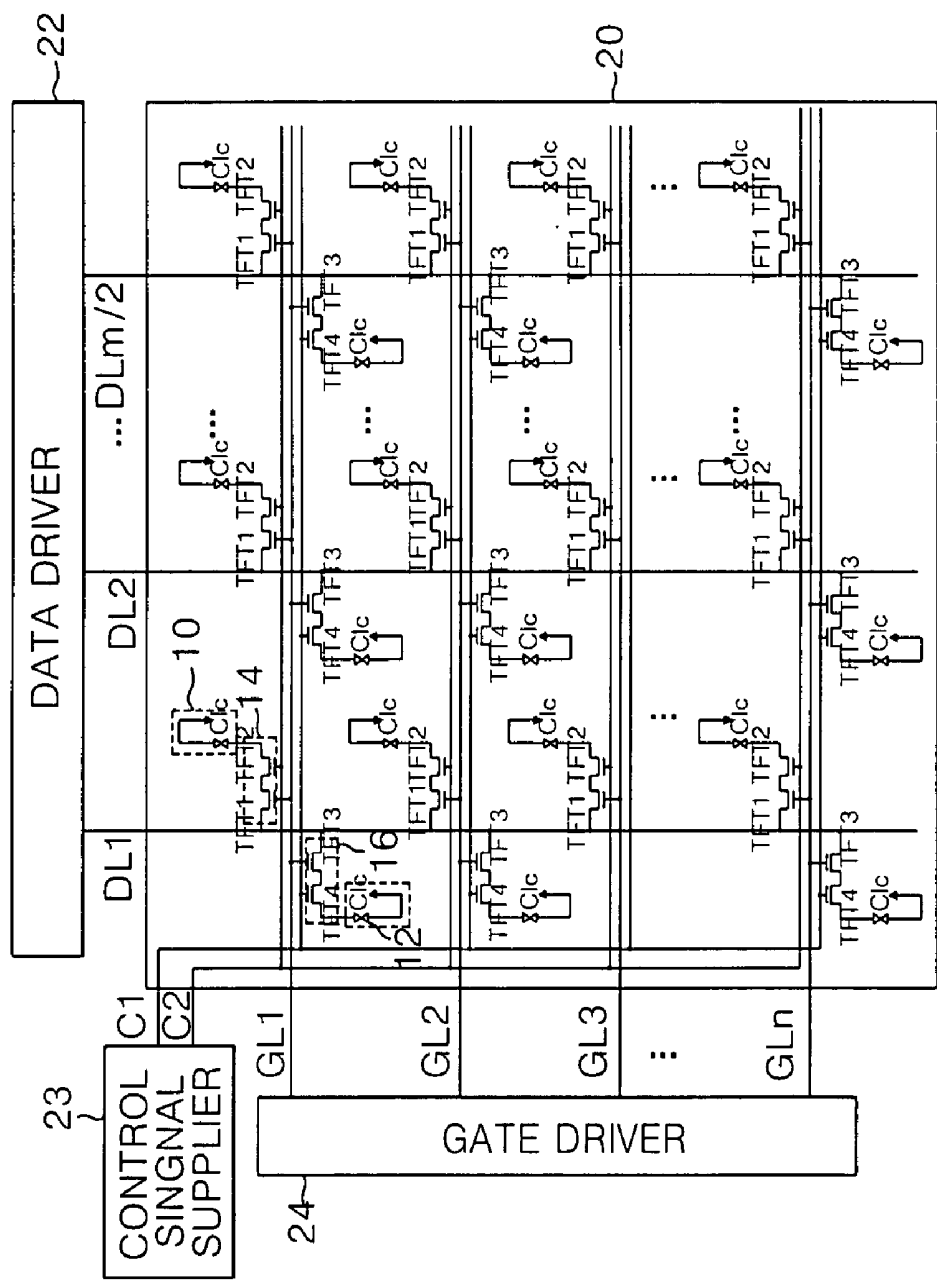


FIG.3

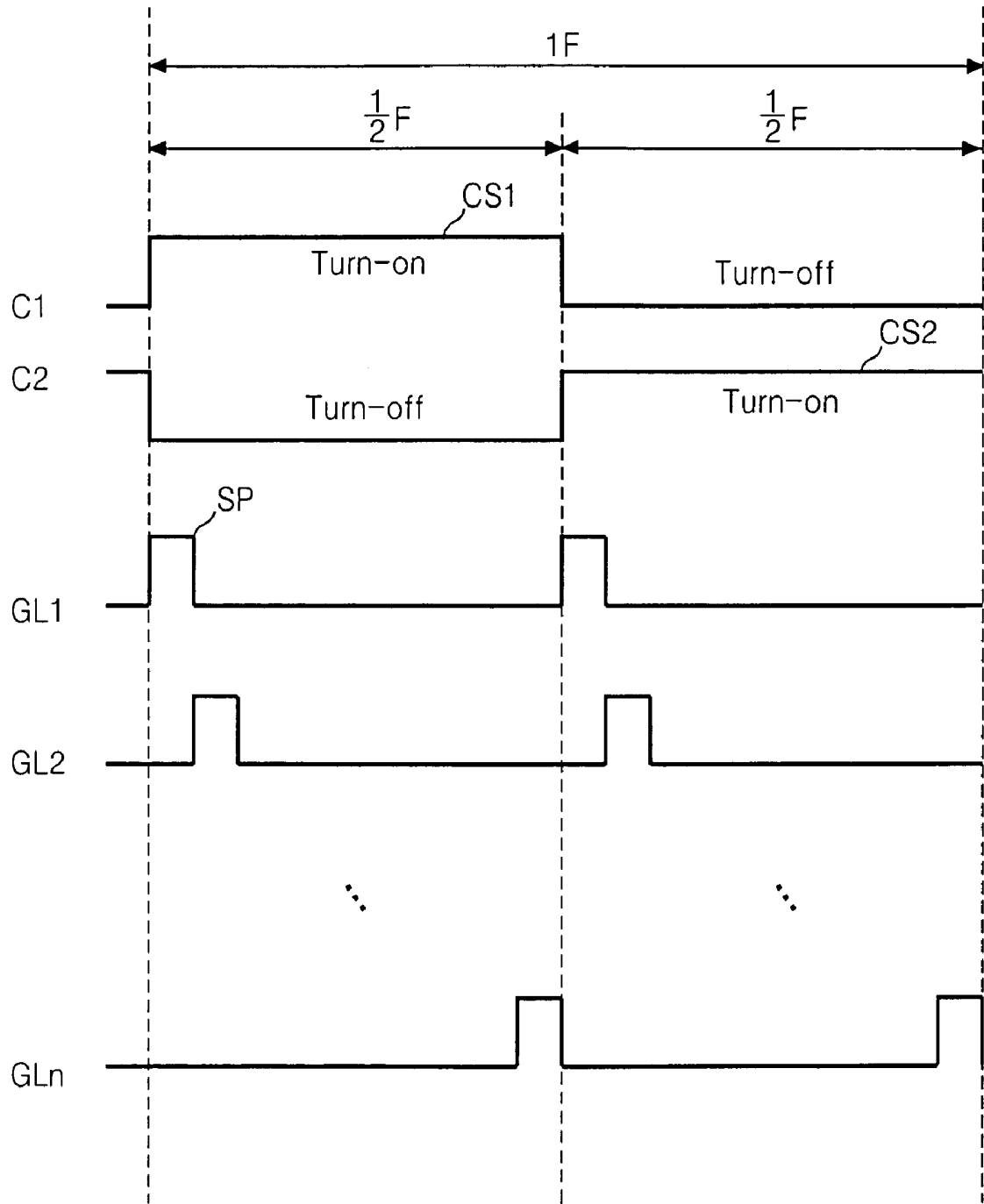


FIG. 4

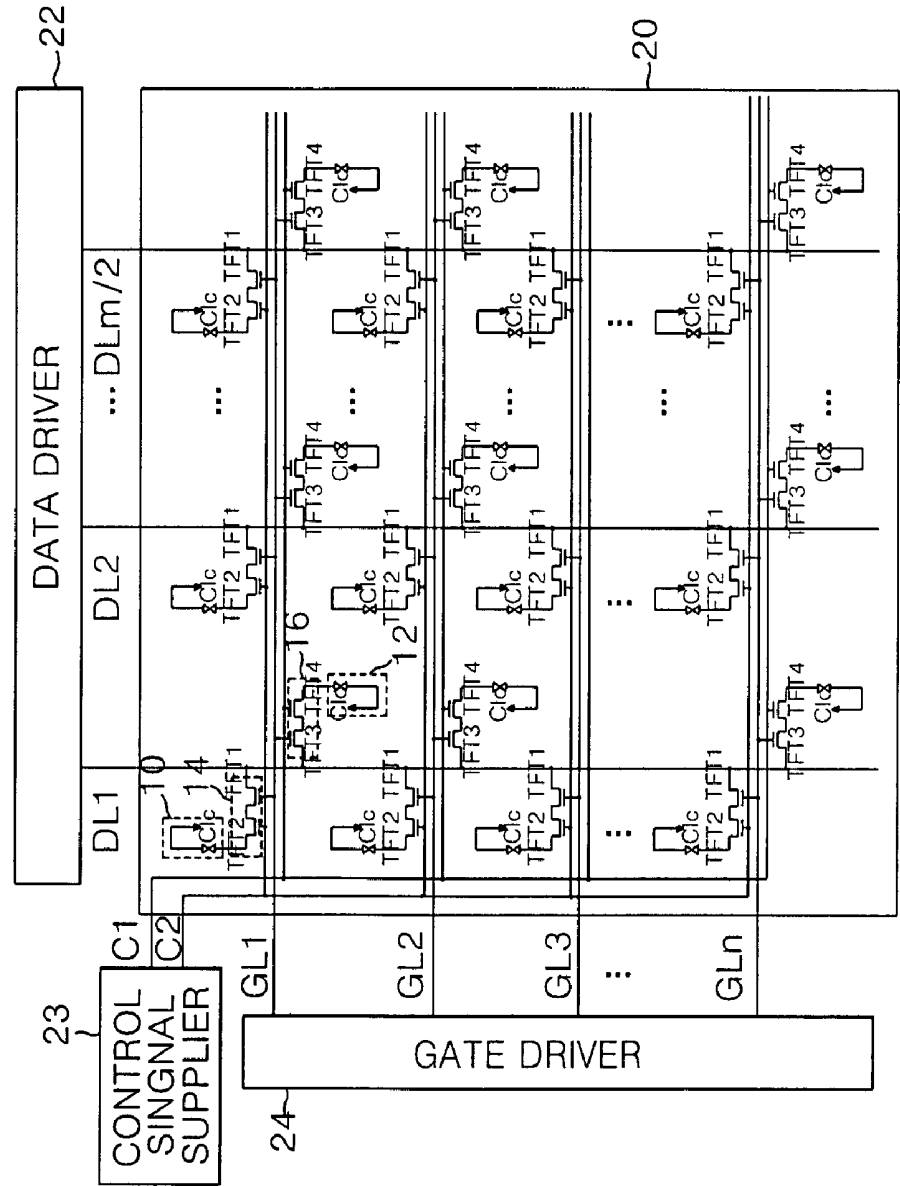


FIG. 5A

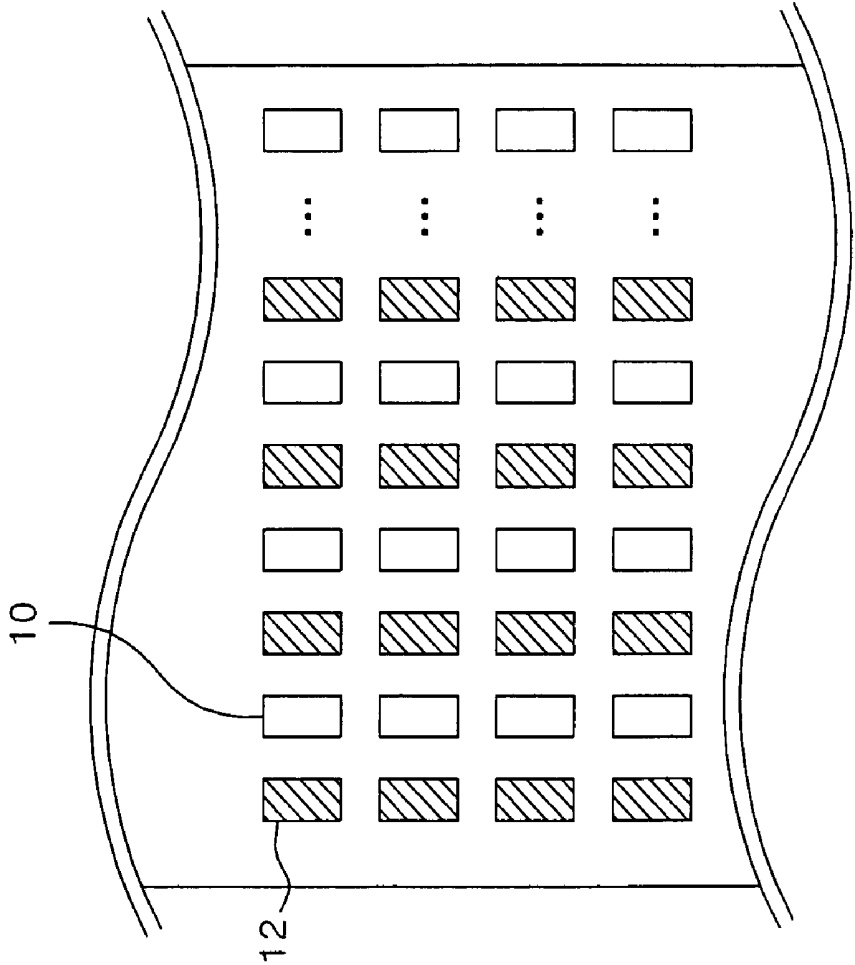


FIG. 5B

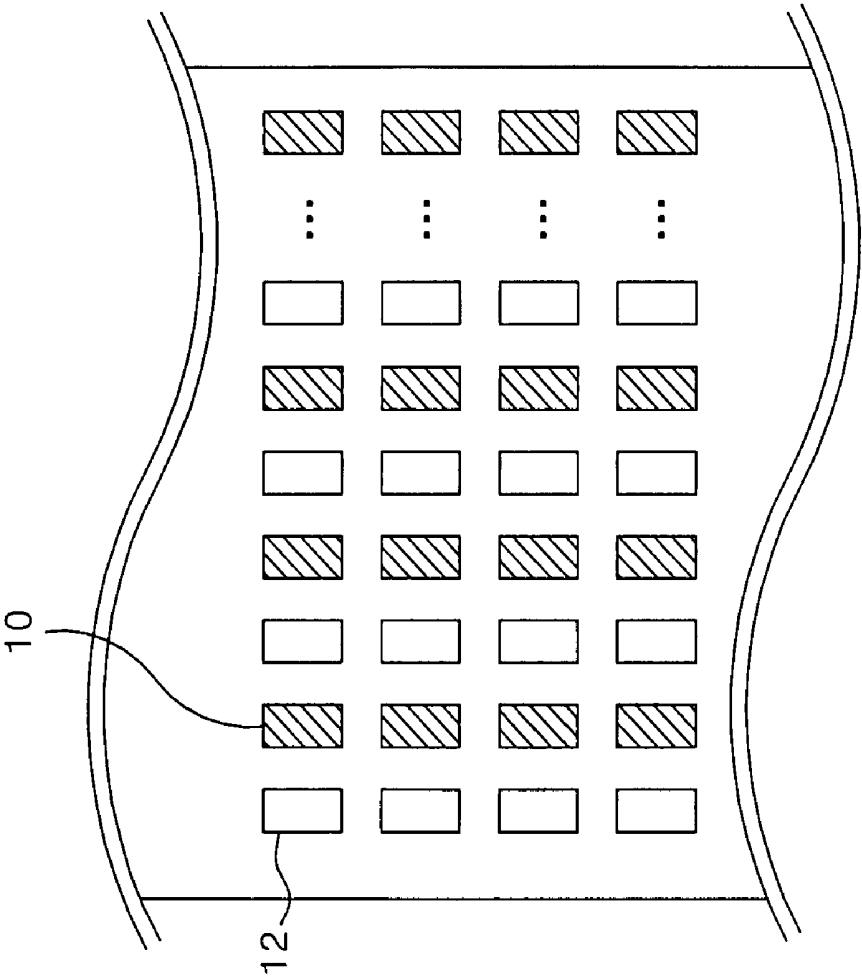


FIG. 6

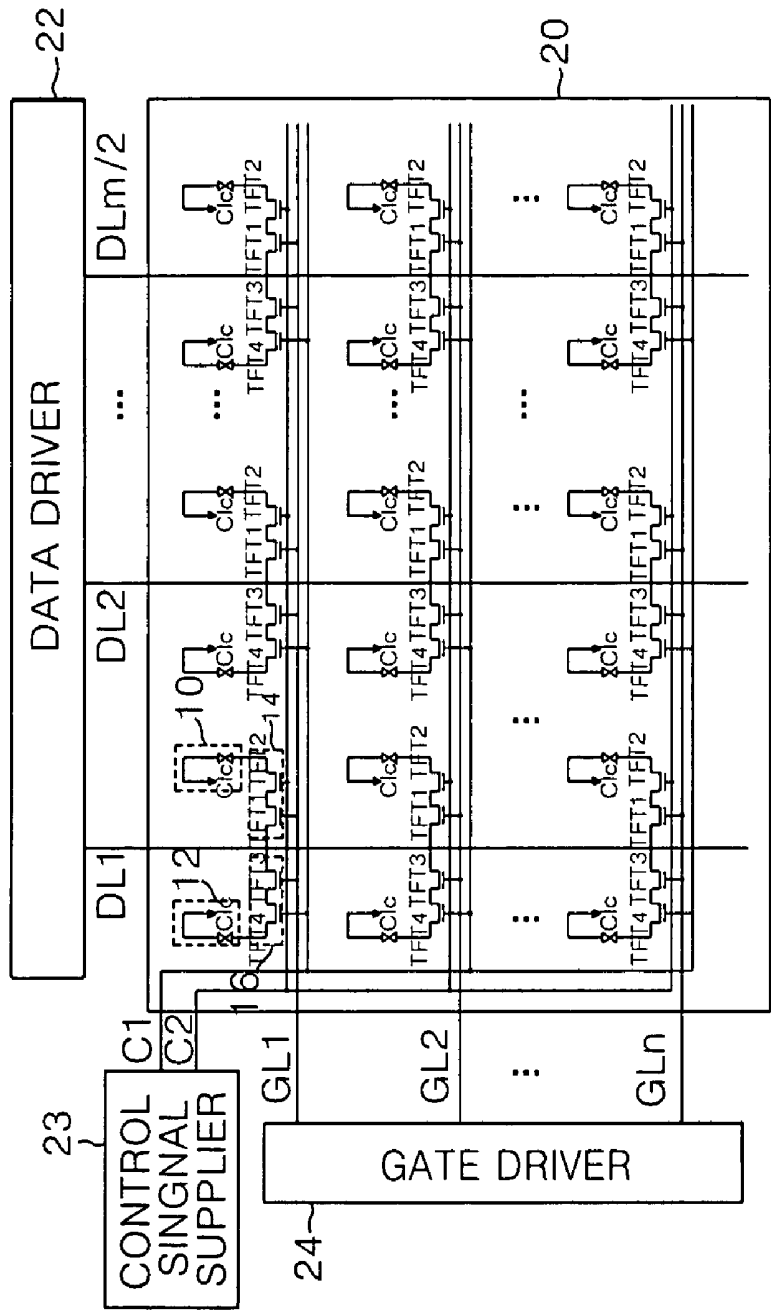
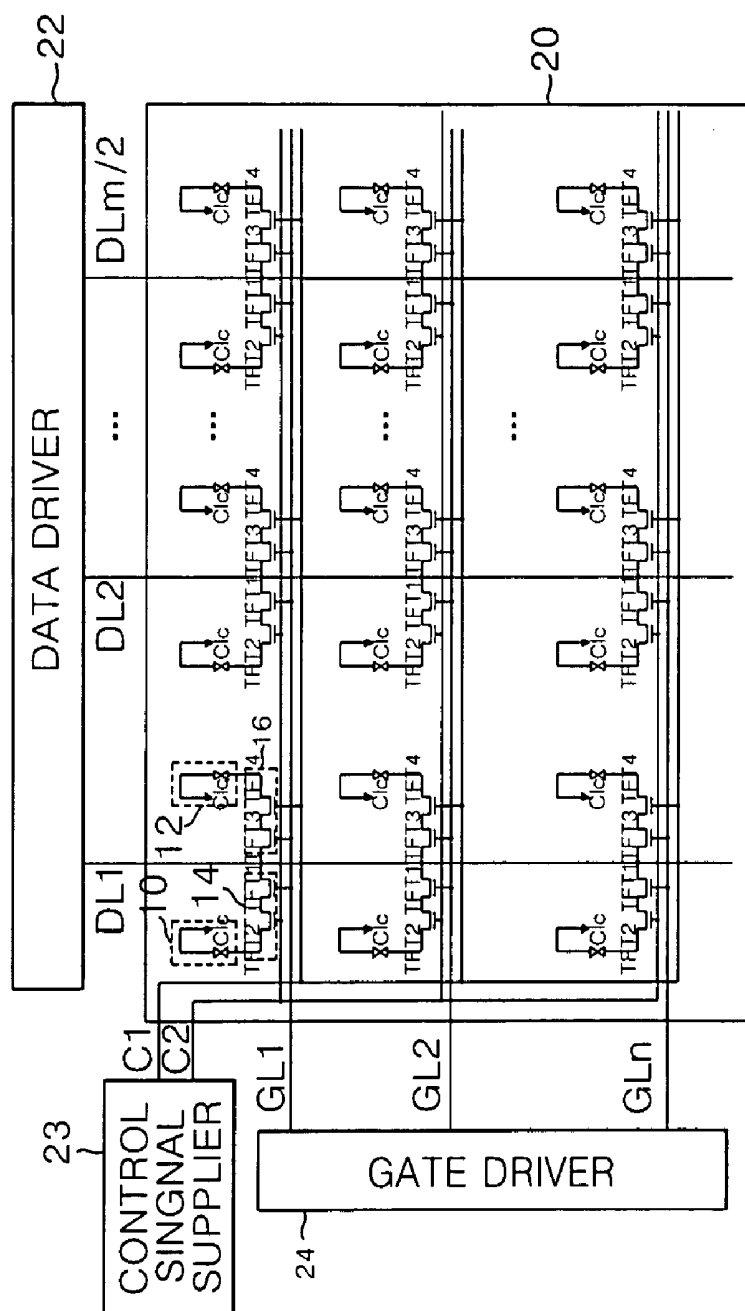


FIG. 7



LIQUID CRYSTAL DISPLAY DEVICE AND DRIVING METHOD THEREOF

[0001] This application claims the benefit of Korean Patent Application No. P 2003-92697 filed in Korea on Dec. 17, 2003, which is hereby incorporated by reference.

BACKGROUND OF THE INVENTION

[0002] 1. Field of the Invention

[0003] This invention relates to a liquid crystal display, and more particularly to a liquid crystal display device and a driving method thereof that are adaptive for reducing the number of data lines as well as the number of data drive integrated circuits corresponding thereto.

[0004] 2. Description of the Related Art

[0005] Generally, a liquid crystal display (LCD) controls the light transmittance of a liquid crystal using an electric field, thereby displaying a picture. To achieve this, the LCD includes a liquid crystal display panel having a pixel matrix and a driving circuit for driving the liquid crystal display panel. The driving circuit drives the pixel matrix such that picture information can be displayed on the display panel.

[0006] **FIG. 1** illustrates a conventional liquid crystal display device.

[0007] Referring to **FIG. 1**, the conventional LCD includes a liquid crystal display panel **2**, a data driver **4** for driving data lines DL1 to DLm of the liquid crystal display panel **2**, and a gate driver **6** for driving gate lines GL1 to GLn of the liquid crystal display panel **2**.

[0008] The liquid crystal display panel **2** has thin film transistors TFT each of which is provided at each crossing of the gate lines GL1 to GLn and the data lines DL1 to DLm and liquid crystal cells connected to the thin film transistors TFT and arranged in a matrix type.

[0009] The gate driver **6** sequentially applies a gate signal to each gate line GL1 to GLn in response to a control signal from a timing controller (not shown). The data driver **4** converts data R, G and B from the timing controller into analog video signals to thereby apply video signals for one horizontal line to the data lines DL1 to DLm every one horizontal period when a gate signal is applied to each gate line GL1 to GLn.

[0010] The thin film transistor TFT applies a signal from the data lines DL1 to DLm to the liquid crystal cell in response to a control signal from the gate lines GL1 to GLn. The liquid crystal cell can be equivalently expressed as a liquid crystal capacitor Clc because it has a common electrode opposed to a pixel electrode connected to the thin film transistor TFT with a liquid crystal therebetween. Such a liquid crystal cell includes a storage capacitor (not shown) connected to a pre-stage gate line in order to keep the data voltage charged in the liquid crystal capacitor Clc until the next data voltage is charged therein.

[0011] The liquid crystal cells of such a related art conventional LCD has a number of vertical lines equal to the number (i.e., m) of the data lines DL1 to DLm because they are provided at crossings of the gate lines DL1 to DLn and the data lines DL1 to DLm. In other words, the liquid crystal cells are arranged in a matrix type in such a manner to make m vertical lines and n horizontal lines.

[0012] The related art LCD requires m data lines DL1 to DLm so as to drive the liquid crystal cells having m vertical lines. Therefore, the related art LCD has a drawback in that a number of data lines DL1 to DLm should be provided to drive the liquid crystal display panel **2** and hence process time and a manufacturing cost are wasted. Furthermore, the related art LCD has a problem in that because a large number of data drive integrated circuits (IC's) are included in the data driver **4** so as to drive the m data lines DL1 to DLm, the manufacture cost is high.

SUMMARY OF THE INVENTION

[0013] Accordingly, it is an advantage of the present invention to provide a liquid crystal display device and a driving method thereof that are adaptive for reducing the number of data lines as well as the number of data drive integrated circuits corresponding thereto.

[0014] In order to achieve these and other advantages of the invention, a liquid crystal display device according to one aspect of the present invention includes a plurality of data lines; a plurality of gate lines provided in a direction crossing the data lines; first and second control lines provided in a direction being parallel to the gate lines; first liquid crystal cells provided at one side of the data lines; second liquid crystal cells provided at the other side of the data lines; a first switching part provided for each first liquid crystal cell to apply video signals supplied from the data lines to the first liquid crystal cells under control of the first control line and the gate line; and a second switching part provided for each second liquid crystal cell to apply video signals supplied from the data lines to the second liquid crystal cells under control of the second control line and the gate line.

[0015] The liquid crystal display device further includes a gate driver for sequentially applying a gate signal to the gate lines during an $\frac{1}{2}$ frame interval; a data driver for applying said video signals to the data lines when said gate signal is applied to the gate lines; and a control signal supplier for supplying control signals alternated for each $\frac{1}{2}$ frame unit to the first and second control lines.

[0016] In the liquid crystal display device, the first switching part includes a first thin film transistor connected to the gate line to be turned on when said gate signal is applied, thereby receiving said video signal; and a second thin film transistor turned on when said control signal is applied to the first control line to apply said video signal to the first liquid crystal cell.

[0017] In the liquid crystal display device, the second switching part includes a third thin film transistor connected to the gate line to be turned on when said gate signal is applied, thereby receiving said video signal; and a fourth thin film transistor turned on when said control signal is applied to the second control line to apply said video signal to the second liquid crystal cell.

[0018] The first and second liquid crystal cells receive said video signals alternately for each $\frac{1}{2}$ frame unit in response to said control signals applied alternately for each $\frac{1}{2}$ frame unit.

[0019] In the liquid crystal display device, the first liquid crystal cells and the first switching part are provided at

odd-numbered vertical lines while the second liquid crystal cells and the second switching part are provided at even-numbered vertical lines.

[0020] Alternatively, the first liquid crystal cells and the first switching part are provided at even-numbered vertical lines while the second liquid crystal cells and the second switching part are provided at odd-numbered vertical lines.

[0021] A method of driving a liquid crystal display device according to another aspect of the present invention includes applying video signals corresponding to data to first liquid crystal cells positioned at the i th vertical line (wherein i is an even number or an odd number) during a first-half $\frac{1}{2}$ frame interval of one frame interval; and applying said video signals corresponding to said data to second liquid crystal cells positioned at the $(i+1)$ th vertical line during a second-half $\frac{1}{2}$ frame interval of said one frame interval.

[0022] In the method, said step of applying said video signals to the first liquid crystal cells includes applying a first control signal to turn on a first thin film transistor included in a first switching part connected to each of the first liquid crystal cells; sequentially applying a gate signal to all the gate lines to sequentially turn on each second thin film transistor included in the first switching part; and applying said video signals to the data lines when said gate signal is applied.

[0023] In the method, said step of applying said video signals to the second liquid crystal cells includes applying a second control signal to thereby turn on a third thin film transistor included in a second switching part connected to each of the second liquid crystal cells; sequentially applying said gate signal to all the gate lines to sequentially turn on each fourth thin film transistor included in the second switching part; and applying said video signals to the data lines when said gate signal is applied.

[0024] Said first and second control signals are applied in such a manner as to alternate with each other for each $\frac{1}{2}$ frame interval.

BRIEF DESCRIPTION OF THE DRAWINGS

[0025] These and other advantages of the invention will be apparent from the following detailed description of the embodiments of the present invention with reference to the accompanying drawings, in which:

[0026] FIG. 1 is a block circuit diagram illustrating a configuration of a conventional liquid crystal display;

[0027] FIG. 2 is a block circuit diagram illustrating a configuration of a liquid crystal display according to a first embodiment of the present invention;

[0028] FIG. 3 is a waveform diagram of control signals applied to the control lines and gate signals applied to the gate lines shown in FIG. 2;

[0029] FIG. 4 is a block circuit diagram illustrating a configuration of a liquid crystal display according to a second embodiment of the present invention;

[0030] FIG. 5A and FIG. 5B depict the liquid crystal cells driven in response to the control signals shown in FIG. 3;

[0031] FIG. 6 is a block circuit diagram illustrating a configuration of a liquid crystal display according to a third embodiment of the present invention; and

[0032] FIG. 7 is a block circuit diagram illustrating a configuration of a liquid crystal display according to a fourth embodiment of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

[0033] Reference will now be made in detail to embodiments of the present invention, examples of which are illustrated in the accompanying drawings.

[0034] Hereinafter, embodiments of the present invention will be described in detail with reference to FIGS. 2 to 7.

[0035] FIG. 2 schematically shows a liquid crystal display (LCD) according to a first exemplary embodiment of the present invention.

[0036] Referring to FIG. 2, the LCD according to the first embodiment of the present invention includes a liquid crystal display panel 20, a data driver 22 for driving data lines DL1 to DL $m/2$ of the liquid crystal display panel 20, a gate driver 24 for driving gate lines GL1 to GL n of the liquid crystal display panel 20, and a control signal supplier 23 for supplying control signals to first and second control lines C1 and C2 provided in parallel to the gate lines GL1 to GL n .

[0037] The liquid crystal display panel 20 is comprised of first and second liquid crystal cells 10 and 12 provided alternately at crossings of the gate lines GL1 to GL n and the data lines DL1 to DL $m/2$, a first switching part 14 for driving the first liquid crystal cell 10, and a second switching part 16 for driving the second liquid crystal cell 12.

[0038] The first and second liquid crystal cells 10 and 12 can be equivalently expressed as a liquid crystal capacitor C_{lc} because they comprise a common electrode opposed to a pixel electrode connected to each of the first and second switching parts 14 and 16 and having a liquid crystal therebetween. Herein, each of the first and second liquid crystal cells 10 and 12 includes a storage capacitor (not shown) connected to a pre-stage gate line (or common electrode) or the control signal lines C1 and C2 in order to keep a voltage of a video signal charged in the liquid crystal capacitor C_{lc} until the next video signal is applied.

[0039] The first liquid crystal cell 10 and the first switching part 14 are provided at, for example, the right side of the data line DL, that is, at even-numbered vertical lines. The second liquid crystal cell 12 and the second switching part 16 are provided at the left side of the data line DL, that is, at odd-numbered vertical lines. In other words, the first and second liquid crystal cells 10 and 12 are provided on opposite sides of a single data line DL running between them. In this case, the first and second liquid crystal cells 10 and 12 receive video signals from the data lines DL positioned adjacently to each other. Accordingly, the LCD according to the first embodiment of the present invention allows the number of data lines DL to be reduced to a half of that in the conventional LCD shown in FIG. 1.

[0040] Alternatively, in the present embodiment, a position of the first and second liquid crystal cells 10 and 12 may be reversed as shown in FIG. 4. In other words, as shown in FIG. 4, the first liquid crystal cell 10 and the first switching part 14 may be provided at the left side of the data line DL while the second liquid crystal cell 12 and the second switching part 16 may be provided at the right side of the data line. In other words, the first liquid crystal cell 10 and the first switching part 14 may be provided at the

odd-numbered vertical lines while the second liquid crystal cell **12** and the second switching part **16** may be provided at the even-numbered vertical lines.

[0041] The first and second control lines **C1** and **C2** are provided in parallel to the gate line **GL** (e.g., at the upper/lower sides of the gate line **GL**) and connected to any one of the first and second switching parts **14** and **16**. Herein, the first control line **C1** is connected to the second switching part **16** while the second control line **C2** is connected to the first switching part **14**.

[0042] The first switching part **14** for driving the first liquid crystal cell **10** includes first and second thin film transistors **TFT1** and **TFT2**. The first thin film transistor **TFT1** is connected to the data line **DL** and the gate line **GL** to be turned on when a gate signal is applied to the gate line **GL**. The second thin film transistor **TFT2** is connected between the first thin film transistor **TFT1** and the first liquid crystal cell **10** to be turned on when a second control signal is applied to the second control line **C2**.

[0043] The second switching part **16** for driving the second liquid crystal cell **12** includes third and fourth thin film transistors **TFT3** and **TFT4**. The third thin film transistor **TFT3** is connected to the data line **DL** and the gate line **GL** to be turned on when a gate signal is applied to the gate line **GL**. The fourth thin film transistor **TFT4** is connected between the third thin film transistor **TFT3** and the second liquid crystal cell **12** to be turned on when a first control signal is applied to the first control line **C1**.

[0044] The gate driver **24** sequentially applies a gate signal **SP** to the gate lines **GL1** to **GLn** for each $\frac{1}{2}$ frame unit as shown in **FIG. 3** in response to the control signal supplied from a timing controller (not shown). In other words, the gate driver **24** in the first embodiment of the present invention drives the gate lines **GL1** to **GLn** for each $\frac{1}{2}$ frame unit.

[0045] The data driver **22** converts data **R**, **G** and **B** signals from the timing controller into analog video signals to apply them to the data lines **DL1** to **DLm/2**. The data driver **22** alternately applies a video signal to be supplied to the first liquid crystal cell **10** and a video signal to be supplied to the second liquid crystal cell **12** for each $\frac{1}{2}$ frame unit. In this case, the LCD according to the first embodiment of the present invention allows the number of data lines **DL1** to **DLm/2** to be reduced to a half of that in the conventional LCD shown in **FIG. 1**, so that the number of data drive IC's included in the data driver **22** also is reduced by half.

[0046] The control signal supplier **23** alternately supplies first and second control signals **CS1** and **CS2** to the first and second control lines **C1** and **C2** for each $\frac{1}{2}$ frame unit as shown in **FIG. 3**. For instance, the control signal supplier **23** can supply the first control signal **CS1** to the first control line **C1** during the first-half $\frac{1}{2}$ frame interval while supplying the second control signal **CS2** to the second control line **C2** during the second-half $\frac{1}{2}$ frame interval. Further, the control signal supplier **23** can supply the second control signal **CS2** to the second control line **C2** during the first-half $\frac{1}{2}$ frame interval while supplying the first control signal **CS1** to the first control line **C1**. Alternatively, the LCD according to the present invention can supply the first and second control signals **CS1** and **CS2** from the timing controller without having the control signal supplier **23** separately as shown in **FIG. 2**.

[0047] A procedure for supplying video signals to the liquid crystal cells **10** and **12** will be described in detail.

[0048] Firstly, the first control signal **CS1** to the first control line **C1** during the first-half $\frac{1}{2}$ frame interval. Then, the fourth thin film transistors **TFT4** connected to the first control line **C1** are turned on. At this time, the second thin film transistors **TFT2** keep a turn-off state.

[0049] During the first-half $\frac{1}{2}$ frame interval, the gate signal **SP** is sequentially applied to the gate lines **GL1** to **GLn**. At this time, the third thin film transistor **TFT3** connected to the gate line **GL** is turned on for each horizontal line. Video signals to be supplied to the second liquid crystal cell **12** are applied to the data lines **DL1** to **DLm/2**. Then, the video signals supplied to the data lines **DL1** to **DLm/2** are applied, via the third and fourth thin film transistors **TFT3** and **TFT4**, respectively, to the second liquid crystal cell **12**. Thus, during the first-half $\frac{1}{2}$ frame interval, the second liquid crystal cells **12** positioned at the odd-numbered vertical lines as shown in **FIG. 5A** are driven. Meanwhile, the first thin film transistors **TFT1** also are sequentially turned on by the gate signals **SP**, but video signals are not applied to the first liquid crystal cell **10** because the second thin film transistors **TFT2** are turned off.

[0050] Thereafter, during the second-half $\frac{1}{2}$ frame interval, the second control signal **CS2** is applied to the second control line **C2**. Then, the second thin film transistors **TFT2** connected to the second control line **C2** are turned on. At this time, the fourth thin film transistors **TFT4** remain off.

[0051] During the second-half $\frac{1}{2}$ frame interval, the gate signal **SP** is sequentially applied to the gate lines **GL1** to **GLn**. At this time, the first thin film transistor **TFT1** connected to the gate line **GL** is turned on for each horizontal line. Video signals to be supplied to the first liquid crystal cell **10** are applied to the data lines **DL1** to **DLm/2**. Then, the video signals supplied to the data lines **DL1** to **DLm/2** are applied, via the first and second thin film transistors **TFT1** and **TFT2**, to the first liquid crystal cell **10**. Thus, during the second-half $\frac{1}{2}$ frame interval, the first liquid crystal cells **10** positioned at the even-numbered vertical lines as shown in **FIG. 5B** are driven. Meanwhile, the third thin film transistors **TFT3** also are sequentially turned on by the gate signals **SP**, but video signals are not applied to the second liquid crystal cell **12** because the fourth thin film transistors **TFT4** are turned off.

[0052] In other words, the LCD according to the first embodiment of the present invention applies the control signals **CS1** and **CS2** alternately for each $\frac{1}{2}$ frame unit to the first and second control lines **C1** and **C2** to thereby alternately turn on the second thin film transistors **TFT2** or the fourth thin film transistors **TFT4**, so that it can alternately drive the first and second liquid crystal cells **10** and **12** for each $\frac{1}{2}$ frame unit. Furthermore, the LCD according to the first embodiment of the present invention supplies desired video signals to the first and second liquid crystal cells **10** and **12** positioned at the left/right sides using a single of data line **DL**, so that it can reduce the number of data lines **DL** and the number of data drive IC's to a half of the prior art, thereby reducing a manufacturing cost thereof.

[0053] Alternatively, in the present invention, the second control signal **CS2** may be applied during the first-half $\frac{1}{2}$ frame interval while the first control signal **CS1** may be

applied during the second-half $\frac{1}{2}$ frame interval. Then, the first liquid crystal cells **10** positioned at the even-numbered horizontal lines as shown in **FIG. 5B** are driven while the second liquid crystal cells **12** positioned at the odd-numbered horizontal lines as shown in **FIG. 5A** are driven. In other words, the LCD according to the present invention can control the sequence in which the first and second control signals CS1 and CS2 are applied to thereby control a driving sequence of the first and second liquid crystal cells **10** and **12**.

[0054] Alternatively, in the present invention, a shape of the liquid crystal display panel **20** can be established variously. For instance, the first and second liquid crystal cells **10** and **12** may be arranged in such a manner to be located at the upper side of the gate lines GL as shown in **FIG. 6** and **FIG. 7**. In other words, in another embodiment of the present invention, first and second liquid crystal cells **10** and **12** positioned at the left/right sides on a basis of the data line DL can be arranged in such a manner to be located at the same horizontal line as shown in **FIG. 6** and **FIG. 7**. In this case, the first liquid crystal cell **10** and the first switching part **14** are arranged at the even-numbered (or odd-numbered) vertical lines while the second liquid crystal cell **12** and the second switching part **16** are arranged at the odd-numbered (or even-numbered) vertical lines. A procedure for applying video signals to the liquid crystal cells **10** and **12** in another embodiment of the present invention is identical to that in the embodiment of the present invention as shown in **FIG. 2** and **FIG. 3**.

[0055] Meanwhile, the LCD according to any one of the embodiments of the present invention is applicable to various liquid crystal panels. For instance, the LCD according to any one of the embodiments of the present invention is applicable to a TN-mode liquid crystal or an IPS-mode liquid crystal, etc.

[0056] As described above, according to the present invention, the liquid crystal cells positioned at the odd-numbered vertical lines and the liquid crystal cells positioned at the even-numbered vertical lines are alternately driven for each $\frac{1}{2}$ frame unit. A single data line drives the liquid crystal cells positioned adjacently with each other at the left/right sides, so the number of data lines can be reduced to about half. Accordingly, the number of data drivers for supplying driving signals to the data lines also can be reduced to about half, thereby reducing a manufacturing cost.

[0057] Although the present invention has been explained by the embodiments shown in the drawings described above, it should be understood to the ordinary skilled person in the art that the invention is not limited to the embodiments, but rather that various changes or modifications thereof are possible without departing from the spirit of the invention. Accordingly, the scope of the invention shall be determined only by the appended claims and their equivalents.

What is claimed is:

1. A liquid crystal display device, comprising:

a plurality of data lines;

a plurality of gate lines provided in a direction crossing the data lines;

first and second control lines provided in a direction being parallel to the gate lines;

first liquid crystal cells provided at one side on a basis of the data lines;

second liquid crystal cells provided at other side on a basis of the data lines;

a first switching part provided for each first liquid crystal cell to apply video signals supplied from the data lines to the first liquid crystal cells under control of the first control line and the gate line; and

a second switching part provided for each second liquid crystal cell to apply video signals supplied from the data lines to the second liquid crystal cells under control of the second control line and the gate line.

2. The liquid crystal display device according to claim 1, further comprising:

a gate driver for sequentially applying a gate signal to the gate lines during an $\frac{1}{2}$ frame interval;

a data driver for applying said video signals to the data lines when said gate signal is applied to the gate lines; and

a control signal supplier for alternately supplying control signals for each $\frac{1}{2}$ frame unit to the first and second control lines.

3. The liquid crystal display device according to claim 2, wherein the first switching part comprises:

a first thin film transistor connected to the gate line turned on when said gate signal is applied, thereby receiving said video signal; and

a second thin film transistor turned on when said control signal is applied to the first control line to apply said video signal to the first liquid crystal cell.

4. The liquid crystal display device according to claim 3, wherein the second switching part comprises:

a third thin film transistor connected to the gate line turned on when said gate signal is applied, thereby receiving said video signal; and

a fourth thin film transistor turned on when said control signal is applied to the second control line to apply said video signal to the second liquid crystal cell.

5. The liquid crystal display device according to claim 4, wherein the first and second liquid crystal cells receive said video signals alternately for each $\frac{1}{2}$ frame unit in response to said control signals applied alternately for each $\frac{1}{2}$ frame unit.

6. The liquid crystal display device according to claim 1, wherein the first liquid crystal cells and the first switching part are provided at odd-numbered vertical lines while the second liquid crystal cells and the second switching part are provided at even-numbered vertical lines.

7. The liquid crystal display device according to claim 1, wherein the first liquid crystal cells and the first switching part are provided at even-numbered vertical lines while the second liquid crystal cells and the second switching part are provided at odd-numbered vertical lines.

8. A method of driving a liquid crystal display device, comprising:

applying video signals corresponding to data to first liquid crystal cells positioned at i th vertical line (wherein i is an even number or an odd number) during a first-half $\frac{1}{2}$ frame interval of one frame interval; and

applying said video signals corresponding to said data to second liquid crystal cells positioned at $(i+1)$ th vertical line during a second-half $\frac{1}{2}$ frame interval of said one frame interval.

9. The method according to claim 8, wherein applying said video signals to the first liquid crystal cells comprises:

applying a first control signal to thereby turn on a first thin film transistor included in a first switching part connected to each of the first liquid crystal cells;

sequentially applying a gate signal to all the gate lines to sequentially turn on each second thin film transistor included in the first switching part; and

applying said video signals to the data lines when said gate signal is applied.

10. The method according to claim 9, wherein applying said video signals to the second liquid crystal cells comprises:

applying a second control signal to thereby turn on a third thin film transistor included in a second switching part connected to each of the second liquid crystal cells;

sequentially applying said gate signal to all the gate lines to sequentially turn on each fourth thin film transistor included in the second switching part; and

applying said video signals to the data lines when said gate signal is applied.

11. The method according to claim 10, wherein said first and second control signals are applied to alternate with each other for each $\frac{1}{2}$ frame interval.

* * * * *

专利名称(译)	液晶显示装置及其驱动方法		
公开(公告)号	US20050134781A1	公开(公告)日	2005-06-23
申请号	US11/012130	申请日	2004-12-16
[标]申请(专利权)人(译)	金宇^ h		
申请(专利权)人(译)	金宇H.		
当前申请(专利权)人(译)	LG DISPLAY CO. , LTD.		
[标]发明人	KIM WOO HYUN		
发明人	KIM, WOO HYUN		
IPC分类号	G02F1/133 G02F1/1362 G09G3/36 G02F1/1343		
CPC分类号	G02F1/13624 G02F1/136286 G09G2310/02 G09G2300/0814 G09G3/3659		
优先权	1020030092697 2003-12-17 KR		
其他公开文献	US7456923		
外部链接	Espacenet USPTO		

摘要(译)

公开了一种用于减少数据线的数量和与其对应的数据驱动集成电路的数量的液晶显示装置及其驱动方法。在该器件中，在与多条数据线交叉的方向上提供多条栅极线。第一和第二控制线沿与栅极线平行的方向设置。基于数据线在一侧提供第一液晶单元。在数据线的基礎上，在另一侧提供第二液晶单元。为每个第一液晶单元提供第一开关部分，以在第一控制线和栅极线的控制下将从数据线提供的视频信号施加到第一液晶单元。为每个第二液晶单元提供第二开关部分，以在第二控制线和栅极线的控制下将从数据线提供的视频信号施加到第二液晶单元。

