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(54) THIN FILM TRANSISTOR-LIQUID CRYSTAL DISPLAY DRIVER

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(57) ABSTRACT

A TFT-LCD driver includes a TFT-LCD panel having a plurality of gate bus lines, a plurality of source bus lines, a plurality of TFT's, and a plurality of liquid crystal cells corresponding to the plurality of TFT's, a gate driver integrated circuit for supplying driving voltages to the gate bus lines to turn the TFT's on and off, a source driver integrated circuit for sequentially supplying analog voltages to the source bus lines so as to input the analog voltages to the plurality of liquid crystal cells through the turned-on TFT's, and a controller for providing control signals to the gate driver integrated circuit and the source driver integrated circuit, wherein the analog voltages supplied from the source driver integrated circuit to the TFT-LCD panel have the same polarity at least twice in sequence, wherein the source driver integrated circuit drives the TFT-LCD panel using one of a dot inversion method and a pixel inversion method.

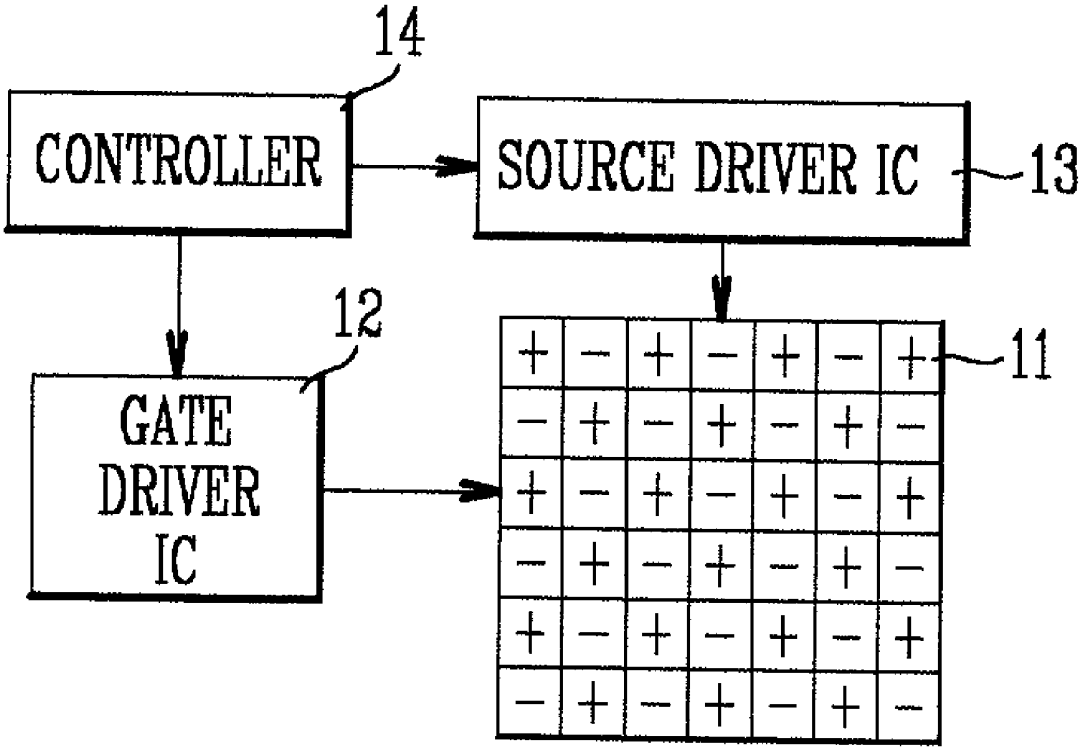


FIG.1
Related Art

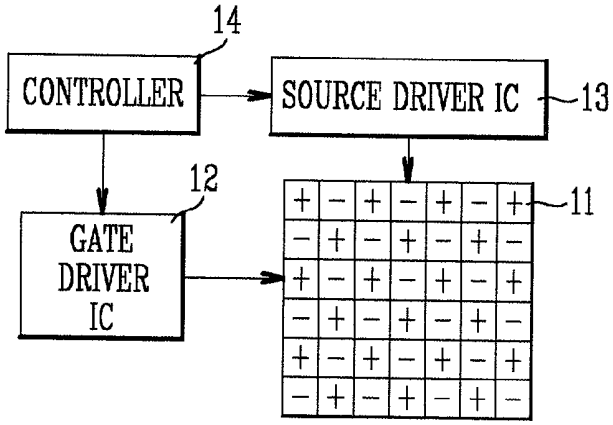


FIG.2
Related Art

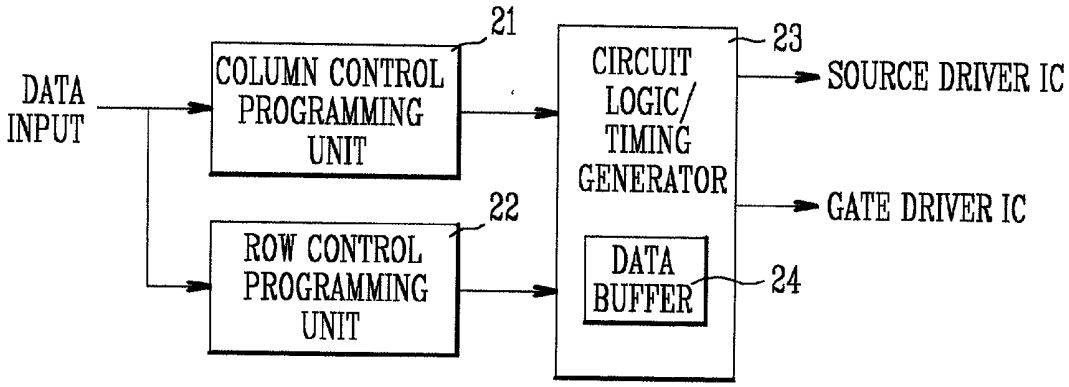


FIG.3

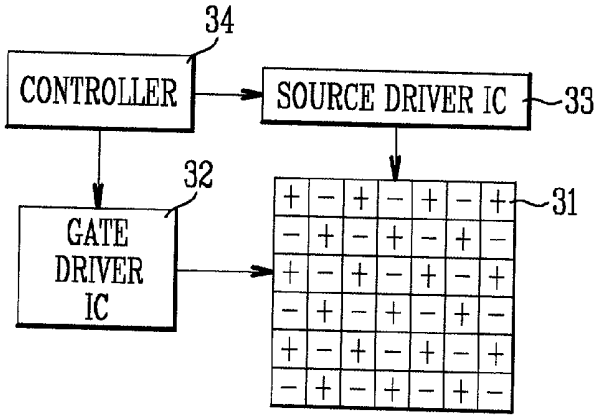

34
CONTROLLER
SOURCE DRIVER IC
33
32
GATE DRIVER IC
31

FIG.4

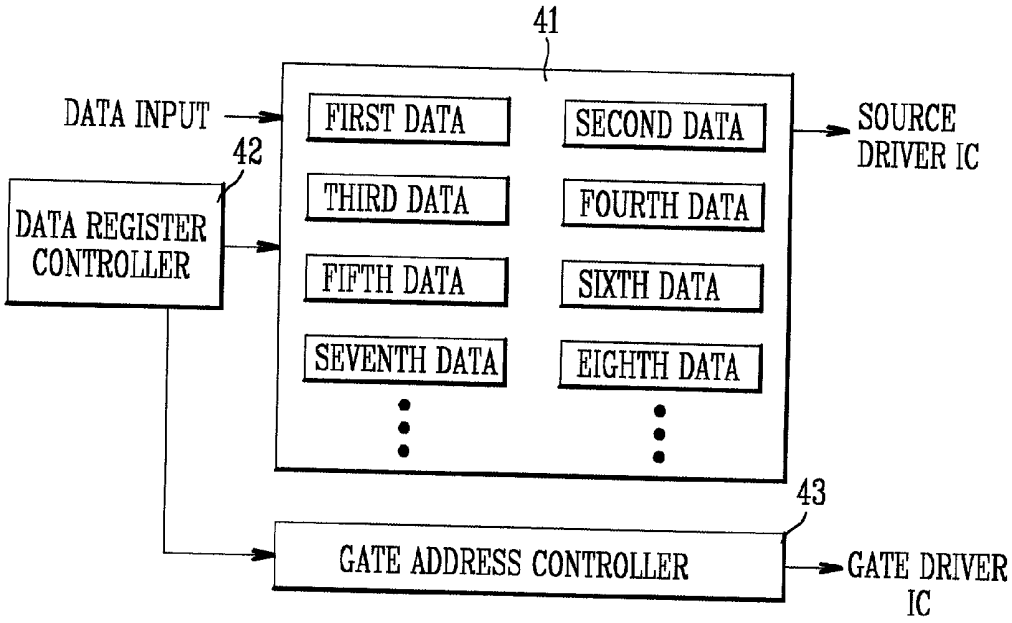
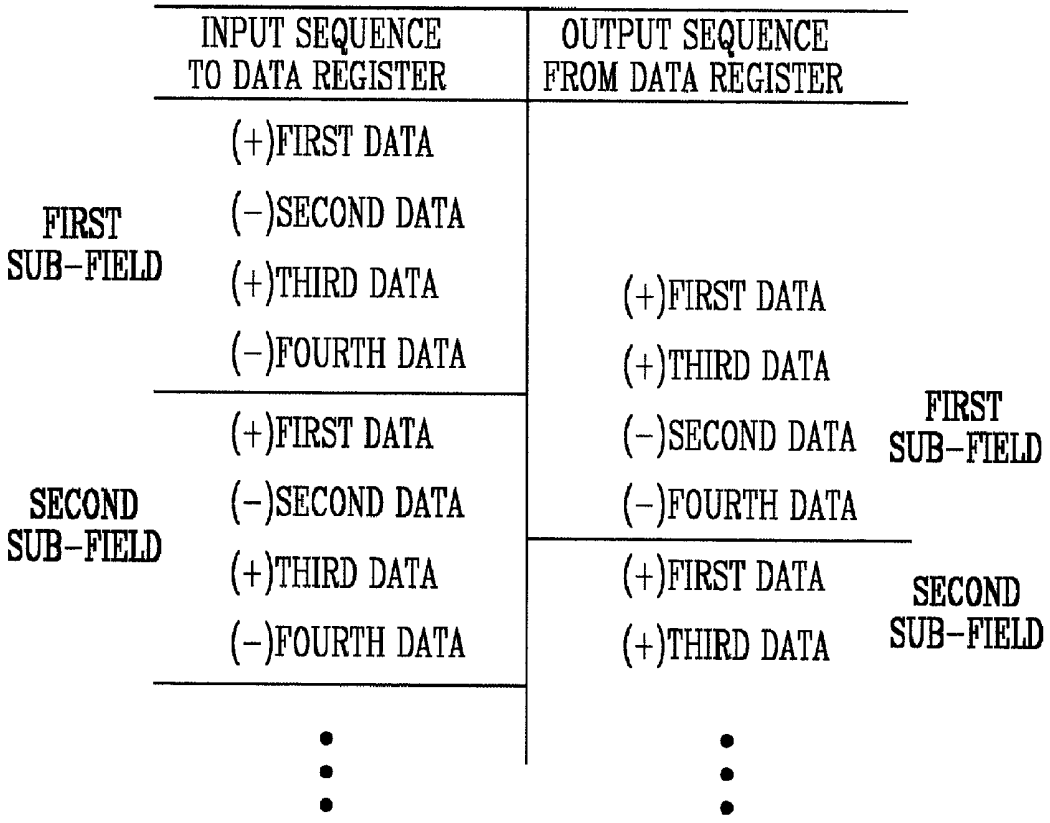

41
DATA INPUT
FIRST DATA
SECOND DATA
SOURCE DRIVER IC
42
DATA REGISTER CONTROLLER
THIRD DATA
FOURTH DATA
FIFTH DATA
SIXTH DATA
SEVENTH DATA
EIGHTH DATA
43
GATE ADDRESS CONTROLLER
GATE DRIVER IC

FIG.5


INPUT SEQUENCE TO DATA REGISTER
OUTPUT SEQUENCE FROM DATA REGISTER
FIRST SUB-FIELD
(+)FIRST DATA
(−)SECOND DATA
(+)THIRD DATA
(−)FOURTH DATA
SECOND SUB-FIELD
(+)FIRST DATA
(−)SECOND DATA
(+)THIRD DATA
(−)FOURTH DATA
(+)FIRST DATA
(+)THIRD DATA
(−)SECOND DATA
(−)FOURTH DATA
FIRST SUB-FIELD
(+)FIRST DATA
(+)THIRD DATA
SECOND SUB-FIELD

THIN FILM TRANSISTOR-LIQUID CRYSTAL DISPLAY DRIVER

[0001] This application claims the benefit of Korean patent application No. 1520/1999, filed Jan. 19, 1999, which is hereby incorporated by reference.

BACKGROUND OF THE INVENTION

[0002] 1. Field of the Invention

[0003] The present invention relates to a TFT-LCD (Thin Film Transistor Liquid Crystal Display) driver, and more particularly, to a TFT-LCD driver that consumes less power.

[0004] 2. Background of the Related Art

[0005] **FIG. 1** is a block diagram of a related art TFT-LCD driver, and **FIG. 2** is a block diagram of a controller for the related art TFT-LCD driver.

[0006] Referring to **FIG. 1, a** related art TFT-LCD includes a TFT-LCD panel **11** having a plurality of gate bus lines and source bus lines, with TFT's (switching devices) formed thereon. Capacitors are formed by injection of a liquid crystal material into a space between an upper plate and a lower plate of the TFT-LCD panel **11**, to form an array of liquid crystal cells. A gate driver IC (Integrated Circuit) **12** is on a left side of the TFT-LCD panel **11** and supplies driving voltages to the gate bus lines in succession to turn the TFT's on and off. A source driver IC **13** is on an upper side of the TFT-LCD panel **11** and sequentially supplies video signal voltages to the source bus lines in the TFT-LCD panel **11**, to apply data voltages to each liquid crystal cell, with the data voltages passing through the turned-on TFT's. A controller **14** provides control signals required for the gate driver IC **12** and the source driver IC **13**.

[0007] As shown in **FIG. 2**, the controller **14** includes column/row control programming units **21** and **22** for receiving external data, and a pixel logic/timing generator **23** with a data buffer for converting outputs from the column/row control programming units **21** and **22** into control signals required for the gate driver IC **12** and the source driver IC **13**

[0008] The operation of the related art TFT-LCD driver will be explained below.

[0009] The controller **14** generates sequential control signals for providing row addresses for received external data, provides the control signals to the gate driver IC **12**, and provides digital R, G, B (Red, Green and Blue) data for row addresses to the source driver IC **13** The gate driver IC **12** supplies sequential driving voltages to the gate bus lines in the TFT-LCD panel **11**, to sequentially turn on the TFT's for the row addresses. Then, the source driver IC **13** receives the digital R, G, B data from the controller **14**, converts the digital R, G, B, data into analog voltages, and supplies the analog voltages to the source bus lines in the TFT-LCD panel **11**. The analog voltages are stored in capacitors in a liquid crystal cell array in the TFT-LCD panel **11** after passing through the TFT's, which are turned on by the gate driver IC **12**. As shown in **FIG. 1**, voltages stored in each liquid crystal cell in the TFT-LCD panel **11** are charged as either (+) or (−) in an alternate manner. The (+) voltage is V_{DD}-V_{COM}, the (−) voltage is V_{COM}-V_{ss}, and V_{COM} is approximately ½(V_{DD}+VSS).

[0010] However, the related art TFT-LCD driver has high power consumption due to the TFT-LCD panel and the source driver IC, because capacitors on data lines in a column direction are charged every time a row is addressed, as the liquid crystal cells in the TFT-LCD panel are alternately charged to (+) or (−) for every frame in sequential row addressing.

SUMMARY OF THE INVENTION

[0011] Accordingly, the present invention is directed to TFT-LCD driver that substantially obviates one or more of the problems due to limitations and disadvantages of the related art

[0012] An object of the present invention is to provide a TFT-LCD driver which consumes less power when driving a source driver IC and the TFT-LCD.

[0013] Additional features and advantages of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the invention. The objectives and other advantages of the invention will be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

[0014] To achieve these and other advantages and in accordance with the purpose of the present invention, as embodied and broadly described, in one aspect of the present invention there is provided a TFT-LCD driver including a TFT-LCD panel having a plurality of gate bus lines, a plurality of source bus lines, a plurality of TFT's, and a plurality of liquid crystal cells corresponding to the plurality of TFT's, a gate driver integrated circuit for supplying driving voltages to the gate bus lines to turn the TFT's on and off, a source driver integrated circuit for sequentially supplying analog voltages to the source bus lines so as to input analog voltages to the plurality of iiquid crystal cells through the turned-on TFT'S, and a controller for providing control signals to the gate driver integrated circuit and the source driver integrated circuit, wherein the analog voltages supplied from the source driver integrated circuit to the TFT-LCD panel have the same polarity at least twice in sequence, wherein the source driver integrated circuit drives the TFT-LCD panel using one of a dot inversion method and a pixel inversion method.

[0015] In another aspect of the present invention there is provided a liquid crystal display including an upper plate, a lower plate facing the upper plate, a plurality of gate bus lines and a plurality of source bus lines on the lower plate, thin film transistors formed at intersections of the gate bus lines and the source bus lines, a liquid crystal cell capacitor array formed at locations corresponding to the thin film transistors between the upper plate and the lower plate, a gate driver circuit for turning the thin film transistors on and off, a source driver circuit for supplying analog voltages to the source bus lines, wherein the analog voltages are stored in the liquid crystal cell capacitor array through the thin film transistors, and a controller for providing control signals to the gate driver circuit and the source driver circuit, wherein analog voltages outputted from the source driver circuit have the same polarity at least twice in a row, and wherein the source driver circuit drives the liquid crystal display using one of a dot inversion method and a pixel inversion method.

[0016] It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE ATTACHED DRAWINGS

[0017] The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate embodiments of the invention and together with the description serve to explain the principles of the invention.

[0018] In the drawings:

[0019] **FIG. 1** is a block diagram of a related art TFT-LCD driver;

[0020] **FIG. 2** is a block diagram of a controller of the related art TFT-LCD driver;

[0021] **FIG. 3** is a block diagram of a TFT-LCD driver in accordance with a preferred embodiment of the present invention,

[0022] **FIG. 4** is a block diagram of a controller of the TFT-LCD driver of the preferred embodiment of the present invention; and

[0023] **FIG. 5** is a table showing sequences of storing and outputting data to/from a controller in the TFT-LCD driver of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

[0024] Reference will now be made in detail to the preferred embodiments of the present invention, examples of which are illustrated in the accompanying drawings.

[0025] Referring to **FIG. 3, a** TFT-LCD driver in accordance with a preferred embodiment of the present invention is used with a TFT-LCD panel **31** having a plurality of gate bus lines, source bus lines, and TFT's. Capacitor cells are formed by injection of a liquid crystal into a space between an upper plate and a lower plate of the TFT-LCD panel **31**. A gate driver IC (Integrated Circuit) **32** is on a left side of the TFT-LCD panel **31** and sequentially supplies driving voltages to the gate bus lines in the TFT-LCD panel **31** to turn the TFT's on and off. A source driver IC **33** is on an upper side of the TFT-LCD panel **31** and sequentially supplies video signal voltages to the source bus lines in the TFT-LCD panel **31**, so that data voltages can be applied to liquid crystal cells through the turned-on TFT's. A controller **34** provides control signals required for the gate driver IC **32** and the source driver IC **33**, so that analog data supplied from the source driver IC **33** to the TFT-LCD panel **31** has the same polarities at least twice in sequence. The source driver IC **33** drives the TFT-LCD panel **31** using a dot inversion method or a pixel inversion method.

[0026] As shown in **FIG. 4**, the controller **34** includes a data register **41** for temporarily receiving and storing external data, a data register controller **42** for providing a control signal to the data register **41** to control a sequence of outputting data from the data register **41** to the source driver IC **33**, and a gate address controller **43** for receiving the control signals from the data register controller **42** to control the gate driver IC **32**. The data outputted to the source driver IC **33** and the signal to the gate driver IC **32** are synchronized.

[0027] The operation of the TFT-LCD driver in accordance with the preferred embodiment will now be explained.

[0028] When the controller **34** receives external data, the data register **41** sequentially stores the external data. Then, the data register controller **42** controls the data register **41** so that, of the data stored in the data register **41**, analog data supplied from the source driver IC **33** to the TFT-LCD panel **31** have the same polarities two or more than two times in sequence, i.e., odd numbered address data are provided twice to the source driver IC **33** before even numbered address data are provided to the source driver IC **33**. The source driver IC **33** drives the TFT-LCD panel **31** using a dot or a pixel inversion method. At the same time, the data register controller **42** controls the gate address controller **43** such that data from the data register **41** supplied to the source driver IC **33** and the signal from the gate driver IC **32** are synchronized.

[0029] Then, upon receiving an output from the gate address controller **43**, the gate driver IC **32** sequentially supplies driving voltages to the gate bus lines in the TFT-LCD panel **31**, to turn on the TFT's for odd numbered address data before turning on the TFT's for even numbered address data. Also, upon reception of odd numbered outputs from the data register **41**, the source driver IC **33** converts odd numbered digital R, G, B address data into analog voltages and provides the analog voltages to the source bus lines in the TFT-LCD panel **31**. Then, the source driver IC **33** receives even numbered outputs from the data register **41**, converts even numbered digital R, G, B data into analog voltages, supplies the analog voltages to the source bus lines in the TFT-LCD panel **31**, and stores the analog voltages in capacitors in the liquid crystal cell array in the TFT-LCD panel **31** by passing them through the TFT's that are turned on by the gate driver IC **32**.

[0030] The sequence of even or odd numbered outputs may also be reversed.

[0031] The operation of the TFT-LCD driver in accordance with a preferred embodiment of the present invention will be explained in detail using the example below.

[0032] Referring to **FIG. 5**, the data register **41** sequentially stores received data in the same manner as in the related art, in a state where four row addresses form a sub-field. The data register controller **42** controls the data register **41** such that the data register **41** supplies a first data and a third data (both with a positive polarity) to the source driver IC **33** before the data register **41** provides a second data and a fourth data (both with a negative polarity) to the source driver IC **33**. The data register controller **42** controls the gate address controller **43** such that the data from the data register **41** to the source driver IC **33** and the signal provided from the gate driver IC **32** are synchronized.

[0033] Then, the gate driver IC **32** receives an output from the gate address controller **43**, supplying driving voltages to the gate bus lines in the TFT-LCD panel **31**, to sequentially turn on the TFT's for the first data, the TFT's for the third data, the TFT's for the second data, and the TFT's for the fourth data. The source driver IC **33** receives an output from

the data register **41**, and, at first, converts first digital R, G, B data into analog voltages and supplies them to the source bus lines in the TFT-LCD panel **31**. Next, the source driver IC **33** converts third digital R, G, B data into analog voltages and supplies them to the source bus lines in the TFT-LCD panel **31**. Then, the source driver IC **33** sequentially converts second digital R, G, B data and fourth digital R, G, B data into analog voltages, sequentially supplies them to the source bus lines in the TFT-LCD panel **31**, and stores them in the capacitors in the liquid crystal cell array in the TFT-LCD panel **31** by passing them through the TFT's that are turned on by the gate driver IC **32**.

[**0034**] In this approach, total power consumption of the source driver IC **33** of the present invention is approximately half the power consumption of the related art source driver, since polarities are changed every time in the related art when four row addresses form a single sub-field, while polarities are changed only half as often in the TFT-LCD driver in accordance with the preferred embodiment of the present invention. Therefore, when eight row addresses form a sub-field, power consumption of the source driver IC **33** of the TFT-LCD driver in accordance with the preferred embodiment of the present invention is reduced to approximately ¼ compared to the power consumption of the related art source driver IC.

[**0035**] Because the TFT-LCD driver of the present invention addresses rows such that the same polarities are repeated at least twice for analog data supplied from the source driver IC to the TFT-LCD panel, with reduced number of alternations from (+) to (−) (and vice versa) per frame compared to the related art, power consumption of the TFT-LCD panel and the source driver IC can be reduced because the number of times required for charging the capacitors in column data lines is reduced.

[**0036**] It will be apparent to those skilled in the art that various modifications and variations can be made in the TFT-LCD driver of the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention cover the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

What is claimed is:

1. A TFT-LCD driver comprising:

a TFT-LCD panel having a plurality of gate bus lines, a plurality of source bus lines, a plurality of TFT's, and a plurality of liquid crystal cells corresponding to the plurality of TFT's;

a gate driver integrated circuit for supplying driving voltages to the gate bus lines to turn the TFT's on and off;

a source driver integrated circuit for sequentially supplying analog voltages to the source bus lines so as to input the analog voltages to the plurality of liquid crystal cells through the turned-on TFT's; and

a controller for providing control signals to the gate driver integrated circuit and the source driver integrated circuit, wherein the analog voltages supplied from the source driver integrated circuit to the TFT-LCD panel have the same polarity at least twice in sequence,

wherein the source driver integrated circuit drives the TFT-LCD panel using one of a dot inversion method and a pixel inversion method.

2. The device as claimed in claim 1, wherein the controller further includes:

a data register for receiving and temporarily storing external data;

a data register controller for controlling a sequence of the external data supplied from the data register to the source driver integrated circuit; and

a gate address controller for synchronizing the external data supplied to the source driver integrated circuit and for receiving the control signals from the data register controller.

3. A liquid crystal display comprising:

an upper plate;

a lower plate facing the upper plate;

a plurality of gate bus lines and a plurality of source bus lines on the lower plate;

thin film transistors formed at intersections of the gate bus lines and the source bus lines;

a liquid crystal cell capacitor array formed at locations corresponding to the thin film transistors between the upper plate and the lower plate;

a gate driver circuit for turning the thin film transistors on and off,

a source driver circuit for supplying analog voltages to the source bus lines, wherein the analog voltages are stored in the liquid crystal cell capacitor array through the thin film transistors; and

a controller for providing control signals to the gate driver circuit and the source driver circuit,

wherein the analog voltages outputted from the source driver circuit have the same polarity at least twice in a row, and

wherein the source driver circuit drives the liquid crystal display using one of a dot inversion method and a pixel inversion method.

4. The device as claimed in claim 1, wherein the controller further includes:

a data register for receiving and temporarily storing external data;

a data register controller for controlling a sequence of the external data supplied from the data register to the source driver circuit; and

a gate address controller for synchronizing the external data supplied to the source driver integrated circuit and for receiving the control signals from the data register controller.

* * * * *

专利名称(译)	薄膜晶体管 - 液晶显示驱动器		
公开(公告)号	US20020171616A1	公开(公告)日	2002-11-21
申请号	US09/484054	申请日	2000-01-18
[标]申请(专利权)人(译)	MIN BYUNG MOO		
申请(专利权)人(译)	MIN BYUNG MOO		
当前申请(专利权)人(译)	MIN BYUNG MOO		
[标]发明人	MIN BYUNG MOO		
发明人	MIN, BYUNG MOO		
IPC分类号	G09G3/36		
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优先权	1019990001520 1999-01-19 KR		
其他公开文献	US6724362		
外部链接	Espacenet USPTO		

摘要(译)

TFT-LCD驱动器包括具有多条栅极总线的TFT-LCD面板，多条源极总线，多个TFT，以及与多个TFT对应的多个液晶单元，栅极驱动器集成电路用于向栅极总线提供驱动电压以打开和关闭TFT的源极驱动器集成电路，用于顺序地向源极总线提供模拟电压，以便通过转向输入模拟电压到多个液晶单元。在TFT上，以及用于向栅极驱动器集成电路和源极驱动器集成电路提供控制信号的控制器，其中从源极驱动器集成电路提供给TFT-LCD面板的模拟电压具有至少两次相同的极性，其中，源极驱动器集成电路使用点反转方法和像素反转方法之一来驱动TFT-LCD面板。