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Su et al. (43) **Pub. Date: Feb. 14, 2008**(54) **LIQUID CRYSTAL DISPLAY PANELS**(30) **Foreign Application Priority Data**(75) Inventors: **Mu-Jen Su**, Hsin-Chu (TW);
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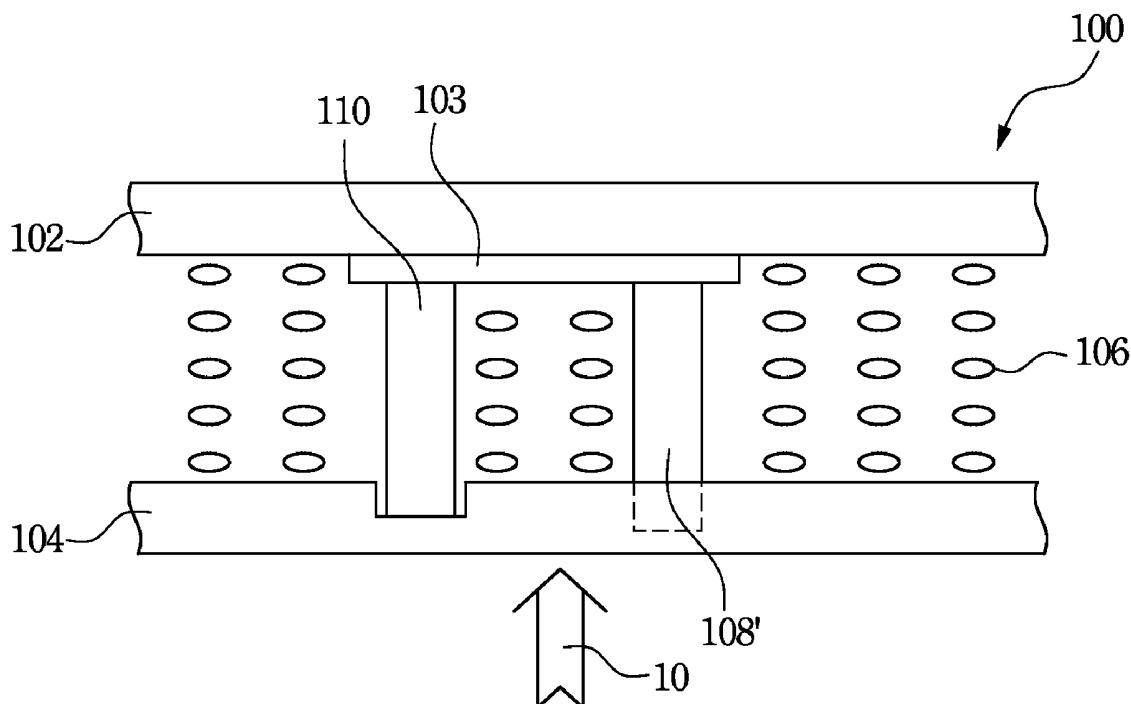
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ATLANTA, GA 30339-5994(57) **ABSTRACT**

A liquid crystal display panel comprises a color filter substrate, a thin film transistor array substrate, and a liquid crystal layer sealed between the two substrates. The color filter substrate has a plurality of first spacers and a plurality of second spacers thereon. The thin film transistor array substrate has a plurality of recesses for containing the second spacers. The ratio of the number of the second spacers to the number of the first spacers is between about 10% and about 90%.

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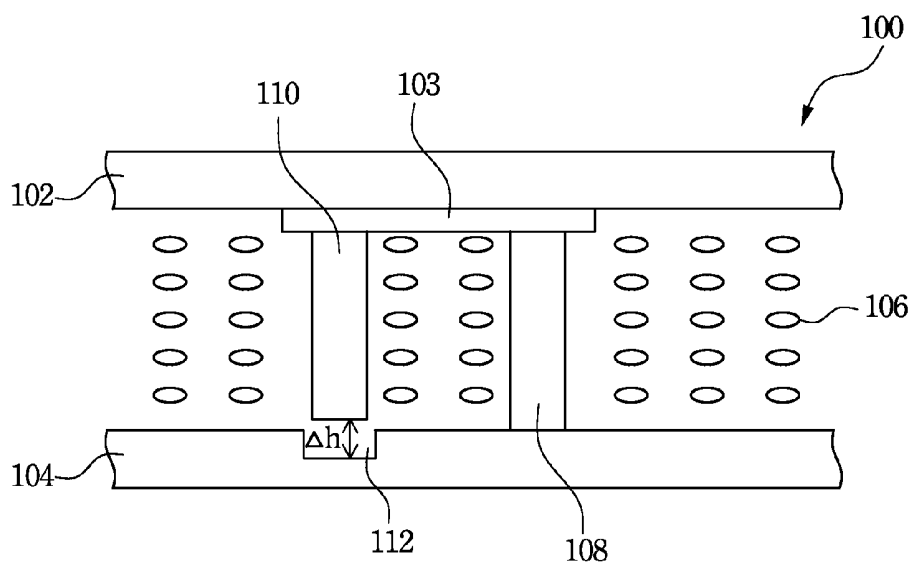


Fig. 1

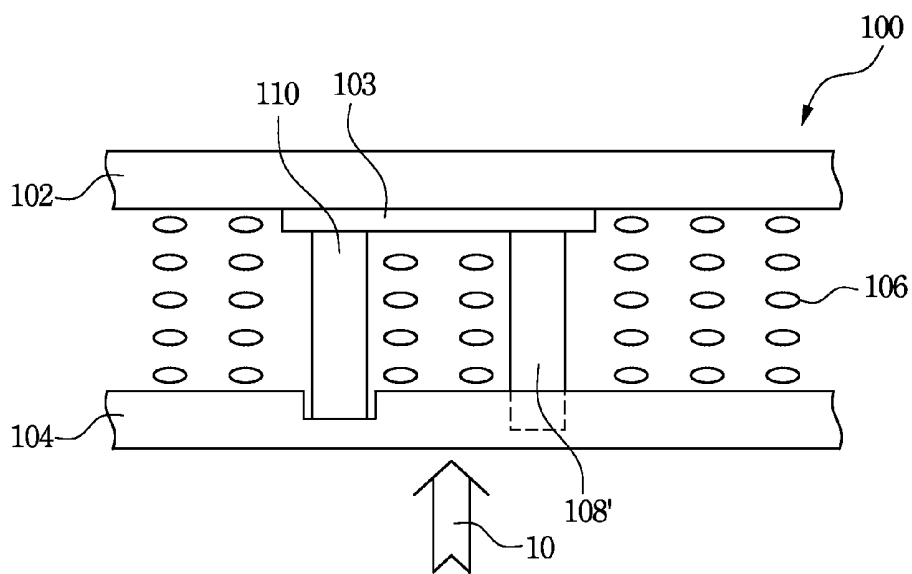


Fig. 2

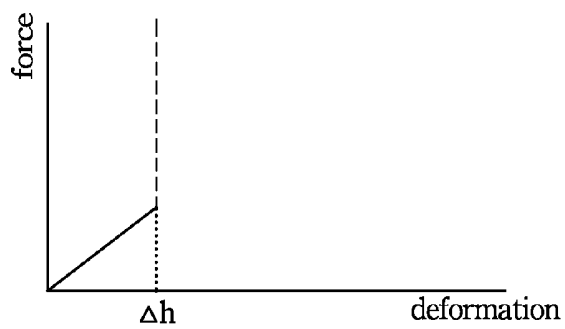


Fig. 3

LIQUID CRYSTAL DISPLAY PANELS

RELATED APPLICATIONS

[0001] The present application is based on, and claims priority to, Taiwan Application Serial Number 95129816, filed Aug. 14, 2006, the disclosure of which is hereby incorporated by reference herein in its entirety.

FIELD OF THE INVENTION

[0002] The present invention is related to a structure of a liquid crystal display panel.

BACKGROUND OF THE INVENTION

[0003] One drop filling (ODF) process is used as a new generation liquid crystal manufacturing technique. The ODF process is widely used for mass production of liquid of crystal because of its advantage of largely shortening the time required to fill liquid crystal. Nevertheless, a bottle neck of the ODF process is its narrow tolerance of the amount of the filled liquid crystal. In detailed description, tolerance of the amount of the filled liquid crystal is very narrow under low temperature. Vacuum bubbles are easily generated under low temperature when the amount of the filled liquid crystal is not accurate, or photo-resist spacers located between the color filter substrate and TFT array substrate are diverged (in other word, vacuum bubble is a vacuum space generated by liquid crystal volume shrinkage under low temperature). Methods for increasing the tolerance of the amount of the filled liquid crystal include, selecting a softer material for photo-resist spacers, decreasing the contact area between the photo-resist spacers and TFT array substrate and so on. These methods allow the distance between liquid crystal cells to be modulable in the manufacturing process to increase tolerance of the amount of the filled liquid crystal.

SUMMARY OF THE INVENTION

[0004] For the purpose of increasing the tolerance of the amount of filled liquid crystal in the ODF process and the purpose of increasing pressure resistivity of the liquid crystal display panel, the embodiments of the present invention provide a liquid crystal display panel with enough pressure resistivity. The display panel comprises a color filter substrate, a TFT array substrate and a liquid crystal layer sealed between both substrates. A plurality of first spacers and a plurality of second spacers are located on the color filter substrate. A plurality of recesses is located on the TFT array substrate for containing the second spacers while the two substrates are sealed face to face. The ratio of the number of second spacers to the number of first spacers is from 10 to 90.

[0005] In another embodiment, the present invention provides a liquid crystal display panel. The display panel comprises a color filter substrate, a TFT array substrate and a liquid crystal layer sealed between both substrates. A plurality of spacers is located on the color filter substrate. A plurality of recesses is located on the TFT array substrate for containing a portion of the spacers while the two substrates

are sealed face to face. The ratio of the number of recesses to the number of spacers is from 90% to 98%.

BRIEF DESCRIPTION OF THE DRAWINGS

[0006] The foregoing aspects and many of the attendant advantages of this invention will become more readily appreciated as the same becomes better understood by reference to the following detailed description, when taken in conjunction with the accompanying drawings, wherein:

[0007] FIG. 1 illustrates a cross-sectional view of one embodiment of the present invention;

[0008] FIG. 2 illustrates a cross-sectional view of the liquid crystal display panel shown in FIG. 1 under pressure; and

[0009] FIG. 3 illustrates a relationship scheme of deformation of the first spacer vs. the force applied on the liquid crystal display panel.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

[0010] In the present ODF process, liquid crystal is dropped on the color filter substrate (or TFT substrate) in vacuum and glue is coated on the rim of the TFT substrate (or color filter substrate). Then, the color filter substrate and the TFT substrate are assembled together. A liquid crystal display panel is accomplished by removing the vacuum source and admitting atmospheric air to compress the color filter substrate or TFT substrate.

[0011] If the photo-resist spacers can not provide enough deformation when the amount of the filled liquid crystal is not accurate or the height of the spacers is not as expected, then the vacuum bubbles will be generated because the height of the spacers and the amount of the filled liquid crystal can not match the space in the liquid crystal cell between the two substrates. Although a softer photo-resist spacer can be used to solve the problem caused by the mismatch between the height of the spacers and the filled liquid crystal, the softer photo-resist spacers do not have enough pressure resistivity so that the yield of liquid crystal display panel is decreased because defects are easily generated by the compressing force required for assembling the module.

[0012] An embodiment of the present invention provides a liquid crystal display panel. FIG. 1 illustrates a cross-sectional view of one embodiment of the present invention. Refer to FIG. 1, a liquid crystal display panel 100 comprises a color filter substrate 102, a TFT substrate 104, and a liquid crystal layer 106 sealed between the color filter substrate 102 and the TFT substrate 104. The color filter substrate 102 has a plurality of first spacers 108 and a plurality of second spacers 110. The TFT substrate 102 has a plurality of recesses 112. The location of the recesses 112 corresponds to the position of the second spacers 110. The size of the recess 112 is proper for containing the second spacer 110. The color filter substrate 102 further comprises a black matrix layer 103. The preferred location of the second spacers 110 is on the black matrix layer 103 to avoid possible light leakage.

[0013] The depth of the recesses is from about 0.2 μm to about 0.8 μm . The height of the first spacers 108 and the second spacers 110 is from about 1 μm to about 10 μm . The ratio of the number of the second spacers 110 to the number of the first spacers is from about 10 to about 90. The location of the recesses 112 corresponds to the position of the second

spacers 110 for providing supporting force when an external force is applied upon the liquid crystal display panel.

[0014] In the present embodiment, the first spacers 108 and the second spacers 110 may be disposed on the color filter substrate (or the TFT array substrate) while the recesses are formed on the TFT array substrate (or the color filter substrate).

[0015] In the present embodiment, the first spacers 108 and the second spacers 110 are different in shape, height or both. The first spacers 108 are located between the color filter substrate 102 and the TFT substrate 104. The TFT substrate 104 has recesses 112 corresponding to the position of the second spacers 110. Δh is defined as the distance between the bottom (top) portion of the recesses 112 and the second spacers 110 as shown in FIG. 1. The shape of the second spacers 110 can be cylinder, cuboid, cone-shape, or other pillar like shape. The width of the cross section of the second spacers 110 is from about 9 μm to about 11 μm .

[0016] Referring to FIG. 2, FIG. 2 illustrates a cross-sectional view of the liquid crystal display panel shown in FIG. 1 under pressure. When an external force 10 provided by air pressure is applied upon the liquid crystal display panel while the volume of the liquid crystal layer shrinks at low temperature, the first spacers 108, such as the photo-resist spacers illustrated in the FIG. 1 will be elastically deformed and become the spacers 108' illustrated in the FIG. 2. The distance between the color filter substrate 102 and TFT substrate 104 is thus decreased so that vacuum bubbles can be avoided. The tolerance of the amount of the filled liquid crystal can be increased to allow the amount of the filled liquid crystal and the height of the spacers to be well matched and vacuum bubbles can be avoided when the change of liquid crystal volume or the drifting of the height of the photo-resist spacers occurs.

[0017] Referring to FIG. 1 and FIG. 2, the top of the second spacers 110 do not contact with the bottom (top) surface of the recesses 112 until the shortened distance between the color filter substrate 102 and TFT substrate 104 reaches Δh . The recess 112 contains the second spacer 110. The cross section area of the recess 112 is equal to or slightly larger than the cross section area of the second spacer 110. For example, in the present embodiment, a height of the first spacer 108 may be substantially equal to that of the second spacer 110. For example, in the present embodiment, the cross section area of the recess 112 may be slightly larger than a smallest or a largest cross section area of the second spacer 110.

[0018] FIG. 3 illustrates a relationship scheme of deformation of the first spacer vs. the force applied on the liquid crystal display panel. When an external force 10 provided by the air-pressure is applied upon the liquid crystal display panel while the volume of the liquid crystal layer shrinks at low temperature, the first spacers is elastically deformed and the distance between the color filter substrate 102 and TFT substrate 104 is decreased. At this moment, the relationship between the shortened distance and the external force applied upon the liquid crystal display panel 100 is illustrated as the solid line in FIG. 3. When the external force increases, the shortened distance between the color filter substrate 102 and TFT substrate 104 reaches Δh and the top of the second spacers 110 contacts with the bottom (top) surface of the recesses 112. The second spacers 110 provide more supporting force to resist the external force. At this moment, the relationship between the shortened distance

and the external force applied upon the liquid crystal display panel 100 is illustrated as the dotted line in FIG. 3. The shortened distance will not be increased anymore when the shortened distance reaches the maximum value of the solid line. Therefore, the liquid crystal display panel can resist more pressure. The structure disclosed in the present embodiment provides higher pressure resistivity to the liquid crystal display panel.

[0019] In another embodiment of the present invention, the first spacers and the second spacers are the same in shape and size. The first spacers and the second spacers are formed in one lithographic process. The ratio of the amount of recesses located on the TFT substrate to the total amount of the spacers is from about 90% to about 98%.

[0020] In the other embodiment of the present invention, the first spacers and the second spacers may be made from different materials. The first spacers are made from more elastic material, such as photo-resist and the second spacers are made from more rigid material, such as silicon oxide, silicon nitride, silicon oxynitride and so on for providing high pressure resistivity to the liquid crystal display panel.

[0021] The disclosures of the embodiments of the present invention provide different shape designs and locations of spacers. The spacers located between the TFT substrate and the color filter substrate provide enough deformation under the compressing step of the ODF process. Therefore, the tolerance of the amount of the filled liquid crystal can be increased and the vacuum bubbles can be either decreased or avoided. Furthermore, when a force is applied upon the liquid crystal display panel, the distance between the color filter substrate and the TFT substrate is decreased and the backup spacers on the color filter substrate are inserted in the recesses on the TFT substrate to provide more supporting force. Then, the liquid crystal display panel has higher pressure resistivity.

[0022] The spacers are located on one side of the color filter substrate. There are two kinds of spacers, major spacers and assistant spacers. The major spacers and the assistant spacers are the same or different in shape, size or both. The assistant spacers correspond to the recesses located on the TFT substrate to provide higher pressure-resistivity to the liquid crystal display panel when the deformation of the major spacers equals to or exceeds Δh . Only the major spacers contact with the TFT substrate and the color filter substrate when the liquid crystal display panel is manufactured. While a force is provided to the liquid crystal display panel, the major spacers are deformed and the deformation of the major spacers reaches Δh , the assistant spacers begin to provide supporting force. Because the contact area between the spacers and the TFT substrate increases, the resistance to the force from outside is also increased and the deformation of the spacers will not be increased any more.

[0023] As is understood by a person skilled in the art, the foregoing preferred embodiments of the present invention are illustrated of the present invention rather than limiting of the present invention. It is intended to cover various modifications and similar arrangements included within the spirit and scope of the appended claims, such as using the design of area change between the spacers and substrate to control the deformation of the spacer to achieve the scheme illustrated in FIG. 3; the spacers are located on the black matrix or passive layer of the color filter substrate; the recesses are located on any position of the TFT substrate for corresponding the assistant spacers; using halftone mask or controlling

the energy of the exposure for forming recesses and spacers, and so on, scope of which should be accorded the broadest interpretation so as to encompass all such modifications and similar structure.

1. A liquid crystal display panel, comprising, at least comprises:

a first substrate having a plurality of first spacers and a plurality of second spacers located thereon;
a second substrate having a plurality of recesses thereon for containing the plurality of second spacers; and
a liquid crystal layer sealed between the first substrate and the second substrate;

wherein a ratio of the number of the second spacers to the number of the first spacers is from about 10 to about 90.

2. The display panel of claim 1, wherein the first spacers and the second spacers are made of the same material.

3. The display panel of claim 1, wherein a depth of the recesses is from about 0.2 μm to about 0.8 μm .

4. The display panel of claim 1, further comprising a black matrix disposed on the first substrate, wherein at least one of the first spacers and the second spacers is disposed on the black matrix.

5. The display panel of claim 1, wherein a height of the first spacers is substantially equal to that of the second spacers.

6. The display panel of claim 1, wherein a cross section area of the recess is slightly larger than a largest cross section area of the second spacer.

7. The display panel of claim 1, wherein a cross section area of the recess is slightly larger than a smallest cross section area of the second spacer.

8. The display panel of claim 1, wherein a cross section area of the recess is slightly larger than a cross section area of the second spacer.

9. The display panel of claim 1, wherein a height of the first spacers is from about 1 μm to about 10 μm .

10. The display panel of claim 1, wherein a height of the second spacers is from about 1 μm to about 10 μm .

11. A liquid crystal panel, comprising:

a first substrate having a plurality of spacers located thereon;

a second substrate having a plurality of recesses thereon for containing a portion of the spacers; and

a liquid crystal layer sealed between the first substrate and the second substrate;

wherein a ratio of the number of the recesses to the number of the spacers is from about 90% to about 98%.

12. The display panel of claim 11, wherein a depth of the recesses is from about 0.2 μm to about 0.8 μm .

13. The display panel of claim 11, further comprising a black matrix located between the plurality of spacers and the first substrate.

14. The display panel of claim 11, wherein a cross section area of the recess is slightly larger than a largest cross section area of the spacer.

15. The display panel of claim 11, wherein a cross section area of the recess is slightly larger than a smallest cross section area of the spacer.

16. The display panel of claim 1, wherein a cross section area of the recess is slightly larger than a cross section area of the spacer.

17. The display panel of claim 11, wherein a height of the spacers is from about 1 μm to about 10 μm .

18. The display panel of claim 1, wherein the first substrate is a color filter substrate and the second substrate is a thin film transistor array substrate.

19. The display panel of claim 11, wherein the first substrate is a color filter substrate and the second substrate is a thin film transistor array substrate.

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专利名称(译)	液晶显示面板		
公开(公告)号	US20080036958A1	公开(公告)日	2008-02-14
申请号	US11/781540	申请日	2007-07-23
[标]申请(专利权)人(译)	友达光电股份有限公司		
申请(专利权)人(译)	友达光电股份有限公司		
当前申请(专利权)人(译)	友达光电股份有限公司		
[标]发明人	SU MU JEN LIN JUNG HSIANG CHANG CHIH MING TSAI CHING YU HSU SHIH CHIA		
发明人	SU, MU-JEN LIN, JUNG-HSIANG CHANG, CHIH-MING TSAI, CHING-YU HSU, SHIH-CHIA		
IPC分类号	G02F1/1339		
CPC分类号	G02F1/13394		
优先权	095129816 2006-08-14 TW		
其他公开文献	US7684004		
外部链接	Espacenet USPTO		

摘要(译)

液晶显示面板包括滤色器基板，薄膜晶体管阵列基板和密封在两个基板之间的液晶层。滤色器基板上具有多个第一间隔物和多个第二间隔物。薄膜晶体管阵列基板具有多个用于容纳第二间隔物的凹槽。第二间隔物的数量与第一间隔物的数量的比率在约10%和约90%之间。

