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(19) **United States**(12) **Patent Application Publication**  
**Chae**(10) **Pub. No.: US 2003/0117552 A1**(43) **Pub. Date: Jun. 26, 2003**(54) **REFLECTIVE LIQUID CRYSTAL DISPLAY  
DEVICE AND METHOD FOR  
MANUFACTURING THE SAME**(52) **U.S. Cl. .... 349/113**(76) **Inventor: Gee Sung Chae, Incheon-kwangyokshi  
(KR)**(57) **ABSTRACT**

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Dec. 26, 2001 (KR) ..... 2001-85288

**Publication Classification**(51) **Int. Cl.<sup>7</sup> ..... G02F 1/1335**

A reflective liquid display device and a method for manufacturing the same which has high luminance. The reflective liquid crystal display device includes a plurality of gate and data lines formed on a reflective substrate to cross with one another, thereby defining a plurality of pixel regions; gate and data pads respectively formed at each one end of the gate and data lines; a thin film transistor connected to the gate and data lines; a passivation layer having a plurality of holes within the pixel region, being formed on an entire surface of the reflective substrate; a first transparent electrode formed on the passivation layer between the holes of the pixel region; a second transparent electrode formed on the passivation layer between the holes of the pixel region and the passivation layer having the holes.

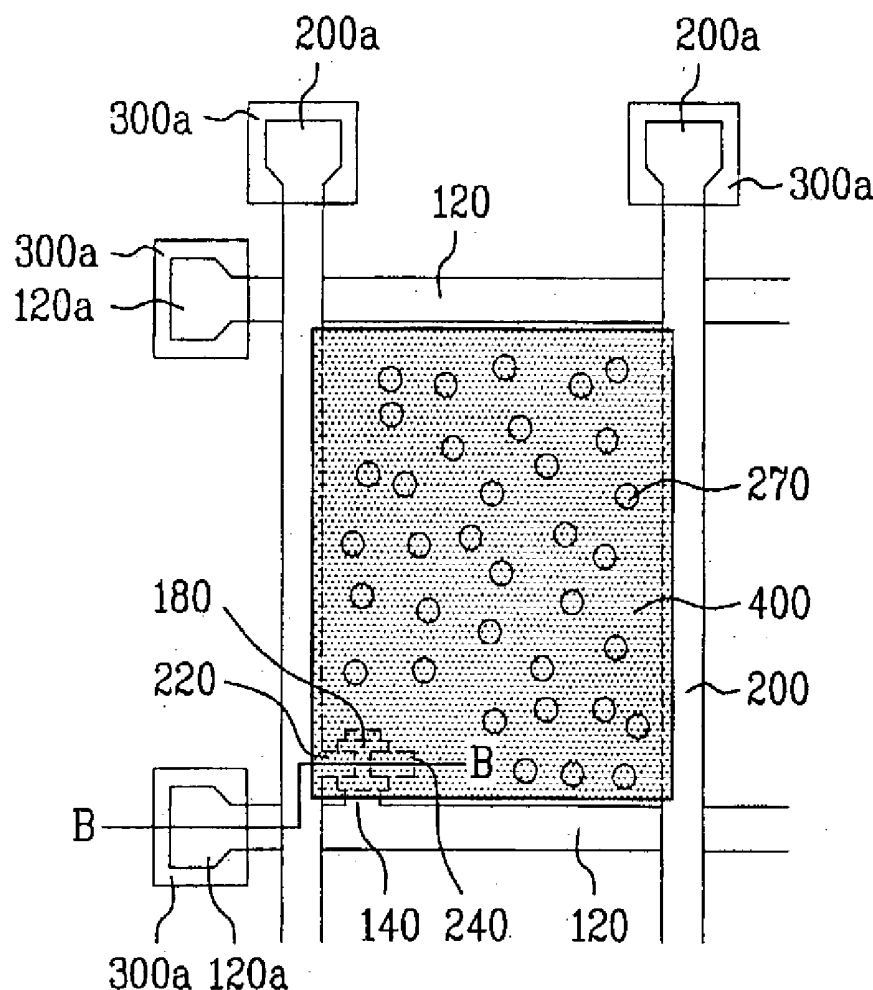


FIG. 1A  
Related Art

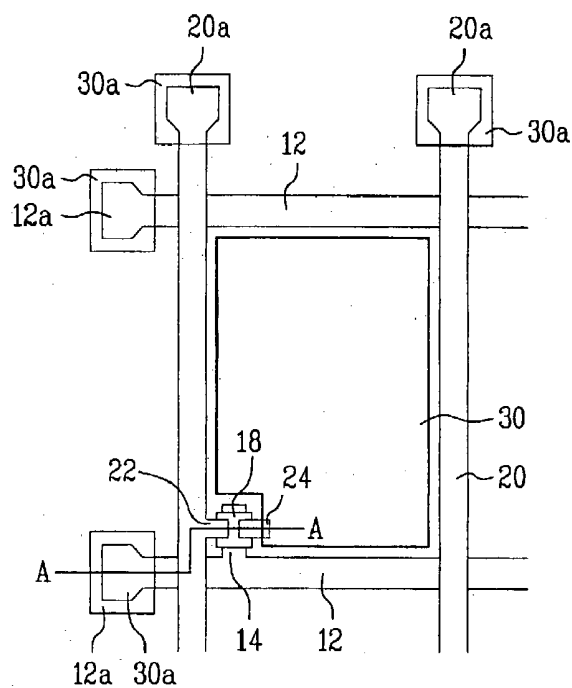


FIG. 1B  
Related Art

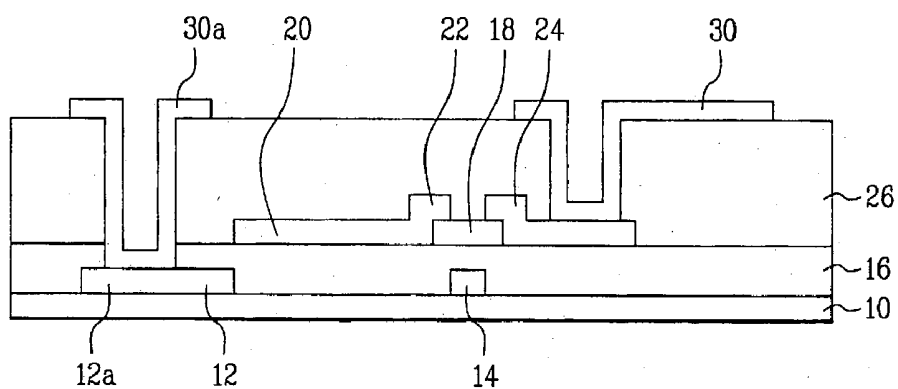


FIG. 2

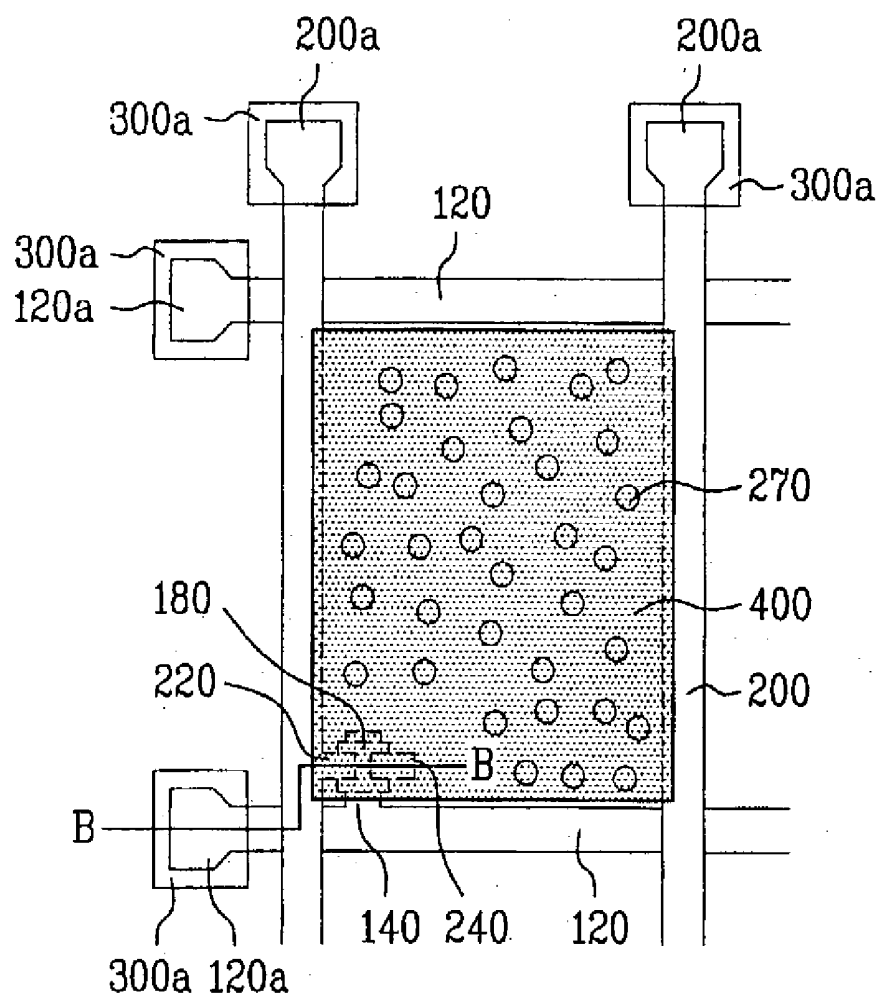


FIG. 3A

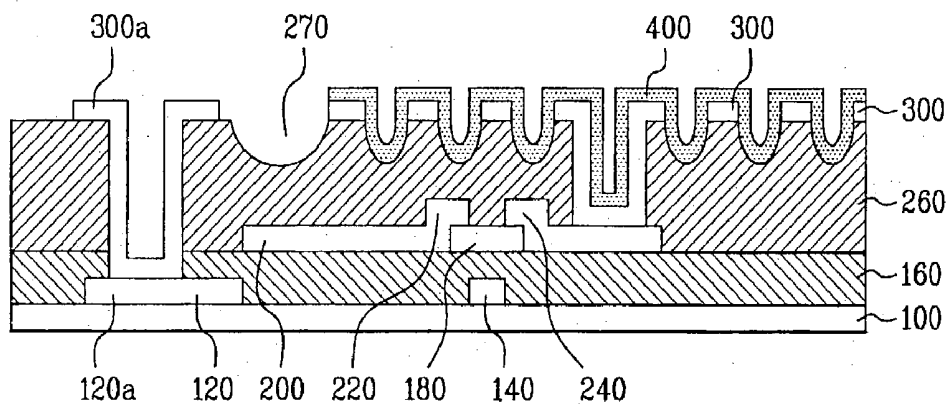


FIG. 3B

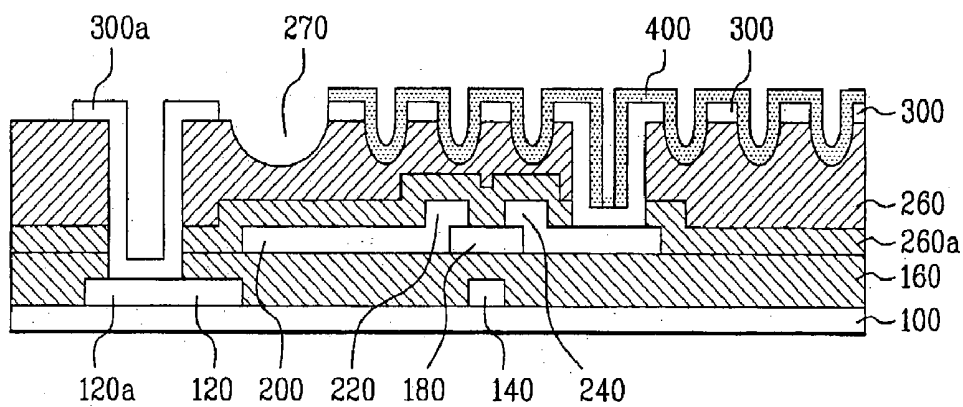


FIG. 4A

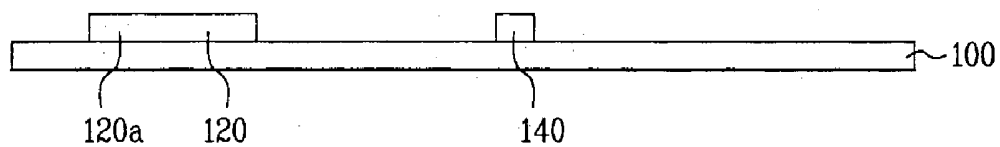


FIG. 4B

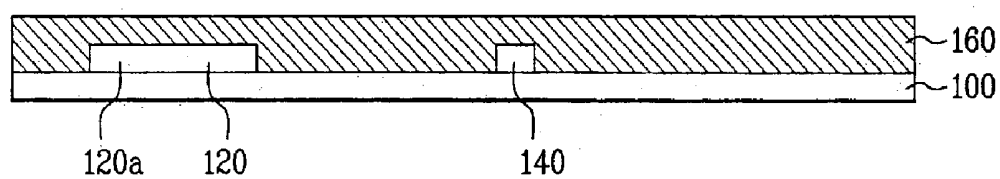


FIG. 4C

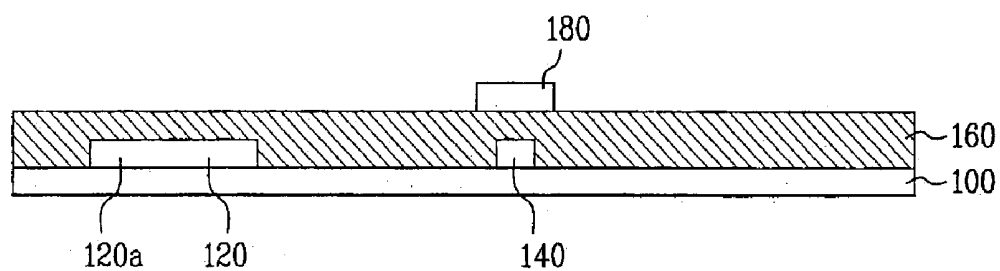


FIG. 4D

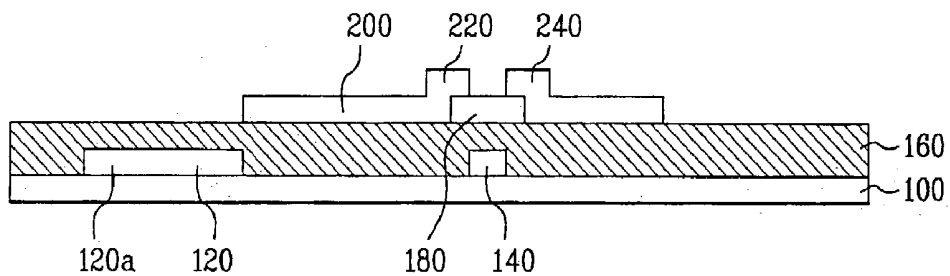


FIG. 4E

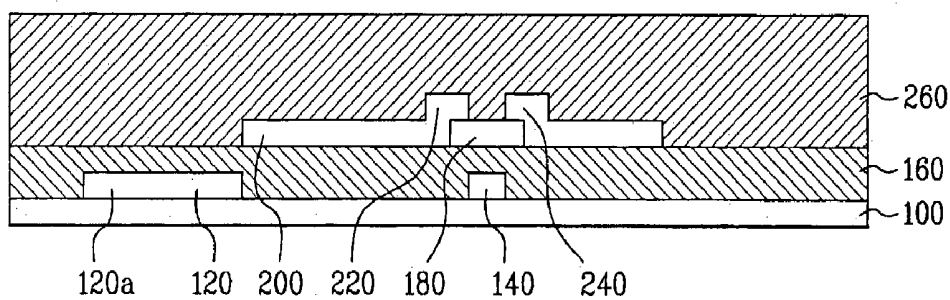


FIG. 4F

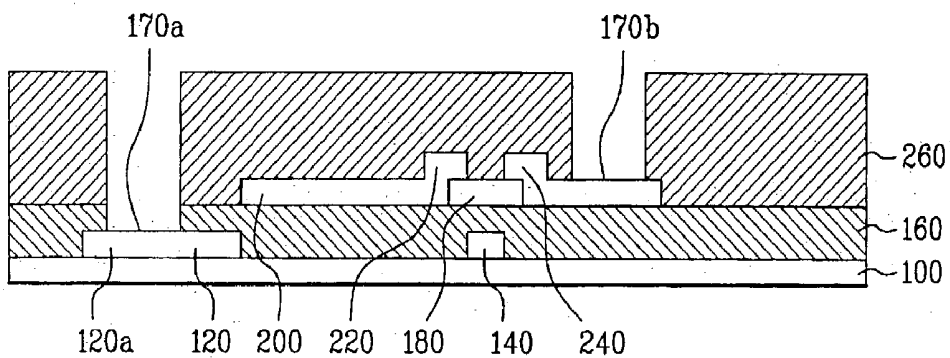


FIG. 4G

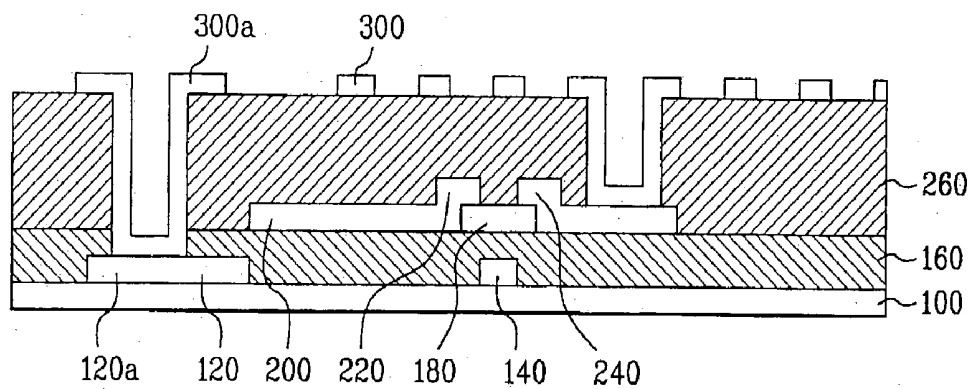


FIG. 4H

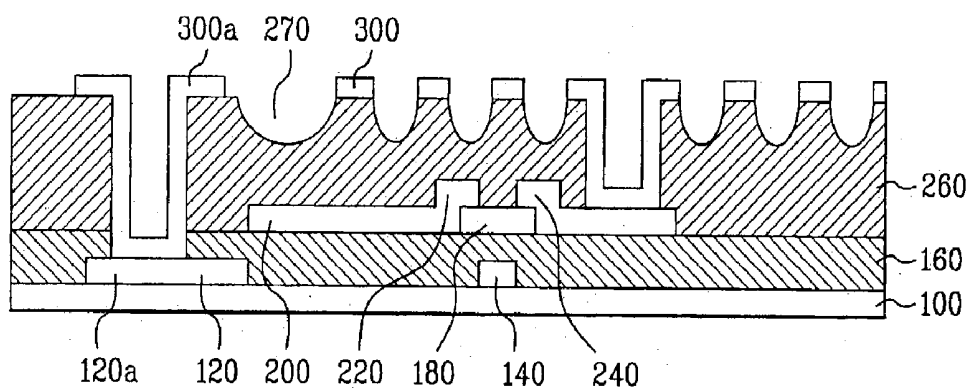
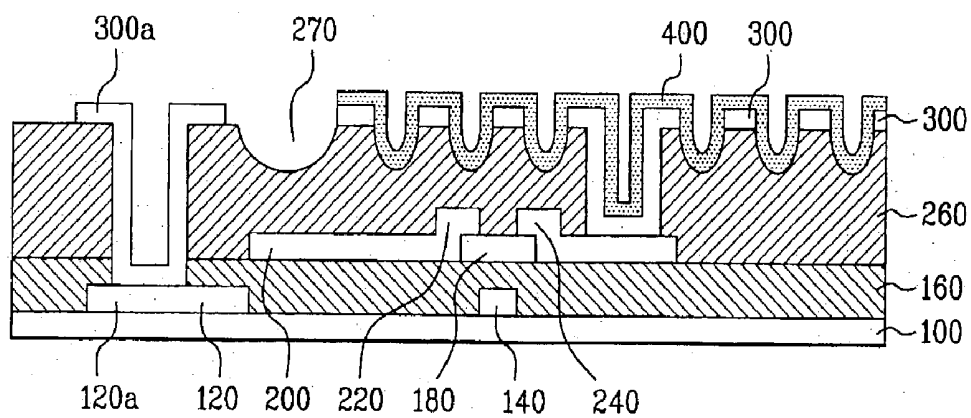


FIG. 4I



# REFLECTIVE LIQUID CRYSTAL DISPLAY DEVICE AND METHOD FOR MANUFACTURING THE SAME

[0001] This application claims the benefit of the Korean Application No. P.2001-85288 filed on Dec. 26, 2001, which is hereby incorporated by reference for all purposes as if fully set forth herein.

## BACKGROUND OF THE INVENTION

### [0002] 1. Field of the Invention

[0003] The present invention relates to a liquid crystal display device, and more particularly, to a reflective liquid crystal display device having high reflectivity and a method for manufacturing the same.

### [0004] 2. Discussion of the Related Art

[0005] A typical liquid crystal display (LCD) device is provided with lower and upper substrates facing each other at a predetermined distance, and a liquid crystal layer formed between the lower and upper substrates. The liquid crystal layer is driven by an electric field formed between the lower and upper substrates, so that the light transmittance is controlled, thereby displaying an image on an LCD panel. However, the LCD device does not emit light by itself, so that a backlight is additionally required to display the image on the LCD panel, or natural light is used as a light source to the LCD device.

[0006] In general, the LCD device is classified into two types, i.e., a transmissive liquid crystal display device using the backlight as the light source, and a reflective liquid crystal display device using the natural light as the light source. The transmissive liquid crystal display device has limitations in that it is hard to obtain ultra-thinness and lightness due to the weight and volume of the backlight. Also, power consumption increases since the backlight is driven. Accordingly, many efforts have been made to study the reflective liquid crystal display (LCD) device using natural light as the light source.

[0007] A related art reflective LCD device will be explained with reference to the accompanying drawings.

[0008] FIG. 1A is a plan view illustrating a unit pixel in a lower substrate of a related art reflective LCD device. FIG. 1B is a sectional view illustrating a unit pixel in a lower substrate of a related art reflective LCD device according to the present invention taken along line A-A of FIG. 1A. Although not shown, a plurality of pixels are formed in a matrix type at left and bottom sides in the context of FIG. 1A.

[0009] As shown in FIG. 1A and FIG. 1B, a plurality of gate lines 12 are horizontally formed on a lower substrate 10, and a gate pad 12a is formed at each one end of the gate lines 12. A plurality of gate electrodes 14 are projected from the gate line at a predetermined interval. A gate insulating layer 16 is formed on an entire surface of the lower substrate, and a semiconductor layer 18 is patterned on the gate insulating layer 16. Then, a plurality of data lines 20 are formed to cross the plurality of gate lines 12, thereby defining a plurality of pixel regions. At this time, a data pad 20a is formed at each one end of the data lines 20, and source/drain electrodes 22 and 24 are formed on the same layer as the data line 20. The source/drain electrodes 22 and

24 are patterned on the semiconductor layer 18, thereby forming a thin film transistor (TFT) with the gate electrode 14.

[0010] A passivation layer 26 is formed on the entire surface of the lower substrate, and reflective electrodes 30 and 30a are formed on the passivation layer 26. At this time, the reflective electrode 30 is connected to the drain electrode 24 of the thin film transistor through a contact hole formed in the passivation layer 26 (not labeled). Also, the reflective electrode 30a is connected to the gate pad 12a and the data pad 20a through a contact hole (not labeled) formed in the passivation layer 26 and the gate insulating layer 16. In general, the reflective electrodes 30 and 30a are made of Aluminum (Al) having high reflectivity and high transmittance.

[0011] In order to achieve the LCD device having high luminance, it is necessary to form the reflective electrode having high reflectivity. The reflective electrode being made of aluminum (Al) may deform during a heat treatment process due to a poor heat-resistance of aluminum (Al). Accordingly, the reflective electrode has to be made of a material having high heat-resistance and high reflectivity.

## SUMMARY OF THE INVENTION

[0012] Accordingly, the present invention is directed to a reflective liquid crystal display device and a method for manufacturing the same that substantially obviates one or more of the problems due to limitations and disadvantages of the prior art.

[0013] An advantage of the present invention is to provide a reflective liquid crystal display device and a method for manufacturing the same, in which a reflective electrode is made of silver (Ag) which has higher reflectivity and higher heat-resistance than aluminum, so that it is possible to prevent the reflective electrode from deforming, thereby forming the reflective liquid crystal display device having high luminance.

[0014] Additional features and advantages of the invention will be set forth in the description which follows, and in part will become apparent from the description, or may be learned by practice of the invention. The objectives and other advantages of the invention will be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

[0015] To achieve these objects and other advantages and in accordance with the purpose of the invention, as embodied and broadly described, a reflective liquid crystal display device includes a plurality of gate and data lines formed on a reflective substrate to cross with one another, thereby defining a plurality of pixel regions; gate and data pads respectively formed at each one end of the gate and data lines; a thin film transistor connected to the gate and data lines; a passivation layer having a plurality of holes within the pixel region, and being formed on an entire surface of the reflective substrate; a first transparent electrode formed on the passivation layer for being electrically connected to the gate and data pads; a second transparent electrode formed on the passivation layer between the holes of the pixel region for being electrically connected to the thin film transistor; and a silver (Ag) alloy layer formed on the second transparent electrode within the pixel region and the passivation layer having the holes.



[0016] The silver alloy has high reflectivity and high heat-resistance, however, the silver alloy has poor resistance to moisture. For this reason, problems may generate during manufacturing process steps if the reflective electrode is made of the silver alloy. Also, if the gate and data pads being connected to a Tape Carrier Package (TCP) are exposed to the outside, problems may generate during a TCP bonding process since moisture easily adheres to the surface of the silver alloy. Accordingly, if the reflective electrode is made of the silver alloy, it is required to provide a stable patterning process in the structure and a stable TCP bonding process in the electrodes.

[0017] In the present invention, the transparent electrode is firstly formed, and then the silver alloy layer is formed on the transparent electrode within the pixel region, thereby solving the aforementioned problems.

[0018] That is, the transparent electrode is formed at a portion for connecting a TCP to the gate and data pads, thereby stably performing TCP bonding. After forming the transparent electrode within the pixel region, the silver alloy layer is formed on the transparent electrode, thereby improving adhesion between the silver alloy layer and a lower layer. Accordingly, it is possible to obtain CD-Loss? and Edge-Profile? that are main elements for patterning.

[0019] An external light incident from one direction is reflected to a direction opposing to the one direction, thereby narrowing a viewing angle of a user. Accordingly, in the present invention, the passivation layer and the transparent electrode are patterned, and then the silver alloy layer having uneven surfaces is formed thereon, thereby dispersing the reflected light.

[0020] It is to be understood that both the foregoing general description and the following detailed description of the present invention are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

#### BRIEF DESCRIPTION OF THE DRAWINGS

[0021] The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate embodiments of the invention and together with the description serve to explain the principles of the invention.

[0022] In the drawings:

[0023] **FIG. 1A** is a plan view illustrating a lower substrate of a related art reflective LCD device;

[0024] **FIG. 1B** is a sectional view illustrating a lower substrate of a related art reflective LCD device taken along line A-A of **FIG. 1A**;

[0025] **FIG. 2** is a plan view illustrating a reflective substrate of a reflective LCD device according to the present invention;

[0026] **FIG. 3A** and **FIG. 3B** are sectional views illustrating a reflective substrate of a reflective LCD device according to the present invention taken along line B-B of **FIG. 2**; and

[0027] **FIG. 4A** to **FIG. 4I** are sectional views illustrating manufacturing process steps of a reflective LCD device according to the present invention.

#### DETAILED DESCRIPTION OF THE INVENTION

[0028] Reference will now be made in detail to an embodiment of the present invention, example of which is illustrated in the accompanying drawings. Wherever possible, the same reference numbers will be used throughout the drawings to refer to the same or like parts.

[0029] **FIG. 2** is a plan view illustrating a reflective substrate of a reflective LCD device according to the present invention. **FIG. 3A** and **FIG. 3B** are sectional views illustrating a reflective substrate of a reflective LCD device according to the present invention taken along line B-B of **FIG. 2**. Although a unit pixel is shown in accompanying drawings, a plurality of pixels are formed in a matrix type at right and bottom sides in the context of the drawings.

[0030] As shown in **FIG. 2** and **FIG. 3A**, a plurality of gate lines **120** are horizontally formed on a substrate **100**, and a gate pad **120a** is formed at each one end of the gate lines **120**. A plurality of gate electrodes **140** are projected from the gate line **120**. The gate line **120**, the gate pad **120a** and the gate electrode **140** may be made of aluminum (Al), Al alloy, molybdenum/aluminum (Mo/Al), or chromium/aluminum (Cr/Al). A gate insulating layer **160** is formed on an entire surface of the substrate **100**, and a semiconductor layer **180** is patterned on the gate insulating layer **160**. At this time, the gate insulating layer may be made of  $\text{SiN}_x$  or  $\text{SiO}_x$ , and the semiconductor layer may be made of amorphous silicon (a-Si) or polycrystalline silicon (p-Si). Although not shown, an ohmic contact layer of  $n^+$  a-Si may be formed on the semiconductor layer **180**.

[0031] Then, a plurality of data lines **200** are formed to cross the plurality of gate lines **120**, thereby defining a plurality of pixel regions. A data pad **200a** is formed at each one end of the data lines **200**. Also, source and drain electrodes **220** and **240** are formed on the same layer as the data line **200**. The source and drain electrodes **220** and **240** are patterned on the semiconductor layer **180**, thereby forming a thin film transistor with the gate electrode **140**. The source and drain electrodes **220** and **240** may be made of Al, Cr, titanium (Ti) or Al alloy.

[0032] A passivation layer **260** is formed on the entire surface of the substrate. The passivation layer **260** includes a plurality of holes **270** within the pixel region and a plurality of contact holes for exposing the drain electrode **240**, the gate pad **120a** and the data pad **200a**. The passivation layer **260** may be formed as an inorganic insulating layer being made of  $\text{SiN}_x$  or  $\text{SiO}_x$ , or an organic insulating layer being made of BenzoCycloButene (BCB) or photoacrylate according to a structure of the LCD device.

[0033] A first transparent electrode **300a** is formed on the passivation layer **260** in the exterior of the pixel region for being connected to the gate pad **120a** and the data pad **200a** through the contact hole. A second transparent electrode **300** is formed on the passivation layer **260** between the holes **270** of the pixel region for being connected to the drain electrode **240** through the contact hole. At this time, the first and second transparent electrodes **300a** and **300** are made of indium tin oxide (ITO).

[0034] A silver (Ag) alloy layer **400** is formed in the pixel region. That is, the silver (Ag) alloy layer **400** is formed on the passivation layer **260** having the plurality of holes **270**

and on the second transparent electrode **300** formed between the holes **270**, so that the silver alloy layer **400** has uneven surfaces. The reflective electrode is formed at the maximum size so as to improve reflectivity. For this reason, the reflective electrode is overlapped with the data line **200**, and to cover the thin film transistor with the reflective electrode.

[0035] If the passivation layer **260** is formed as the organic insulating layer, the organic insulating layer is contacted with the semiconductor layer **180** that is a channel layer of the thin film transistor, thereby increasing the deterioration characteristics of the thin film transistor. In this case, the passivation layer **260** is made of an organic insulating layer such as BenzoCycloButene (BCB) or photoacrylate between the reflective electrode **200** and the data line **200**/thin film transistor so as to insulate the reflective electrode from the data line **200** and the thin film transistor.

[0036] If the passivation layer **260** is formed as the organic insulating layer, the organic insulating layer contacts the semiconductor layer **180** that is a channel layer of the thin film transistor, thereby deteriorating characteristics of the thin film transistor. For this reason, the inorganic insulating layer **260a** being made of  $\text{SiN}_x$  or  $\text{SiO}_x$  is additionally formed between the organic insulating layer and the semiconductor layer, shown in FIG. 3B. That is, the passivation is preferably formed as a double layer of the inorganic insulating layer **260a** and the organic insulating layer **260**.

[0037] Also, an alignment layer may be formed on the silver alloy layer **400**. The alignment film may be formed using any of polyamide, polyimide compound, polyvinylalcohol (PVA) and polyamic acid with rubbing. The alignment film may be formed from any material having photo-reaction such as polyvinylcinnamate (PVCN), polysiloxanecinnamate (PSCN) or cellulosecinnamate (CelCN) with rubbing.

[0038] Although not shown, a black matrix is formed on a substrate opposing to the reflective substrate at a predetermined distance for preventing a light from leaking in regions except the pixel region. A color filter layer is formed on the black matrix, and a common electrode is formed on the color filter layer. An overcoat layer may be additionally formed between the color filter layer and the common electrode so as to protect the color filter layer and to flatten the substrate. An alignment layer may be formed on the common electrode. Then, a liquid crystal layer is formed between the substrates.

[0039] FIG. 4A to FIG. 4I are sectional views illustrating manufacturing process steps of a reflective LCD device according to the present invention, which illustrates sectional views taken along line B-B of FIG. 2.

[0040] As shown in FIG. 4A, Al, Al alloy, Mo/Al or Cr/Al is deposited on the substrate **100** by sputtering, and is patterned by photolithography, thereby forming the gate line **120**, the gate pad **120a** and the gate electrode **140**. Then,  $\text{SiN}_x$  or  $\text{SiO}_x$  is deposited on the entire surface of the substrate by plasma chemical vapor deposition (CVD) method, thereby forming the gate insulating layer **160**, as shown in FIG. 4B.

[0041] Referring to FIG. 4C, the amorphous silicon or the polycrystalline silicon is deposited on the substrate by a CVD method, and then is patterned, thereby forming the

semiconductor layer **180**. The ohmic contact layer may be additionally formed on the semiconductor layer **180**.

[0042] As shown in FIG. 4D, a metal such as Al, Cr, Ti or Al alloy is deposited on the substrate by sputtering, and then is patterned by photolithography, thereby forming the data line **200**, the data pad, the source electrode **220** and the drain electrode **240**.

[0043] As shown in FIG. 4E, the passivation layer **260** may be formed as the inorganic insulating layer being made of  $\text{SiN}_x$  or  $\text{SiO}_x$  on the entire surface of the substrate, or may be formed as the organic insulating layer being made of BenzoCycloButene (BCB) or photoacrylate. Also, it is possible to form the passivation layer as the double layer of the organic insulating layer and the inorganic insulating layer. That is, the inorganic insulating is formed, and then the organic insulating layer is formed on the inorganic insulating layer.

[0044] As shown in FIG. 4F, the passivation layer **260** and the gate insulating layer **160** are partially etched, thereby forming the first contact hole **170a** for exposing the gate pad **120a** and the data pad. The second contact hole **170b** exposing the drain electrode **240** is formed by etching the passivation layer **260**.

[0045] Referring to FIG. 4G, a transparent metal such as ITO is deposited by sputtering, thereby forming the first and second transparent electrodes **300a** and **300** for being connected to the pad and the drain electrodes through the first and second contact holes **300a** and **300**, respectively. The second transparent electrode **300** is patterned within the pixel region in a predetermined shape. The second transparent electrode **300** is formed in an island shape, for example.

[0046] Referring to FIG. 4H, the passivation layer is partially etched between the patterned second transparent electrodes **300**, thereby forming the hole **270**.

[0047] As shown in FIG. 4I, the silver (Ag) alloy layer **400** is formed on the patterned second transparent electrode **300** and the hole **270**, so that the reflective electrode is formed, thereby forming the reflective substrate for the reflective LCD device.

[0048] In the reflective LCD device according to the present invention, the reflective electrode is made of silver (Ag) alloy having high reflectivity, thereby obtaining high luminance in the reflective LCD device. Also, it is possible to form the reflective electrode having uneven surfaces in simple process steps.

[0049] Furthermore, the transparent electrode is formed at a portion for connecting a Tape Carrier Package (TCP) to the gate and data pads, thereby performing TCP bonding process stably. After forming the transparent electrode in the pixel region, the silver alloy layer is formed on the transparent electrode of the pixel region, thereby stably adhering the silver alloy layer to a lower layer.

[0050] It will be apparent to those skilled in the art than various modifications and variation can be made in the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention cover the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

What is claimed is:

1. A reflective liquid crystal display device comprising:
  - a plurality of gate and data lines formed on a reflective substrate to cross with one another, thereby defining a plurality of pixel regions;
  - gate and data pads respectively formed at each one end of the gate and data lines;
  - a thin film transistor connected to the gate and data lines;
  - a passivation layer having a plurality of holes within the pixel region, being formed on an entire surface of the reflective substrate;
  - a first transparent electrode formed on the passivation layer for being electrically connected to the gate and data pads;
  - a second transparent electrode formed on the passivation layer between the holes of the pixel region for being electrically connected to the thin film transistor; and
  - a silver (Ag) alloy layer formed on the second transparent electrode within the pixel region and the passivation layer having the holes.
2. The reflective liquid crystal display device as claimed in claim 1, wherein the passivation layer is formed as an organic insulating layer.
3. The reflective liquid crystal display device as claimed in claim 2, wherein the organic insulating layer is made of one of BenzoCycloButene (BCB) and photoacrylate.
4. The reflective liquid crystal display device as claimed in claim 1, wherein the passivation is formed as a double layer of an inorganic insulating layer and an organic insulating layer.
5. The reflective liquid crystal display device as claimed in claim 4, wherein the inorganic insulating layer is made of one of  $\text{SiN}_x$  and  $\text{SiO}_x$ , and the organic insulating layer is made of one of BenzoCycloButene (BCB) and photoacrylate.
6. The reflective liquid crystal display device as claimed in claim 1, wherein the silver alloy layer is partially overlapped with the data line, and covers the thin film transistor.
7. The reflective liquid crystal display device as claimed in claim 1, wherein the transparent electrode is made of indium tin oxide (ITO).
8. The reflective liquid crystal display device as claimed in claim 1, wherein an alignment layer is additionally formed on the silver alloy layer.
9. The reflective liquid crystal display device as claimed in claim 1, further comprising:
  - an opposite substrate facing to the reflective substrate;
  - a black matrix, a color filter layer and a common electrode for being sequentially formed on the opposite substrate; and
  - a liquid crystal layer formed between the reflective and opposite substrates.
10. A method for manufacturing a reflective LCD device comprising:
  - forming a gate line, a gate pad and a gate electrode on a reflective substrate;

- forming a gate insulating layer on an entire surface of the reflective substrate;
  - forming a semiconductor layer on the gate insulating layer;
  - forming a data line for crossing the gate line so as to define a pixel region, a data pad, and source/drain electrodes on the semiconductor layer;
  - forming a passivation layer on an entire surface of the reflective substrate;
  - forming a first contact hole by etching the gate insulating layer and the passivation layer for exposing the gate and data pads, and a second contact hole by etching the passivation layer for exposing the drain electrode;
  - forming a first transparent electrode being connected to the gate and data pads through the first contact hole, and a second transparent electrode being connected to the drain electrode through the second contact hole, the second transparent electrode being patterned in a pre-determined shape within the pixel region;
  - forming a hole by etching the passivation layer between patterned second transparent electrodes; and
  - forming a silver (Ag) alloy layer on the patterned second transparent electrode and the passivation layer of the hole shape between the patterned second transparent electrodes.
11. The method as claimed in claim 10, wherein the passivation layer is formed as an organic insulating layer.
  12. The method as claimed in claim 11, wherein the organic insulating layer is made of one of BenzoCycloButene (BCB) and photoacrylate.
  13. The method as claimed in claim 10, wherein the passivation layer is formed in a double layer as forming an organic insulating layer on an inorganic insulating layer.
  14. The method as claimed in claim 13, wherein the inorganic insulating layer is made of one of  $\text{SiN}_x$  and  $\text{SiO}_x$ , and the organic insulating layer is made of one of BenzoCycloButene (BCB) and photoacrylate.
  15. The method as claimed in claim 10, wherein an alignment layer is additionally formed after forming a silver alloy layer.
  16. The method as claimed in claim 15, wherein the alignment layer is rubbed.
  17. The method as claimed in claim 15, wherein the alignment layer is rubbed with any material having photo reaction.
  18. The method as claimed in claim 10, wherein the gate line, gate pad, and gate electrode are formed by sputtering a metal made of one of Al, Al alloy, Mo/Al, and Cr/Al and then patterning the metal.
  19. The method as claimed in claim 10, wherein the data line, data pads, and source/drain electrodes are formed by sputtering a metal made of one of Al, Cr, Ti, and Al alloy and then patterning the metal.
  20. The method as claimed in claim 15, wherein the second transparent electrode is formed in an island shape.

\* \* \* \* \*

|                |                                                 |         |            |
|----------------|-------------------------------------------------|---------|------------|
| 专利名称(译)        | 反射型液晶显示装置及其制造方法                                 |         |            |
| 公开(公告)号        | <a href="#">US20030117552A1</a>                 | 公开(公告)日 | 2003-06-26 |
| 申请号            | US10/316045                                     | 申请日     | 2002-12-11 |
| [标]申请(专利权)人(译) | CHAE GEE SUNG                                   |         |            |
| 申请(专利权)人(译)    | CHAE GEE SUNG                                   |         |            |
| 当前申请(专利权)人(译)  | LG DISPLAY CO. , LTD.                           |         |            |
| [标]发明人         | CHAE GEE SUNG                                   |         |            |
| 发明人            | CHAE, GEE SUNG                                  |         |            |
| IPC分类号         | G02F1/1335 G02F1/1343 G02F1/1362                |         |            |
| CPC分类号         | G02F1/133553 G02F1/136227 G02F1/13439           |         |            |
| 优先权            | 1020010085288 2001-12-26 KR                     |         |            |
| 其他公开文献         | US6947108                                       |         |            |
| 外部链接           | <a href="#">Espacenet</a> <a href="#">USPTO</a> |         |            |

#### 摘要(译)

一种反射型液晶显示装置及其制造方法，其具有高亮度。反射型液晶显示装置包括形成在反射基板上的多条栅极线和数据线，以彼此交叉，从而限定多个像素区域；栅极和数据焊盘分别形成在栅极和数据线的每一端；薄膜晶体管连接到栅极和数据线；在所述像素区域内具有多个孔的钝化层，形成在所述反射基板的整个表面上；形成在钝化层上的第一透明电极；第二透明电极，形成在像素区域的孔之间的钝化层上；形成在像素区域内的第二透明电极上的银（Ag）合金层和具有这些孔的钝化层。

