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Wei

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(54) **LIQUID CRYSTAL DISPLAY**

(56) **References Cited**

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(73) Assignee: **Chunghwa Picture Tubes, Ltd.**,
Taoyuan (TW)

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 1049 days.

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(74) Attorney, Agent, or Firm — Jianq Chyun IP Office

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G09G 3/36 (2006.01)

(52) **U.S. Cl.** **345/87; 345/95; 345/100; 345/205**

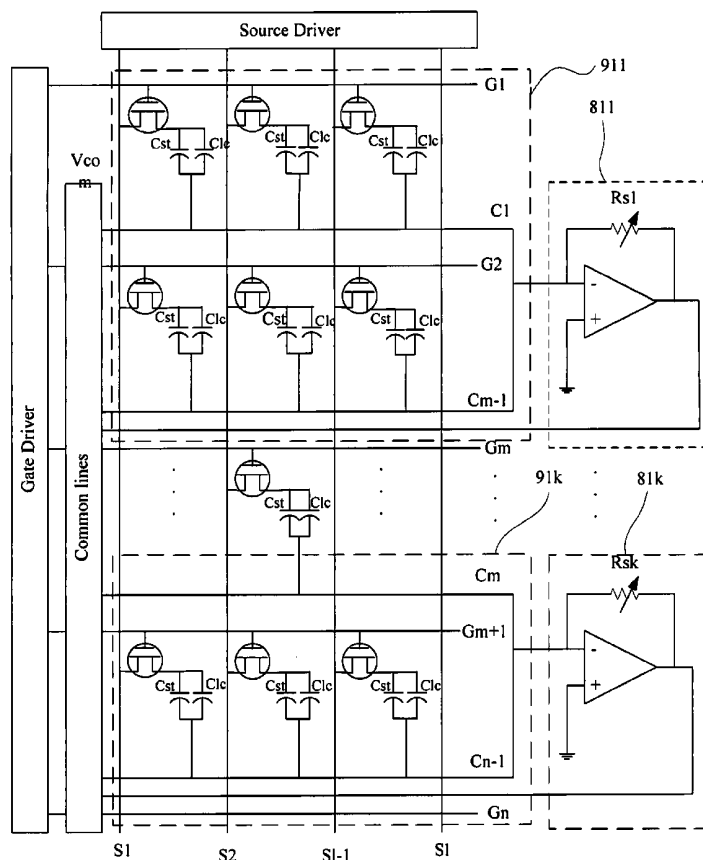
(58) **Field of Classification Search** **345/87–100, 345/204–205**

See application file for complete search history.

(57) **ABSTRACT**

A liquid crystal display includes a first substrate. A plurality of scan lines and a plurality of data lines are disposed on the first substrate to define a plurality of pixel regions. A plurality of common line groups is disposed on the first substrate, and each of the common line groups includes a plurality of common lines parallel to the scan lines. And, at least, a voltage regulator is electrically connected to one of the common line groups, wherein the voltage regulator can stabilize the pixel voltage to decrease the flicker of the liquid crystal display.

10 Claims, 7 Drawing Sheets



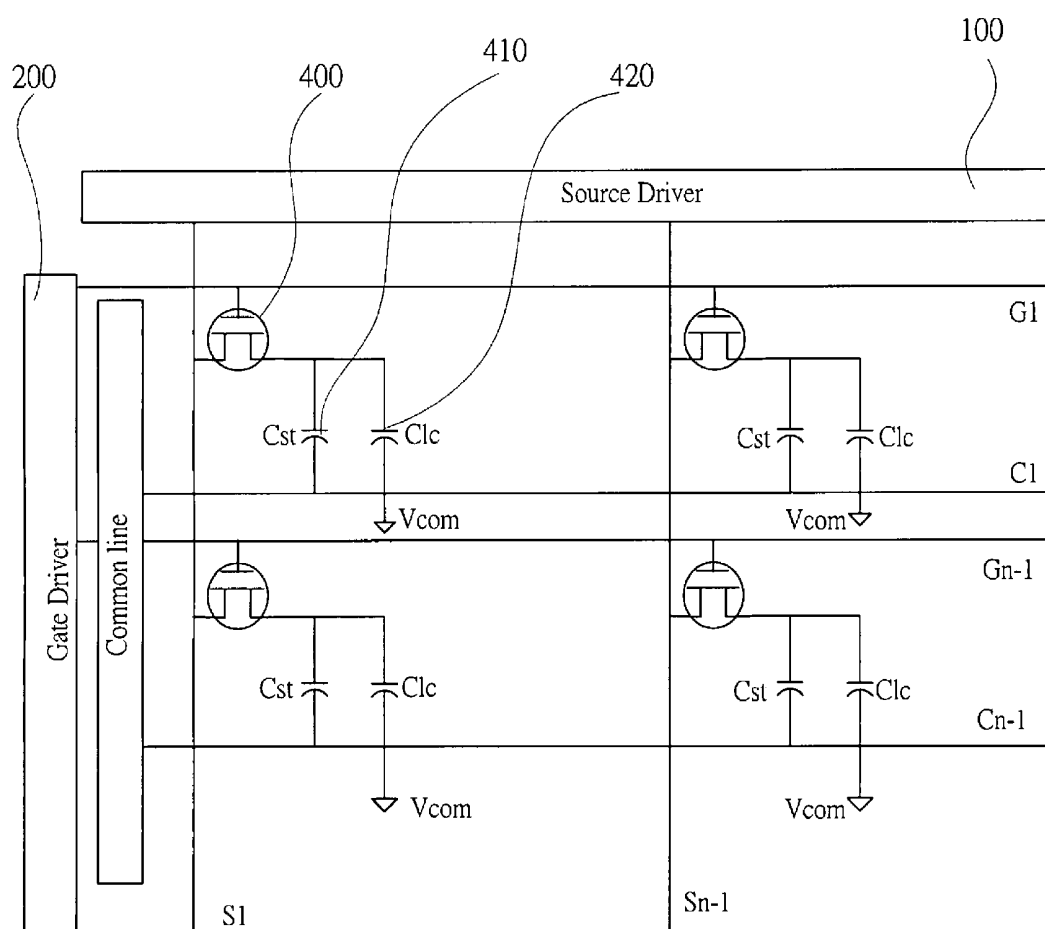


Fig.1(Prior Art)

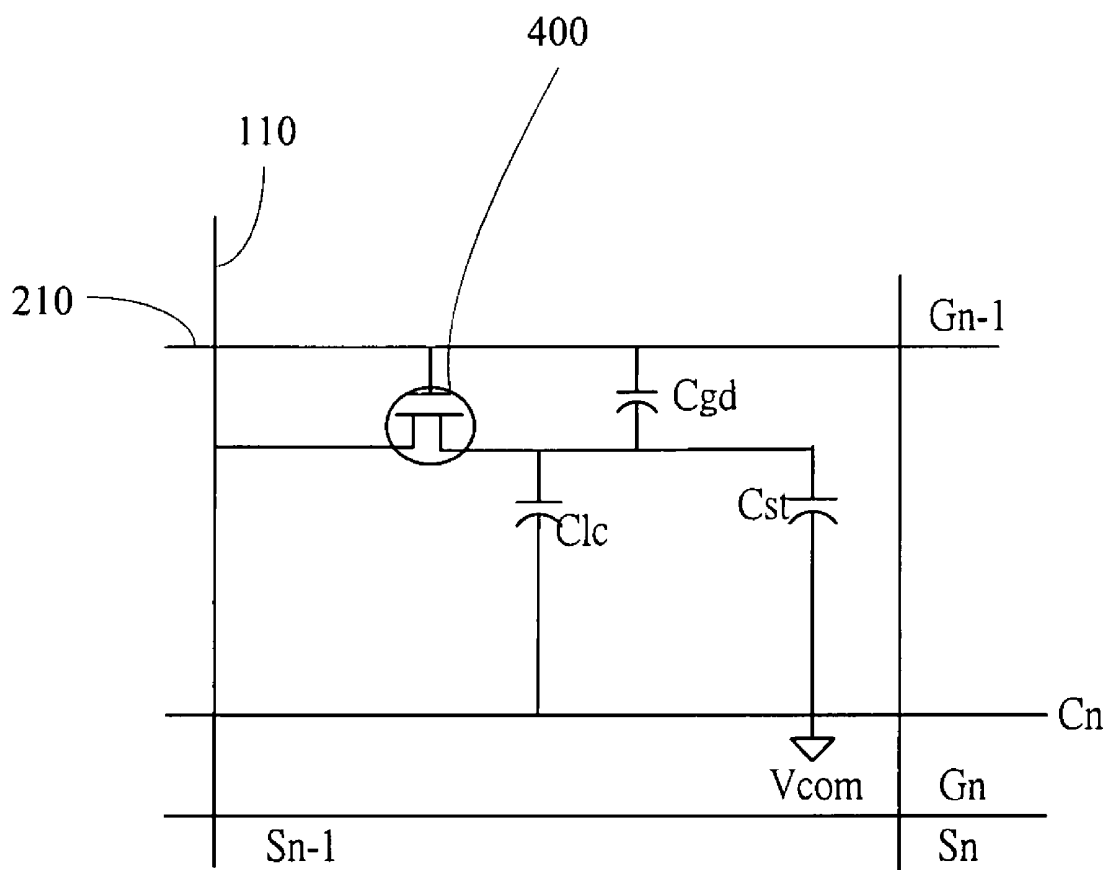


Fig.2(Prior Art)

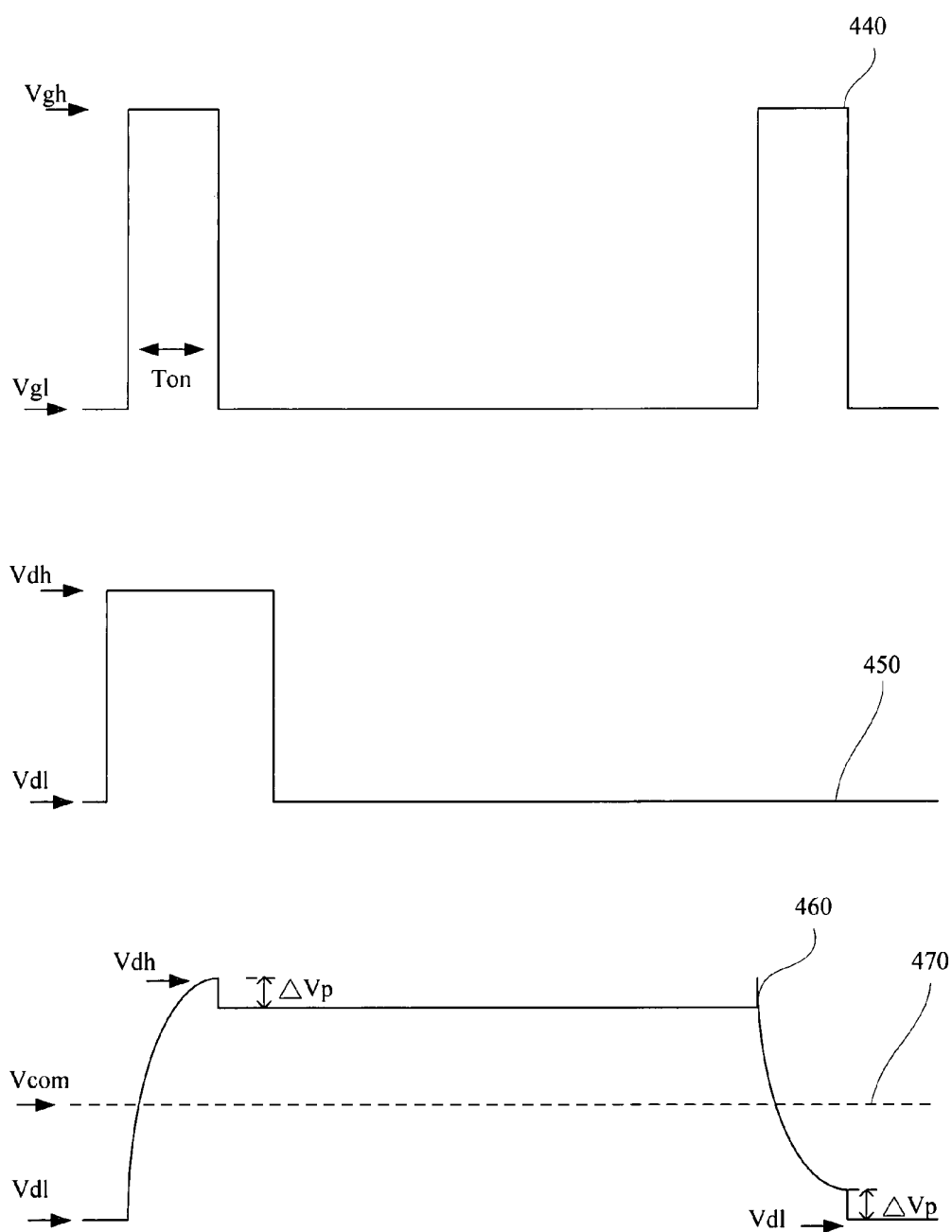


Fig.3(Prior Art)

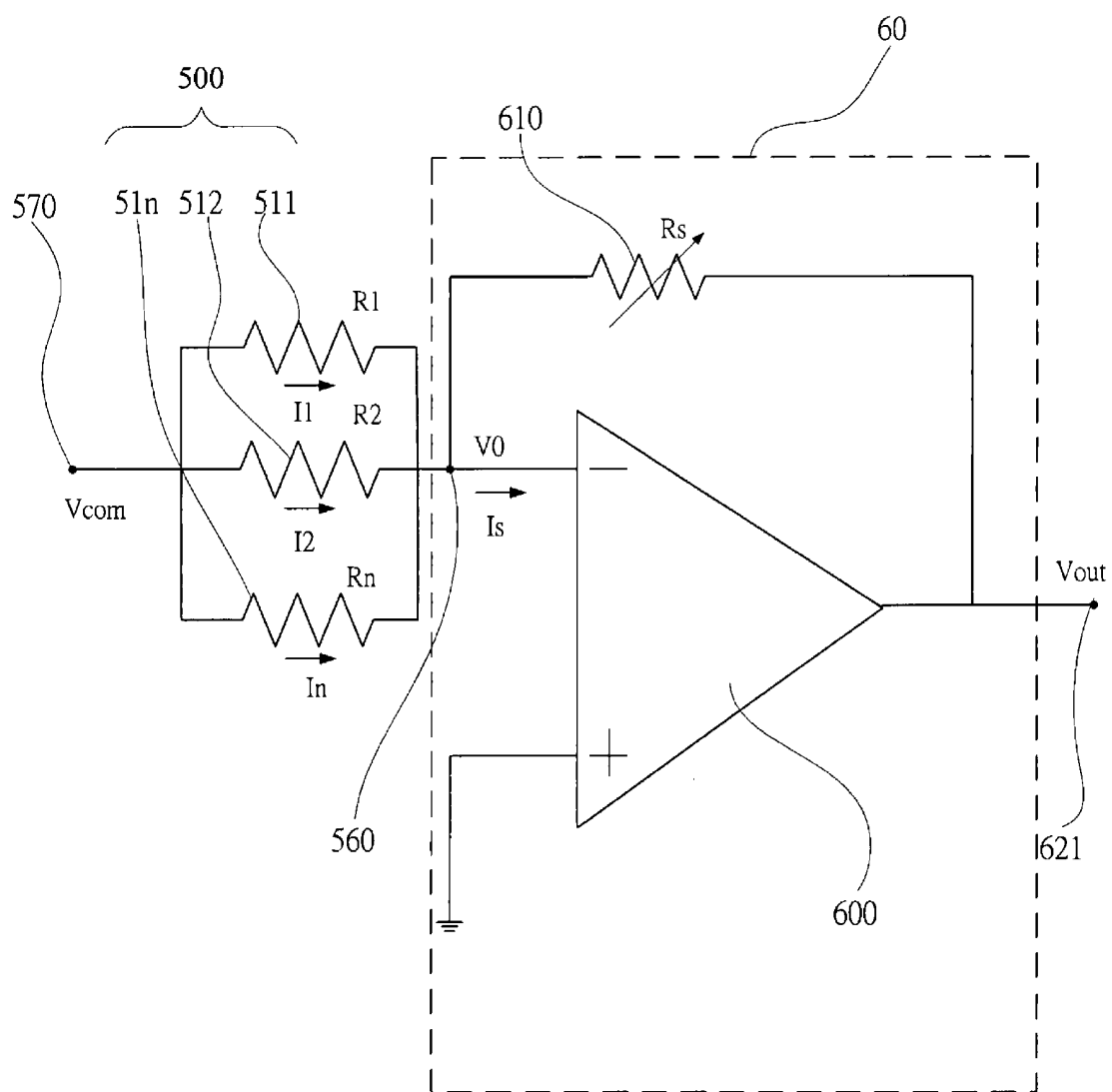


Fig.4

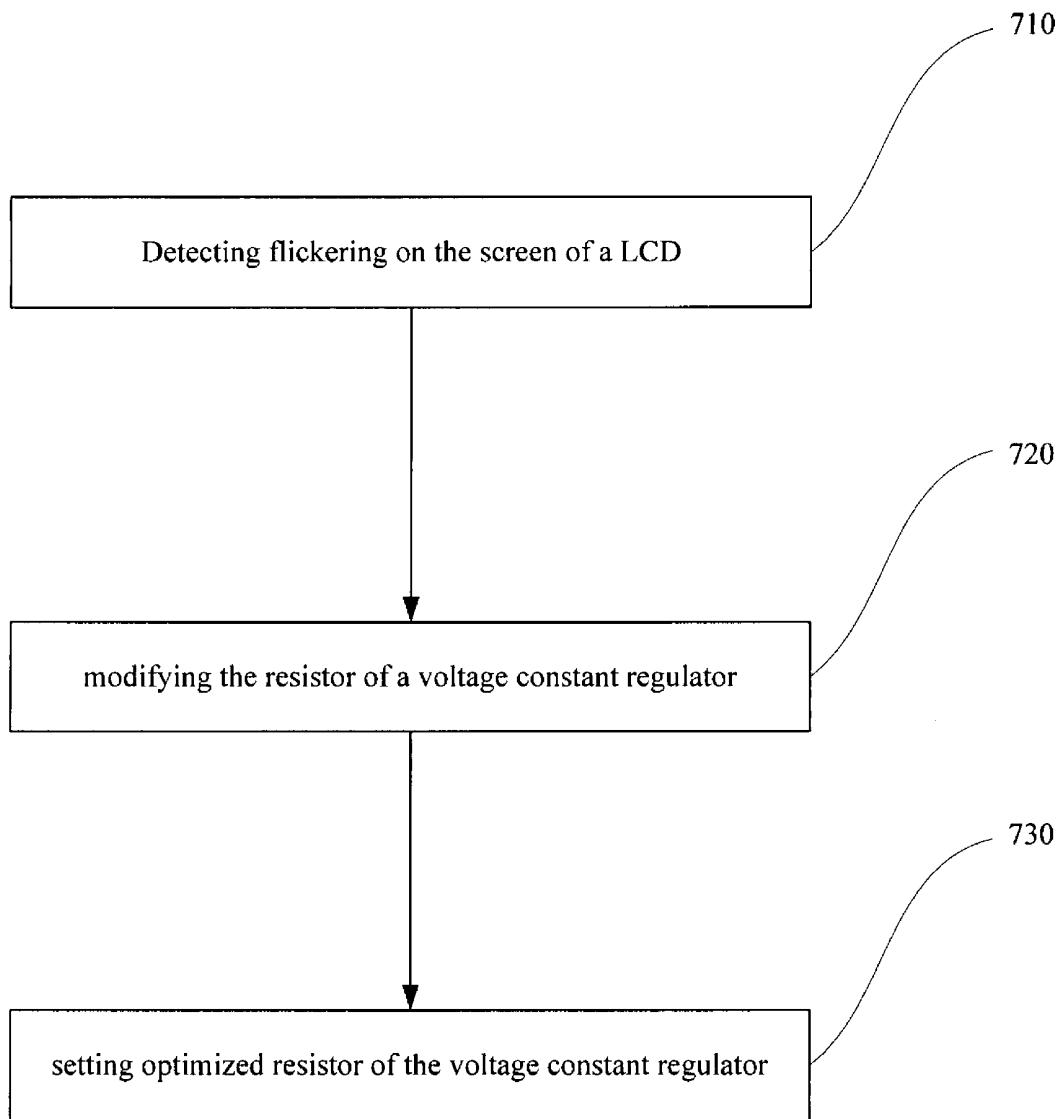


Fig.5

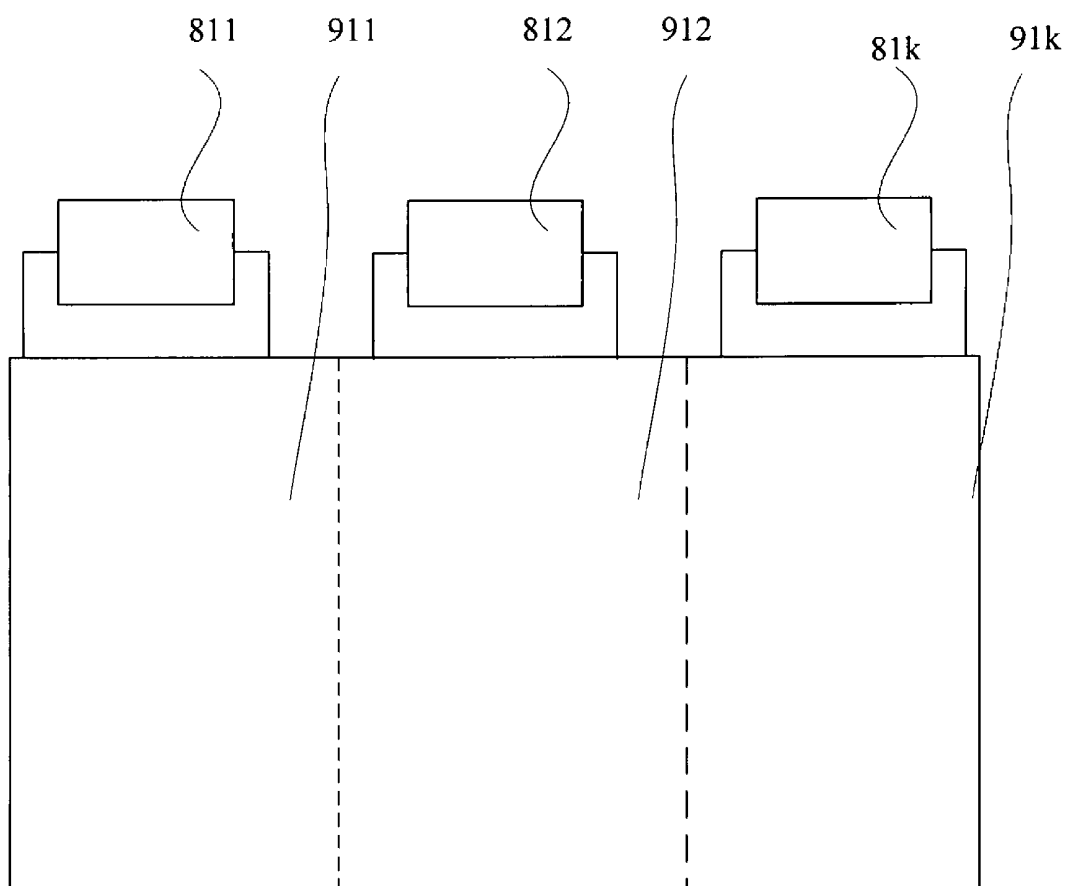


Fig.6

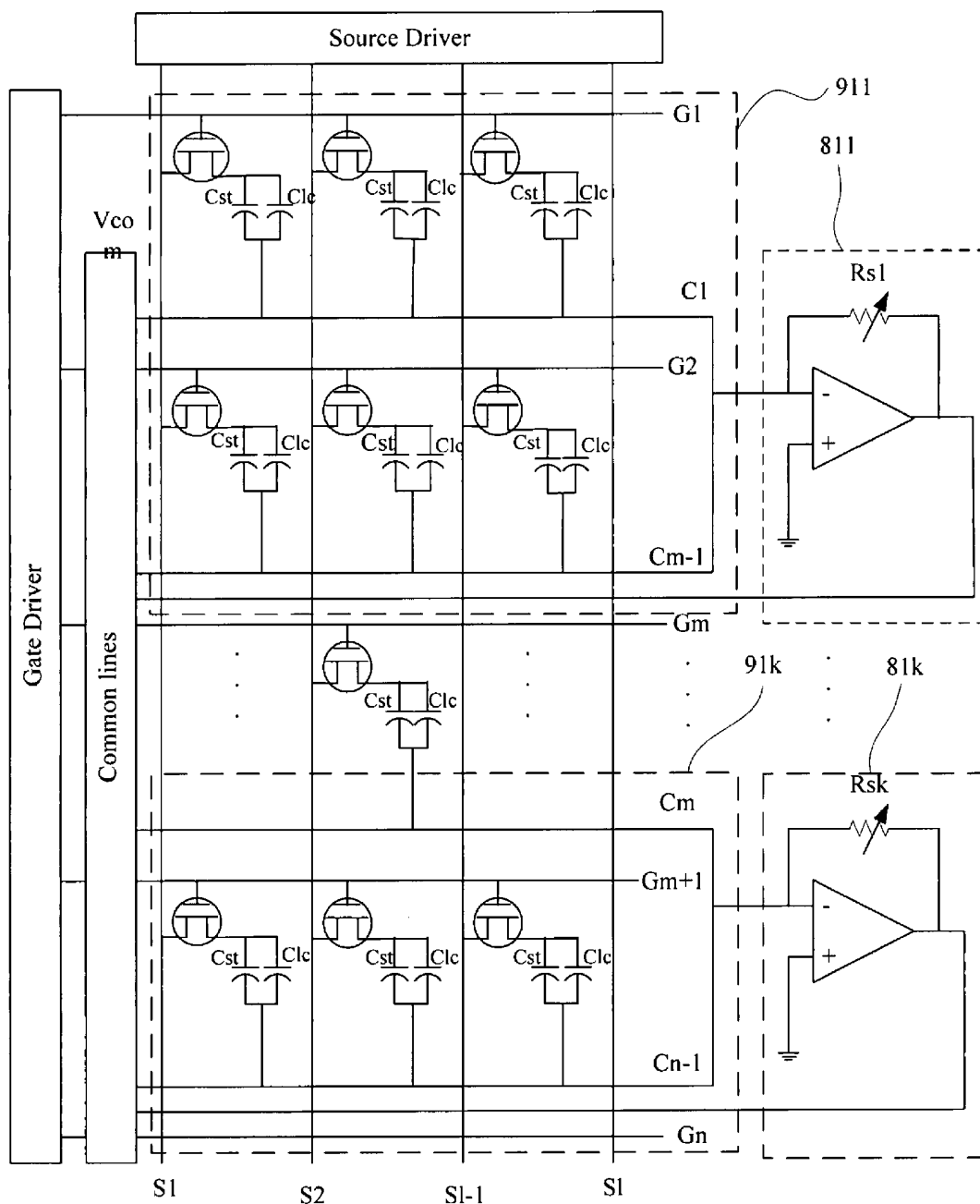


Fig.7

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LIQUID CRYSTAL DISPLAY

FIELD OF THE INVENTION

This invention relates to a liquid crystal display (LCD), especially, which includes at least a voltage regulator to diminish the flickering.

BACKGROUND OF THE RELATED ART

A liquid crystal display (denoted LCD) drives a plurality of active devices, such as thin film transistors, by a plurality of scan lines and writes data into the pixel electrodes by the data lines. The different lengths of the wires dissipate and delay the voltage waveforms so that a wrong data is written into the pixel electrodes. Additionally, a parasitical capacitor caused by the material or manufacturing also distorts the voltage waveforms by a feedthrough voltage generated by the parasitical capacitor. It is impossible to fix and unify the feedthrough voltage during manufacturing, so how to conquer the feedthrough voltage to diminish the flicker of a LCD is important.

FIG. 1 is a diagram showing the equivalent circuit of part of a known TFT-LCD. Gate driver 200 drives the scan lines $G1 \dots Gn-1$ sequentially to switch on the thin film transistors (denoted TFTs), and the source driver 100 writes a data (voltage) to the data lines $S1 \dots Sn-1$. For example, a scan line $G1$ and a data line $S1$ electrically connect to a TFT 400 of a pixel, in which the gate electrode, the source electrode and the drain electrode of the TFT 400 connect to the scan line $G1$, data line $S1$ and pixel electrode respectively. The pixel includes a storage capacitor (Cst) 410 and a liquid crystal capacitor (C_{lc}) 420, and the storage capacitor (Cst) 410 maintains the voltage on the pixel electrode until that the scan driver drives the scan line again, and the liquid crystal capacitor (C_{lc}) 420 provides a voltage across the liquid crystal in the pixel. The pixel electrode couples a common electrode to form the liquid crystal capacitor (C_{lc}) 420 to provide the voltage across the liquid crystal, which is pixel voltage.

FIG. 2 is a diagram showing the equivalent circuit of a pixel. The gate electrode connects to a scan line 210, the source electrode connects to a data line 110, and the drain electrode connects to a pixel electrode to write the data into the pixel electrode. As shown in the diagram, there exists a parasitical capacitor between the gate electrode and the drain electrode of the TFT 400.

FIG. 3 is a diagram of voltage waveforms illustrating the voltage variation of a pixel electrode. When the scan line voltage 440 raises from V_{gl} to V_{gh} to switch on a TFT 400, the data line voltage 450 charges the pixel electrode during a duty time T_{on} , so the pixel voltage 460 rises up from V_{dl} to V_{dh} . After the duty time T_{on} , the scan line voltage 440 goes down to V_{gl} to switch off the TFT, and the data line voltage 450 falls from V_{dh} to V_{dl} . Since the storage capacitor holds the pixel voltage 460, so the pixel voltage 460 does not fall to V_{dl} . Theoretically, the pixel voltage 460 should hold at V_{dh} , but a parasitical capacitor C_{gd} pulls down the pixel voltage 460 for a feedthrough voltage ΔV_p . The voltage difference between the pixel voltage and the common voltage 470 on the common lines shifts for a feedthrough voltage ΔV_p to flicker the screen of a TFT-LCD.

For diminishing or vanishing the flicker, US. App. No. 2005/0018121 discloses a teaching of zigzagging the wires between data lines and source driver or between scan lines and gate driver to a similar length to cancel out the wire delay, but does not eliminate the parasitical capacitor.

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Next, U.S. Pat. No. 6,933,917 discloses a teaching of connecting the scan lines to control circuits to provide impedance. Each control circuit connects a scan line to a transistor, where the gate electrode of the transistor connects a variable resistor and then to a power supply, one electrode to a common line. The impedance generated by the control circuit is much larger than that generated by the TFT of a pixel, so that, in relatively, the impedance generated by the TFT of a pixel can be neglected almost. It means the feedthrough voltage ΔV_p decreases relatively to diminish the flicker.

The feedthrough voltage ΔV_p varies from pixel to pixel, so the same impedance cannot diminish all flickers on the screen of a TFT-LCD. It is still an important topic to develop a new skill to solve this problem.

SUMMARY OF THE INVENTION

An object of this invention provides a LCD, which connects at least a voltage regulator, and the voltage regulator adjusts the common voltage to cancel out the feedthrough voltage to vanish the flicker.

Another object of this invention provides a method of adjusting a voltage regulator to vanish or diminish the flicker.

According to an embodiment of this invention, a liquid crystal display includes a first substrate; a plurality of scan lines and a plurality of data lines disposed on the first substrate to define a plurality of pixel regions thereon; a plurality of common line groups disposed on the first substrate and essentially parallel to the scan lines, wherein each of the common line groups includes a plurality of common lines; at least a voltage regulator electrically connected to one of the common line groups; and a second substrate opposed to the first substrate.

A voltage regulator, according to an embodiment of this invention, includes an inverting adder and a resistor. The inverting adder includes a positive input connected to the ground wire, a negative input and an output, where the resistor connects the negative input and the output, and the negative input connects to the common lines. The output provides a compensative voltage varying with the resistor, so that the resistor adjusts the compensative voltage. When the output connects to the compensative point, the compensative voltage will adjust the voltage on the compensative point, in this example, the compensative point is electrically connected to one of common lines in one of common line groups. When the voltage regulator provides an optimized compensative voltage, the flicker on the screen of the LCD will be diminished to the minimum or vanished.

According to an embodiment of this invention, a method of adjusting voltage regulators connected to a TFT-LCD diminishes or diminishes the flicker on the screen of a LCD. The first step is to detect the screen for a flicker. If the flicker occurs on the screen, then the resistance of the resistor of the voltage regulator is reset to a new value; if not, the resistance of the resistor is retained. When the flicker is minimal, the resistance of the resistor is the optimal resistance. Like that, when all areas of the screen of the LCD are optimized, the LCD is optimized.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a diagram showing the equivalent circuit of a conventional TFT-LCD.

FIG. 2 is a diagram showing the equivalent circuit of a single pixel of a conventional TFT-LCD.

FIG. 3 is a diagram showing the voltage waveform of a single pixel of a conventional TFT-LCD.

FIG. 4 is a diagram showing the equivalent circuit of a TFT-LCD according to an embodiment of this invention, where the circuit of the TFT-LCD connects to a voltage regulator.

FIG. 5 is a flow chart illustrating a method of adjusting the voltage regulator to diminish the flicker on the screen of a TFT-LCD according to an embodiment of this invention.

FIG. 6 is a diagram showing the screen of a TFT-LCD according to an embodiment of this invention, where the screen includes many areas.

FIG. 7 is a diagram showing the equivalent circuits of a TFT-LCD according to an embodiment of this invention, where each equivalent circuit corresponds to an area on the screen.

DETAILED DESCRIPTION OF THE INVENTION

A liquid crystal display (LCD) includes a first substrate. The first substrate includes a plurality of scan lines and data lines to define a plurality of pixel regions thereon, and a common line is disposed to correspond to a pixel region. A plurality of common line groups are disposed on the first substrate and essentially parallel to the scan lines, wherein each of the common line groups includes a plurality of common lines. Each of the pixel regions includes an active device, such as a TFT, and a pixel electrode, where the pixel electrode opposes to a corresponding common line. The gate electrode, the source electrode and the drain electrode of the TFT connect a scan line, a data line and a pixel electrode respectively. When the gate voltage on the scan line switches on the TFT, the source voltage (data) on the data line is written into the pixel electrode. A voltage on each of common lines in one of common line group provides the common voltage V_{com} , and the unstable common voltage V_{com} flickers the screen of the LCD. The common lines connect to a voltage regulator, and the voltage regulator provides a stable common voltage V_{com} to diminish the flicker.

The following employs the drawings and embodiments to illustrate this invention.

FIG. 4 is a diagram showing the equivalent circuit of a TFT-LCD according to an embodiment of this invention, where the TFT-LCD connects to a voltage regulator. An input end 570 provides a common voltage V_{com} to a common line group 500, which includes many common lines 511, 512 . . . 51n. $R_1, R_2 . . . R_n$ represent the intrinsic impedance of the common lines 511, 512 . . . 51n, and $I_1, I_2 . . . I_n$ represent the current through $R_1, R_2 . . . R_n$ and converge at a monitor point 560. The monitor point 560 connects to the input of a voltage regulator 60, and the output of the voltage regulator 60 connects to a compensative point; in general, the compensative point is a common line or the monitor point 560.

The voltage regulator 60 includes an inverting adder 600 and a variable resistor R_s 610, wherein the inverting adder 600 includes a positive input connected to a ground wire, a negative input and an output 621, and the variable resistor R_s 610 connects across the negative input and the output 621. The negative input and the output 621 are the input and the output 621 of the voltage regulator 60, where the output 621 provides a compensative voltage.

The common lines 511, 512 . . . 51n have various intrinsic impedances caused by the manufacture or various lengths of the common lines 511, 512 . . . 51n, as shown as $R_1, R_2 . . . R_n$ in FIG. 5. The currents $I_1, I_2 . . . I_n$ pass the $R_1, R_2 . . . R_n$, and converge at a monitor point 560. The total current is $I_s = I_1 + I_2 + . . . + I_n$ and the voltage is V_0 at the monitor point 560.

The compensative voltage on the output 621 of the voltage regulator 60 is V_{out} , and the current passing the variable

resistor 610 is I_s . The relation between I_s and V_{out} is represented by $I_s = (V_0 - V_{out})/R_s$ or $V_{out} = V_0 - I_s R_s$.

The output 621 of the inverting adder 600 connects a compensative point, maybe a common line or the monitor point 560. Like that, the compensative voltage V_{out} adjusts the voltage on the compensative point. Once the flicker occurs on the screen of the TFT-LCD, the resistance of the variable resistor R_s 610 of the voltage regulator 60 is reset to diminish the flicker down to the minimum to improve the quality of the screen, and the adjusting method will be explained later.

For example, in an ideal condition, the flickering phenomenon does not occur. The common voltage $V_{com} = 5V$ (volts), the voltage V_0 on the monitor point 560 = 0V and the net resistance of the impedance $R_1, R_2 . . . R_n$ of the common lines 511, 512 . . . 51n = 1Ω (ohms) are assumed. It can be deduced that $I_s = 5A$ (amperes) and $V_{out} = I_s R_s - V_0 = 5V$.

When the distortion of the common voltage V_{com} results in flickering, such as V_{com} becomes 3V, and the net resistance of the impedance $R_1, R_2 . . . R_n$ of the common lines 511, 512 . . . 51n = 1Ω (ohms) retains at 1Ω due to the characteristic of semiconductor, the current reduces to 3A. The resistance of the variable resistor R_s is reset to 5/3Ω (ohms) to hold the compensative voltage V_{out} at 5V, and then V_{out} compensates the voltage on the compensative point. In this example, the compensative point is electrically connected to one of common lines in one of common line groups, and the common voltage V_{com} is adjusted to 5V, so that the common voltage V_{com} is stabilized to diminish the flicker.

The following, refer to FIG. 5, explains the adjusting method for optimizing the variable resistor R_s .

Step 710 is to detect the screen for a flicker. If the flicker occurs then the resistance of the variable resistor R_s should be reset; if not, the R_s should be retained.

Step 720 is to adjust the resistance of the variable resistor R_s to find out the optimal resistance, which should diminish the flicker down to the minimum or diminish the flicker.

Step 730 is to reset the resistance of the variable resistor R_s to the optimal resistance.

FIG. 6 is a diagram showing many voltage regulators applied to a TFT-LCD, according to an embodiment of this invention, where the TFT-LCD includes many areas 911, 912 . . . 91k, and each area corresponds to a common line group, and a common line group connects to a voltage regulator, where the voltage regulators are shown as 811, 812 . . . 81k.

FIG. 7 shows the equivalent circuits of the various areas of a TFT-LCD shown in FIG. 6. The voltage regulators 811, 812 . . . 81k and the areas 911, 912 . . . 91k in FIG. 7 are corresponding to the 811, 812 . . . 81k and 911, 912 . . . 91k in FIG. 6 respectively, where FIG. 7 shows out the equivalent circuits of the voltage regulators 811, 812 . . . 81k.

Refer to FIG. 7, $G_1, G_2 . . . G_m, G_{m+1} . . . G_n$ represent scan lines, $S_1, S_2 . . . S_{I-1}, S_I$ represent data lines and $C_1, C_2 . . . C_{m-1}, C_m . . . C_n$ represent common lines. The common lines are grouped and parallel connected to form k common line groups corresponding to k areas on the screen in FIG. 6. Each of the common line groups includes at least one common line, but different common line groups may have the different amount of common lines. Each of the common line groups connects to a voltage regulator, and the voltage regulators compensate the common voltages $V_{com1}, V_{com2} . . . V_{comk}$ corresponding to areas of the screen.

For each area of the screen, the resistor of each voltage regulator is adjustable independently to diminish the flicker down to the minimum or diminish the flicker. It is obvious that the resistor of a different voltage regulator may have a differ-

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ent resistance. When flicker on each area of the screen of a TFT-LCD is diminished to minimum or vanished, the TFT-LCD is optimized.

Although the present invention has been explained in relation to its preferred embodiment, it is to be understood that other modifications and variation can be made without departing the spirit and scope of the invention as claimed.

What is claimed is:

1. A liquid crystal display, comprising:

a first substrate;

a plurality of scan lines and a plurality of data lines disposed on the first substrate to define a plurality of pixel regions thereon;

a plurality of voltage regulators;

a plurality of common line groups disposed on the first substrate and essentially parallel to the scan lines, each of the common line groups comprises a plurality of common lines and the common line groups are connected to the voltage regulators respectively;

a plurality of pixels, wherein each of the pixels is configured on each of the pixel regions, and the pixels are connected to the common lines, and the pixels connected to the same common line in the same common line group are connected to only one common line and the pixels connected to the common lines belonging to different common line groups do not connect to the same common line; and

a second substrate opposed to the first substrate.

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2. A liquid crystal display according to claim 1, wherein each of the voltage regulators comprises an inverting adder and a resistor.

3. A liquid crystal display according to claim 2, wherein the resistor is a variable resistor.

4. A liquid crystal display according to claim 2, wherein the inverting adder comprises a positive input, a negative input and an output, wherein the positive input connects a ground wire, the negative input connects the resistor, and the output connects the resistor and a compensative point.

5. A liquid crystal display according to claim 4, wherein the compensative point is connected to one of the common lines in one of common line groups.

6. A liquid crystal display according to claim 1, wherein each of the pixel regions comprises an active device and a pixel electrode therein, wherein the active device is electrically connected to one of the data lines and scan lines and the pixel electrode is electrically connected to the active device.

7. A liquid crystal display according to claim 1, wherein the amount of the common lines of any two of the common line groups are the same or different.

8. A liquid crystal display according to claim 1, wherein any two of the voltage regulators have identical or different resistances.

9. A liquid crystal display according to claim 1, wherein the first substrate is an active matrix substrate.

10. A liquid crystal display according to claim 1, wherein the second substrate is a color filter substrate.

* * * * *

专利名称(译)	液晶显示器		
公开(公告)号	US7952547	公开(公告)日	2011-05-31
申请号	US11/790022	申请日	2007-04-23
[标]申请(专利权)人(译)	中华映管股份有限公司		
申请(专利权)人(译)	中华映管有限公司		
当前申请(专利权)人(译)	中华映管股份有限公司.		
[标]发明人	WEI LIN CHIEH		
发明人	WEI, LIN-CHIEH		
IPC分类号	G09G3/36		
CPC分类号	G09G3/3655 G09G2320/0247 G09G2320/0219		
优先权	095132727 2006-09-05 TW		
其他公开文献	US20080055215A1		
外部链接	Espacenet USPTO		

摘要(译)

液晶显示器包括第一基板。多条扫描线和多条数据线设置在第一基板上以限定多个像素区域。多个公共线组设置在第一基板上，并且每个公共线组包括与扫描线平行的多个公共线。并且，至少，电压调节器电连接到公共线组之一，其中电压调节器可以稳定像素电压以减小液晶显示器的闪烁。

