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Oct. 13, 2005(54) **STRUCTURE OF LCD PANEL AND METHOD
OF MANUFACTURING THE SAME****Publication Classification**(76) Inventors: **Hung Meng Yi**, Tao Yuan Shien (TW);
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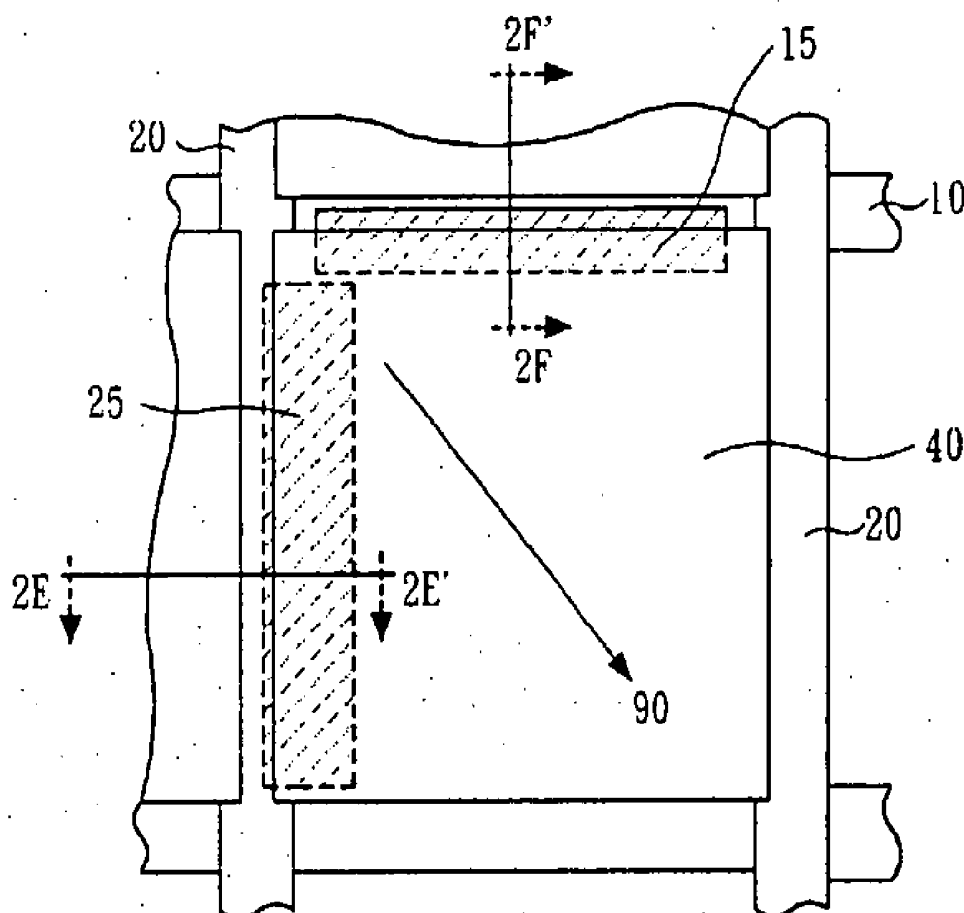
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ABSTRACT

Pixel electrodes in a liquid crystal panel extend to cover the entire pixel regions. The light leakage is generated only on two sides of the pixel region due to the uncontinuous alignment of liquid crystal molecules when the liquid crystal panel is driven by dot inversion. Therefore, in the present invention, light-shielding layer is formed on only the corresponding region of the TFT substrate. The aperture ratio of the liquid crystal panel according to the present invention can increase and the light-shielding layer can be employed to repair the gate lines and data lines.

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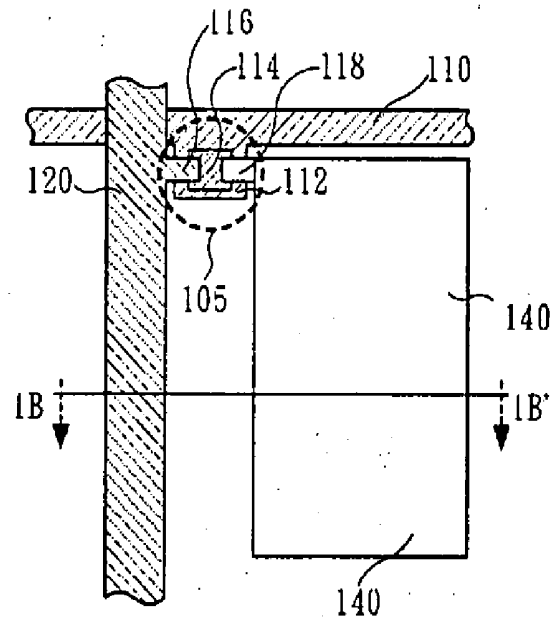


FIG. 1A (Prior Art)

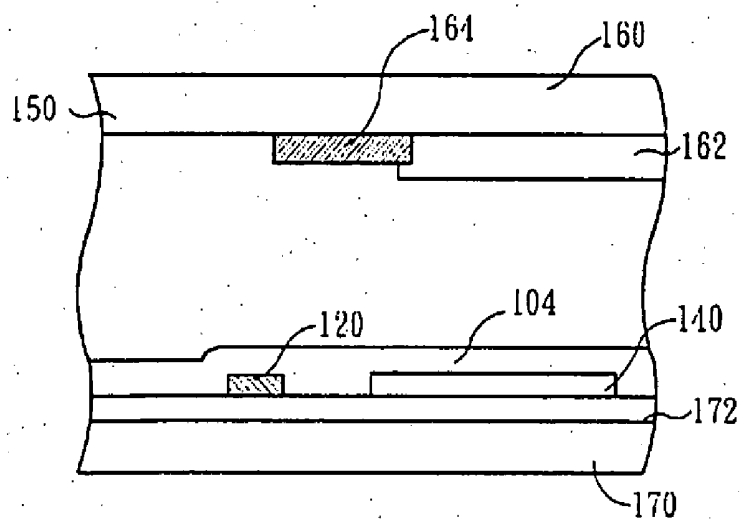


FIG. 1B (Prior Art)

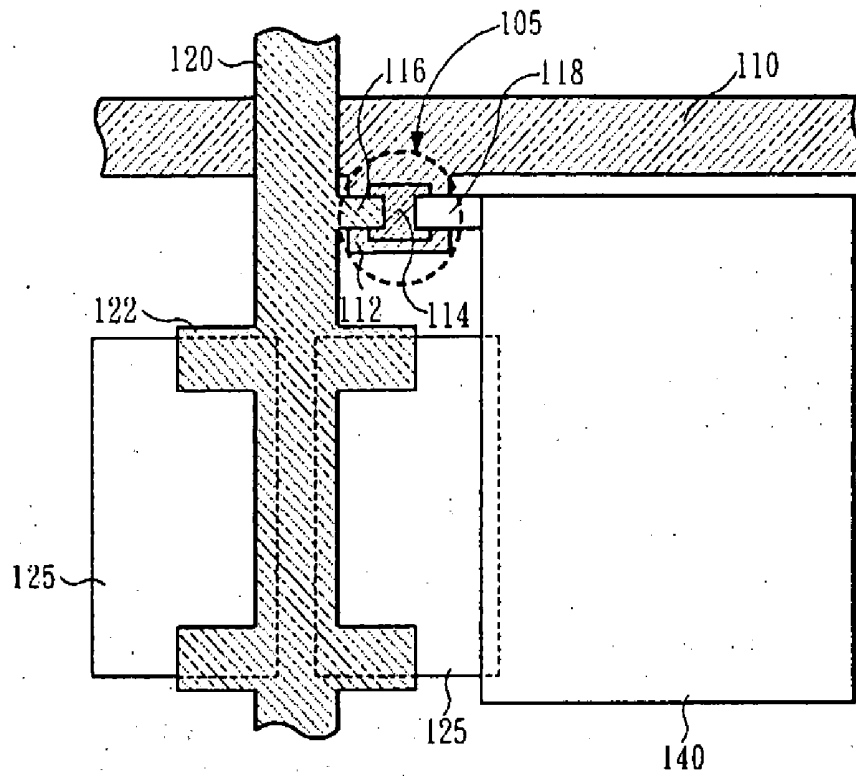


FIG. 1C (Prior Art)

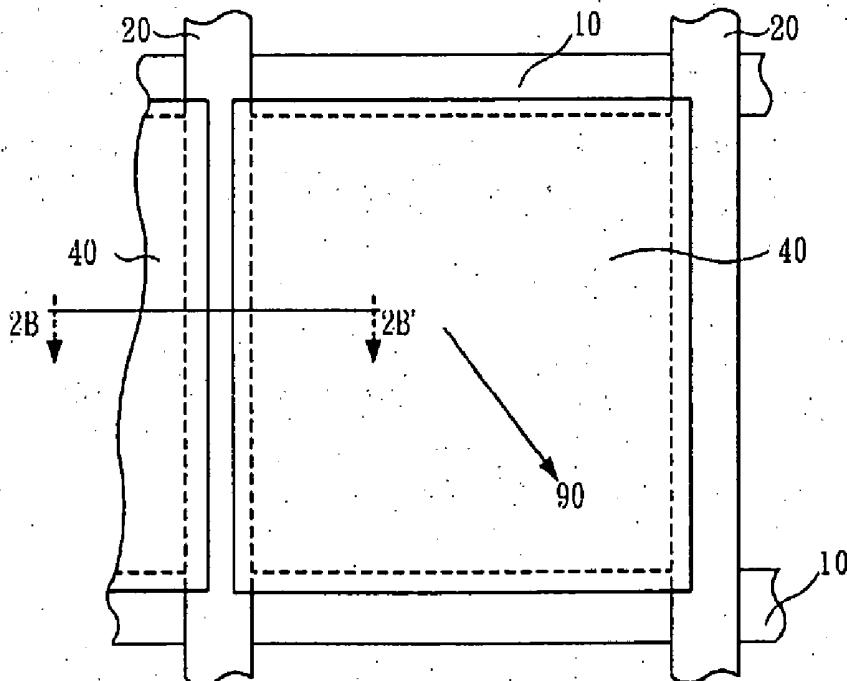


FIG. 2A

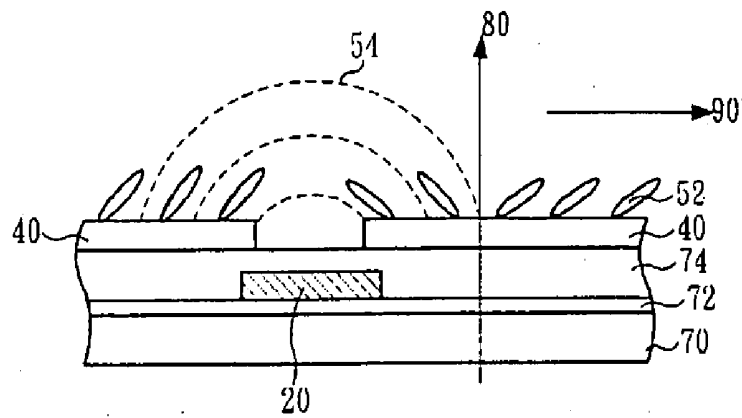


FIG. 2B

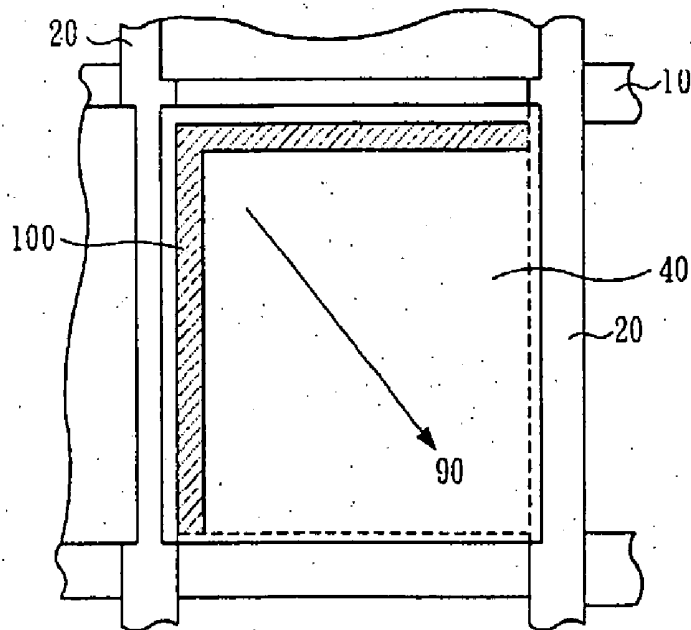


FIG. 2C

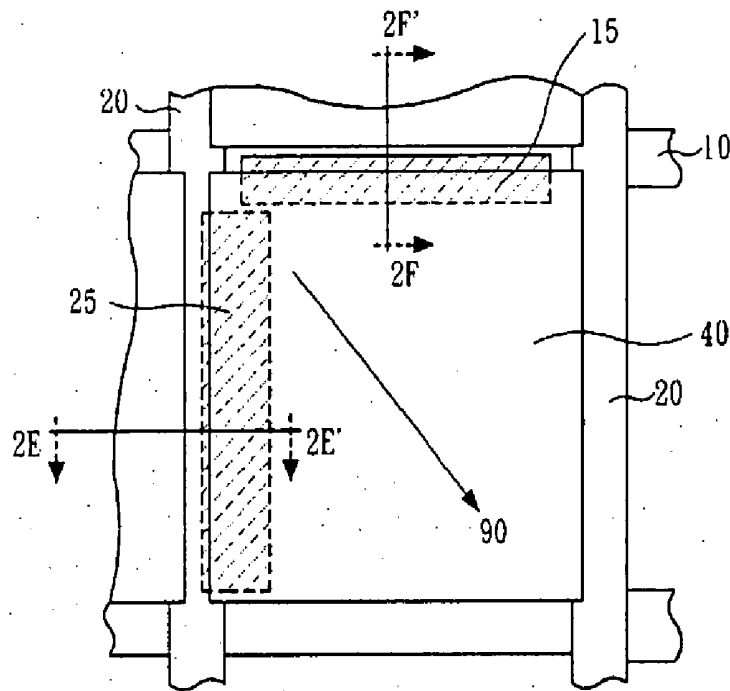


FIG. 2D

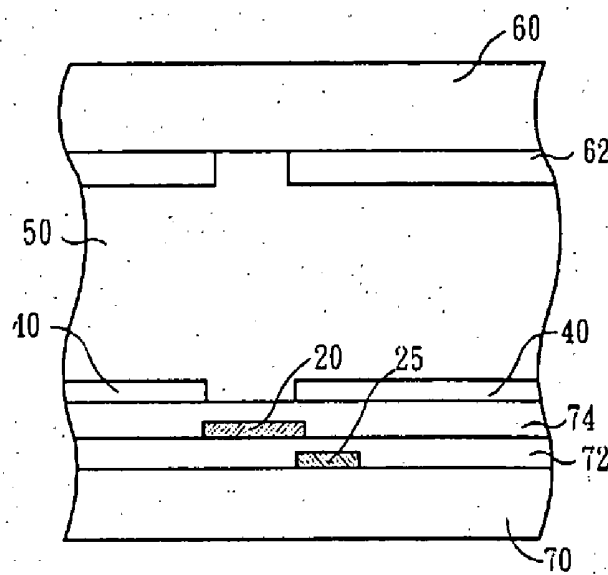


FIG. 2E

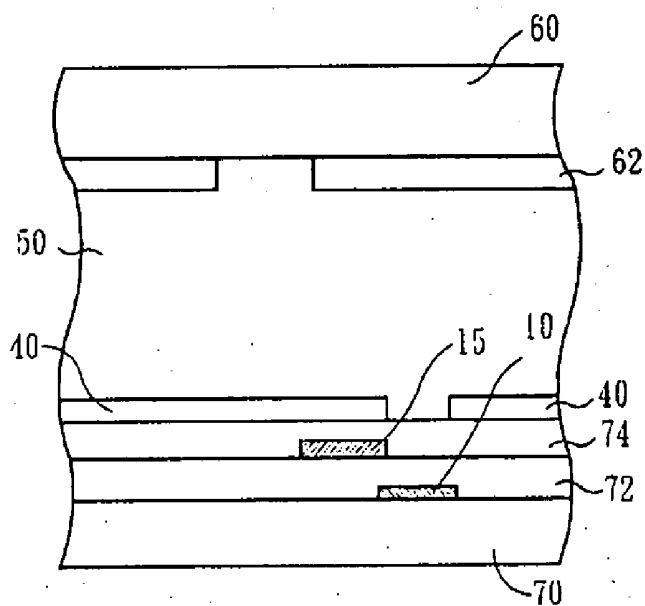


FIG. 2F

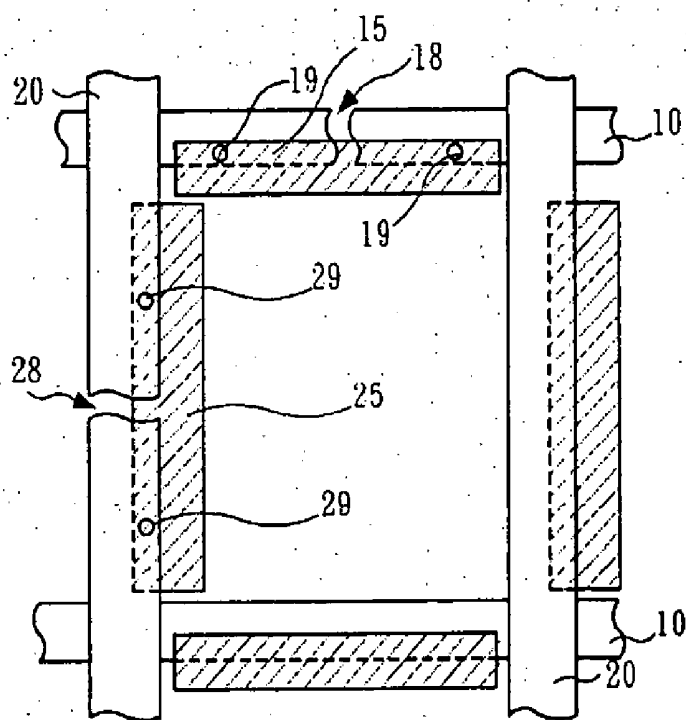


FIG. 2G

STRUCTURE OF LCD PANEL AND METHOD OF MANUFACTURING THE SAME

BACKGROUND OF THE INVENTION

[0001] 1. Field of the Invention

[0002] This invention relates to a liquid crystal display (LCD) panel structure and method for manufacturing the same, and more particularly to an LCD panel structure driven by dot inversion and method for manufacturing the same.

[0003] 2. Description of the Prior Art

[0004] FIGS. 1A and FIG. 1B show a conventional LCD panel, which comprises a color filter (CF) substrate 160, a thin film transistor (TFT) substrate 170, and a liquid crystal layer 150 injected in between the CF substrate 160 and the TFT substrate 170. A plurality of thin film transistors (TFTs) 105, gate lines 110, data lines 120, and pixel electrodes 140 are formed on the TFT substrate 170. A black matrix 164 and a plurality of color filters 162 are formed on the CF substrate 160. Each of the TFTs 105 mainly comprises a gate electrode 112, a gate insulating layer (not shown), a semiconductor layer 114, a source electrode 116, a drain electrode 118. The gate electrode 112 is electrically connected with a corresponding gate line 110; the source electrode 116 is electrically connected with a corresponding data line 120; and the drain electrode 118 is electrically connected with a corresponding pixel electrode 140. The alignment of liquid crystal molecules on the pixel electrode 140 is controlled by a voltage of the pixel electrode 140 applied through the TFT 105. However, the pixel electrode 140 of the conventional LCD panel is not covered the entire area surrounded with the data lines 120 and the gate lines 110. As shown in FIG. 1A, the pixel electrode 140 and the gate line 110, and the pixel electrode 140 and the data line 120 are spaced with a gap. Namely, the gate line 110 and the data line 120 are not covered by the pixel electrode 140. Hence, the alignment of the liquid crystal molecules on the gap can not be controlled by the pixel electrode 140, and a problem of light leakage will be generated in the gap.

[0005] In order to avoid the problem of light leakage, the opaque black matrix is formed on partial area of the CF substrate 160 corresponding to the gap. FIG. 1B is a cross-section schematic diagram along the line 1B-1B' in FIG. 1A. An insulating layer 172 is formed on the TFT substrate 170, the data lines 120 and the pixel electrodes 140 are formed on the insulating layer 172, and a passivation layer 104 is covered on the data lines 120 and the pixel electrodes 140 to protect them. The black matrix 164 and the color filters 162 are formed on the lower surface of the CF substrate 160, the position of the black matrix 164 is corresponding to the area of light leakage, and the position of the color filters 162 are corresponding to the area of the pixel electrodes 140.

[0006] The structure of LCD panel mentioned above overcomes the problem of light leakage by using light-shielding structure of the black matrix 164. However, the assembly precision for combining the CF substrate 160 and TFT substrate 170 of LCD panel is not good. Consequently, the dimension of the black matrix 164 must be very larger than the dimension of the gap between the pixel electrodes 140 and the gate lines 110 and between the pixel electrodes 140

and the data lines 120 for defeating the assembly imprecision resulted in the light leakage. Nevertheless, the larger black matrix 164 will reduce an aperture ratio of the LCD panel. Therefore, U.S. Pat. No. 6,476,882 discloses the light-shielding structure formed on the TFT substrate 170. FIG. 1C shows a TFT substrate structure of a LCD panel of U.S. Pat. No. 6,476,882. In the present invention, the definition for source/drain of TFT is according to the common definition, and is different from the definition in the specification of the patent mentioned above. In such structure of LCD panel, an opaque light-shielding layer 125 is formed on the TFT substrate. In order to provide a more clear description for shapes and relationships of elements, the portions of the light-shielding layer 125 covered by the pixel electrode 140, the data line 120 and the protruding structure 122 is drawn with dot-line. Because the photolithography process is more accurate than the assembly process for the LCD panel, the dimension of the light-shielding layer 125 is smaller than the black matrix 164 shown in FIG. 1B. Hence, the aperture ratio can be increased by replacing the black matrix with the light-shielding layer mentioned above. Furthermore, the light-shielding layer has a repairing function of connecting the light-shielding layer with the protruding structure while the data line is broken.

[0007] Unfortunately, in the LCD panel mentioned above, gaps between the pixel electrode and the gate lines, and between the pixel electrode and the data lines are existing in each pixel region defined by two adjacent data lines and two adjacent gate lines. Hence, whether the light-shielding structure is the black matrix on the CF substrate or the opaque light-shielding structure on the TFT substrate, the light-shielding structure must be formed on the four sides of the pixel regions. The aperture ratio is still affected, and the quality of the LCD panel can not rise.

SUMMARY OF THE INVENTION

[0008] In those conventional arts, the structure of LCD panel has those problems and drawbacks. One of objectives of the present invention is to provide a structure of LCD panel having a high aperture ratio for raising the quality of LCD panel.

[0009] Another objective of present invention is to avoid the problem of assembly imprecision by forming an opaque light-shielding layer on the TFT substrate, so the aperture ratio of the LCD panel can be further raised.

[0010] A further objective of present invention is to repair the broken data lines or gate lines with the corresponding light-shielding layer, so the product yield can be increased.

[0011] As aforementioned, the present invention provides a structure of LCD panel driven by dot inversion. The structure of LCD panel comprises an upper substrate, a lower substrate, a liquid crystal layer, a plurality of gate lines, a plurality of data lines, a pixel electrode layer, a plurality of thin film transistors, and a plurality of first opaque structures. The lower substrate is parallel to the upper substrate. The liquid crystal layer is between the upper substrate and the lower substrate. The color filter layer is on a lower surface of the upper substrate. The plurality of gate lines is on an upper surface of the lower substrate. The plurality of data lines is on the upper surface of the lower substrate and perpendicular to the plurality of gate lines, and two of adjacent gate lines and two of adjacent data lines

define a pixel region. The pixel electrode layer is on the upper surface of the lower substrate, and the pixel electrode layer comprises a plurality of pixel electrodes that each pixel electrode is in a corresponding pixel region and a portion of the pixel electrode overlaps the data lines and the gate lines which define the pixel region. The plurality of thin film transistors is on the upper surface of the lower substrate, and each thin film transistor, which comprises a gate electrode, a drain electrode, and a source electrode, is in a corresponding pixel region or on a corresponding gate line. The gate electrode is electrically connected with a corresponding gate line, the source electrode is electrically connected with a corresponding data line, and the drain electrode is electrically connected with a corresponding pixel electrode. The plurality of first opaque structures and a plurality of second opaque structures are on the upper surface of the lower substrate for shielding a light leakage area of the LCD panel, the plurality of first opaque structures is parallel to the plurality of gate lines, and the plurality of second opaque structures is parallel to the plurality of data lines. In such case, each first opaque structure is on one side of a gate line of a corresponding pixel region, and each second opaque structure is on one side of a data line of a corresponding pixel region.

[0012] The present invention also provides a TFT substrate of a LCD panel. The TFT substrate comprises a substrate, and a plurality of pixel units on the substrate. Each pixel unit comprises four lines, a pixel electrode layer, a thin film transistor, and a first light-shielding layer. The four lines are on four sides of the pixel unit, and the four lines comprise a first gate line, a second gate line, a first data line, and a second data line. The first gate line and the second gate line are on two opposite sides of the pixel unit and parallel to each other, and the first data line and the second data line are on other two opposite sides of the pixel unit and perpendicular to the first gate line and the second gate line. The pixel electrode layer is in the pixel unit and the pixel electrode layer overlaps at least one of the four lines. The thin film transistor is in the pixel unit. The first light-shielding layer is in the pixel unit and near a first side of four sides of the pixel unit.

[0013] The present invention further provides a repairing method for an LCD panel. The LCD panel comprises a plurality of first light-shielding lines and a plurality of pixel electrodes that overlaps two gate lines and two data lines of a corresponding pixel region, and each first light-shielding lines is parallel to and overlaps a line of the gate lines and the data lines on a corresponding pixel region. The method mentioned above comprises the step of irradiating with a laser at least one of the first light-shielding lines to electrically connect the first light-shielding line with the line via at least two connecting points.

BRIEF DESCRIPTION OF THE DRAWINGS

[0014] FIG. 1A is a schematic diagram of a structure of LCD panel in the conventional arts;

[0015] FIG. 1B is a cross-section diagram along a line 1B-1B' in FIG. 1A;

[0016] FIG. 1C is a schematic diagram of an TFT substrate having a light-shielding layer in the conventional arts;

[0017] FIG. 2A is a schematic diagram of a structure of LCD panel with a high aperture ratio;

[0018] FIG. 2B is a cross-section diagram along a line 2B-2B' in FIG. 2A;

[0019] FIG. 2C is a schematic diagram of light leakage area of the structure of LCD panel in FIG. 2A;

[0020] FIG. 2D is a schematic diagram of a structure of LCD panel in a preferred embodiment according to the present invention;

[0021] FIG. 2E is a cross-section diagram along a line 2E-2E' in FIG. 2D;

[0022] FIG. 2F is a cross-section diagram along a line 2F-2F' in FIG. 2D; and

[0023] FIG. 2G is a schematic diagram of repairing broken lines in the LCD panel according to the present invention.

DESCRIPTION OF THE PREFERRED EMBODIMENT

[0024] Some sample embodiments of the invention will now be described in greater detail. Nevertheless, it should be recognized that present invention can be practiced in a wide range of other embodiments besides those explicitly described, and the scope of the present invention is expressly not limited expect as specified in the accompanying claims.

[0025] Then, the components of the different elements are not shown to scale. Some dimensions of the related components are exaggerated and meaningless portions are not drawn to provide a more clear description and comprehension of the present invention.

[0026] In order to increase the aperture ratio of LCD panel, the LCD panel of the present invention has pixel electrodes partially overlapping gate or data lines on a corresponding pixel region. As shown in FIG. 2A, each pixel electrode 40 partially overlap two gate lines 10 and two data lines 20 that are on the four side of a pixel region. The data line 20, and the pixel electrode 40, the covered areas of the gate line 10, the data line 20, and the pixel electrode 40 are drawn by dotted line for providing a more clear space relationship among the gate line 10, and only some pixel electrodes are drawn for providing a more concise description. Alignment of the liquid crystal molecules (not shown in FIG. 2A) in the LCD panel is parallel to an orientation direction 90. Electric lines of force are generated between the pixel electrodes and the common electrodes on the CF substrate while a voltage is applied to the pixel electrode, and the alignment direction of the liquid crystal molecules is changed to be parallel to the electric lines of force. Thus the controllable region of the alignment of the liquid crystal molecules comprises the entire pixel region, and the LCD panel of the present invention has a higher aperture ratio compared with the LCD panel in conventional arts.

[0027] For avoiding the flickering resulted from that the LCD panel needs to be driven periodically with alternating voltage, the most LCD panels are divided into a plurality of domains and the polarities of the voltage applied to two adjacent domains are opposite (e.g., dot inversion). Therefore, human eyes can feel a flicker-free image with an averaged brightness at any time. The present invention is suitable for the LCD panel driven by the method of domain inversion mentioned above, especially for the LCD panel

driven by dot inversion. The present invention will be described below with embodiments of an LCD panel driven by dot inversion.

[0028] FIG. 2B is a cross-section diagram along a line 2B-2B' in FIG. 2A. As shown in FIG. 2B, in the LCD panel driven by dot inversion, each of two adjacent pixel electrodes 40 has the reverse polarities of voltage and electric lines of force 54 are generated between the two adjacent pixel electrodes 40. An insulating layer 72 is formed on a lower substrate 70, called "TFT substrate" in some prior arts. A plurality of data lines 20 is formed on the insulating layer 72 and covered with a passivation layer 74, and a plurality of pixel electrodes 40 is formed on the passivation layer 74. Due to the pre-tilted orientation of the LCD panel, the liquid crystal molecules 52 are arranged along the orientation direction 90. A component 90' of orientation direction 90 is the component along the line 2B-2B'. Hence, in FIG. 2B, on the area of the two pixel electrodes 40 having stronger electric lines of force 54, the liquid crystal molecules 52 are arranged particularly by the electric lines of force 54, toward the opposite direction of the pre-tilted orientation. The alignment direction of the liquid crystal molecules 52 on the area of stronger electric lines of force 54 is changed. Contrariwise, on the area of weaker electric lines of force 54, the liquid crystal molecules 52 are still arranged by the structure of the pre-tilted orientation, along the orientation direction. Thus the alignment direction of the liquid crystal molecules 52 is not uncontinuous. It is obvious that the light easily passes through in the border between the different alignment directions without the control of the liquid crystal molecules 52. Therefore, the light of a back light module 80 transmits through the lower substrate 70 and passes through the border with uncontinuous alignment of liquid crystal molecules to result in the light leakage of domain.

[0029] FIG. 2C is a schematic diagram of light leakage area 100 of the structure of LCD panel driven by dot inversion. Similarly, only some pixel electrodes 40 are drawn in FIG. 2C for providing a more concise description. Obviously, the light leakage area 100 is only on two sides of four sides of a pixel region defined by two adjacent gate lines and two adjacent data lines. Hence, as shown in FIG. 2D (not showing the TFTs near the intersection of a data line and a gate line), opaque light-shielding layers 15 and 25 are formed to cover only the light leakage area 100 of each pixel region, and the problem of light leakage can be overcome to increase the contrast of the LCD panel.

[0030] FIG. 2E is a cross-section diagram along a line 2E-2E' in FIG. 2D. The structure of LCD panel is mainly composed of an upper substrate 60 (called "CF substrate" in some prior arts), an lower substrate 70 parallel to the upper substrate 60, and a liquid crystal layer 50 between the two substrate mentioned above. The upper substrate 60 has a color filter layer 62 formed thereon. The lower substrate 70 has a plurality of data lines 20 and a plurality of gate lines 10 (shown in FIG. 2F), a plurality of pixel electrodes 40, a plurality of light-shielding layers 25, a insulating layer 72, a passivation layer 74, and a plurality of thin film transistors (not shown) formed thereon. Each pixel electrodes 40 partially overlap the data lines 20. Each light-shielding layer 25 is formed on the lower substrate 70 and covered with the insulating layer 72. The position of the light-shielding layer 25 is on the side of a corresponding data line 20 and corresponding to the border with uncontinuous alignment of

liquid crystal molecules for shielding the light leakage area 100 near the data line 20. Furthermore, the light-shielding layer 25 partially overlaps the data line 20. The light-shielding, layer 25 can be formed after or before (i.e., above or below) the data line 20. It is preferable that the light-shielding layer 25 is formed before (i.e., below) the data line 20, and thus the light-shielding layer 25 and gate lines 10 can be formed by the same reticle. Namely, the formation of the light-shielding layer 25 is performed by only changing the pattern of mask, without increasing the steps of process, so the formation of the light-shielding layer 25 does not almost increase the cost of the LCD panel.

[0031] FIG. 2F is a cross-section diagram along a line 2F-2F' in FIG. 2D. Similarly, the LCD panel mainly comprises an upper substrate 60, an lower substrate 70 which is parallel to the upper substrate 60, a liquid crystal layer 50, a color filter layer 62, a plurality of gate lines 10, a plurality of pixel electrodes 40, a plurality of light-shielding layers 15, a insulating layer 72, a passivation layer 74, and a plurality of thin film transistors (not shown). The light-shielding layer 15 is formed on the sides of a corresponding gate line 10 and corresponding to the border with uncontinuous alignment of liquid crystal molecules for shielding the light leakage area 100 near the gate line 10. Furthermore, the light-shielding layer 15 partially overlaps the gate line 10 and can be formed after or before (i.e., above or below) the gate line 10. It is preferable that the light-shielding layer 15 is formed above the gate line 10, and thus the light-shielding layer 25 and data lines 20 can be formed by the same reticle.

[0032] The light-shielding layers 15 and 25 not only has the function of shielding light, but also are employed to repair the broken data lines 20 and gate lines 10 or other electrical problems. As shown in FIG. 2G, the overlapping areas of the gate line 10/the light-shielding layer 15 and the data line 20/the light-shielding layer 25 respectively on two sides of the broken portions 18 and 28 of gate line 10 and data line 20 are irradiated by a laser beam. Thus the irradiated overlapping areas are melted to electrically connect to form one or more connecting points 19 and 29. The gate line 10 and the light-shielding layer 15 (and the data line 20 and the light-shielding layer 25) are electrically connected with each other through these connecting points 19 (and connecting points 29). Signals on the gate lines 10 and the data lines 20 are transmitted by passing the light-shielding layers 15 and 25, so the light-shielding layers 15 and 25 have the function of repairing broken line.

[0033] Although specific embodiments have been illustrated and described, it will be obvious to those skilled in the art that various modifications may be made without departing from what is intended to be limited solely by the appended claims.

What is claimed is:

1. A structure of LCD panel driven by dot inversion, comprising:

- an upper substrate;
- a lower substrate parallel to said upper substrate;
- a liquid crystal layer between said upper substrate and said lower substrate;
- a color filter layer on a lower surface of said upper substrate;

a plurality of gate lines on an upper surface of said lower substrate;

a plurality of data lines on said upper surface of said lower substrate and perpendicular to said plurality of gate lines, and two of adjacent said gate lines and two of adjacent said data lines defining a pixel region;

a pixel electrode layer on said upper surface of said lower substrate, said pixel electrode layer comprising a plurality of pixel electrodes that each pixel electrode is in a corresponding pixel region and a portion of said pixel electrode overlaps said data lines and said gate lines which define said pixel region;

a plurality of thin film transistors on said upper surface of said lower substrate, each of said thin film transistors comprising a gate electrode, a drain electrode, and a source electrode and being in a corresponding pixel region or on a corresponding gate line, wherein said gate electrode is electrically connected with a corresponding gate line, said source electrode is electrically connected with a corresponding data line, and said drain electrode is electrically connected with a corresponding pixel electrode; and

a plurality of first opaque structures and a plurality of second opaque structures on said upper surface of said lower substrate for shielding a light leakage area of said LCD panel, said plurality of first opaque structures parallel to said plurality of gate lines, and said plurality of second opaque structures parallel to said plurality of data lines;

wherein each first opaque structure is on one side of a gate line of a corresponding pixel region, and each second opaque structure is on one side of a data line of a corresponding pixel region.

2. The structure of LCD panel according to claim 1, wherein said plurality of first opaque structures and said plurality of data lines are formed by the same reticle.

3. The structure of LCD panel according to claim 1, wherein said plurality of second opaque structures and said plurality of gate lines are formed by the same reticle.

4. The structure of LCD panel according to claim 1, wherein each of said first opaque structures overlaps a portion of a corresponding gate line.

5. The structure of LCD panel according to claim 4, wherein at least one of said first opaque structure is electrically connected with said corresponding gate line via at least two connecting points.

6. The structure of LCD panel according to claim 1, wherein each of said second opaque structures overlaps a portion of a corresponding data line.

7. The structure of LCD panel according to claim 6, wherein at least one of said second opaque structure is electrically connected with said corresponding data line via at least two connecting points.

8. A TFT substrate of a LCD panel, said TFT substrate comprising:

a substrate; and

a plurality of pixel units on said substrate, each pixel unit comprising:

four lines on four sides of said pixel unit, comprising a first gate line, a second gate line, a first data line, and a second data line, said first gate line and said second

gate line on two opposite sides of said pixel unit and parallel to each other, and said first data line and said second data line on other two opposite sides of said pixel unit and perpendicular to said first gate line and said second gate line;

a pixel electrode layer in said pixel unit and said pixel electrode layer overlapping at least one of said four lines;

a thin film transistor in said pixel unit; and

a first light-shielding layer in said pixel unit and near a first side of four sides of said pixel unit.

9. The TFT substrate according to claim 8, wherein said LCD panel is driven by dot inversion.

10. The TFT substrate according to claim 9, wherein said first side is gone side corresponding to said overlapping portion, and said first light-shielding layer overlaps a line overlapped by said pixel electrode.

11. The TFT substrate according to claim 10, wherein said first light-shielding layer are electrically connected with said line via at least two connecting points.

12. The TFT substrate according to claim 11, wherein said connecting points are formed by laser irradiating.

13. The TFT substrate according to claim 9 further comprising a second light-shielding layer in said pixel unit and near a second side of said pixel unit, perpendicular to said first side, and said second light-shielding layer perpendicular to said first light-shielding layer and overlapping a line on said second side.

14. The TFT substrate according to claim 13, wherein said second light-shielding layer are electrically connected with said line via at least two connecting points.

15. The TFT substrate according to claim 14, wherein said connecting points are formed by laser irradiating.

16. The TFT substrate according to claim 13, wherein said first light-shielding layer and said line overlapped with second light-shielding layer are formed by the same reticle.

17. The TFT substrate according to claim 13, wherein said second light-shielding layer and said line overlapped with first light-shielding layer are formed by the same reticle.

18. A repairing method for an LCD panel which comprises a plurality of first light-shielding lines and a plurality of pixel electrodes that overlaps two gate lines and two data lines of a corresponding pixel region, and each first light-shielding line is parallel to and overlaps a line of said gate lines and said data lines on a corresponding pixel region, said method comprising:

irradiating with a laser at least one of said first light-shielding lines to electrically connect said first light-shielding line with said line via at least two connecting points.

19. The method according to claim 18, wherein said LCD panel further comprises a plurality of second light-shielding lines perpendicular to said first light-shielding lines, and each of said second light-shielding lines is parallel to and overlaps a line of said gate lines and said data lines on a corresponding pixel region.

20. The method according to claim 19, further comprising:

irradiating with a laser at least one of said second light-shielding lines to electrically connect said second light-shielding line with said line via at least two connecting points.

专利名称(译)	LCD面板的结构及其制造方法		
公开(公告)号	US20050225691A1	公开(公告)日	2005-10-13
申请号	US10/961156	申请日	2004-10-12
[标]申请(专利权)人(译)	洪易中号 雄仔Ç		
申请(专利权)人(译)	洪易中号 雄仔Ç		
当前申请(专利权)人(译)	友达光电股份有限公司		
[标]发明人	YI HUNG MENG HUNG TSAI CHU		
发明人	YI, HUNG MENG HUNG, TSAI CHU		
IPC分类号	G02F1/1368 G02F1/133 G02F1/1335 G02F1/1343 G02F1/136 G09F9/30		
CPC分类号	G02F1/133512 G02F2201/123 G02F1/134309		
优先权	093110019 2004-04-09 TW		
其他公开文献	US7224415		
外部链接	Espacenet USPTO		

摘要(译)

液晶面板中的像素电极延伸以覆盖整个像素区域。当液晶面板由点反转驱动时，由于液晶分子的不连续排列，仅在像素区域的两侧产生漏光。因此，在本发明中，仅在TFT基板的相应区域上形成遮光层。根据本发明的液晶面板的开口率可以增加，并且可以采用遮光层来修复栅极线和数据线。

