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[FR/FR]; 1 rue de la Chauminais, F-35135 CHANTEPIE (FR). **DOYEN, Didier** [FR/FR]; La Débinerie, F-35340 LA BOUEXIERE (FR).

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(74) Agent: **RUELLAN-LEMONNIER, Brigitte**; THOMSON multimedia, 46 Quai Alphonse Le Gallo, F-92648 BOULOGNE cedex (FR).

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(71) Applicant (*for all designated States except US*): **THOMSON LICENSING S.A.** [FR/FR]; 46 Quai Alphonse Le Gallo, F-92100 BOULOGNE-BILLANCOURT (FR).

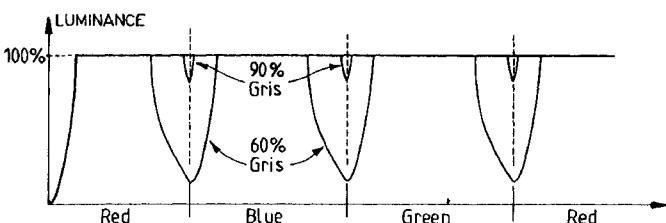
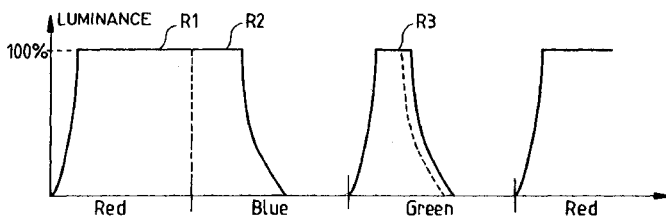
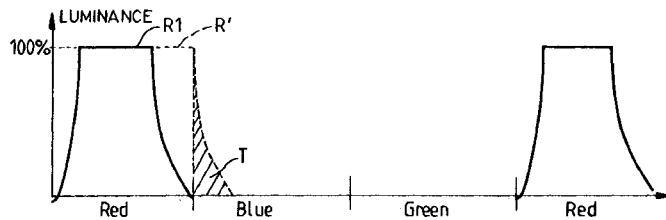
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(72) Inventors; and

(75) Inventors/Applicants (*for US only*): **BOREL, Thierry**

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(54) Title: METHOD OF IMPROVING THE LUMINOUS EFFICIENCY OF A SEQUENTIAL-COLOUR MATRIX DISPLAY



(57) Abstract: The present invention relates to a method of improving the luminous efficiency of a sequential-colour matrix display, the display being driven using an addressing method of the pulse width modulation or PWM type. This method comprises, for each pixel of a subframe, the following steps:- comparison of the pixel colour value of the preceding subframe with a reference value so as to provide an overlap value depending on the period of overlap with the current subframe;- if the pixel colour value of the current subframe less the overlap value gives a positive value, a time offset is added to the pixel colour value of the current subframe;- if the pixel colour value of the current subframe less the overlap value gives a negative value, the pixel colour value of the current subframe is forced to be zero. The invention applies to LCOS or LCD displays.



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**Method of improving the luminous efficiency of a
sequential-colour matrix display**

The present invention relates to a method of improving
5 the luminous efficiency of a sequential-colour matrix
display. It relates especially to matrix displays in
which the electrooptic valve consists of a liquid-
crystal valve, more particularly a valve of the LCOS
(Liquid Crystal On Silicon) type.

10

Liquid-crystal display (LCD) panels used in direct
viewing displays or in projection displays are based on
a matrix scheme with an active element at each pixel.
Various addressing methods are used to generate the
15 grey levels corresponding to the luminance to be
displayed at the selected pixel. The most conventional
method is an analogue method whereby the active element
is switched for a line period in order to transfer the
analogue value of the video signal to the capacitor of
20 the pixel. In this case, the liquid crystal material is
oriented in a direction that depends on the value of
the voltage stored on the capacitor of the pixel. The
incoming light polarization is then modified, and
analysed by a polarizer so as to create the grey
25 levels. One of the problems with this method stems from
the response time of the liquid crystal, which depends
on the grey levels to be generated. Thus, when this
method is used to drive the electrooptic valve of a
sequential-colour matrix display in which the
30 electrooptic valve, especially the LCOS valve, is
successively illuminated with red, green and blue
colour filters, the very short response time between
the intermediate grey levels results in very poor
saturation of the colours in the image when one colour
35 is not completely eliminated during illumination by the
next colour.

To remedy this type of drawback, there has been
proposed in the prior art, for example in the patent

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US 6,239,780, a method of driving a matrix display using a pulse width modulation or PWM technique. In this case, the pixels of the liquid-crystal display are addressed in on/off mode, the "on" mode corresponding to saturation of the liquid crystal. The grey levels are given by the width of the pulse. With such an addressing method, the dynamics of the display panel are improved since the transition time now represents only a small proportion of the total opening time of the liquid-crystal cell, whatever the value of the luminance.

This addressing method is particularly beneficial when it is used with a sequential-colour optical engine using a single electrooptic valve, more particularly a LCOS valve, which is illuminated in succession with the colours red, green and blue. This method, since an on/off mode is used, benefits from a more rapid response time, this being constant whatever the grey level that has to be rendered.

However, although this method has the advantage of improving the response time of the liquid crystal and thus of obtaining optimum colour saturation for the video content, nevertheless the luminous efficiency decreases proportionally with the response time of the liquid crystal.

The object of the present invention is therefore to provide a method for improving this efficiency in the case of a sequential-colour matrix display, in which the display is driven using an addressing method of the pulse width modulation or PWM type.

Consequently, the subject of the present invention is a method of improving the luminous efficiency of a sequential-colour matrix display, the display being driven using an addressing method of the pulse width

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modulation or PWM type, characterized, for each pixel of a subframe, by the following steps:

5 - comparison of the pixel colour value of the preceding subframe with a reference value so as to provide an overlap value depending on the period of overlap with the current subframe;

10 - if the pixel colour value of the current subframe less the overlap value gives a positive value, a time offset is to be added to the pixel colour value of the current subframe;

- if the pixel colour value of the current subframe less the overlap value gives a negative value, the pixel colour value of the current subframe is forced to be zero.

15

According to another feature of the present invention if the pixel colour value of the current subframe less the overlap value gives a negative value, the pixel colour value of the preceding subframe and the colour value of the next subframe are modified so as to maintain the original tint, while at the same time reducing the luminance.

25 In accordance with the present invention, the steps described above apply in succession to each sequential colour of a frame. Moreover, the pixel colour value of a subframe depends on the width of the PWM-type addressing pulse. The reference value depends on the response time of the material forming the display and the time offset depends on the response time of the material forming the display and on the duration of the subframe.

35 Other features and advantages of the present invention will become apparent on reading the description given below of one embodiment of the present invention, this description being given with reference to the drawings appended hereto, in which:

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- Figure 1 is a schematic representation of a matrix display driven using an addressing method of the pulse width modulation or PWM type, to which the present invention can apply;

5 - Figures 2a to 2e show the various signals for driving the display of Figure 1;

- Figures 3a to 3c are curves giving the luminance value in the case of a display driven using a PWM-type addressing method, whereby saturation is preserved;

10 - Figures 4a to 4c are figures similar to Figures 3a to 3c in the case in which priority is given to luminance as opposed to colour saturation;

- Figures 5a to 5c are figures identical to Figures 3a to 3c and 4a to 4c giving the luminance
15 obtained in the case of the method of the present invention;

- Figure 6 is a diagram in block form of a circuit for implementing the method of the present invention;

- Figure 7 is a diagram in block form showing the
20 circuit of Figure 6 applied to the three colours red, blue and green;

- Figure 8 is a diagram giving the luminance as a function of time, allowing the principle applied in the present invention to be explained; and

25 - Figures 9 and 10 are luminance curves explaining the correction function applied in the present invention.

To simplify the description in the figures, the same or
30 similar elements will have the same references.

We will firstly describe, with reference to Figure 1, an embodiment of a matrix display to which the present invention may apply. This matrix display comprises an
35 electrooptic valve, more particularly a LCOS-type display panel. Figure 1 shows very schematically a picture element or pixel 1 of the display panel. This pixel 1 is indicated symbolically by a capacitor C_{pixel} connected between the back electrode CE and, in the

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embodiment shown, the output of a voltage-time converter 2 for implementing an addressing method of the pulse width modulation or PWM type.

5 As shown schematically, the voltage-time converter 2 comprises an operational amplifier 20 whose negative input receives a ramp-shaped signal, labelled Ramp, and whose other input receives a positive voltage corresponding to the charge on a capacitor 21. The
10 charge on the capacitor 21 is controlled by a switching system, more particularly a transistor 22 mounted between one electrode of the capacitor and the input of the voltage-time converter. This switching device consists of a transistor whose gate receives a pulse,
15 labelled Dxfer.

As shown in Figure 1, the picture element or pixel 1 is connected to a row N and a column M of the matrix via a switching circuit such as a transistor 3. More
20 specifically, the gate of the transistor 3 is connected to a row N of the matrix, which is itself connected to a row driver 4. Moreover, one of the electrodes of the transistor, for example the source, is connected to the input of the voltage-time converter 2, while the other
25 electrode or drain is connected to one of the columns M of the matrix, this column being connected to a column driver 5 which receives the video signal to be displayed. Moreover, a capacitor Cs is mounted in parallel with the pixel capacitor as input to the
30 voltage-time converter in order to store the video signal value when the said pixel is selected. The column driver 5 and row driver 4 are conventional circuits. The column driver 5 receives the video signal to be displayed, "Video in", and is controlled by a
35 clock signal Cclk and a start pulse Hstart. The row driver 4 allows the rows to be addressed sequentially and receives a clock signal Rclk and a start pulse Vstart.

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The mode of operation of the display panel when it is used in a sequential-colour display, namely when, during a frame T , a wheel carrying three, green, blue and red, colour filters makes one complete revolution in order to illuminate the valve sequentially, will be explained with reference to Figures 2a to 2e.

As shown in Figure 2a, a pulse I is applied at the start of each subframe $T/3$ to the row N so as to turn on the switching transistor 3. When the switching transistor 3 is turned on, the capacitor C_s charges up to a voltage corresponding to the video signal present on the column M . That is to say, if a green colour filter lies opposite the display during the first subframe $T/3$, the capacitor C_s charges up to a value labelled V_{green} in Figure 2b. During the next subframe, namely at time $T/3$, a new pulse I is applied to the row N , allowing the capacitor C_s to charge up to a voltage labelled V_{blue} , corresponding to the colour blue lying at that moment opposite the display. Likewise, at time $2T/3$, a new pulse I is applied to the row N and the capacitor C_s charges up to a voltage labelled V_{red} in Figure 2b. With the display in Figure 1 driven using a PWM addressing method, the values V_{green} , V_{blue} , V_{red} stored in succession on the capacitor C_s are applied to the capacitor C_{pixel} via the voltage-time converter 2 which operates in the following manner.

A pulse I' is applied within a subframe to the gate D_{xfer} of the switching transistor 22 so as to turn it on. In this case, the voltage stored on the capacitor C_s is transferred to the capacitor 21 mounted in parallel and connected to one of the input terminals of the operational amplifier 20. As shown in Figure 2d, at the end of the pulse I' applied to the gate D_{xfer} , a ramp r is applied to the negative input of the operational amplifier 20. In this way, a voltage V_{pixel} , the duration of which corresponds to the voltage V_{green} stored on the capacitor 21, is obtained

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as output from the operational amplifier 20, as shown in Figures 2d and 2e. The same applies in the case of the subframes that correspond to the passing of the blue and red colour filters in the case in which the display in Figure 1 is used for sequential colour display.

We will now explain, with reference to Figures 3a to 3c, 4a to 4c and 5a to 5c, the problem that the method of the present invention seeks to solve, this being applied especially to a matrix display like that described with reference to Figure 1.

Figures 3a to 3c show the luminance values obtained when it is desired to have saturated colours. In this case, it may be clearly seen that the loss of luminous efficiency is due to the fact that the liquid crystal in the case of an LCOS valve requires long rise and fall times, namely of a few milliseconds. Thus, in Figure 3a, which shows a 100% saturated red pixel being addressed, the subframe labelled Red receives a 100% luminance signal R1 over the duration of the subframe, whereas the subframes labelled Blue and Green receive no signal. There is no overlap between the colours and colour saturation is maintained. Figure 3b shows the addressing of a pastel red pixel. In this case, the subframe Red is addressed by a pulse R1 throughout the duration of the subframe, whereas the subframes Blue and Green are addressed by pulses R2, R3 for a shorter time. In this case too, in order to maintain saturation of the colours, there is no overlap of the colours of one subframe with another. Figure 3c shows the addressing of a white pixel. In this case, each subframe, Red, Blue, Green, is addressed by identical pulses R1, R2, R3 over the entire period of each subframe. Because of the pulse rise and fall times, a loss of luminous efficiency shown symbolically by the bold lines between each pulse in Figure 3c, is observed.

Figures 4a, 4b and 4c are figures identical to Figures 3a, 3b and 3c, but in the case in which priority is given to luminance and not to colour saturation. In the case of a 100%-saturated red pixel being addressed, as shown in Figure 4a, the pulse R1 is therefore applied during the Red subframe over a period t_1 greater than the time $T/3$, so that the pulse fall time overlaps the subframe labelled Blue. In this way, some of the blue light passes through the red, producing a pink pixel. Figure 4b shows the case in which a pastel red pixel is being addressed. In the same way, the Red subframe is addressed by a 100% saturated pulse R1, with a pulse fall time starting at the end of the subframe and overlapping the Blue subframe. The Blue subframe is addressed by a 30% Blue pulse R2 and the Green subframe by a 30% Green pulse R3. Since the Green pulse does not have the same starting point, a time offset t_2 must be added in order to compensate for the rise time of the liquid crystal, as shown by the solid and dotted lines in Figure 4b.

Figure 4c shows a white pixel being addressed. In this case, a perfect white is obtained in the case of the Red, Blue and Green subframes, as shown by the single pulse R.

The results obtained with the method used in the present invention to improve the luminous efficiency will now be described with reference to Figure 5a, 5b and 5c.

In this case, the method used consists, for each pixel of a subframe, in comparing the pixel colour value of the preceding subframe with a reference value so as to deliver an overlap value that depends on the period of overlap with the current subframe and then, if the pixel colour value of the current subframe less the overlap value gives a positive value, a time offset is

to be added to the pixel colour value of the current subframe, and if the pixel colour value of the current subframe less the overlap value gives a negative value, the pixel colour value of the current subframe is forced to be zero.

The results of this method are shown, for example, in Figure 5a in which, during the subframe labelled Red, a 100% luminance signal R1 is applied and the dotted part R' shows that colour saturation is maintained when the Red subframe is addressed, while slightly reducing the luminance by an amount equivalent to the overlap time represented by the hatched part.

According to a variant of the method, if the pixel colour value of the current subframe less the overlap value gives a negative value, the pixel colour value of the preceding subframe and the colour value of the next subframe are modified so as to maintain the original tint, while at the same time reducing the luminance. This is shown, for example, in Figure 5b, which gives an example of a pastel red pixel being addressed. In this case, the Red subframe is addressed by a pulse R1 which overlaps the Blue subframe addressed by a pulse R2, as in the case of Figure 4b, and the Green subframe is addressed by a pulse R3. In accordance with the method, the pastel colours maintain their original luminance level.

Shown in Figure 5c is an example of addressing a completely white pixel or one having a 60% or 90% grey level, as shown. In this case, the pulses for the Red, Blue and Green subframes are identical and of the same duration, the duration varying depending on the desired grey level.

An example of implementation of an electronic circuit allowing the method described above to be employed will now be described with reference to Figures 6, 7 and 8.

As shown more particularly in Figure 6, which shows a circuit 100 using the invention for the colour red, the preceding colour value, namely the value R₂, is sent to a look-up table, labelled LUT1 101, which outputs an overlap datum proportional to the period of overlap with the Blue subframe. This datum is sent to the input of a circuit 102 which subtracts the overlap value from the current blue colour value B₁. A B-overlap value is obtained as output from the circuit 102. This value is sent as input to a comparator 103, more particularly to the + terminal of the comparator 103, the - terminal of which is connected to earth. The output from the comparator 103 is sent to two switching circuits 105, 106, 107 as trigger value for the switches 105, 106 and 107. Moreover, one of the inputs of the switch 105 receives the previous colour value R₂, which is also sent to a circuit 104 that fulfils a correction function, which will be described below. The circuit 104 also receives the B-overlap value.

The output from the correction circuit 104 is sent to the other input terminal of the switching circuit 105, which gives as output a value R_{OUT} for the red output value. The previous colour value R₂ is also sent to a second look-up table LUT2 102 which gives, as output, an offset value labelled Offset. This offset value Offset is sent to one input terminal of an adder 108, the other terminal of which receives a blue colour value B₁ so as to give, as output, a B+Offset colour value which is sent to one of the inputs of the switching circuit 106, the other input of which is connected to earth. A blue colour value labelled B₂ is obtained as output from the switching circuit 106.

Moreover, a green colour signal labelled G_{IN} is sent to a circuit 109 fulfilling a correction function, which receives the signal B-overlap as input. The output from the correction circuit 109 is sent to one of the inputs

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of a switching circuit 107, while the other input of the switching circuit 107 receives the colour value G_{IN} . The switching circuit 107 is controlled by the signal coming from the comparator 103 and gives a colour value
5 signal G_1 as output.

Figure 7 shows three circuits 100, 200, 300 identical to the circuit shown in Figure 6, making it possible to carry out the method described above in succession for
10 the colours red, F_R , blue, F_B , and green, F_G . As shown in Figure 7, the output B_2 and the output G_1 coming from the circuit 100 are sent to the circuit 200 and a red colour value R_{IN} is sent as input to the circuit 200. The circuit 200 makes it possible to obtain the blue
15 colour value B_{OUT} . The same applies in the case of the circuit 300, which receives as input the green colour value G_2 and the red colour value R_1 output by the circuit 200 and a blue colour value B_{IN} and which gives as output the green colour value G_{OUT} and the red colour
20 value R_2 and the blue colour value B_1 which are fed back into the circuit 100 carrying out the improvement function in the case of the red colour R_{OUT} .

The operation of the circuits in Figures 6 and 7 will
25 be explained below. Thus, the red colour value R_2 is sent to the table LUT1 100 which includes reference values depending on the response time of the material forming the display, the content of this table being explained below.

30 The overlap value is subtracted from the blue colour value B_1 so as to give B-overlap. If this value is greater than zero, the switching element 105 outputs the colour value R_2 onto R_{OUT} and the B+Offset value is added to the blue channel B_2 , the switch 106 being
35 positioned as shown in Figure 6. The green value G_1 as output is also equal to the input value G_{IN} , the switch 107 being positioned as shown in Figure 6. If the B-overlap value is less than zero, the switch 106

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switches to the earthed input and the blue value B_2 is set to zero. In this case, the switches 105 and 107 switch to their input connected to the correction function circuits 104 and 109, respectively, and the values of the outputs R_{OUT} and G_1 are reduced by an amount that maintains the original tint value, while reducing the luminance.

As will be explained below, the correction function consists of a block based on multipliers that reduce the red and green values, in the case of Figure 6, depending on the B-Overlap value.

In the embodiment in Figure 6, the overlap data and the offset data are obtained from two tables LUT1 101 and LUT2 102. However, these data could be calculated from one another by solving, for example, the system of two equations in two unknowns below:

$$\begin{aligned} S_{\text{overlap}}\% &= f(t_{\text{video}}) \\ S_{\text{offset}}\% &= g(t_{\text{video}}) \\ \Rightarrow S_{\text{offset}}\% &= g(f^{-1}(S_{\text{overlap}}\%)). \end{aligned}$$

As explained below, the Overlap and Offset values depend on the response time of the liquid crystal material and on the duration of the subframe.

An illustration of the values contained in the table LUT1 101 will now be given with reference to Figure 8. Figure 8 characterizes an example of a liquid crystal LC having linear rise and fall times in order to simplify the demonstration.

The label S_{offset} corresponds to a lack of luminance in the blue subframe labelled Blue, induced by the rise-time and fall-time characteristics of the liquid crystal. To correct this, it is necessary to add a time offset to the blue value. This offset is labelled t_{offset} .

S_{overlap} corresponds to the contamination of the green value with the blue value. Two cases may occur, as described above:

- the pixel colour is not saturated. In this case, the blue colour is not modified, nor is the green colour;

- the pixel colour must be saturated. In this case, the blue value must be reduced by a value corresponding to S_{overlap} = green value.

Consequently, the other two colour values must be reduced by the same value in order to maintain constant tint. This is the role of the correction functions in Figure 6. If S_{overlap} and S_{offset} are calculated as a function of the video signal of the preceding subframe, T_{video}, the rise and fall times, T_r and T_f and the subframe period T, the calculation results in:

$$S_{overlap} = \frac{1}{2}(t_{video} + T_f - T)^2 \cdot \frac{L_{max}}{T_f} \quad \text{if } t_{video} + T_f \geq T$$

$S_{overlap} \% = \frac{S_{overlap}}{S_{max}} = \frac{1}{2} \frac{(t_{video} + T_f - T)^2}{T T_f}$	if $t_{video} \geq T - T_f$
$S_{overlap} \% = 0$	if $t_{video} \leq T - T_f$

$S_{offset} \% = \frac{S_{offset}}{S_{max}} = \frac{1}{2} \frac{T_r (T - t_{video})^2}{T T_f^2}$	if $t_{video} \geq T - T_f$
$S_{offset} \% = \frac{S_{offset}}{S_{max}} = \frac{1}{2} \frac{T_r}{T}$	if $t_{video} \leq T - T_f$

S_{overlap} and S_{offset} are loaded into the tables LUT1 101 and LUT2 102. If the video signal is encoded over N bits, the percentage value must be multiplied by 2^N-1.

One way of carrying out the correction function, which may be implemented in the circuits 104 and 109 of

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Figure 6, will now be described with reference to Figures 9 and 10. The upper part of Figure 9 shows a theoretical video signal having a first pulse RV of duration equal to one subframe, a second, very short pulse BV during the next subframe and a third pulse GV of duration less than the duration of the third subframe. In this case, as regards luminance and as shown in part B in Figure 9, there is an overlap value coming from the first subframe, namely the Red subframe in the embodiment shown, with the second or Blue subframe. Since the value of the blue colour is very low, an error is observed which does not allow the tint to be maintained. This is shown by the dotted line T, which crosses the falling edge of the Red luminance pulse. The same applies to the colour green. In this case, a correction function must be active in order to maintain the tint. This correction function reduces the value of the preceding colour (namely red in the embodiment shown) in such a way that the overlap value is equal to the value desired for the colour blue. This is shown in Figure 10, in which it may be seen that the dotted line T crosses the falling edge when the blue value is approximately equal to zero. This correction function may be used with adders and multipliers, depending on the transfer below, taking as assumption the fact that the data is encoded over eight bits.

When B-Overlap < 0:

$$R_{out} = R_2 \times \left(1 - \frac{\text{Overlap} - B_1}{255} \right)$$

$$B_2 = 0$$

$$G_1 = G_{out} \times \left(1 - \frac{\text{Overlap} - B_1}{255} \right)$$

30

The same function can be applied to the other colours.

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It is obvious to a person skilled in the art that the above examples have been given merely as an illustration.

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CLAIMS

1. Method of improving the luminous efficiency of a sequential-colour matrix display, the display being driven using an addressing method of the pulse width modulation or PWM type, characterized, for each pixel of a subframe, by the following steps:
 - comparison of the pixel colour value of the preceding subframe with a reference value so as to provide an overlap value depending on the period of overlap with the current subframe;
 - if the pixel colour value of the current subframe less the overlap value gives a positive value, a time offset is to be added to the pixel colour value of the current subframe;
 - if the pixel colour value of the current subframe less the overlap value gives a negative value, the pixel colour value of the current subframe is forced to be zero.
2. Method according to Claim 1, characterized in that, if the pixel colour value of the current subframe less the overlap value gives a negative value, the pixel colour value of the preceding subframe and the colour value of the next subframe are modified so as to maintain the original tint, while at the same time reducing the luminance.
3. Method according to either of Claims 1 and 2, characterized in that the above steps apply in succession to each sequential colour of a frame.
4. Method according to one of Claims 1 to 3, characterized in that the pixel colour value of a subframe depends on the width of the PWM-type addressing pulse.

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5. Method according to one of Claims 1 to 4, characterized in that the reference value depends on the response time of the material forming the display.
- 5
6. Method according to one of Claims 1 to 5, characterized in that the time offset depends on the response time of the material forming the display and on the duration of the subframe.
- 10
7. Method according to either of Claims 5 and 6, characterized in that the reference value and the time offset are stored separately in two separate tables.
- 15
8. Method according to either of Claims 5 and 6, characterized in that the reference value and the time offset are calculated from each other.

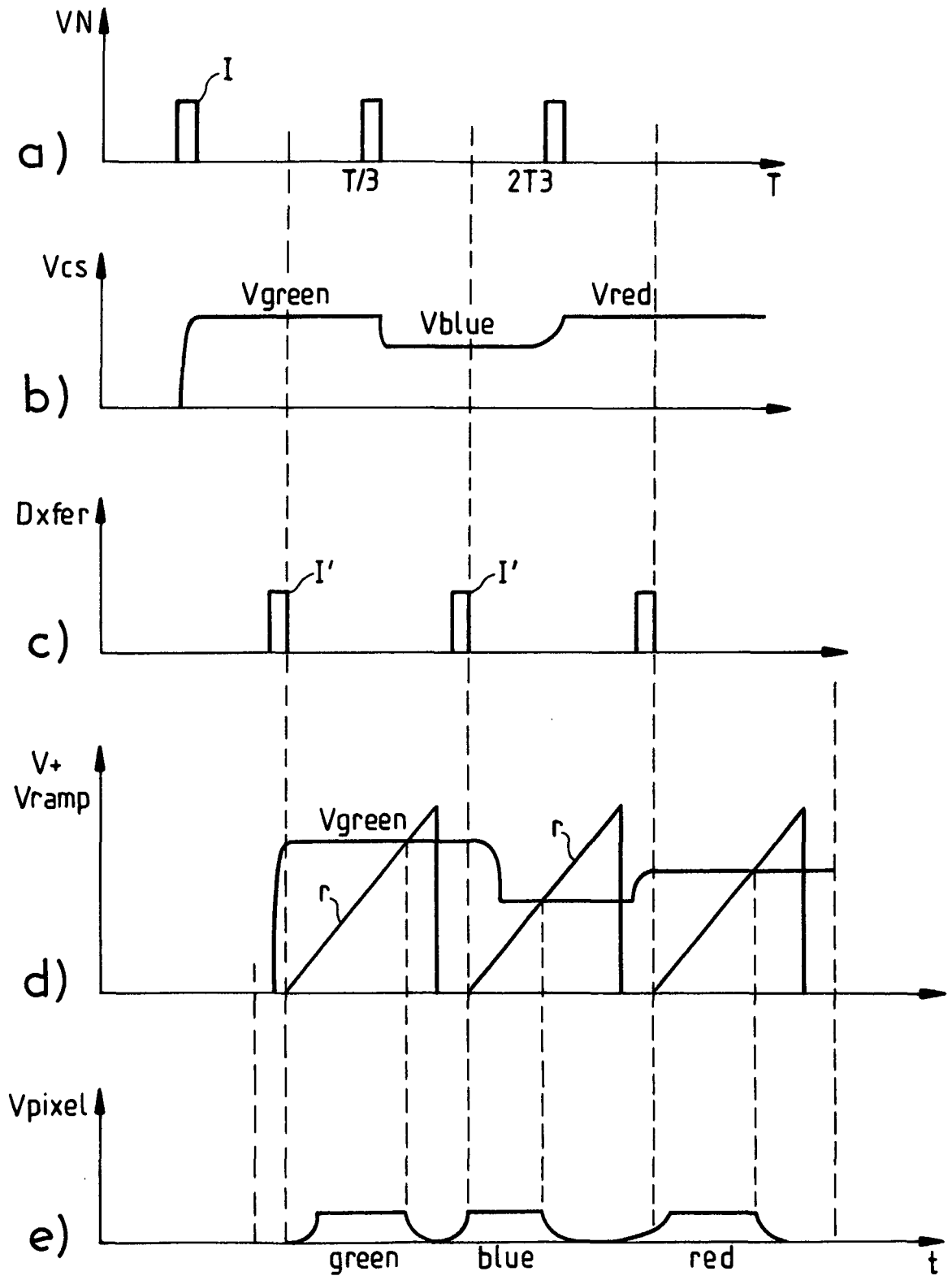


FIG.2

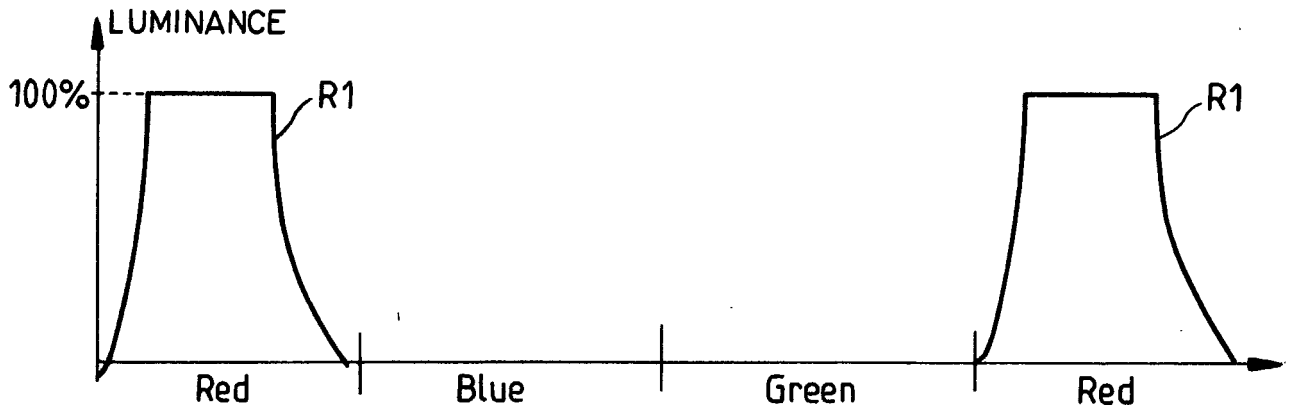


FIG.3a

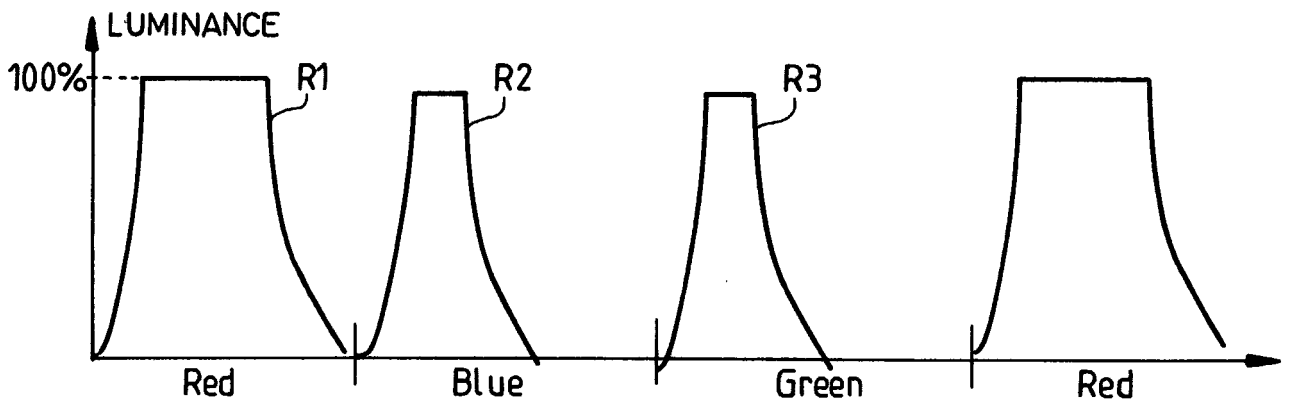


FIG.3b

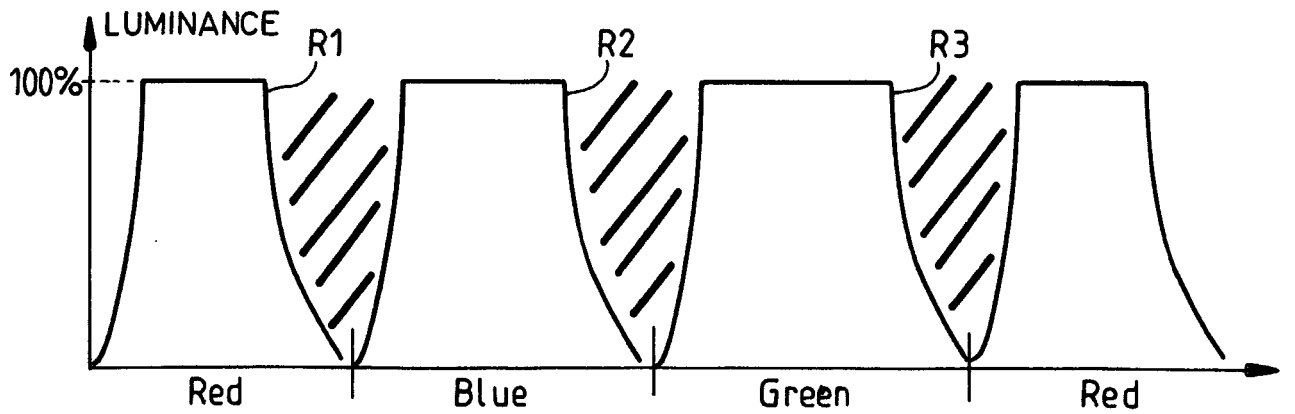


FIG.3c

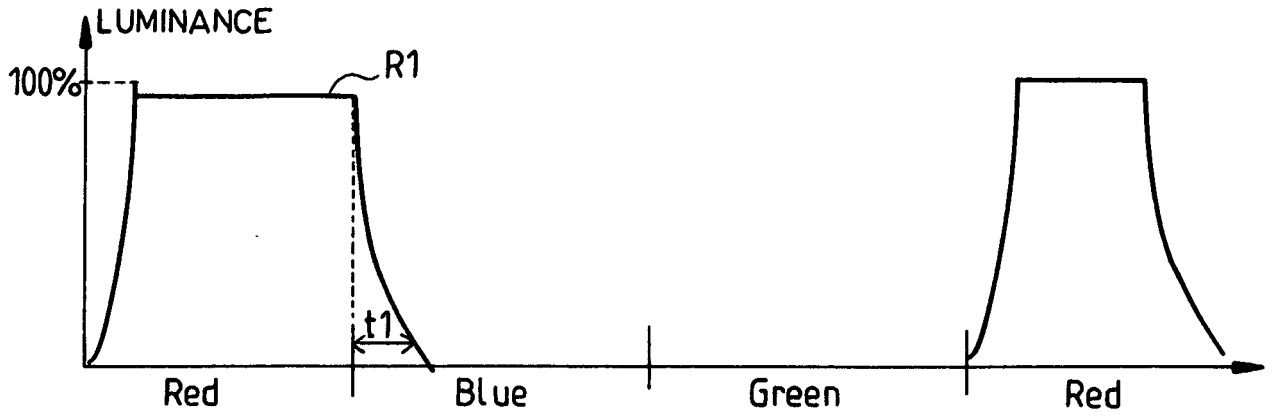


FIG. 4a

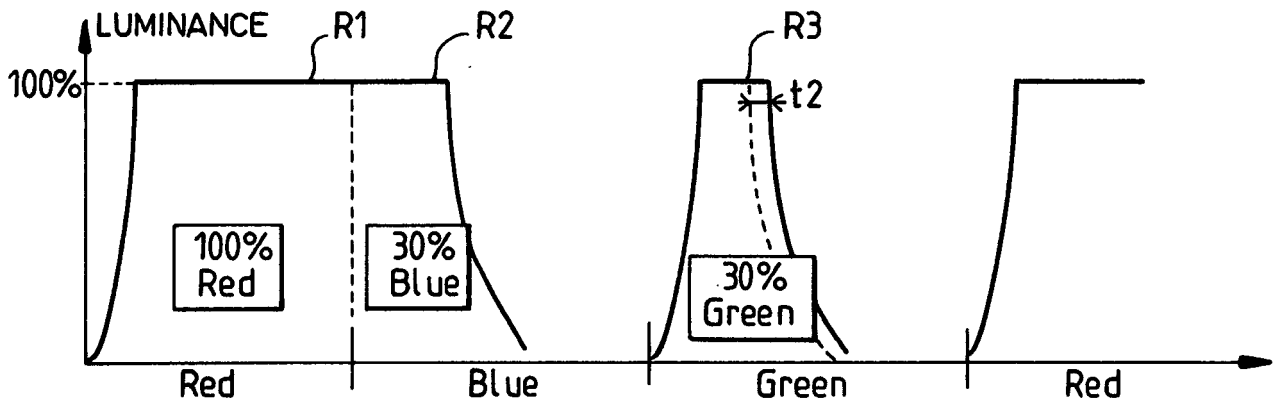


FIG. 4b

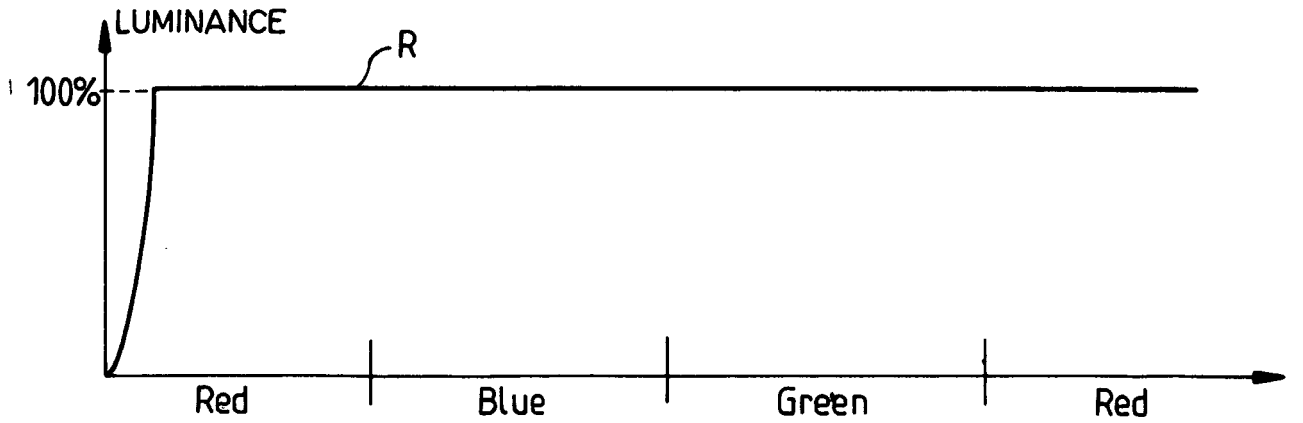


FIG. 4c

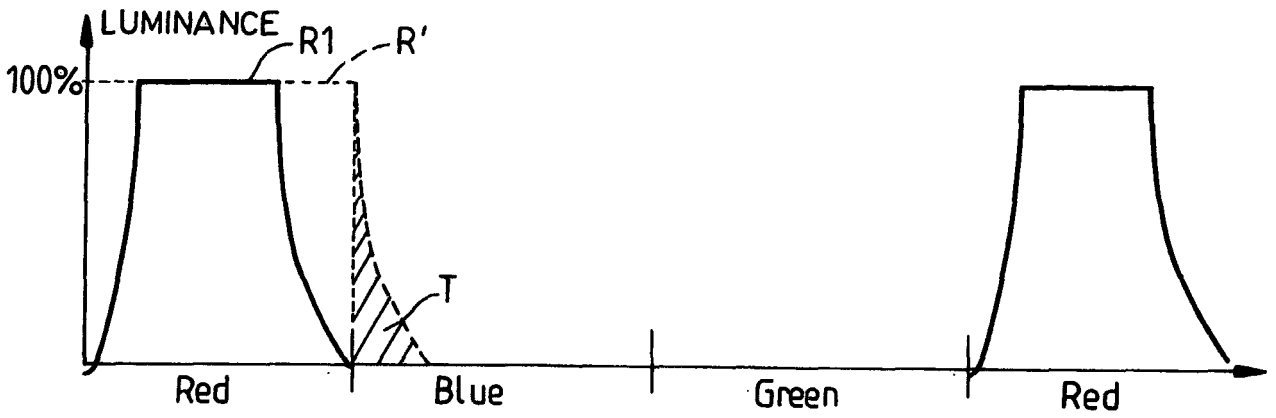


FIG.5a

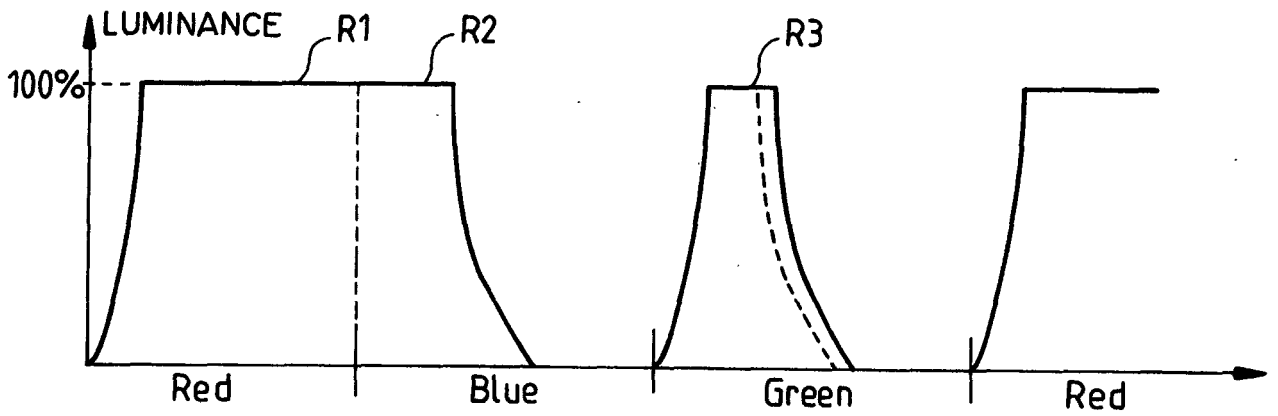


FIG.5b

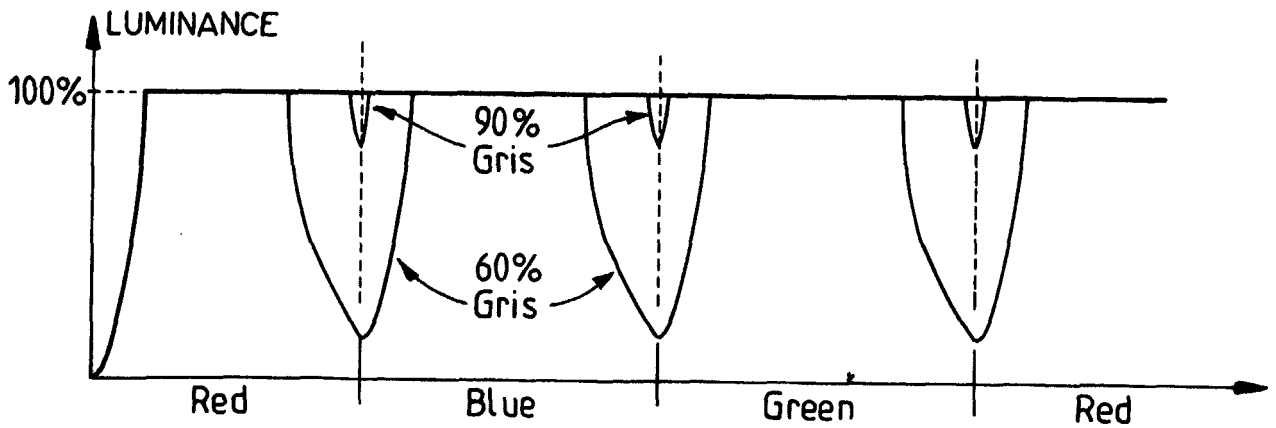


FIG.5c

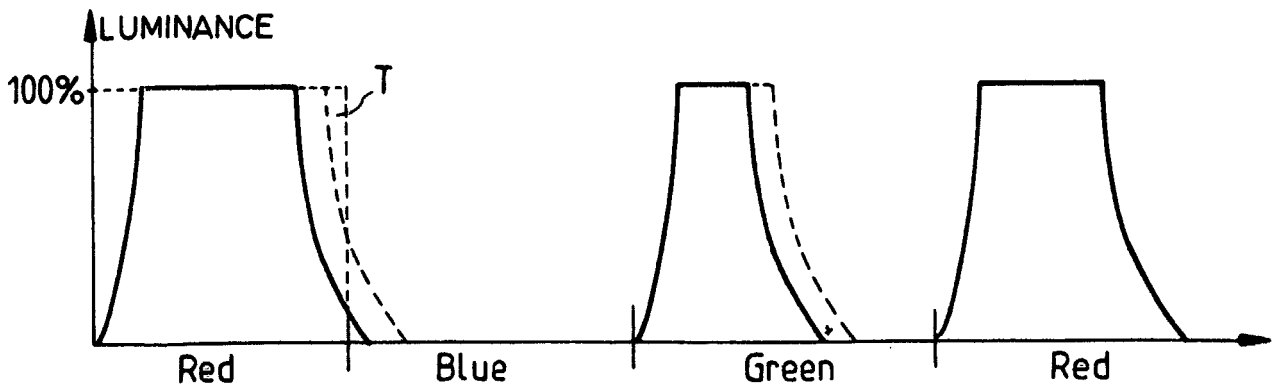
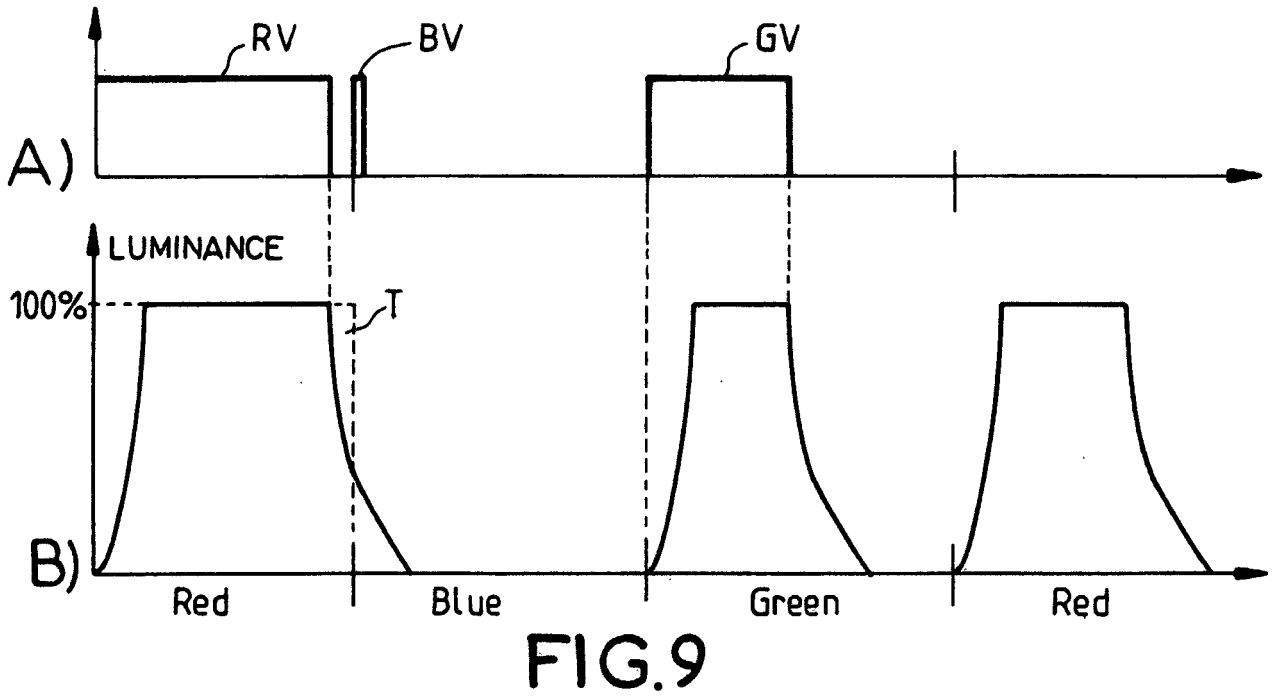
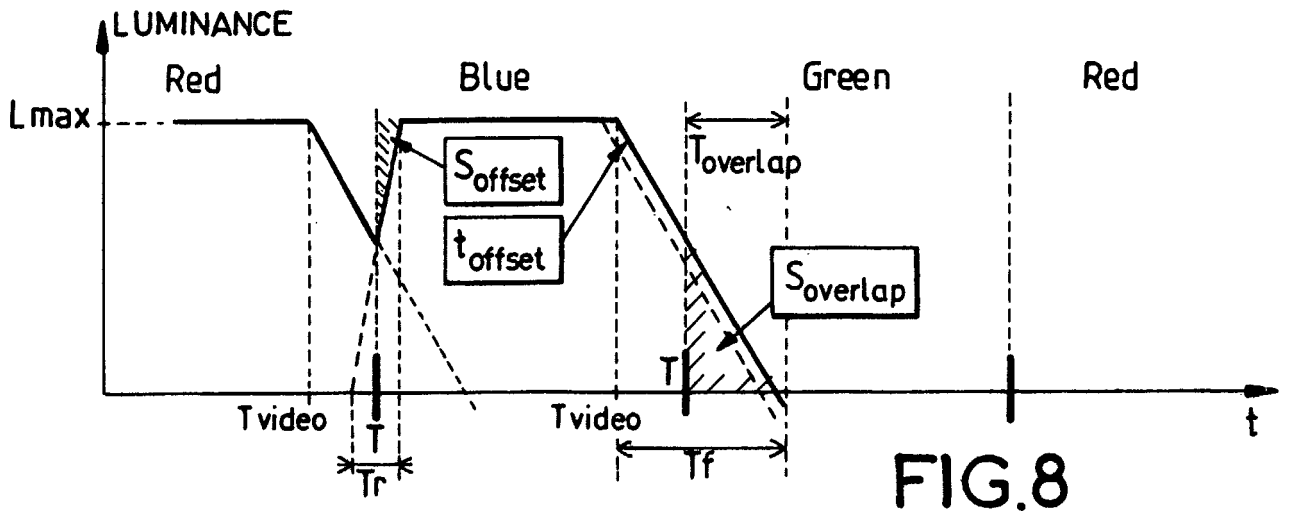


FIG.10

INTERNATIONAL SEARCH REPORT

International Application No
PCT/EP 02/12941

A. CLASSIFICATION OF SUBJECT MATTER IPC 7 G09G3/36				
According to International Patent Classification (IPC) or to both national classification and IPC				
B. FIELDS SEARCHED				
Minimum documentation searched (classification system followed by classification symbols) IPC 7 G09G				
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched				
Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal				
C. DOCUMENTS CONSIDERED TO BE RELEVANT				
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.		
A	US 5 461 397 A (FENG YU ET AL) 24 October 1995 (1995-10-24) abstract column 1, line 27 - line 31 column 4, line 15 - line 29; figure 2 column 6, line 60 -column 7, line 12; figures 1A,1B column 8, line 29 - line 48 --- -/--	1-8		
<table style="width: 100%; border: none;"> <tr> <td style="width: 50%; border: none;"> <input checked="" type="checkbox"/> Further documents are listed in the continuation of box C. </td> <td style="width: 50%; border: none;"> <input checked="" type="checkbox"/> Patent family members are listed in annex. </td> </tr> </table>			<input checked="" type="checkbox"/> Further documents are listed in the continuation of box C.	<input checked="" type="checkbox"/> Patent family members are listed in annex.
<input checked="" type="checkbox"/> Further documents are listed in the continuation of box C.	<input checked="" type="checkbox"/> Patent family members are listed in annex.			
* Special categories of cited documents :				
<table style="width: 100%; border: none;"> <tr> <td style="width: 50%; border: none; vertical-align: top;"> *A* document defining the general state of the art which is not considered to be of particular relevance *E* earlier document but published on or after the international filing date *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) *O* document referring to an oral disclosure, use, exhibition or other means *P* document published prior to the international filing date but later than the priority date claimed </td> <td style="width: 50%; border: none; vertical-align: top;"> *T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art. *&* document member of the same patent family </td> </tr> </table>			*A* document defining the general state of the art which is not considered to be of particular relevance *E* earlier document but published on or after the international filing date *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) *O* document referring to an oral disclosure, use, exhibition or other means *P* document published prior to the international filing date but later than the priority date claimed	*T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art. *&* document member of the same patent family
A document defining the general state of the art which is not considered to be of particular relevance *E* earlier document but published on or after the international filing date *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) *O* document referring to an oral disclosure, use, exhibition or other means *P* document published prior to the international filing date but later than the priority date claimed	*T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art. *&* document member of the same patent family			
Date of the actual completion of the international search <p style="text-align: center; font-size: 1.2em;">27 February 2003</p>		Date of mailing of the international search report <p style="text-align: center; font-size: 1.2em;">06/03/2003</p>		
Name and mailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl. Fax: (+31-70) 340-3016		Authorized officer <p style="text-align: center; font-size: 1.2em;">Corsi, F</p>		

INTERNATIONAL SEARCH REPORT

International Application No

PCT/EP 02/12941

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>US 5 767 828 A (MCKNIGHT DOUGLAS J) 16 June 1998 (1998-06-16) abstract column 1, line 42 -column 2, line 3 column 2, line 62 -column 3, line 5 column 5, line 19 - line 41; figures 1A,1B column 6, line 58 -column 7, line 14; figures 1C-1E column 7, line 43 -column 9, line 23; figures 2A-3D column 9, line 18 - line 23 column 10, line 5 - line 19 column 13, line 46 - line 63; figures 5,6A ---</p>	1-8
A	<p>US 4 907 862 A (SUNTOLA TUOMO S) 13 March 1990 (1990-03-13) abstract column 2, line 48 - line 53 column 3, line 1 -column 4, line 15 column 6, line 6 - line 20; figures 1A,1B column 7, line 34 - line 65; figures 3A,3B -----</p>	1-8

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/EP 02/12941

Patent document cited in search report	Publication date	Patent family member(s)	Publication date	
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专利名称(译)	提高顺序彩色矩阵显示器的发光效率的方法		
公开(公告)号	EP1449194A1	公开(公告)日	2004-08-25
申请号	EP2002785397	申请日	2002-11-19
[标]申请(专利权)人(译)	汤姆森特许公司		
申请(专利权)人(译)	汤姆森许可S.A.		
当前申请(专利权)人(译)	汤姆森许可		
[标]发明人	BOREL THIERRY DOYEN DIDIER		
发明人	BOREL, THIERRY DOYEN, DIDIER		
IPC分类号	G02F1/133 G09G3/20 G09G3/36		
CPC分类号	G09G3/3611 G09G3/2014 G09G2310/0235		
优先权	2001015425 2001-11-29 FR		
其他公开文献	EP1449194B1		
外部链接	Espacenet		

摘要(译)

本发明涉及一种提高顺序彩色矩阵显示器的发光效率的方法，该显示器使用脉冲宽度调制或PWM型的寻址方法驱动。对于子帧的每个像素，该方法包括以下步骤：- 将前一子帧的像素颜色值与参考值进行比较，以便根据与当前子帧的重叠周期提供重叠值；- 如果当前子帧的像素颜色值减去重叠值给出正值，时间偏移量加到当前子帧的像素颜色值；- 如果当前子帧的像素颜色值减去重叠值给出负值，当前子帧的像素颜色值被强制为零。本发明适用于LCOS或LCD显示器。