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(54) **3D PANEL AND PREPARATION METHOD THEREFOR, AND 3D DISPLAY DEVICE PROVIDED WITH 3D PANEL**

(57) An embodiment of the present invention provides a 3D panel. The 3D panel includes a first substrate, a second substrate, and liquid crystals configured to be filled between the first substrate and the second substrate, wherein both the first substrate and the second substrate are provided with a common electrode and a signal electrode. Since the substrates at two sides of the 3D panel of the present invention have electrodes which

can be independently controlled, the switching of a grating electrode function between the upper and lower substrates can be achieved by switching the signal to the two substrates, thereby improving the application range of the 3D panel and the compatibility and versatility of the 3D panel. In addition, the embodiments of the present invention also disclose a method for producing the 3D panel and a 3D display apparatus having the 3D panel.

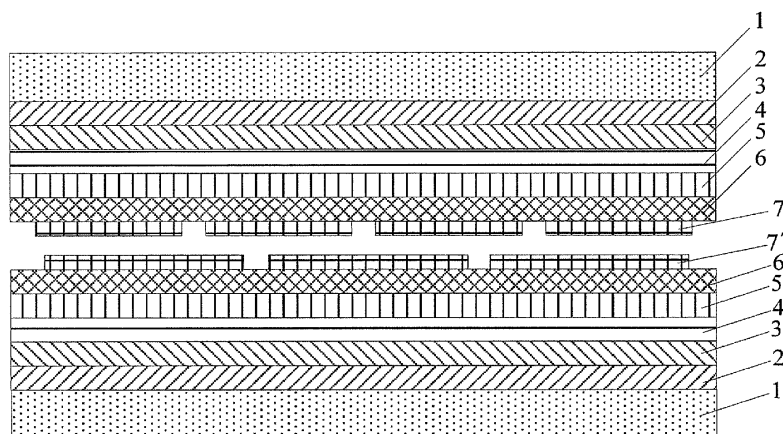


Fig. 1

Description

CROSS-REFERENCE TO RELATED APPLICATION

[0001] This application claims the benefit of Chinese Patent Application No.201410520459.4 filed on September 30, 2014 entitled "3D Panel, Method for Producing the Same, And 3D Display Apparatus Having the Same" with the State Intellectual Property Office of China, the whole disclosure of which is incorporated herein by reference.

BACKGROUND OF THE INVENTION

Field of the Invention

[0002] The present disclosure relates to a technical field of 3D display, in particular, to an active grating 3D panel, a method for producing the same, and a 3D display apparatus having the 3D panel.

Description of the Related Art

[0003] Stereoscopic display (i.e., 3D display) has become a trend of display field. A basic principle of the stereoscopic display is to generate stereoscopic effect by parallax. In other words, a left eye of one person can only see a left eye picture and a right eye thereof only can see a right eye picture, wherein the left eye picture and the right eye picture constitute a pair of stereopicture images having parallax from each other.

[0004] One method for achieving the 3D display is to employ a serial display. Specifically, at a first time point, a display device displays the left eye picture, and at this time only the left eye of a viewer can see the displayed picture. At a second time point, the display device displays the right eye picture and only the right eye of the viewer can see the displayed picture. The viewer will feel the left and right eyes see the left and right eye pictures at the same time, due to persistence of images onto a eye retinas thereof, thereby producing a stereoscopic vision.

[0005] Another method for achieving the 3D display is to employ a parallel display. Specifically, at the same time point, a part of pixels on the display device display contents of the left eye picture, and the other part of pixels display contents of the right eye picture. Gratings, polaroid glasses or the like are used to enable the right eye to see the display content of a part of the pixels and the left eye to see the display content of the other part of the pixels, thereby producing a stereoscopic vision.

[0006] As for the 3D display mode in which 3D spectacles are needed, it gradually cannot meet users' requirements to the 3D display, because the 3D spectacles are easy to be lost and damaged, and are not convenient to be used when too many viewers are present, and the stereoscopic images cannot be presented only through the 3D display devices. For a naked eye 3D display mode,

for example, grating type of naked eye 3D display apparatus, because the grating is directly provided into the display panel without needing 3D spectacles, it is more convenient for viewing and thus it gets more and more concerns.

SUMMARY OF THE INVENTION

[0007] An embodiment of the present invention provides a 3D panel for the naked eye 3D display as described above. Specifically, embodiments of the present invention provide a 3D panel, a method for producing the same and a 3D display apparatus comprising the 3D panel. It makes full use of space at two sides of the 3D display panel, and gratings having two periods are manufactured on an upper substrate and a lower substrate of the 3D panel. The switching of physical viewpoint number of 3D display in the same panel can be achieved by switching signals, and at the same time it can also achieve compatibility of the same 3D panel with two different resolutions, that is, the purpose that two 3D displays can share the same 3D panel can be achieved, and finally the economic benefits of reducing development costs can be obtained.

[0008] In accordance with one aspect of the present invention, it provides a 3D panel, comprising:

a first substrate;
a second substrate; and
liquid crystals, configured to be filled between the first substrate and the second substrate, wherein both the first substrate and the second substrate are provided thereon with a common electrode and a signal electrode.

[0009] In one example, the first substrate comprises, in sequence from top to bottom, a first base substrate, a first common electrode, a first common electrode lead, a first insulation layer, a first signal electrode lead, a second insulation layer and a first signal electrode; and the second substrate comprises, in sequence from bottom to top, a second base substrate, a second common electrode, a second common electrode lead, a third insulation layer, a second signal electrode lead, a fourth insulation layer and a second signal electrode, wherein the first signal electrode is arranged to face the second signal electrode.

[0010] In one example, both the first signal electrode and the second signal electrode are a plurality of stripped electrodes, and an arrangement direction of the stripped electrodes in the first signal electrode is interlaced with that of the stripped electrodes in the second signal electrode.

[0011] In one example, the plurality of stripped electrodes constituting the first signal electrode are evenly arranged and have a first period; the plurality of stripped electrodes constituting the second signal electrode are evenly arranged and have a

second period which is different from the first period.

[0012] In one example, spacing between the plurality of stripped electrodes in the first signal electrode is larger than or equals to 10 micrometers; and spacing between the plurality of stripped electrodes in the second signal electrode is larger than or equals to 10 micrometers.

[0013] In one example, the first common electrode lead and the first signal electrode lead are in the form of frame, and a signal access part is provided at one side of the first base substrate; and

the second common electrode lead and the second signal electrode lead are in the form of frame, and a signal access part is provided at one side of the second base substrate.

[0014] In one example, all of the first common electrode, the first signal electrode, the second common electrode and the second signal electrode are made of transparent conductive materials.

[0015] In one example, the first and second common electrodes are planar electrodes.

[0016] In one example, the 3D panel further comprises a signal control module, configured to enable the signal electrode lead of the corresponding substrate when the first or second substrate is used as a working electrode.

[0017] In one example, in a case that the first substrate is served as the working electrode, the first common electrode lead is opened, the first signal electrode lead is accessed with a signal, the second common electrode lead and the second signal electrode lead are accessed with a common electrode signal, and the first substrate functions as a grating;

in a case that the second substrate is served as the working electrode, the second common electrode lead is opened, the second signal electrode lead is accessed with a signal, the first common electrode lead and the first signal electrode lead are accessed with a common electrode signal, and the second substrate functions as a grating.

[0018] In accordance with another aspect of the present invention, it also provides a 3D display apparatus, comprising the 3D panel as described above.

[0019] In accordance with a further aspect of the present invention, it also provides a method for producing a 3D panel, comprising the steps of:

forming a first common electrode layer on a first base substrate, which is used as a first common electrode of pixels;

forming a first conductive layer on the first common electrode, and patterning the first conductive layer to form a first common electrode lead;

forming a first insulation layer on the first common electrode lead;

forming a second conductive layer on the first insulation layer and patterning the second conductive layer to form a first signal electrode lead;

forming a second insulation layer on the first signal

electrode lead, and forming vias onto the second insulation layer, which expose a partial region of the first signal electrode lead;

forming a first signal electrode layer on the second insulation layer and patterning the first signal electrode layer to form a first signal electrode, which is electrically connected with the first signal electrode lead by means of the vias, so as to obtain a first substrate;

obtaining a second substrate by repeating the same steps as those for producing the first substrate;

attaching edges of the first and second substrates together, so that the first signal electrode of the first substrate faces the second signal electrode of the second substrate, and liquid crystals are filled between the first substrate and the second substrate, thereby forming the 3D panel.

[0020] By producing single electrodes onto substrates at two sides of the 3D panel, the present invention achieves the switching of viewpoint number in terms of physical level, so that the viewpoint number can be adjusted. In addition, the electrodes on substrates at two sides of the 3D panel in the present invention can be controlled independently. The switching of a grating electrode function between the upper and lower substrates can be achieved by switching the signals to the two substrates, thereby improving the application range of the 3D panel and the compatibility and versatility of the 3D panel.

BRIEF DESCRIPTION OF THE DRAWINGS

[0021]

Fig. 1 is a schematic sectional view showing a structure of 3D panel in accordance with an embodiment of the present invention;

Fig. 2 is a schematic sectional view showing a structure of 3D panel in accordance with an embodiment of the present invention;

Figs. 3A-3F' are flow charts showing a producing process of the 3D panel in accordance with an embodiment of the present invention; and

Fig. 4 is a sectional view of the produced 3D panel in accordance with an embodiment of the present invention.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS OF THE INVENTION

[0022] In order that objects, solutions and advantages of the present disclosure will become more apparent and explicit, the embodiments will be described hereinafter in detail with reference to the accompanied drawings.

[0023] In accordance with one aspect of the present invention, it provides a 3D panel, for example, for a naked eye 3D display. As shown in Fig. 1, the 3D panel includes

a first substrate, a second substrate, and liquid crystals filled between the first substrate and the second substrate. Both the first substrate and the second substrate are provided with a common electrode and a signal electrode.

[0024] In an embodiment of the present invention, the first substrate includes, in sequence from top to bottom, a first base substrate 1, a first common electrode 2, a first common electrode lead 3, a first insulation layer 4, a first signal electrode lead 5, a second insulation layer 6 and a first signal electrode 7. The first base substrate 1 can be made from glasses, silicon wafer, quartz, plastics or the similar materials.

[0025] The first common electrode 2 and the first signal electrode 7 are made from a transparent conductive material, which may be a transparent metal film, a transparent metallic oxide film, a non-metallic oxide film, ferroelectric materials dispersed with conductive particles or the like. The film may be in the form of a single layer, two layers, a plurality of layers or multiple layers, non-doping type, doping type, or multi-elements type. Preferably, the transparent conductive material is a metallic oxide film, for example an ITO film.

[0026] The first common electrode lead 3 and the first signal electrode lead 5 are made of a conductive material. Preferably, such conductive material is metal material.

[0027] The first insulation layer 4 is used to insulate the first common electrode lead 3 from the first signal electrode lead 5.

[0028] A plurality of vias are formed on the second insulation layer 6, so as to partially insulate the first signal electrode lead 5 from the first signal electrode 7. The vias can expose a partial region of the first signal electrode lead 5. The first signal electrode 7 is electrically connected with the first signal electrode lead 5 by means of the vias.

[0029] The first insulation layer 4 and the second insulation layer 6 are made of a transparent insulation material.

[0030] In an example of the present invention, the first common electrode lead 3 and the first signal electrode lead 5 are in the form of frame. A signal access part is provided at one side of the first base substrate 1. It should be understood by those skilled in the art that the signal access part of the first common electrode lead 3 and the signal access part of the first signal electrode lead 5 are positioned close to each other (as shown in Fig. 3D), in order to facilitate accessing of signal. This cannot be interpreted as limiting to the present invention.

[0031] Further, the first common electrode lead 3 is located at an outer periphery of the first base substrate 1 (as shown in Fig. 3B), and the first signal electrode lead 5 is located at an inner side of the first common electrode lead 3 (as shown in Fig. 3D).

[0032] Of course, the first common electrode lead 3 and the first signal electrode lead 5 may be in other forms, as long as they can be electrically connected with the first common electrode 2 and the first signal electrode 7,

respectively.

[0033] In one example of the present invention, the first common electrode 2 can be a planar electrode, and the first signal electrode 7 can be composed of a plurality of stripped electrodes.

[0034] In one example of the present invention, the first signal electrode 7 is composed of a plurality of stripped electrodes, each of which is electrically connected with the first signal electrode lead 5 by the respective vias. Further, the plurality of stripped electrodes are placed to be inclined. In one preferable example, the plurality of stripped electrodes of the first signal electrode 7 are evenly arranged and have a first period.

[0035] In an embodiment of the present invention, the second substrate includes, in sequence from bottom to top, a second base substrate 1', a second common electrode 2', a second common electrode lead 3', a third insulation layer 4', a second signal electrode lead 5', a fourth insulation layer 6' and a second signal electrode 7'. The second base substrate 1' can be made from glasses, silicon wafer, quartz, plastics or the similar materials.

[0036] The second common electrode 2' and the second signal electrode 7' are made from a transparent conductive material, which may be a transparent metal film, a transparent metallic oxide film, non-metallic oxide film, or ferroelectric materials dispersed with conductive particles. The film may be in the form of a single layer, two layers, a plurality of layers or multiple layers, non-doping type, doping type, or multi-elements type. Preferably, the transparent conductive material is a metallic oxide film, for example ITO film.

[0037] The second common electrode lead 3' and the second signal electrode lead 5' are made of a conductive material. Preferably, such conductive material is metal material.

[0038] The third insulation layer 4' is used to insulate the second common electrode lead 3' from the second signal electrode lead 5'.

[0039] A plurality of vias are formed on the fourth insulation layer 6', so as to partially insulate the second signal electrode lead 5' from the second signal electrode 7'. The vias can expose a partial region of the second signal electrode lead 5'. The second signal electrode 7' is electrically connected with the second signal electrode lead 5' by means of the vias.

[0040] The third insulation layer 4' and the fourth insulation layer 6' are made of a transparent insulation material.

[0041] In an example of the present invention, the second common electrode lead 3' and the second signal electrode lead 5' are in the form of frame. A signal access part is provided at one side of the second base substrate 1'. It should be understood by those skilled in the art that the signal access part of the second common electrode lead 3' and the signal access part of the second signal electrode lead 5' are positioned close to each other (as shown in Fig. 3D'), in order to facilitate accessing of signal. This cannot be interpreted as limiting to the present

invention.

[0042] Further, the second common electrode lead 3' is located at an outer periphery of the second base substrate 1' (as shown in Fig. 3B'), and the second signal electrode lead 5' is located at an inner side of the second common electrode lead 3' (as shown in Fig. 3D').

[0043] Of course, the second common electrode lead 3' and the second signal electrode lead 5' may be in other forms, as long as they can be electrically connected with the second common electrode 2' and the second signal electrode 7', respectively.

[0044] In one example of the present invention, the second common electrode 2' can be a planar electrode, and the second signal electrode 7' can be composed of a plurality of striped electrodes.

[0045] In one example of the present invention, the second signal electrode 7' is composed of a plurality of striped electrodes, each of which is electrically connected with the second signal electrode lead 5' by the respective vias. Further, the plurality of striped electrodes are placed to be inclined. In one preferable example, the plurality of striped electrodes of the second signal electrode 7' are evenly arranged and have a second period which is different from the first period.

[0046] The first signal electrode 7 is disposed to face the second signal electrode 7'.

[0047] In one example of the present invention, the striped electrodes of the first signal electrode 7 and those of the second signal electrode 7' are arranged to interlace with each other. For example, as shown in Figs. 3F and 3F', the striped electrodes of the first signal electrode 7 and those of the second signal electrode 7' are respectively arranged to be inclined, and they are inclined in different directions. That is, the striped electrodes of the first signal electrode 7 are not parallel to those of the second signal electrode 7'.

[0048] Further, the first substrate and the second substrate have the same structure, and they are attached together in such way that the signal electrodes thereof face each other. In this way, the 3D panel is symmetrical with respect to an axis which is a center line between the first substrate and the second substrate. In the present example, because the first and second substrate are identical in terms of structure, when producing the first substrate and the second substrate, for example, producing the common electrode lead, the signal electrode lead and other identical pattern designs, the same mask can be used. Due to the above-mentioned shared designs of the mask, it is possible to reduce the producing cost of the 3D panel.

[0049] In the above examples, an enabling time of inputting signals to the first signal electrode 7 is different from that of inputting signals to the second signal electrode 7', for example, the time of inputting signals to the first signal electrode 7 and that of inputting signals to the second signal electrode 7' being stagger. In this way, switching the viewpoint number of the 3D panel in terms of physical level can be achieved by switching signals of

the first and second substrates. Also, two kinds of liquid crystal panels having different resolutions can be incorporated into one and the same 3D panel, that is, to achieve the purpose of two kinds of 3D display devices sharing the same 3D panel, and finally to obtain the economic benefits of reducing the development costs.

[0050] In one example of the present invention, the 3D panel also includes a signal control module, configured to enable the signal electrode lead of the corresponding substrate when the viewpoints of the first or second substrate are switched on, i.e., the signal electrode lead of the corresponding substrate is accessed with the signal (the signal is inputted into the signal electrode lead of the corresponding substrate). Specifically, when the viewpoints of the first substrate are switched on, that is, the first substrate is served as a working electrode, the first common electrode lead 3 is opened, the first signal electrode lead 5 is accessed with the signal, the second common electrode lead 3' and the second signal electrode lead 5' are accessed with a common electrode signal. At this time, the first substrate functions as a grating. When the viewpoints of the second substrate are switched on, that is, the second substrate is served as a working electrode, the second common electrode lead 3' is opened, the second signal electrode lead 5' is accessed with the signal, the first common electrode lead 3 and the first signal electrode lead 5 are accessed with a common electrode signal. At this time, the second substrate functions as a grating. As such, the viewpoint number of the 3D panel can be adjusted as actually required. In addition, since the electrodes on the substrates at two sides of the 3D panel according to the present invention can be controlled independently, the switching of the grating electrode function between the first and second substrates (i.e., an upper substrate and a lower substrate) can be achieved by switching the signals to the two substrates, thereby improving the application range of the 3D panel and the compatibility and versatility of the 3D panel.

[0051] In accordance with another aspect of the present invention, it also provides a 3D display panel, which includes the above 3D panel, an insulating glass 8 and a LCD panel 9, as shown in Fig. 2.

[0052] In accordance with a further aspect of the present invention, it also provides a 3D display apparatus, including the 3D panel as described above.

[0053] In accordance with a further aspect of the present invention, it also provides a producing method for the 3D panel, for example, as shown in Figs. 3A-3F', including the following steps.

[0054] A first common electrode layer is formed on a first base substrate 1, serving as a first common electrode 2 (Vcom) of pixels, wherein the first common electrode 2 is a planar electrode, as shown in Fig. 3A.

[0055] Preferably, the first base substrate 1 can be made from glasses, silicon wafer, quartz, plastics or the similar materials.

[0056] The first common electrode layer is made from a transparent conductive material, and thus is not shown

in Fig. 3. The transparent conductive material may be a transparent metal film, a transparent metallic oxide film, a non-metallic oxide film, or ferroelectric materials dispersed with conductive particles. The film may be in the form of a single layer, two layers, a plurality of layers or multiple layers, non-doping type, doping type, or multi-elements type. Preferably, the transparent conductive material is a metallic oxide film, for example an ITO film.

[0057] A first conductive layer is formed on the first common electrode 2, and a first common electrode lead 3 is formed by a patterning process, as shown in Fig. 3B. The first conductive layer is made of a conductive material, and preferably the conductive material is metal material. Furthermore, the first conductive layer can be formed by a deposition process, and the first common electrode lead 3 can be formed by the patterning process such as exposing, etching or the like.

[0058] In one example of the present invention, the first common electrode lead 3 is in the form of frame. A signal access part is provided at one side of the first base substrate 1.

[0059] Of course, the first common electrode lead 3 can also be in other forms, as long as it can be electrically connected with the first common electrode 2.

[0060] A first insulation layer 4 is formed on the first common electrode lead 3, to insulate the first common electrode lead 3 from the first signal electrode lead 5, as shown in Fig. 3C. Because the first insulation layer 4 is made of a transparent insulation material, it is not shown in Fig. 3C. The first insulation layer 4 can be formed by a process, such as depositing.

[0061] A second conductive layer is formed on the first insulation layer 4, and a first signal electrode lead 5 is formed by a patterning process, as shown in Fig. 3D. The second conductive layer is made of a conductive material. Preferably, the conductive material is metal material. Furthermore, the second conductive layer can be formed by a deposition process, and the first signal electrode lead 5 can be formed by the patterning process such as exposing, etching or the like.

[0062] In an example of the present invention, the first signal electrode lead 5 is also in the form of frame. Also, a signal access part of the first signal electrode lead 5 is provided at the same side where the signal access part of the first common electrode lead 3 is located. It should be understood by those skilled in the art that the signal access part of the first common electrode lead 3 and the signal access part of the first signal electrode lead 5 are positioned close to each other, in order to facilitate accessing of signal. This cannot be interpreted as limiting to the present invention.

[0063] Similarly, the first signal electrode lead 5 may also be in other forms, as long as it can be electrically connected with the first signal electrode 7.

[0064] A second insulation layer 6 is formed on the first signal electrode lead 5, and a plurality of vias are formed on the second insulation layer 6, so as to partially insulate the first signal electrode lead 5 from the first signal elec-

trode 7. The vias can expose a partial region of the first signal electrode lead 5, as shown in Fig. 3E. Because the second insulation layer 6 is made of a transparent insulation material, it is not shown in Fig. 3E. The second insulation layer 6 can be formed by a process, such as depositing; and the vias can be formed by a process such as etching.

[0065] A first signal electrode layer is formed on the second insulation layer 6, and a first signal electrode 7 can be formed by a patterning process. The first signal electrode 7 is electrically connected with the first signal electrode lead 5 through vias. In this way, the first substrate is obtained as shown in Fig. 3F.

[0066] Specifically, the first signal electrode 7 is made from a transparent conductive material, which may be a transparent metal film, a transparent metallic oxide film, a non-metallic oxide film, or ferroelectric materials dispersed with conductive particles. The film may be in the form of a single layer, two layers, a plurality of layers or multiple layers, non-doping type, doping type, or multi-elements type. Preferably, the transparent conductive material is a metallic oxide film, for example an ITO film.

[0067] In one example of the present invention, the first signal electrode 7 is composed of a plurality of stripped electrodes, each of which is electrically connected with the first signal electrode lead 5 by the respective vias. Further, the plurality of stripped electrodes are placed to be inclined. In one preferable example, the plurality of stripped electrodes of the first signal electrode 7 are evenly arranged and have a first period.

[0068] The second substrate can be obtained by repeating the above steps, as shown in Figs. 3A', 3B', 3C', 3D', 3E', and 3F'. The difference is that when producing the second signal electrode 7' of the second substrate, it should be ensured that the arrangement direction of the plurality of stripped electrodes within the second signal electrode 7' are interlaced with that of the plurality of stripped electrodes within the first signal electrode 7. For example, as shown in Figs. 3F and 3F', the inclination directions of the plurality of stripped electrodes within the first signal electrode 7 and the plurality of stripped electrode within the second signal electrode 7' are different from each other. In one preferable example, the plurality of stripped electrodes of the second signal electrode 7' are evenly arranged and have a second period which is different from the first period. In addition, the spacing between the plurality of stripped electrodes within the first signal electrode 7 is larger or equals to 10 micrometers, and the spacing between the plurality of stripped electrodes within the second signal electrode 7' is larger or equals to 10 micrometers.

[0069] The first substrate and the second substrate are attached together, and liquid crystals are filled between the first substrate and the second substrate, so as to form the 3D panel.

[0070] Further, the first substrate and the second substrate have the same structure, and they are attached together in such way that the signal electrodes thereof

face each other. In this way, the 3D panel is symmetrical with respect to an axis which is a center line between the first substrate and the second substrate. In the present example, because the first and second substrate are identical in terms of structure, when the second common electrode lead 3' and the second signal electrode lead 5' as well as other identical pattern designs are formed on the second substrate, the same mask as used for producing the first substrate can be used. Due to the above shared designs of the mask, it is possible to reduce the producing cost of the 3D panel.

[0071] In one example of the present invention, the first substrate is attached to the second substrate by the necessary materials such as TN mode liquid crystals, sphere spacers, and polarizers. Fig. 4 is a sectional view showing the 3D display panel obtained in accordance with one embodiment of the present invention. As shown in Fig. 4, the common electrode leads and signal electrode leads of the first and second substrates are respectively connected to a flexible printed circuit (FPC) board by anisotropic conductive adhesives; and the two dark regions between the first and second substrates are sealant.

[0072] Currently, an active type grating 3D display panel is conventionally designed with only one periodic grating, and thus it is compatible with only one viewpoint number display of a liquid crystal panel. In this way, it is not convenient for utilization by the user and production by the manufacturers. With the 3D panel produced by the above technical solutions in accordance with the present invention, an upper substrate and a lower substrate thereof (i.e., the first and second substrate) are formed with the common electrode and the signal electrode thereon, but the signal electrodes of the first and second substrates have enabling times which are staggered. The times when the signal electrodes of the first substrate and the second substrate are accessed with the signals are staggered. In this way, switching the viewpoint number of the 3D panel in terms of physical level can be achieved by switching signals between the first and second substrates. Also, two kinds of liquid crystal panels having different resolutions can be incorporated into one 3D panel, that is, to achieve the purpose of two kinds of 3D display devices sharing the same 3D panel, and finally to obtain the economic benefits of reducing the development costs.

[0073] With the 3D panel produced by the above technical solutions, the signal electrode lead of the corresponding substrate is enabled when the viewpoints of the first or second substrate are switched on, i.e., the signal electrode lead of the corresponding substrate is accessed with the signal. Specifically, when the viewpoints of the first substrate are switched on, that is, the first substrate is served as a working electrode, the first common electrode lead is opened, the first signal electrode lead is accessed with the signal, the second common electrode lead and the second signal electrode lead are accessed with a common electrode signal. At this time, the first substrate functions as a grating. When the

viewpoints of the second substrate are switched on, that is, the second substrate is served as a working electrode, the second common electrode lead is opened, the second signal electrode lead is accessed with the signal, the first common electrode lead and the first signal electrode lead are accessed with a common electrode signal. At this time, the second substrate functions as a grating. As such, the viewpoint number of the 3D panel can be adjusted as actually required. In addition, since the electrodes on the substrates at two sides of the 3D panel according to the present invention can be controlled independently, the switching of the grating electrode function between the first and second substrates (i.e., the upper and lower substrates) can be achieved by switching the signals to the two substrates, thereby improving the application range of the 3D panel and the compatibility and versatility of the 3D panel.

[0074] The objects, solutions and advantageous effects of the present disclosure have been described in details with reference to the above specific embodiments. It should be understood that the above embodiments are given only by ways of examples instead of limiting the present invention. Any changes, equivalent replacement, modification within the spirit and principles of the disclosure can be made by those skilled in the art and should fall into the scope of the present invention.

Claims

1. A 3D panel, comprising:

a first substrate;
a second substrate; and
liquid crystals filled between the first substrate and the second substrate,
wherein both the first substrate and the second substrate are provided with a common electrode and a signal electrode.

2. The 3D panel according to claim 1, wherein the first substrate comprises, in sequence from top to bottom, a first base substrate, a first common electrode, a first common electrode lead, a first insulation layer, a first signal electrode lead, a second insulation layer and a first signal electrode; and the second substrate comprises, in sequence from bottom to top, a second base substrate, a second common electrode, a second common electrode lead, a third insulation layer, a second signal electrode lead, a fourth insulation layer and a second signal electrode, wherein the first signal electrode is arranged to face the second signal electrode.

3. The 3D panel according to claim 2, wherein both the first signal electrode and the second signal electrode are composed of a plurality of stripped

electrodes, and an arrangement direction of the stripped electrodes in the first signal electrode is interlaced with that of the stripped electrodes in the second signal electrode.

4. The 3D panel according to claim 3, wherein the plurality of stripped electrodes in the first signal electrode are evenly arranged and have a first period; the plurality of stripped electrodes in the second signal electrode are evenly arranged and have a second period which is different from the first period. 5
5. The 3D panel according to claim 3 or 4, wherein spacing between the plurality of stripped electrodes in the first signal electrode is larger than or equals to 10 micrometers; and spacing between the plurality of stripped electrodes in the second signal electrode is larger than or equals to 10 micrometers. 10
6. The 3D panel according to claim 2, wherein the first common electrode lead and the first signal electrode lead are in the form of frame, and a signal access part is provided at one side of the first base substrate; and the second common electrode lead and the second signal electrode lead are in the form of frame, and a signal access part is provided at one side of the second base substrate. 15
7. The 3D panel according to claim 2, wherein all of the first common electrode, the first signal electrode, the second common electrode and the second signal electrode are made of transparent conductive materials. 20
8. The 3D panel according to claim 2, wherein the first and second common electrodes are planar electrodes. 25
9. The 3D panel according to claim 1, further comprising a signal control module, configured to enable the signal electrode lead of the corresponding substrate when the first or second substrate is used as a working electrode. 30
10. The 3D panel according to claim 9, wherein in a case that the first substrate is served as the working electrode, the first common electrode lead is opened, the first signal electrode lead is accessed with a signal, the second common electrode lead and the second signal electrode lead are accessed with a common electrode signal, and the first substrate functions as a grating; 35
- in a case that the second substrate is served as the working electrode, the second common electrode lead is opened, the second signal electrode lead is 40

accessed with a signal, the first common electrode lead and the first signal electrode lead are accessed with a common electrode signal, and the second substrate functions as a grating.

11. A 3D display apparatus, comprising the 3D panel according to any one of claims 1-10. 5
12. A method for producing a 3D panel, comprising the steps of: 10
 - forming a first common electrode layer on a first base substrate, which is used as a first common electrode of pixels;
 - forming a first conductive layer on the first common electrode, and patterning the first conductive layer to form a first common electrode lead;
 - forming a first insulation layer on the first common electrode lead;
 - forming a second conductive layer on the first insulation layer and patterning the second conductive layer to form a first signal electrode lead;
 - forming a second insulation layer on the first signal electrode lead, and forming vias onto the second insulation layer, the vias exposing a partial region of the first signal electrode lead;
 - forming a first signal electrode layer on the second insulation layer and patterning the first signal electrode layer to form a first signal electrode, which is electrically connected with the first signal electrode lead by means of vias, so as to obtain a first substrate;
 - obtaining a second substrate by repeating the same steps as above-mentioned for producing the first substrate;
 - attaching edges of the first and second substrates together, so that the first signal electrode of the first substrate faces the second signal electrode of the second substrate, and liquid crystals are filled between the first substrate and the second substrate, thereby forming the 3D panel.
13. The method according to claim 12, wherein both the first signal electrode and the second signal electrode are composed of a plurality of stripped electrodes, and the first signal electrode and the second signal electrode are disposed so that an arrangement direction of the stripped electrodes in the first signal electrode is interlaced with that of the stripped electrodes in the second signal electrode. 45
14. The method according to claim 13, wherein the plurality of stripped electrodes in the first signal electrode are evenly arranged and have a first period; the plurality of stripped electrodes in the second signal electrode are evenly arranged and have a second 50

period which is different from the first period.

15. The method according to claim 12, wherein
all of the first common electrode, the first signal elec- 5
trode, the second common electrode and the second
signal electrode are made of transparent conductive
materials.
16. The method according to claim 12, wherein 10
the first common electrode in the first substrate and
the second common electrode in the second sub-
strate are planar electrodes.
17. The method according to claim 12, wherein 15
the signal electrode lead of the corresponding sub-
strate is enabled when the first or second substrate
is used as a working electrode.

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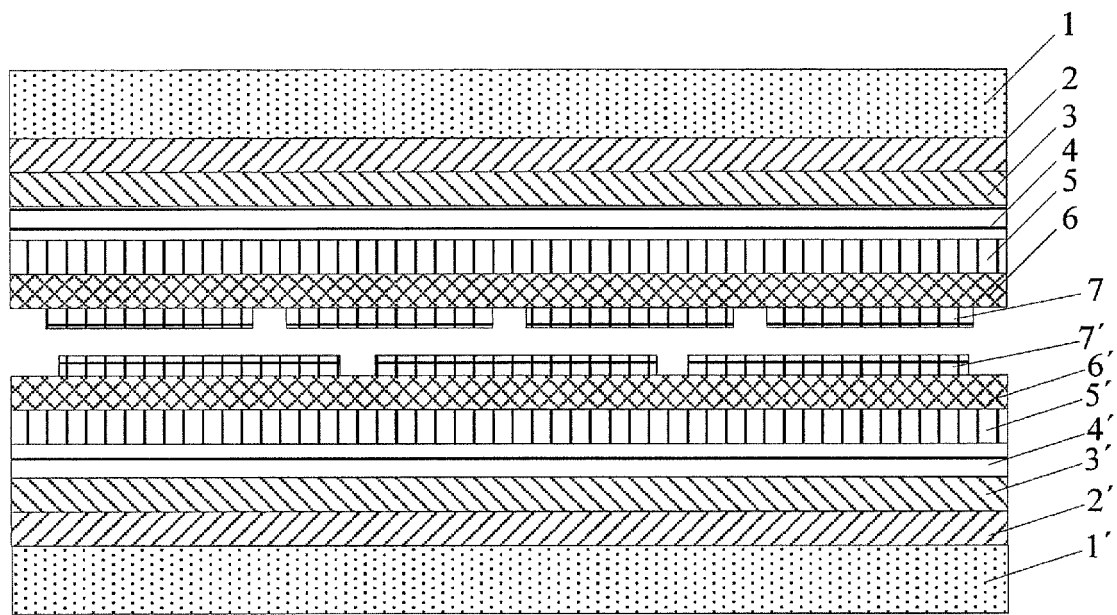


Fig. 1

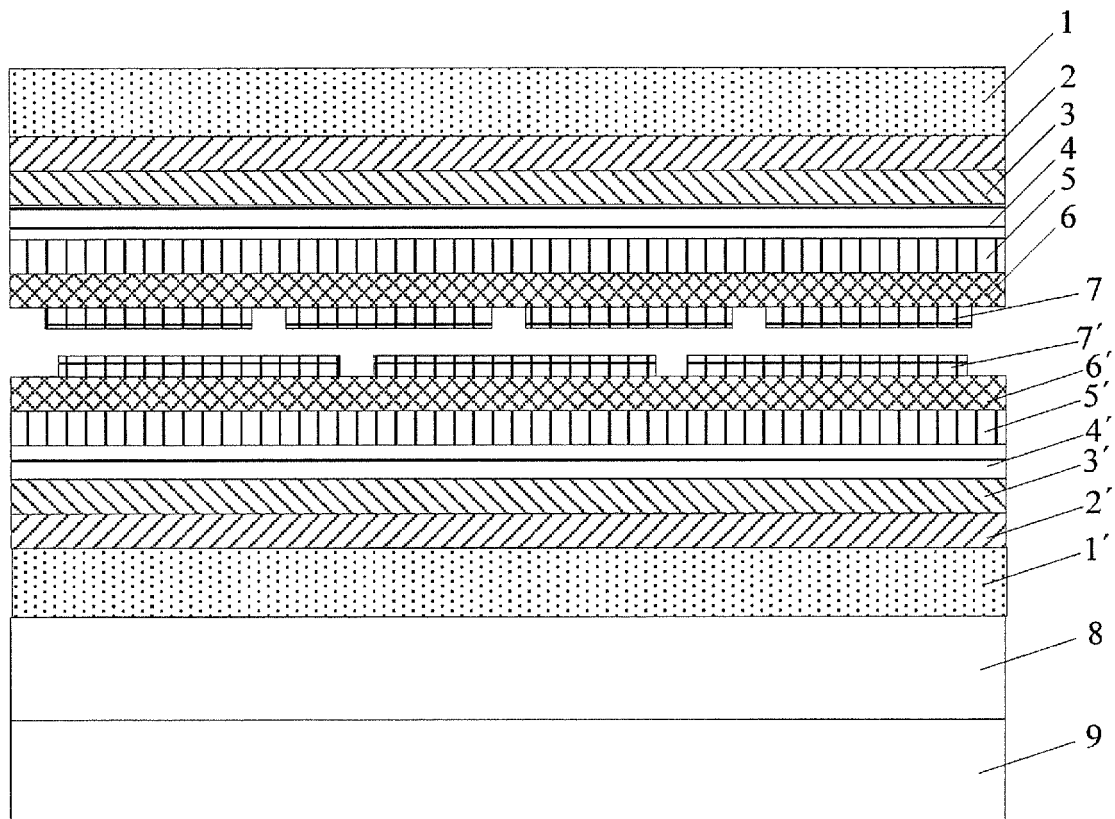


Fig. 2

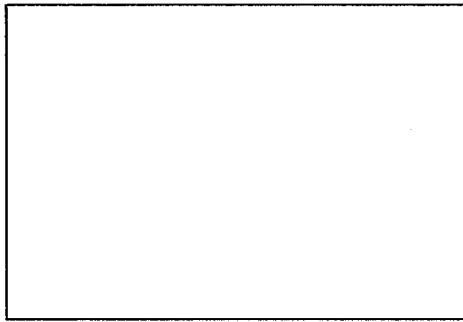


Fig. 3A

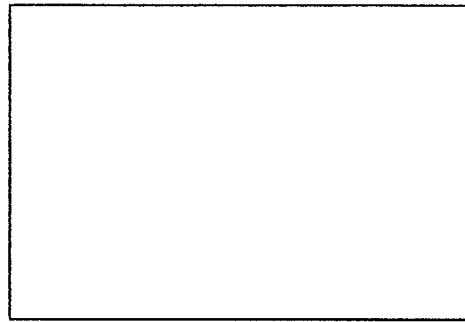


Fig. 3A'

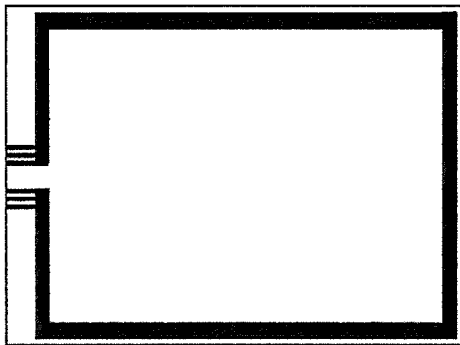


Fig. 3B

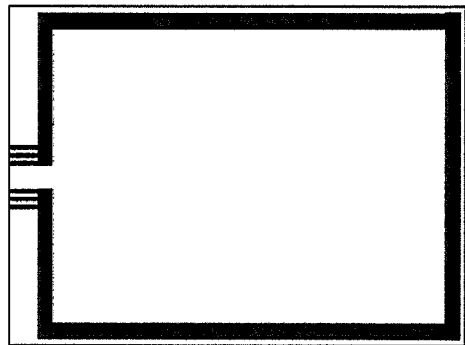


Fig. 3B'

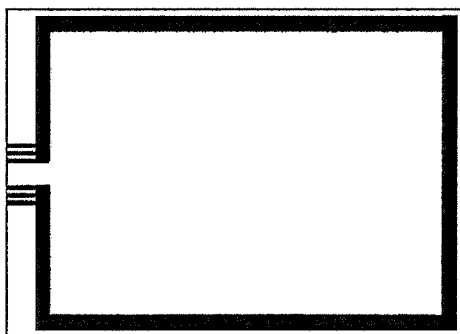


Fig. 3C

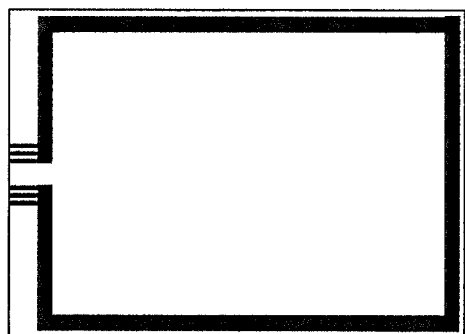


Fig. 3C'

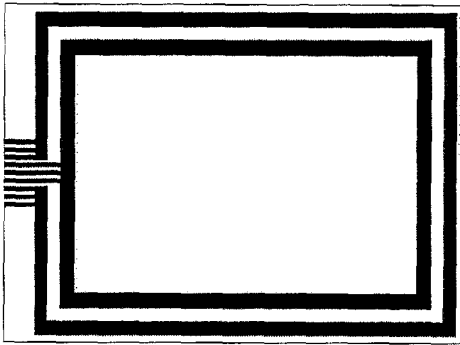


Fig. 3D

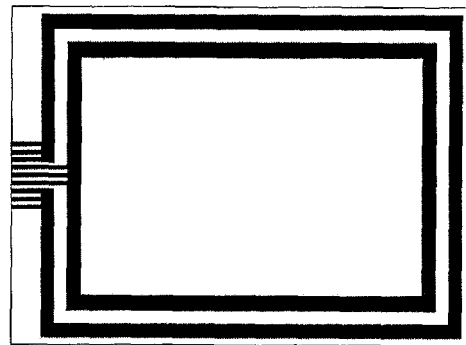


Fig. 3D'

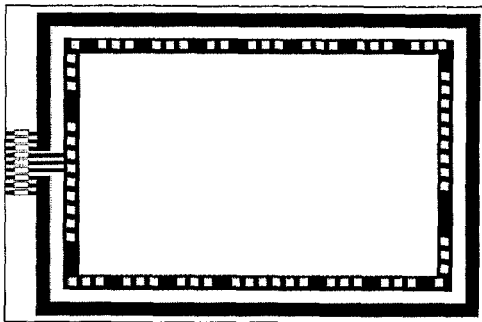


Fig. 3E

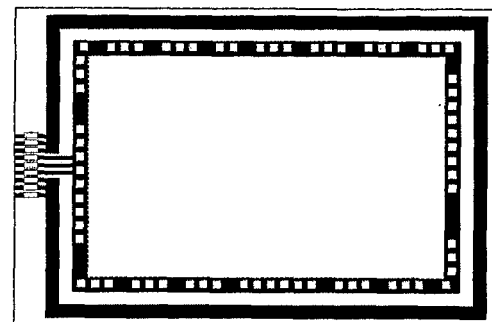


Fig. 3E'

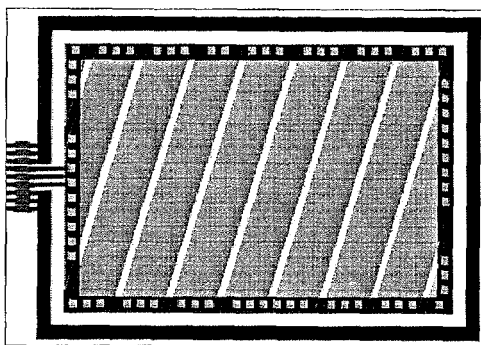


Fig. 3F

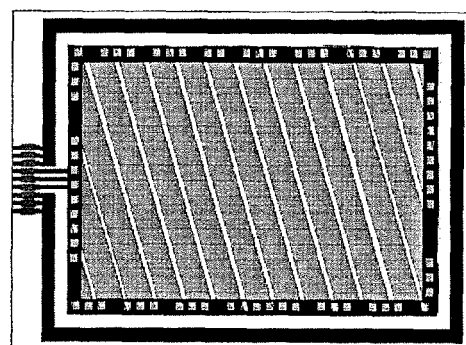


Fig. 3F'

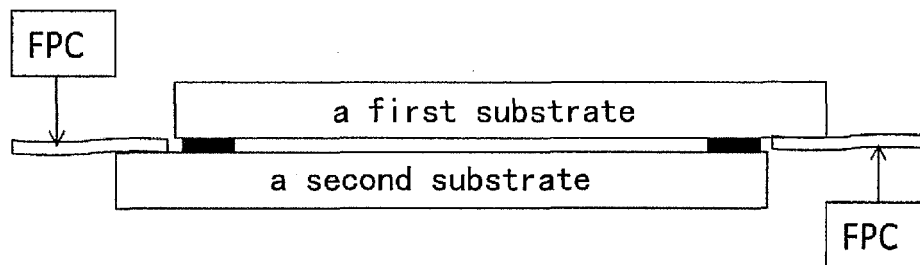


Fig. 4

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2015/071968

A. CLASSIFICATION OF SUBJECT MATTER

G02F 1/1343 (2006.01) i; G02B 27/22 (2006.01) i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G02F.; G02B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

CNPAT; CNKI; EPODOC; WPI: panel or display, common electrode, signal electrode or data electrode or pixel electrode or display electrode, insulating layer or insulating film, second or another or contraposition or relative, width or cycle or interval or distance or size, different or differ, "3D" or (three 2w dimension?) or three-dimension?, (liquid 2w crystal?) or LCD?, substrate?, common+, signal? or pixel? or data? or digital?, electrode?, both+ or all or two

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
PX	CN 104298019 A (BOE TECHNOLOGY GROUP CO., LTD. et al.), 21 January 2015 (21.01.2015), description, paragraphs [0006]-[0093], and claims 1-13	1-17
A	CN 103984107 A (BOE TECHNOLOGY GROUP CO., LTD. et al.), 13 August 2014 (13.08.2014), description, paragraphs [0045]-[0052] and [0069]-[0081], and figures 3 and 7-11	1-17
A	CN 102819153 A (BOE TECHNOLOGY GROUP CO., LTD.), 12 December 2012 (12.12.2012), description, paragraphs [0028]-[0038], and figures 1 and 2	1-17
A	CN 103383510 A (BOE TECHNOLOGY GROUP CO., LTD. et al.), 06 November 2013 (06.11.2013), the whole document	1-17

☒ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

* Special categories of cited documents:	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"A" document defining the general state of the art which is not considered to be of particular relevance	
"E" earlier application or patent but published on or after the international filing date	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"O" document referring to an oral disclosure, use, exhibition or other means	"&" document member of the same patent family
"P" document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search 07 June 2015 (07.06.2015)	Date of mailing of the international search report 12 June 2015 (12.06.2015)
Name and mailing address of the ISA/CN: State Intellectual Property Office of the P. R. China No. 6, Xitucheng Road, Jimenqiao Haidian District, Beijing 100088, China Facsimile No.: (86-10) 62019451	Authorized officer ZHONG, Jie Telephone No.: (86-10) 82245109

INTERNATIONAL SEARCH REPORT

International application No.
PCT/CN2015/071968

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C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2013278847 A1 (SAMSUNG DISPLAY CO., LTD.), 24 October 2013 (24.10.2013), the whole document	1-17
A	JP H08313938 A (HITACHI LTD.), 29 November 1996 (29.11.1996), the whole document	1-17

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Form PCT/ISA/210 (continuation of second sheet) (July 2009)

INTERNATIONAL SEARCH REPORT
 Information on patent family members

International application No.

PCT/CN2015/071968

Patent Documents referred in the Report	Publication Date	Patent Family	Publication Date
CN 104298019 A	21 January 2015	None	
CN 103984107 A	13 August 2014	None	
CN 102819153 A	12 December 2012	JP 2014038326 A	27 February 2014
		EP 2696239 A2	12 February 2014
		US 2014043570 A1	13 February 2014
		EP 2696239 A3	26 February 2014
		KR 20140021490 A	20 February 2014
		CN 102819153 B	11 March 2015
		EP 2813887 A3	14 January 2015
		EP 2813887 A2	17 December 2014
CN 103383510 A	06 November 2013	WO 2015003457 A1	15 January 2015
US 2013278847 A1	24 October 2013	KR 20130117295 A	25 October 2013
JP H08313938 A	29 November 1996	None	

Form PCT/ISA/210 (patent family annex) (July 2009)

REFERENCES CITED IN THE DESCRIPTION

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Patent documents cited in the description

- CN 201410520459 [0001]

专利名称(译)	3D面板及其制备方法，以及具有3D面板的3D显示装置		
公开(公告)号	EP3203312A1	公开(公告)日	2017-08-09
申请号	EP2015749962	申请日	2015-01-30
[标]申请(专利权)人(译)	京东方科技集团股份有限公司 北京京东方显示技术有限公司		
申请(专利权)人(译)	京东方科技集团股份有限公司. 北京京东方显示技术有限公司.		
当前申请(专利权)人(译)	京东方科技集团股份有限公司. 北京京东方显示技术有限公司.		
[标]发明人	ZHANG ZHENYU HUANG CHANGGANG LIAO YANPING		
发明人	ZHANG, ZHENYU HUANG, CHANGGANG LIAO, YANPING		
IPC分类号	G02F1/1343 G02B27/22		
CPC分类号	G02B30/27 G02F1/134309 G02B30/00 G02F1/1345 G02F1/1343 G02F1/133345 G02F1/1341 G02F1/13439		
代理机构(译)	POTTER CLARKSON LLP		
优先权	201410520459.4 2014-09-30 CN		
其他公开文献	EP3203312A4 EP3203312B1		
外部链接	Espacenet		

摘要(译)

本发明的实施例提供了一种3D面板。3D面板包括第一基板，第二基板和配置为填充在第一基板和第二基板之间的液晶，其中第一基板和第二基板都设置有公共电极和信号电极。由于本发明的3D面板两侧的基板具有可以独立控制的电极，因此可以通过将信号切换到两个基板来实现上下基板之间的光栅电极功能的切换，从而改善了3D面板的应用范围以及3D面板的兼容性和多功能性。此外，本发明的实施例还公开了一种用于制造3D面板的方法和具有该3D面板的3D显示设备。

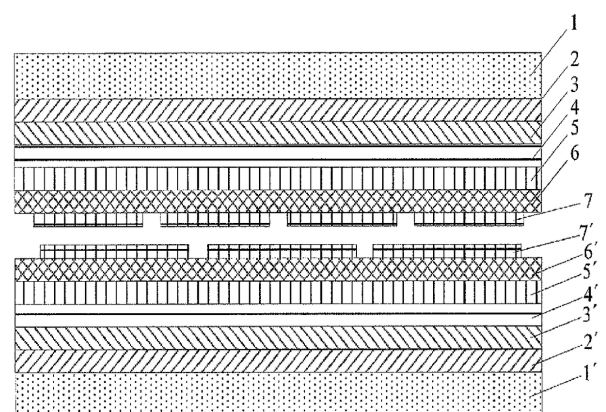


Fig. 1