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(54) **Blur reduction in liquid crystal displays by frame rate control**

(57) Reducing fast motion artifacts in an LCD panel by receiving a video stream at a first frame rate which is then downsampled to a second frame rate. The downsampled video stream is then upsampled to a third frame

rate and a voltage is applied to a pixel element such that the pixel element transitions from a first pixel value to a predetermined second pixel value within a period of time consistent with the third frame rate.

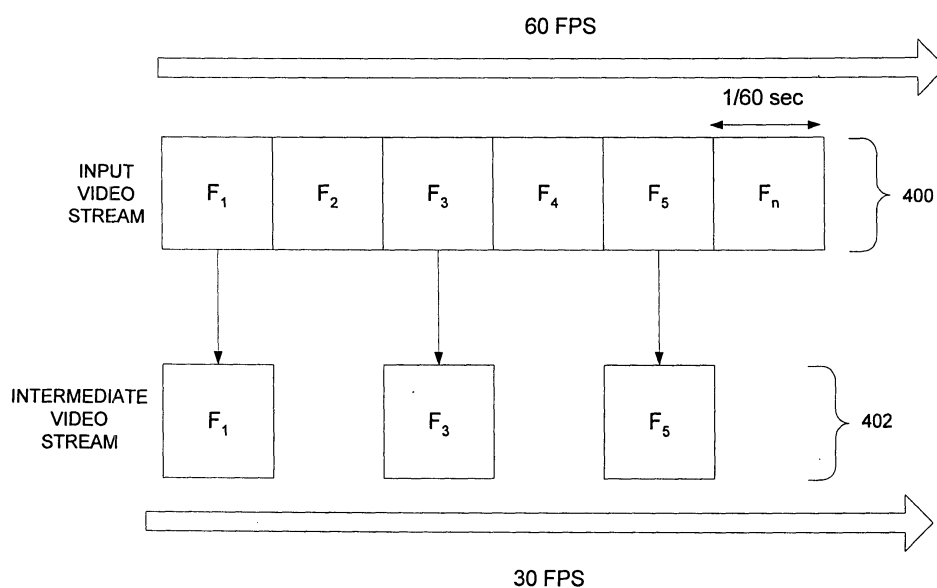


Fig. 4A

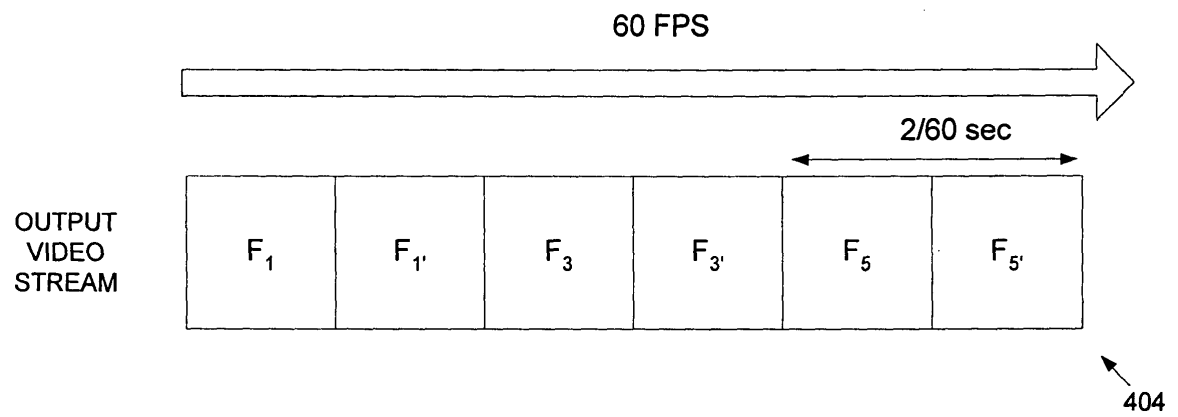


FIG. 4B



DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	US 2002/015104 A1 (ITOH GOH [JP] ET AL) 7 February 2002 (2002-02-07) * paragraphs [0005], [0006], [0051] - [0104], [0122] - [0128]; figures 1,3,5,7,12,21,22 *	1-16	INV. G09G3/36
A	US 2004/041745 A1 (CHEN LI-YI [TW]) 4 March 2004 (2004-03-04) * paragraphs [0021] - [0026]; figures 3,4a *	1	
A	US 6 008 790 A (SHINGU TOSHIKI [JP] ET AL) 28 December 1999 (1999-12-28) * column 3, line 5 - column 4, line 30; figures 2-4 *	1	
			TECHNICAL FIELDS SEARCHED (IPC)
			G09G
The present search report has been drawn up for all claims			
Place of search		Date of completion of the search	Examiner
Munich		25 June 2008	Harke, Michael
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 05 25 3483

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
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25-06-2008

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专利名称(译)	通过帧速率控制减少液晶显示器的数量		
公开(公告)号	EP1607934A3	公开(公告)日	2008-08-06
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[标]申请(专利权)人(译)	创世纪微芯片公司		
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其他公开文献	EP1607934A2		
外部链接	Espacenet		

摘要(译)

通过以第一帧速率接收视频流来减少LCD面板中的快速运动伪影，然后将其下采样到第二帧速率。然后将下采样的视频流上采样到第三帧速率，并且将电压施加到像素元素，使得像素元素在与第三帧速率一致的时间段内从第一像素值转变到预定的第二像素值。

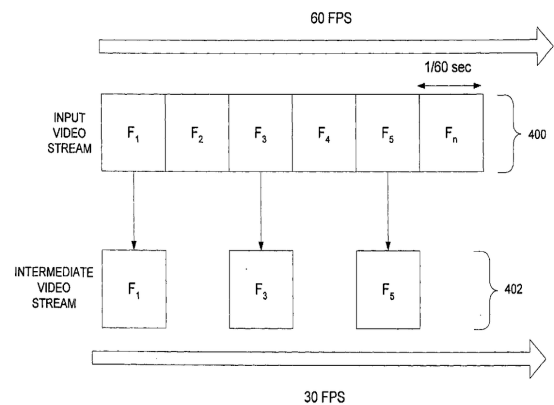


Fig. 4A