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(54) LIQUID CRYSTAL DRIVE APPARATUS AND GRADATION DISPLAY METHOD

(57) A voltage with a predetermined pattern is applied to liquid crystals to drive the liquid crystals during a unit drive period of the liquid crystals and an application pattern according to the gradation data is set taking into account a value obtained by integrating the amount of transmitted light of liquid crystals at various points in

time when each application pattern is applied to the liquid crystals. This allows a fine gradation display even if the liquid crystals are driven by only ON/OFF of a maximum rated voltage. As a result, it is possible to drive the liquid crystals at high speed and produce a multi-gradation display.

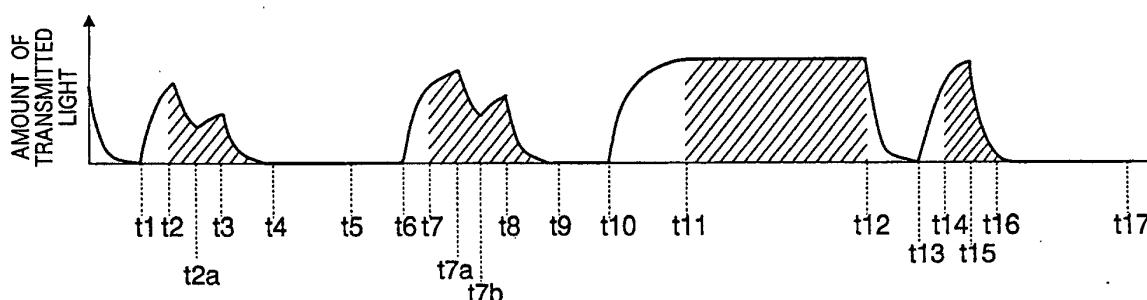


FIG. 15B

Description**Technical Field**

[0001] The present invention relates to a liquid crystal drive apparatus and gradation display method, and more particularly, to a liquid crystal drive apparatus and gradation display method according to a new gradation display system.

Background Art

[0002] An active matrix type liquid crystal display apparatus producing a multi-gradation display is known in the prior art. This multi-gradation display is performed by selecting one reference voltage corresponding to the gradation display data from among as many reference voltages as display gradations using an analog switch and driving the liquid display apparatus at the selected reference voltage.

[0003] FIG.1 is a block diagram showing a conventional liquid crystal drive apparatus for driving an active matrix type liquid crystal display apparatus. This liquid crystal drive apparatus is provided with first latch 1, second latch 2 and decoder 3 for every vertical pixel line of the liquid crystal display apparatus.

[0004] First latch 1 reads 3-bit gradation data D0 to D2 that specify 8 gradations for each vertical pixel line during one horizontal scanning period. That is, this gradation data D0 to D2 are latched by first latch 1 and held for only one horizontal scanning period.

[0005] Second latch 2 supplies gradation data D0 to D2 held in first latch 1 to decoder 3 in next one horizontal scanning period. Decoder 3 decodes gradation data D0 to D2 from second latch 2 and outputs decoded signals S0 to S7 to control terminals of analog switches A0 to A7 respectively.

[0006] These analog switches A0 to A7 selectively output reference voltages V0 to V7 supplied to the input terminal in association with decoded signals S0 to S7. That is, one of reference voltages V0 to V7 is selected by decoded signals S0 to S7 and output as a liquid crystal drive voltage.

[0007] Reference voltages V0 to V7 correspond to gradation levels as shown in FIG.2. Therefore, a reference voltage is selected based on the gradation data, the reference voltage is output to the liquid crystal panel as a voltage to be applied, and in this way the amount of transmitted light corresponding to the applied voltage is obtained allowing a gradation display.

[0008] However, the conventional liquid crystal drive apparatus is not sufficient to drive liquid crystals at high speed. In line with widespread use of the Internet there is a growing demand for high-speed transmission of large-volume data such as images in recent years and multi-gradations are also required to be implemented. Displaying moving pictures in particular requires high-speed drive and a multi-gradation display of liquid crys-

tales.

Disclosure of Invention

[0009] It is an object of the present invention to provide a new liquid crystal drive apparatus and gradation display method capable of driving liquid crystals at high speed and displaying multi-gradations as well.

[0010] This object is attained when a predetermined voltage is applied to liquid crystals by setting a time during which a voltage is applied to liquid crystals taking into account an area obtained by integrating an amount of transmitted light at various points in time of the liquid crystals over an LED light-emitting period.

Brief Description of Drawings**[0011]**

FIG.1 is a block diagram showing an outlined configuration of a conventional liquid crystal drive apparatus;

FIG.2 illustrates a relationship between light transmittance and applied voltage;

FIG.3 is a block diagram showing an outlined configuration of a liquid crystal drive apparatus according to Embodiment 1 of the present invention;

FIG.4 illustrates a look-up table at the liquid crystal drive apparatus shown in FIG.3;

FIG.5A illustrates a relationship between light transmittance and time when application of a voltage is started;

FIG.5B illustrates a relationship between light transmittance and time when application of a voltage is stopped;

FIG.6 illustrates a relationship between an applied voltage and time;

FIG.7 illustrates a relationship between an applied voltage and time for each gradation;

FIG.8A illustrates voltage application timing;

FIG.8B illustrates voltage application timing;

FIG.8C illustrates voltage application timing;

FIG.9 is a block diagram showing an outlined configuration of a liquid crystal drive apparatus according to Embodiment 2 of the present invention;

FIG.10 illustrates a pattern table at the liquid crystal drive apparatus shown in FIG.9;

FIG.11 illustrates voltage application patterns;

FIG.12A illustrates a relationship between an amount of transmitted light and time when a certain voltage is applied;

FIG.12B illustrates a relationship between an amount of transmitted light and time when a pattern voltage of pattern #3 in FIG.11 is applied;

FIG.13 is a block diagram to illustrate the creation of a look-up table used for a liquid crystal drive apparatus according to Embodiment 3 of the present invention;

FIG.14 is a characteristic curve to illustrate gamma correction;

FIG.15A is a drive voltage waveform chart showing an example of a pattern voltage applied to liquid crystals;

FIG.15B illustrates an area of an amount of transmitted light when the pattern voltage in FIG.15A is applied;

FIG.16A is a drive voltage waveform chart according to a conventional variable application voltage system;

FIG.16B illustrates an amount of transmitted light when the voltage in FIG.16A is applied;

FIG.17 is a block diagram showing an outlined configuration of a liquid crystal drive apparatus according to Embodiment 4 of the present invention;

FIG.18 illustrates a temperature characteristic of liquid crystals; and

FIG.19 is a block diagram showing an outlined configuration of a liquid crystal drive apparatus according to Embodiment 5 of the present invention.

Best Mode for Carrying out the Invention

[0012] With reference now to the attached drawings, embodiments of the present invention will be explained in detail below.

(Embodiment 1)

[0013] FIG.3 is a block diagram showing an outlined configuration of a liquid crystal drive apparatus according to Embodiment 1 of the present invention. Liquid crystal drive apparatus 10 according to Embodiment 1 is provided with application time control section 102 that controls a voltage application time according to gradation data, look-up table 101 that associates gradation with application time (ON-time) and switch 103 that outputs a constant voltage generated by constant voltage generation circuit 105 to LCD panel 20 according to the ON-time control signal output from application time control section 102.

[0014] As shown in FIG.4, look-up table 101 is a table that associates a gradation level with an application time during which the switch is ON. Here, a gradation display of the liquid crystal drive apparatus according to the present invention will be explained using FIG.5 to FIG.7.

[0015] FIG.5 illustrates a relationship between light transmittance and time, FIG.6 illustrates a relationship between an applied voltage and time and FIG. 7 illustrates a relationship between an applied voltage and time for each gradation.

[0016] When a voltage is applied to liquid crystals and the liquid crystals respond to this by allowing light to penetrate, the liquid crystals have light transmittance as shown in FIG. 5A. In FIG.5A, suppose the time required for the light transmittance to change from 10% to 90% is τ ON.

[0017] On the other hand, when voltage application to the liquid crystals is stopped and light is shut off, the liquid crystals have light transmittance as shown in FIG. 5B. In FIG.5B, suppose the time required for the light transmittance to change from 90% to 10% is τ OFF.

[0018] As is apparent from FIG. 5A and FIG. 5B, τ OFF is longer than τ ON. This means that there is a difference between the time after application of a voltage until liquid crystals respond to this allowing light to penetrate and the time after voltage application is stopped until light is shut off.

[0019] In this case, response speed τ ON of liquid crystals is expressed as $kG^2/(V^2 \cdot V_{th}^2)$, response speed τ OFF is expressed as $k'G^2$ (k, k' : constants, V : applied voltage, V_{th} : threshold voltage, G : cell gap). As is seen from this expression, the response speed of liquid crystals differs between voltage application (τ ON) and stoppage of voltage application (τ OFF) . Thus, the rate of voltage variation with time differs when a voltage is applied and when application of a voltage is stopped, that is, the rate of voltage variation with time is asymmetric.

[0020] As shown in FIG.6, the (rise) time to reach an applied voltage value of liquid crystals differs when voltage 2.5 V is applied and when voltage 5 V is applied, and the time to reach the applied voltage value when voltage 5 V is applied is shorter.

[0021] As described above, when a voltage is applied to liquid crystals, the liquid crystals respond to this (opens up the aperture) allowing light to penetrate. When application of a voltage continues for a certain time, the liquid crystals continue to respond thereto and remain open continuously allowing light to penetrate. An amount of transmitted light for the duration of that time can be considered as a value obtained by integrating the applied voltage for the duration of that time. That is, the hatching area in FIG.6 can be considered to indicate an amount of transmitted light. To be specific, the amount of transmitted light in the case of the applied voltage of 5 V is the area expressed by leftward ascending lines in FIG.6, while the amount of transmitted light in the case of the applied voltage of 2.5 V is the area expressed by rightward ascending lines in FIG.6.

[0022] In gradation displays by a conventional liquid crystal drive, reference voltages such as 2.5 V and 5 V as shown in FIG.6 are preset and the reference voltages are applied to the liquid crystals. As described above, when the amount of transmitted light is considered as a total amount of opening time, that is, applied voltage \times time (area expressed with hatching in FIG.6), it is possible to control the application duration (t0 to t7) with the applied voltage kept constant as shown in FIG.7. In other words, in FIG.7, when the application time is changed, the waveform changes from a rise to fall and the area in the waveform (applied voltage \times time) changes accordingly. As a result, the amount of transmitted light varies, which allows a gradation display to be implemented.

[0023] Since such a gradation display can keep the

applied voltage constant, it is possible to perform timing control the application condition or non-application condition, that is, digital control. Digital control facilitates control. Furthermore, control is performed at all gradation levels with a relatively high applied voltage which results in quicker response of liquid crystals, which makes it possible to shorten the liquid crystal drive time as a whole.

[0024] Next, an operation of the liquid crystal drive apparatus in the above-described configuration will be explained.

[0025] Gradation data indicating gradation levels in a gradation display is input to application time control section 102 of liquid crystal drive apparatus 10. The gradation data is expressed with, for example, 3 bits in the case of 8 gradations and set as gradation levels 0 to 7.

[0026] Upon receipt of the gradation data, application time control section 102 references look-up table 101 shown in FIG.4 and sets an application time (ON-time) corresponding to the gradation data. Then, application time control section 102 outputs an ON-time control signal to switch 103 for the decided ON-time. In this way, a gradation display is carried out by digital-controlling the application time corresponding to a predetermined applied voltage as shown in FIG.8A to 8C.

[0027] Switch 103 turns ON the switch according to the ON-time control signal from application time control section 102 to apply a voltage to pixels of LCD panel 20. That is, switch 103 supplies a signal voltage to the source electrode line according to the ON-time control signal to drive liquid crystals.

[0028] In this way, the liquid crystal drive apparatus according to this embodiment allows a multi-gradation display through digital control. This facilitates control in a multi-gradation display. Furthermore, time control is performed at all gradation displays with a relatively high applied voltage which results in quicker response of liquid crystals, which makes it possible to shorten the liquid crystal drive time as a whole. Furthermore, as a voltage is applied in a digitized manner through time control with the liquid crystal drive voltage kept constant, which eliminates the need for a D/A (digital/analog) converter which is normally required for a liquid crystal drive apparatus.

(Embodiment 2)

[0029] FIG.9 is a block diagram showing an outlined configuration of a liquid crystal drive apparatus according to Embodiment 2 of the present invention. The liquid crystal drive apparatus according to Embodiment 2 is provided with application time control section 102 that controls a voltage application time according to gradation data, pattern table 104 that associates a gradation with an application time (ON-pattern) and switch 103 that outputs a constant voltage generated by constant voltage generation circuit 105 to LCD panel 20 according to an ON pattern control signal output from applica-

tion time control section 102.

[0030] As shown in FIG.10, pattern table 104 is a table that associates a gradation level with an application pattern for turning ON the switch. As applied patterns, there can be, for example, patterns whereby a predetermined liquid crystal drive time as shown in FIG.11 is divided into a plurality of blocks at which application or non-application of a voltage is selected.

[0031] When a voltage is applied to liquid crystals, a rise and fall are asymmetric as shown in FIG.5A and FIG.5B. Therefore, taking advantage of this asymmetry, even if a voltage application time is the same, when different patterns are used as shown in FIG.11, the area of applied voltage \times time varies depending on a combination of voltage application units (one block in the patterns in FIG.11). As a result, it is possible to perform a finer gradation display than Embodiment 1.

[0032] For example, instead of changing a voltage application time between LED unit light emission periods as in the case of conventional PWM control, this embodiment changes a voltage application pattern within a unit light emission period. The "LED unit light emission period" here refers to a period after LEDs (light-emitting diodes) provided for respective liquid crystals start to emit light until the LEDs stop light emission.

[0033] By the way, this embodiment is supposed to perform a display using a field sequential method, use an LED array as backlight and flash this LED array at high speed. That is, the above-described unit light emission period corresponds to one LED array lighting-up period.

[0034] Thus, by changing the voltage application pattern within the LED unit light emission period, it is possible to perform a much finer gradation display compared to conventional PWM control, for example.

[0035] Then, an operation of the liquid crystal drive apparatus in the above-described configuration will be explained.

[0036] Gradation data indicating gradation levels in a gradation display is input to application time control section 102 of liquid crystal drive apparatus 10. The gradation data is expressed with, for example, 4 bits in the case of 16 gradations and set as gradation levels 0 to 15.

[0037] Upon receipt of the gradation data, application time control section 102 references pattern table 104 shown in FIG. 10 and decides an application pattern (ON pattern) corresponding to the gradation data. Then, application time control section 102 outputs an ON pattern control signal to switch 103 for the decided ON pattern.

[0038] Switch 103 turns ON the switch according to the ON pattern control signal from application time control section 102 to apply a voltage to pixels of LCD panel 20. That is, switch 103 supplies a signal voltage to the source electrode line according to the ON pattern control signal to drive liquid crystals.

[0039] In this way, the liquid crystal drive apparatus according to this embodiment allows a multi-gradation

display through digital control. This facilitates control in a multi-gradation display. Furthermore, time control is performed at all gradation levels with a relatively high applied voltage which results in quicker response of liquid crystals, and therefore it is possible to shorten the liquid crystal drive time as a whole. Furthermore, as a constant liquid crystal drive voltage is applied in a digitized manner through time control, there is no need for a D/A (digital/analog) converter, which is normally required for a liquid crystal drive apparatus.

[0040] Furthermore, the liquid crystal drive apparatus according to this embodiment expresses gradations by combining voltage application units using asymmetry between rise and fall of voltage application, and therefore it is possible to display more gradations.

[0041] Furthermore, by changing voltage application patterns within an LED unit light emission period allows a finer gradation display.

(Embodiment 3)

[0042] This embodiment sets a voltage application time (or voltage application pattern) corresponding to a gradation considering the area obtained by integrating the amount of transmitted light of liquid crystals at various points in time over an LED light emission period when a maximum rated voltage of the liquid crystals is applied. More specifically, as shown in FIG. 12, the area (area indicated by hatching of the drawing) obtained by integrating the waveform amount of transmitted light that penetrates the liquid crystals when a drive voltage is applied over the LED light emission period is associated with each gradation.

[0043] That is, the liquid crystals are driven in such a way that the area of the hatching area in FIG.12 increases as the input gradation data shows higher gradations. Since the applied voltage is actually set to be constant at a maximum rated voltage of the liquid crystals, the area of the hatching is changed according to the gradation by changing the voltage application time (or voltage application pattern). By the way, FIG.12A illustrates a variation in an amount of transmitted light of liquid crystals with time when an applied voltage is set to ON during a period from time t_0 to t_1 , and FIG.12B illustrates a variation in an amount of transmitted light of liquid crystals with time when a voltage of a predetermined pattern is applied to liquid crystals. More specifically, FIG.12B shows a case where the voltage of applied pattern #3 in FIG.11 is applied and shows a case where an ON voltage is applied during a period from time t_0 to t_2 , a period from time t_3 to t_4 and a period from time t_5 to t_6 .

[0044] Thus, the liquid crystal drive apparatus of this embodiment sets a voltage application time for liquid crystals by associating the area obtained by integrating the amount of transmitted light over the LED light emission period with each gradation, and in this way even if liquid crystals are driven by a constant applied voltage, it is possible to perform a fine gradation display as if liq-

uid crystals were driven by an analog voltage.

[0045] Furthermore, by applying an ON/OFF pattern voltage to liquid crystals taking into account the area of the amount of transmitted light during the LED light emission period, it is possible to perform a much finer gradation display according to gradation data. That is, as is apparent from a comparison between FIG.12A and FIG.12B, applying an ON/OFF pattern voltage (FIG. 12B) makes it possible to select the area of the amount of transmitted light during the LED light emission period in a finer way, and therefore finer gradation expression is possible. For example, when a 10-bit ON/OFF pattern is set, 1024 ways of gradation expression is possible for each of R, G and B.

[0046] Furthermore, this embodiment is intended to apply an ON/OFF pattern voltage to liquid crystals at a predetermined time before the time at which the LED actually emits light. As a result, desired transmittance can be obtained from the time at which the LED starts to emit light, and therefore it is possible to increase brightness of the display screen without the need to increase the LED output.

[0047] Such a liquid crystal drive apparatus can be implemented by creating look-up table 101 of above-described liquid crystal drive apparatus 10 in Embodiment 1 as shown below. FIG.13 shows an apparatus to create look-up table 101 and reference table 101 stores a voltage application time (or voltage application pattern) associated with the gradation data.

[0048] The look-up table creation apparatus allows gradation data to be input to application time setting circuit 201. Application time setting circuit 201 sets a plurality of application times (or a plurality of application patterns) for every gradation specified by gradation data. That is, application time setting circuit 201 sets a plurality of application times for one piece of gradation data from short to long application times one by one. The application time (or application pattern) set in this way is used as an ON/OFF control signal of switch 202.

[0049] When a constant voltage (maximum rated voltage 5 [v] in the case of this embodiment) is always input from constant voltage generation circuit 203 to switch 202 and this voltage is applied to liquid crystals of LCD panel 20 as a drive voltage for the time set by application time setting circuit 201.

[0050] LCD panel 20 is provided with brightness sensors 204 and an amount of transmitted light obtained from brightness sensors 204 is sent to integration circuit 205. Integration circuit 205 calculates the area indicated by hatching of FIG.12 by integrating the amount of transmitted light over the LED light emission period and sends this area to gradation decision circuit 206. Gradiation decision circuit 206 is also fed gradation data. Gradiation decision circuit 206 compares each gradation with the integrated area and sends a write control signal to allow the data to be written in look-up table 101 when the area corresponding to the gradation is input.

[0051] Look-up table 101 is given gradation data and

application time information (or application pattern information) as write information and the gradation data is associated with the application time (or application pattern) and written when gradation decision circuit 206 enables a write. Thus, look-up table 101 stores the voltage application time (or voltage application pattern) corresponding to each gradation taking into account the area of the hatching in FIG.12.

[0052] By the way, when an actual image is displayed, it is ideal to select points in such a way that the relationship between gradation and brightness is plotted on a gamma curve as shown in FIG.14. At this time, when a voltage with a different application pattern is applied to the liquid crystals within the light emission period of each LED as shown in this embodiment, it is possible to create very many gradations depending on the application patterns, which makes it easier to select points on the gamma curve and allows high precision gamma correction.

[0053] Then, an operation of the liquid crystal drive apparatus of this embodiment will be explained using FIG.15. FIG.15A shows a drive voltage waveform applied to the liquid crystals. FIG.15B is a waveform chart showing an amount of transmitted light of the liquid crystals when the pattern voltage in FIG.15A is applied. Furthermore, parts marked R, G and B in the figure indicate the LED light emission periods of respective colors.

[0054] That is, when a drive voltage is applied at time t1, the amount of transmitted light starts to rise from this time t1. When time t2 is reached, an R (red) LED emits light. Then, when the application of the drive voltage ends at time t2, the amount of transmitted light starts to fall from this time t2. Then, when an ON voltage is applied from time t2a to time t3, the amount of transmitted light rises during this period. Then, when the application of the drive voltage ends at time t3, the amount of transmitted light starts to fall from this time t3 and the amount of light is reduced to 0 at time t4. By the way, the amount of transmitted light continues to rise for a period from time t1 to time t2, but since no LED emits light, no LCD display is produced.

[0055] Likewise, when a drive voltage is applied at time t6, the amount of transmitted light starts to rise from this time t6. When a G (green) LED starts to emit light at time t7, an LCD display starts from this time t7. Then, the application of the drive voltage ends at time t7a, the amount of transmitted light starts to fall from this time t7a. Then, an ON voltage is applied for a period from time t7b to time t8, the amount of transmitted light rises. Then, the application of the drive voltage ends at time t8, the amount of transmitted light starts to fall from this time t8, the amount of transmitted light is reduced to 0 at time t9 and the display ends.

[0056] Likewise, when a drive voltage is applied at time t10, the amount of transmitted light starts to rise from this time t10 and when a B (blue) LED starts to emit light at time t11, an LCD display starts from this time t11. Then, when the application of the drive voltage ends at

time t12, light emission of LED also stops and the display ends. By the way, the area enclosed by the amount of transmitted light and light emission period reaches a maximum with this B (blue) display, which means that this liquid crystal displays a maximum gradation.

[0057] Likewise, when a drive voltage is applied at time t13, the amount of transmitted light starts to rise from this time t13, and when an R (red) LED starts to emit light at time t14, an LCD display starts from this time t14. Then, when the application of the drive voltage ends at time t15, the amount of transmitted light starts to fall from this time t15, the amount of transmitted light is reduced to 0 at time t16 and the display ends.

[0058] As shown above, the liquid crystal drive apparatus of this embodiment is designed to drive liquid crystals by a maximum rated voltage, and therefore the waveform of the amount of transmitted light rises and falls abruptly as shown in FIG.15B, making it possible to increase the response speed of liquid crystals. This also allows, for example, the frame frequency to be increased.

[0059] Moreover, since a voltage application time is set taking into account the area obtained by integrating the amount of transmitted light over an LED light emission period, it is possible to produce a fine gradation display suited to gradations.

[0060] In addition, applying an ON/OFF pattern voltage taking into consideration the area obtained by integrating the amount of transmitted light over an LED light emission period allows a much finer gradation display according to gradation data.

[0061] Here, FIG.16 shows a waveform obtained by driving liquid crystals according to a conventional variable application voltage system as an example of a comparison with the liquid crystal drive apparatus of this embodiment. According to this liquid crystal drive system, the applied voltage value is increased as the specified gradation increases.

[0062] That is, when a medium drive voltage is applied over a period from time t1 to time t3, an amount of transmitted light according to this voltage value is obtained from liquid crystals. Likewise, when a relatively large drive voltage is applied over a period from time t4 to time t6, a relatively large amount of transmitted light according to this voltage value is obtained from liquid crystals.

[0063] When a maximum drive voltage is applied over a period from time t7 to time t9, a maximum amount of transmitted light according to this voltage value is obtained from liquid crystals. Furthermore, when a small drive voltage is applied over a period from time t10 to time t12, a small amount of transmitted light according to this voltage value is obtained from liquid crystals. By the way, an LCD display is actually produced over a period from time t2 to t3, a period from time t5 to t6, a period from time t8 to t9 and a period from time t11 to t12, during which the respective RGB LEDs emit light.

[0064] During liquid crystal driving according to this

variable application voltage system, a drive voltage value is set by focusing attention on an average height of the waveform of the amount of transmitted light during each display period. For example, a drive voltage is set in such a way that an average height of the amount of transmitted light over the period from time t_2 to time t_3 satisfies the specified gradation.

[0065] In contrast, during liquid crystal driving according to the amount of transmitted light integration system of this embodiment, liquid crystals are driven taking into account the integrated area of the amount of transmitted light, and therefore it is possible to express more visually appealing fine gradations than the conventional liquid crystal drive system.

[0066] Thus, the liquid crystal drive apparatus of this embodiment controls a drive voltage based on a value obtained by integrating the amount of transmitted light from liquid crystals, which results in a quicker change of a drive voltage applied to liquid crystals than the response time (ON/OFF) of the liquid crystals. This makes it possible to control the level of aperture of each liquid crystal at optimal timing and obtain desired brightness.

(Embodiment 4)

[0067] FIG.17 shows a configuration of a liquid crystal drive apparatus according to Embodiment 4 of the present invention, wherein the components corresponding to those in FIG.3 are assigned the same reference numerals. This liquid crystal drive apparatus is provided with a temperature sensor 301 near LCD panel 20. Upon detecting an ambient temperature of liquid crystals, temperature sensor 301 sends the detection result to correction circuit 302 as temperature information.

[0068] Correction circuit 302 corrects an ON-time control signal output from application time control section 102 based on the temperature information. Here, liquid crystals have a temperature characteristic as shown in FIG.18 that the response speed of liquid crystals slows down and the amount of transmitted light decreases as a temperature decreases. In consideration of this respect, this embodiment performs corrections on an ON-time control signal in such a way that the ON time is extended as the ambient temperature of liquid crystals decreases.

[0069] Thus, such a configuration can also produce an effect of implementing a liquid crystal drive apparatus with consideration given to the temperature characteristic of liquid crystals and with further improved gradation display accuracy in addition to the effects obtained by above-described Embodiments 1 to 3.

(Embodiment 5)

[0070] FIG.19 shows a configuration of a liquid crystal drive apparatus according to Embodiment 5 of the present invention, wherein the components corresponding to those in FIG.3 are assigned the same reference

numerals. This liquid crystal drive apparatus is provided with a brightness detection section 401 at an unobtrusive position peripheral to LCD panel 20. In this embodiment, brightness detection section 401 is constructed of a detection cell placed in a liquid crystal cell array and a photosensor that detects brightness of this detection cell. The brightness detection result detected by the photosensor is sent to correction circuit 402 as brightness information.

[0071] Correction circuit 402 is also fed gradation data in addition to the brightness information from brightness detection section 401 and correction circuit 402 compares the brightness information with the gradation data. Then, when the brightness information is different from the gradation data, an ON-time control signal output from application time control section 102 is corrected according to the difference. More specifically, when the brightness indicated by the brightness information is smaller than the gradation indicated by the gradation data, the ON-time control signal is corrected so that the ON-time is extended.

[0072] Here, when used for an extended period of time, brightness of an LED has a tendency to reduce due to secular variation. The brightness of a B (blue) LED out of RGB in particular may drastically drop due to secular variation. In consideration of this respect, this embodiment performs corrections on the ON-time control signal in such a way that the ON-time is extended as the brightness of transmitted light of liquid crystals decreases. In addition, this embodiment performs corrections for recovering white balance by changing a current value of each color according to the brightness information. This provides a liquid crystal drive apparatus with improved brightness balance.

[0073] Thus, this embodiment produces an effect of implementing a liquid crystal drive apparatus with further improved gradation display accuracy also taking into account a reduction of brightness due to secular variation of LEDs in addition to the effects obtained by above-described Embodiments 1 to 3.

(Other Embodiments)

[0074] This embodiment is applicable to LCD panel liquid crystal molecule operating modes such as TN (Twisted Nematic) mode, STN (Super Twisted Nematic) mode, ferroelectric crystal mode, birefringence mode, guest/host mode, dynamic scattering mode, phase transition mode, etc.

[0075] The foregoing embodiments have described the case where the applied voltage is 5V, but the present invention is not limited to this and is also applicable to cases where the applied voltage is other than 5 V.

[0076] Above-described embodiment 3 has mainly described the case where data based on a transmitted light quantity integration system is stored in look-up table 101 of Embodiment 1, and therefore a voltage application time is set taking into account the area obtained

by integrating the amount of transmitted light of liquid crystals over an LED light emission period, but the present invention is not limited to this and it is also possible to store data based on a transmitted light quantity integration system in pattern table 104 of Embodiment 2. In this case, it is possible to detect the amount of transmitted light of liquid crystals when a voltage of a certain pattern is applied to liquid crystals and set a voltage application pattern suitable to each gradation taking into account the area obtained by integrating this amount of transmitted light over an LED light emission period.

[0077] As described above in Embodiment 2, the pattern voltage application method of the present invention in particular is designed to apply a pattern voltage according to gradation data within a single LED light emission period and thereby produce a finer LCD display according to gradation data than conventional PWM control. In addition to this, by determining this application pattern according to the above-described integrated area, the present invention can produce a much finer LCD display according to gradation data.

[0078] Above-described embodiment 4 has described the case where a voltage application time is corrected according to the temperature detection result, but the present invention is not limited to this and can be modified so that the voltage application pattern is corrected according to the temperature detection result.

[0079] Likewise, above-described embodiment 5 has described the case where a voltage application time is corrected according to the brightness detection result, but the present invention is not limited to this and can be modified so that the voltage application pattern is corrected according to the brightness detection result.

[0080] Furthermore, the above-described embodiments have described the case where a pulse pattern control system taking into account a value obtained by integrating the amount of transmitted light according to the present invention is applied to control without using any D/A converter, but the present invention is not limited to this and is also applicable to control using a D/A converter. For example, a combination of a D/A converter capable of expressing specific gradations (e.g., 4 gradations) (can be a two-gradation D/A converter in the case of digital control in this embodiment) and the drive system (e.g., 4-value voltage application pattern) can express far more gradations.

[0081] Furthermore, the foregoing embodiments have described the case where the liquid crystal drive apparatus and liquid crystal drive method of the present invention are applied to a liquid crystal display apparatus based on a field sequential system, but the present invention is not limited to this and can also attain effects similar to those of the above-described embodiments even if the present invention is applied to other liquid crystal display apparatuses based on, for example, a color filter system or projector system.

[0082] Furthermore, the present invention is not limited to the foregoing embodiments, but can be imple-

mented with various modifications.

[0083] (1) The liquid crystal drive apparatus of the present invention includes a setting section that sets a voltage application time for liquid crystals based on gradation data and a voltage supply section that supplies a predetermined applied voltage to liquid crystals for the voltage application time set by the setting section, wherein the setting section sets a voltage application time according to the gradation data taking into account the area obtained by integrating the amount of transmitted light of liquid crystals at various points in time over an LED light emission period when a constant voltage is applied to liquid crystals.

[0084] According to this configuration, a gradation display is performed by only controlling the voltage application time without changing the applied voltage value, which makes control in multi-gradation displays easier. Furthermore, gradations are expressed with an amount of transmitted light integrated of continuously changing liquid crystals, and can therefore provide a finer gradation display according to the gradation data than conventional PWM (Pulse Width Modulation), etc.

[0085] (2) The setting section in (1) of the liquid crystal drive apparatus of the present invention sets a voltage application time with reference to a table which associates gradations with voltage application times.

[0086] This configuration makes it easier to set a voltage application time according to gradations depending on the performance, etc. of liquid crystals to be driven.

[0087] (3) The table in (2) of the liquid crystal drive apparatus of the present invention is created by detecting the amount of transmitted light of liquid crystals varying with time during each period when a maximum rated voltage of liquid crystals is applied to the liquid crystals for different periods, calculating the area by integrating the detected amount of transmitted light over an LED light emission period and associating the area obtained with the gradation data to associate the gradation data with the voltage application time.

[0088] According to this configuration, the table stores a voltage application time suitable for each liquid crystal for every gradation beforehand, and therefore applying a voltage according to the voltage application time stored in this table allows a gradation display quite suitable for the input gradation data to be performed.

[0089] (4) The liquid crystal drive apparatus of the present invention includes a setting section that sets a voltage application pattern for liquid crystals based on gradation data and a voltage supply section that supplies a predetermined applied voltage to liquid crystals according to the voltage application pattern set by the setting section, wherein a gradation display is produced by controlling the amount of transmitted light within a unit LED light emission period according to the voltage application pattern.

[0090] According to this configuration, a gradation display is performed by changing the pattern of a voltage applied to liquid crystals within the unit LED light emis-

sion period, which allows a finer gradation display according to the gradation data than conventional PWM (Pulse Width Modulation), etc.

[0091] (5) The setting section in (4) of the liquid crystal drive apparatus of the present invention sets a voltage application pattern according to gradation data taking into account the area obtained by integrating the amount of transmitted light over an LED light emission period at various points in time when the voltage application pattern is applied to liquid crystals.

[0092] According to this configuration, by associating the area obtained by integrating the amount of transmitted light over an LED light emission period with each gradation and setting a pattern of a voltage applied to liquid crystals, it is possible to produce a fine gradation display as if the liquid crystals were driven at an analog voltage even if the liquid crystals are driven at a certain applied voltage. Moreover, gradations are expressed with the amount of transmitted light integrated of continuously varying liquid crystals, and the present invention can therefore provide a much finer gradation display according to the gradation data than conventional PWM (Pulse Width Modulation), etc.

[0093] (6) The setting section in (5) of the liquid crystal drive apparatus of the present invention sets a voltage application pattern with reference to a table which associates gradations with voltage application patterns.

[0094] This configuration makes it easier to set a voltage application pattern according to gradations depending on the performance, etc. of liquid crystals to be driven.

[0095] (7) The table in (6) of the liquid crystal drive apparatus of the present invention is created by detecting the amount of transmitted light of liquid crystals which varies depending on the application patterns with time when voltages of different patterns are applied to liquid crystals, calculating the area by integrating the detected amount of transmitted light over an LED light emission period and associating the area with the gradation data to associate the gradation data with the voltage application patterns.

[0096] According to this configuration, the table stores a voltage application pattern suitable for each liquid crystal for every gradation beforehand, and therefore applying a voltage according to the voltage application patterns stored in this table allows a gradation display quite suitable for the input gradation data to be performed.

[0097] (8) Furthermore, the voltage supply sections in (1) to (7) of the liquid crystal drive apparatus of the present invention do not supply an intermediate voltage between a maximum voltage and minimum voltage and only supply the maximum voltage and minimum voltage to liquid crystals to perform a gradation display.

[0098] According to this configuration, liquid crystals are only driven at a maximum voltage (e.g., 5 [v]) and minimum voltage (0 [v]) of rated voltages, and therefore the response of liquid crystals speeds up and it is pos-

sible to achieve an amount of transmitted light corresponding to the required gradation. As a result, the liquid crystals can be driven at high speed.

[0099] (9) Furthermore, the liquid crystal drive apparatus of the present invention is provided with a temperature sensor placed peripheral to liquid crystals to detect an ambient temperature of liquid crystals, wherein the setting section corrects a voltage application time or voltage application pattern according to the detection result of the temperature sensor.

[0100] According to this configuration, when the response of liquid crystals slows down as the ambient temperature of the liquid crystals decreases, the setting section corrects the voltage application time so that the voltage application time is extended accordingly or corrects the voltage application pattern. As a result, it is possible to always perform a gradation display according to the input gradation data irrespective of the state of liquid crystals.

[0101] (10) Furthermore, the liquid crystal drive apparatus of the present invention is also provided with a brightness detection section placed peripheral to liquid crystals to detect brightness of light that penetrates liquid crystals, wherein the setting section corrects a voltage application time or voltage application pattern according to the detection result of the brightness detection section.

[0102] According to this configuration, when the amount of LED light emission decreases due to secular variation and the display brightness decreases, the setting section corrects the voltage application time so that the voltage application time is extended accordingly or corrects the voltage application pattern. As a result, it is possible to always perform a gradation display with good brightness balance and according to the input gradation data irrespective of secular variation, etc. of LEDs.

[0103] (11) Furthermore, the liquid crystal drive apparatus of the present invention is a liquid crystal drive apparatus based on a field sequential system that allows LEDs of R, G and B colors to emit light sequentially and changes an aperture ratio of liquid crystals provided for the LEDs of the respective colors by a voltage applied to the liquid crystals and provided with a setting section that sets a voltage applied to liquid crystals based on gradation data and a voltage supply section that supplies the applied voltage set by the setting section to liquid crystals, wherein the applied voltage supplied by the voltage supply section is an ON/OFF pattern pulse voltage according to the gradation to be displayed and the an ON/OFF pattern is selected by associating the gradation with the amount of transmitted light from the liquid crystals integrated within the LED light emission period when each ON/OFF pattern voltage is applied to the liquid crystals.

[0104] According to this configuration, a gradation display is carried out by changing ON/OFF patterns for liquid crystals within a unit LED light emission period,

and therefore it is possible to provide a finer gradation display according to gradation data than a conventional PWM (Pulse Width Modulation), etc. Furthermore, since an ON/OFF pattern to be applied to liquid crystals is selected by associating the area obtained by integrating the amount of transmitted light over the LED light emission period with each gradation, and in this way even if liquid crystals are driven by only ON/OFF, it is possible to perform a much finer gradation display as if liquid crystals were driven by an analog voltage. That is, a gradation is expressed with a value obtained by integrating an amount of transmitted light of continuously varying liquid crystals, which allows a much finer gradation display according to gradation data.

[0105] (12) Furthermore, the liquid crystal drive apparatus of the present invention is constructed in such a way that the setting section in (11) divides the light emission period of each color LED into a plurality of voltage application periods and sets as many binary data items indicating whether or not to apply an ON voltage for each divided period as divided voltage application periods.

[0106] This configuration makes it easier to set an ON/OFF pattern according to each gradation.

[0107] (13) Furthermore, the liquid crystal drive apparatus of the present invention is constructed in such a way that the voltage supply section in (11) supplies an ON/OFF pattern voltage to liquid crystals a predetermined time ahead of the time at which an LED actually starts to emit light.

[0108] This configuration applies an ON/OFF pattern voltage to liquid crystals a predetermined time ahead of the time at which an LED actually starts to emit light, and therefore it is possible to obtain desired transmittance from the time at which the LED starts to emit light. As a result, it is possible to increase brightness of the display screen without increasing the LED output.

[0109] (14) Furthermore, the gradation display method of the present invention includes a step of setting a voltage application pattern for liquid crystals within a unit LED light emission period based on gradation data and a step of supplying a predetermined voltage to liquid crystals according to the voltage application pattern set in the setting step and is characterized by producing a gradation display according to the voltage application pattern.

[0110] As described above, the present invention can provide a new liquid crystal drive apparatus and gradation display method capable of performing multi-gradation displays through digital control and driving liquid crystals at high speed.

[0111] This application is based on the Japanese Patent Application No.2000-391136 filed on December 22, 2000 and the Japanese Patent Application No. 2001-218440 filed on July 18, 2001, entire content of which is expressly incorporated by reference herein.

Industrial Applicability

[0112] The present invention relates to a liquid crystal drive apparatus and gradation display method and is applicable, for example, to a liquid crystal drive apparatus and gradation display method based on a field sequential system.

10 Claims

1. A liquid crystal drive apparatus comprising:

a setting section that sets a voltage application time for liquid crystals based on gradation data; and
a voltage supply section that supplies a predetermined applied voltage to liquid crystals for the voltage application time set by said setting section, wherein said setting section sets a voltage application time according to the gradation data taking into account the area obtained by integrating the amount of transmitted light of liquid crystals at various points in time over an LED light emission period when a constant voltage is applied to said liquid crystals.

2. The liquid crystal drive apparatus according to claim 1, wherein said setting section sets said voltage application time with reference to a table which associates gradations with voltage application times.

3. The liquid crystal drive apparatus according to claim 2, wherein said table is created by detecting the amount of transmitted light of liquid crystals varying with time during each period when a maximum rated voltage of said liquid crystals is applied to said liquid crystals for different periods, calculating the area by integrating the detected amount of transmitted light over an LED light emission period and associating said area with the gradation data to associate the gradation data with said voltage application time.

4. A liquid crystal drive apparatus comprising:

a setting section that sets a voltage application pattern to liquid crystals based on gradation data; and
a voltage supply section that supplies a predetermined applied voltage to liquid crystals according to the voltage application pattern set by said setting section, wherein a gradation display is produced by controlling the amount of transmitted light within a unit LED light emission period according to said voltage application pattern.

5. The liquid crystal drive apparatus according to claim

4, wherein said setting section sets a voltage application pattern according to gradation data taking into account the area obtained by integrating the amount of transmitted light over an LED light emission period at various points in time when said voltage application pattern is applied to said liquid crystals.

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6. The liquid crystal drive apparatus according to claim 5, wherein said setting section sets a voltage application pattern with reference to a table which associates gradations with voltage application patterns.

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7. The liquid crystal drive apparatus according to claim 6, wherein said table is created by detecting the amount of transmitted light of liquid crystals which varies depending on the application patterns with time when voltages of different patterns are applied to said liquid crystals, calculating the area by integrating the detected amount of transmitted light over an LED light emission period and associating said area with the gradation data to associate the gradation data with said voltage application patterns.

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8. The liquid crystal drive apparatus according to claim 4, wherein said voltage supply section does not supply an intermediate voltage between a maximum voltage and minimum voltage and only supply the maximum voltage and minimum voltage to said liquid crystals to perform a gradation display.

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9. The liquid crystal drive apparatus according to claim 4, further comprising a temperature sensor placed peripheral to said liquid crystals to detect an ambient temperature of said liquid crystals, wherein said setting section corrects said voltage application time or said voltage application pattern according to the detection result of the temperature sensor.

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10. The liquid crystal drive apparatus according to claim 4, further comprising a brightness detection section placed peripheral to said liquid crystals to detect brightness of light penetrating said liquid crystals, wherein said setting section corrects said voltage application time or said voltage application pattern according to the detection result of said brightness detection section.

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11. A liquid crystal drive apparatus based on a field sequential system that allows LEDs of R, G and B colors to sequentially emit light and changes an aperture ratio of liquid crystals provided in association with the LEDs of the respective colors by a voltage applied to the liquid crystals, comprising:

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a voltage supply section that supplies the applied voltage set by said setting section to said liquid crystals,

wherein said applied voltage supplied by said voltage supply section is an ON/OFF pattern pulse voltage according to the gradation to be displayed and the ON/OFF pattern is selected by associating the gradation with a value obtained by integrating the amount of transmitted light from the liquid crystals within an LED light emission period when each ON/OFF pattern voltage is applied to the liquid crystals.

12. The liquid crystal drive apparatus according to claim 11, wherein said setting section divides each color LED light emission period into a plurality of voltage application periods and sets as many binary data items indicating whether or not to apply an ON voltage for each divided period as divided voltage application periods.

13. The liquid crystal drive apparatus according to claim 11, wherein said voltage supply section supplies said ON/OFF pattern voltage to said liquid crystals a predetermined time ahead of the time at which an LED actually starts to emit light.

14. A gradation display method comprising:

a step of setting a voltage application pattern to liquid crystals within a short light emission period based on gradation data; and

a step of supplying a predetermined voltage to liquid crystals according to the voltage application pattern set in said setting step, wherein a gradation display is produced according to said voltage application pattern.

a setting section that sets a voltage applied to the liquid crystals based on gradation data; and

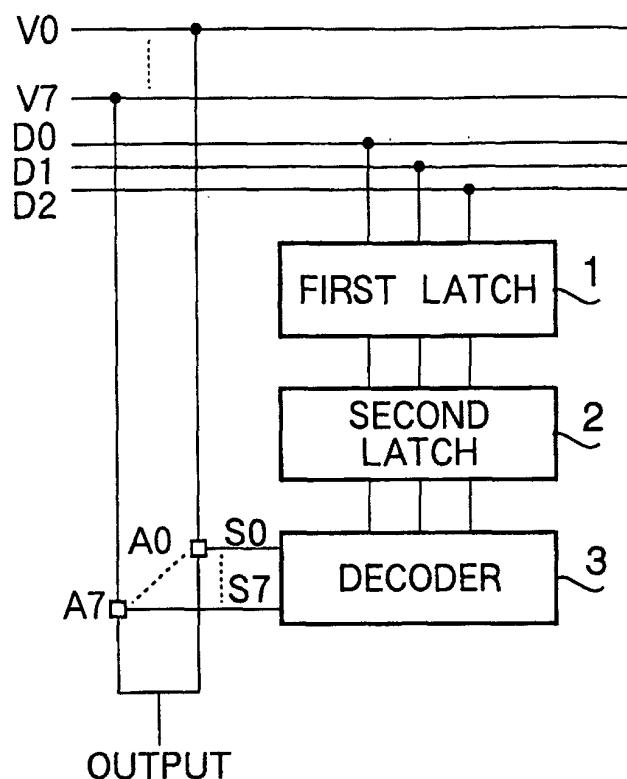


FIG. 1

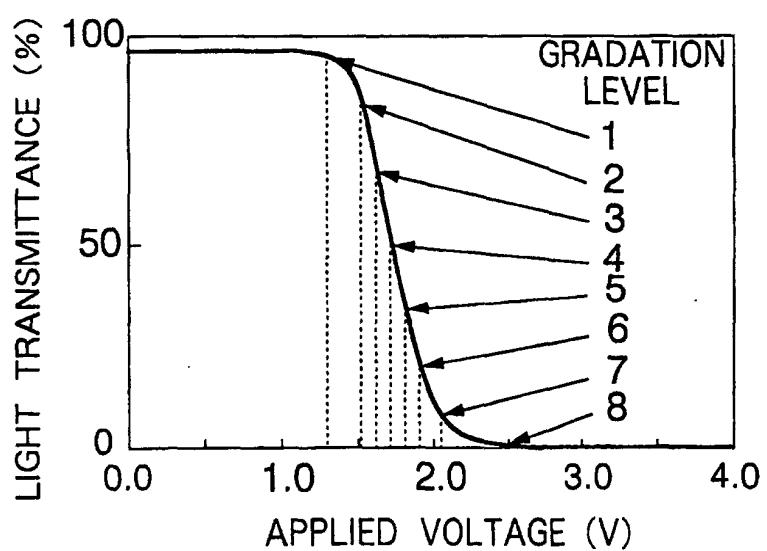


FIG. 2

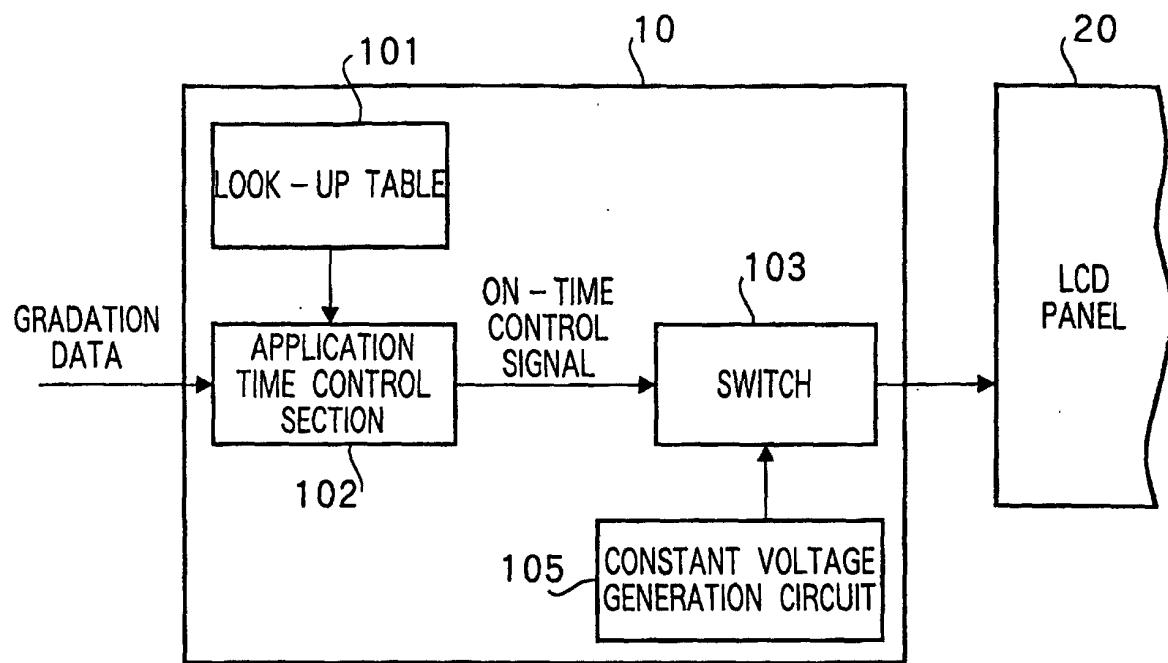


FIG. 3

GRADATION	ON - TIME
0	○○ μ s
1	×× μ s
2	△△ μ s
⋮	⋮

FIG. 4

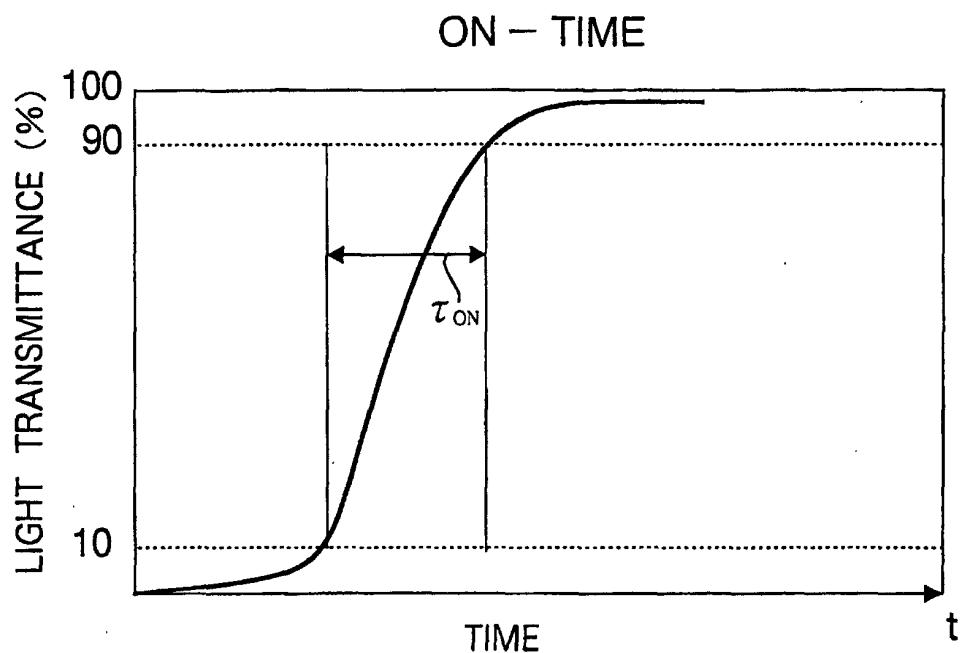


FIG. 5A

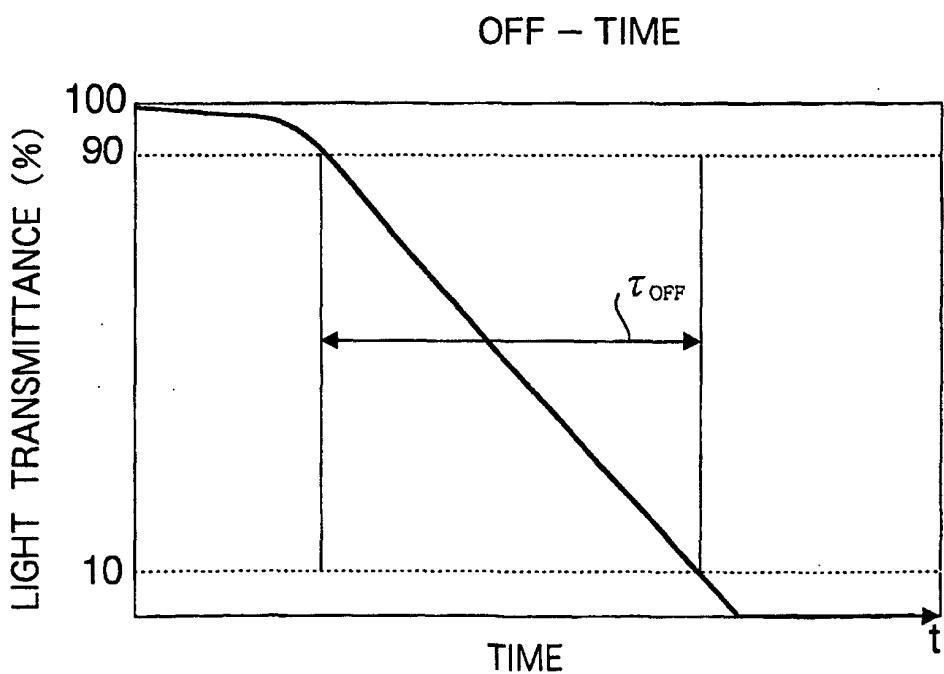


FIG. 5B

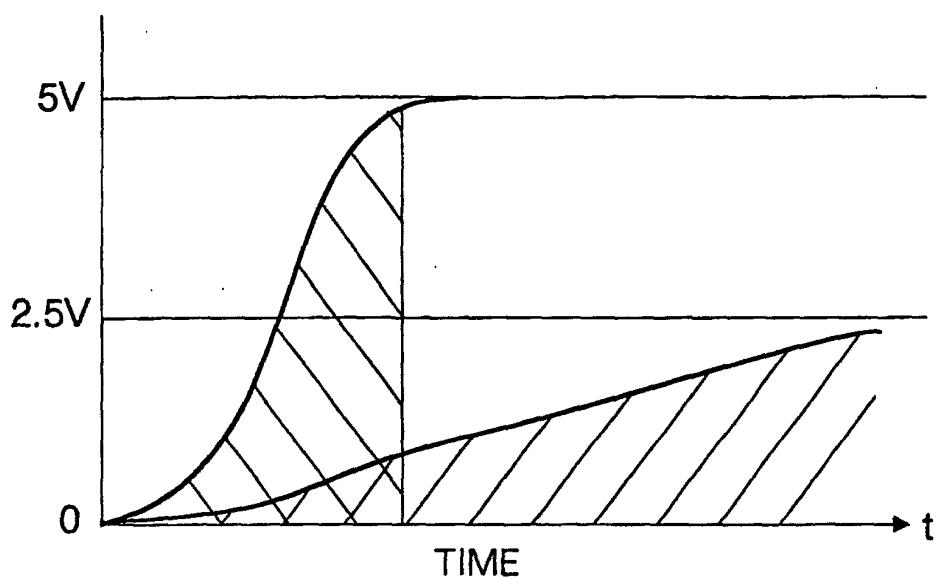


FIG. 6

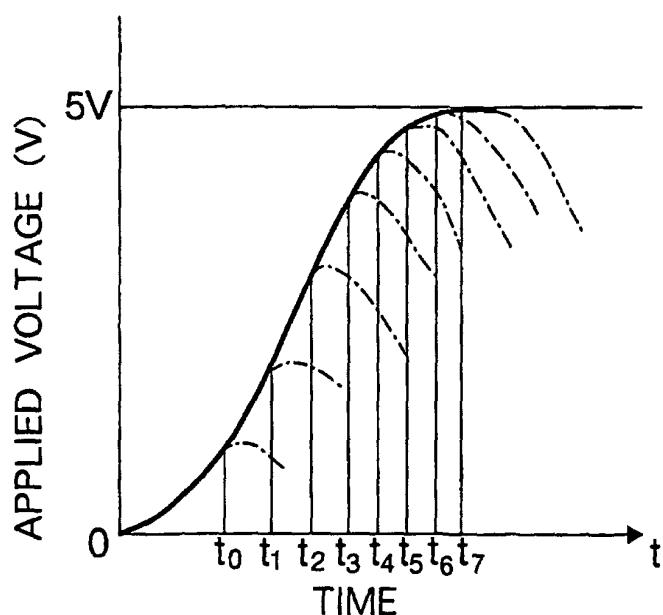


FIG. 7

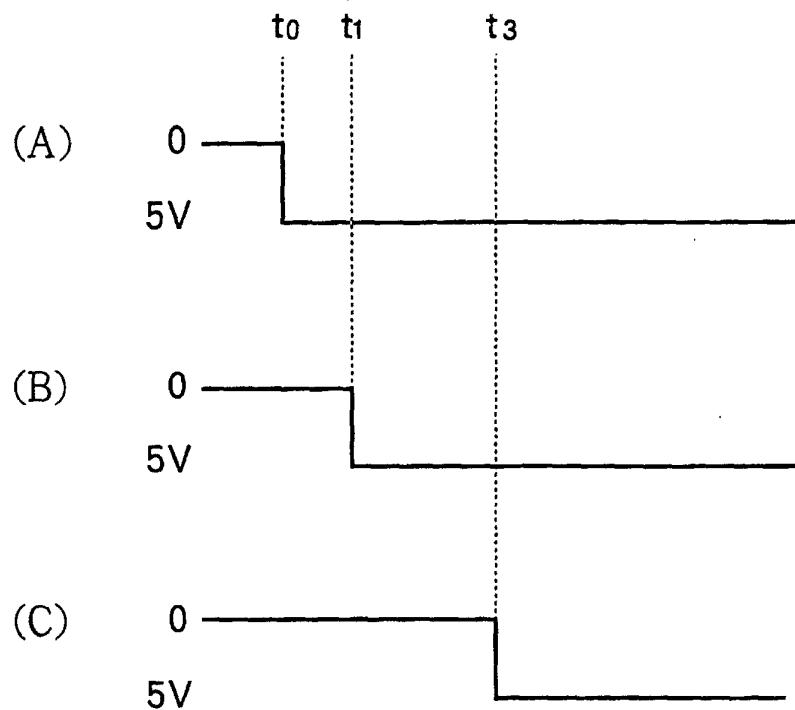


FIG. 8

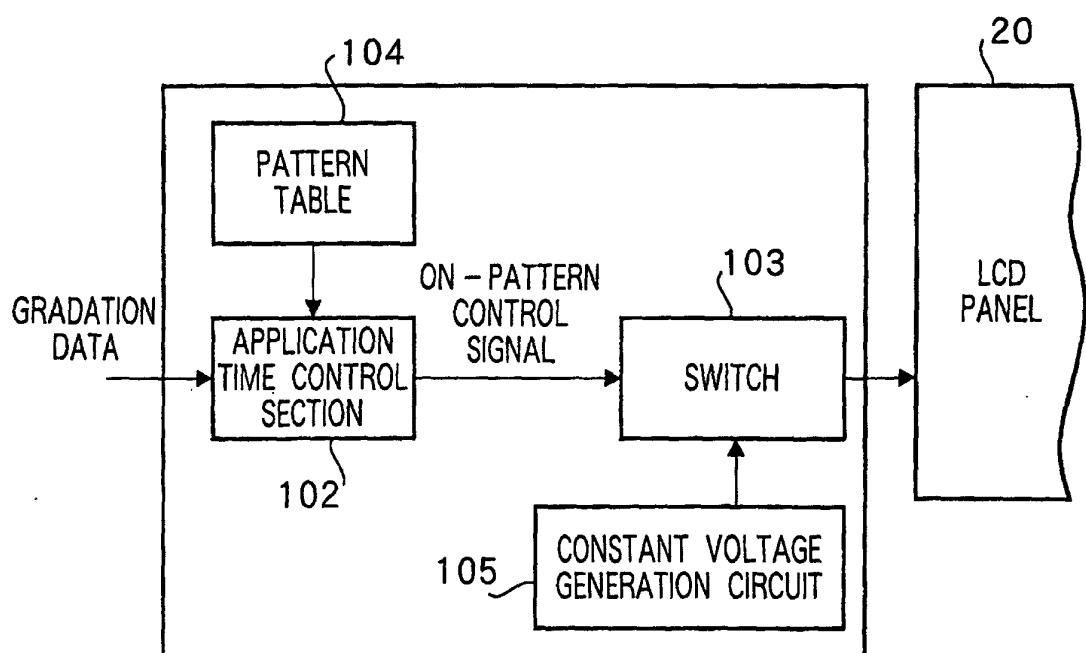


FIG. 9

GRADATION	ON PATTERN
0	#1
1	#2
2	#3
⋮	⋮

FIG. 10

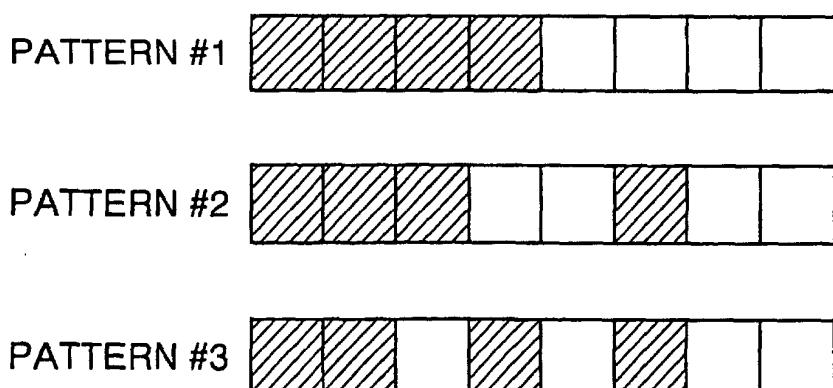


FIG. 11

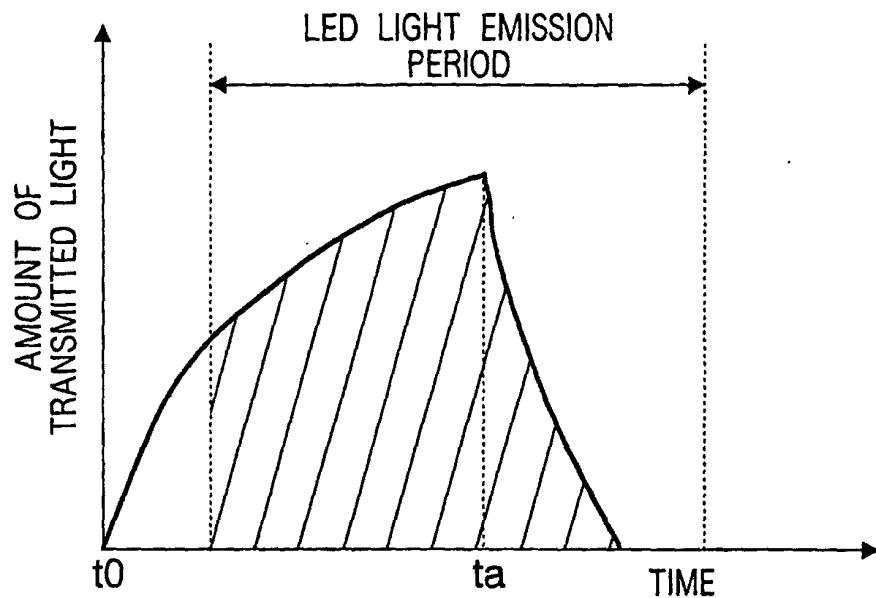


FIG. 12A

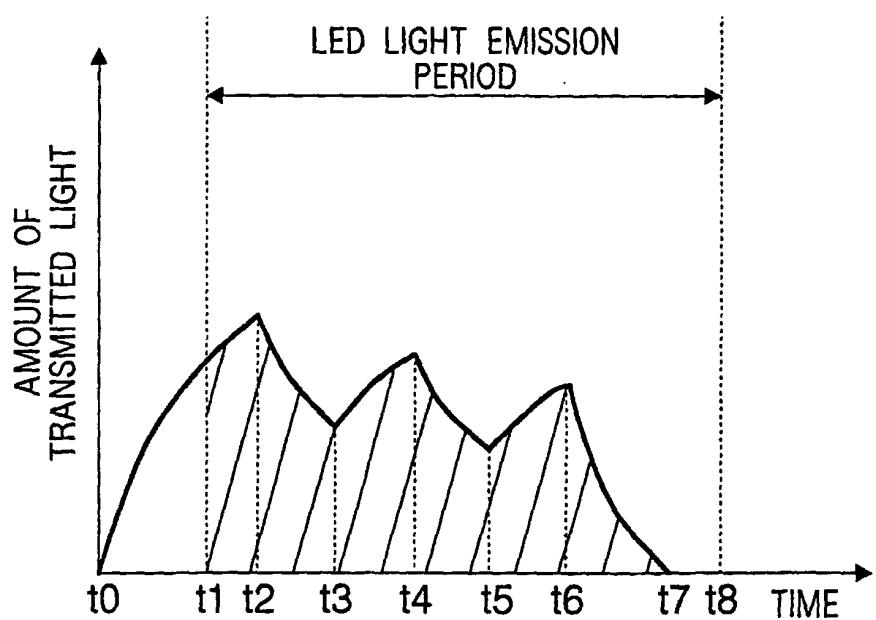


FIG. 12B

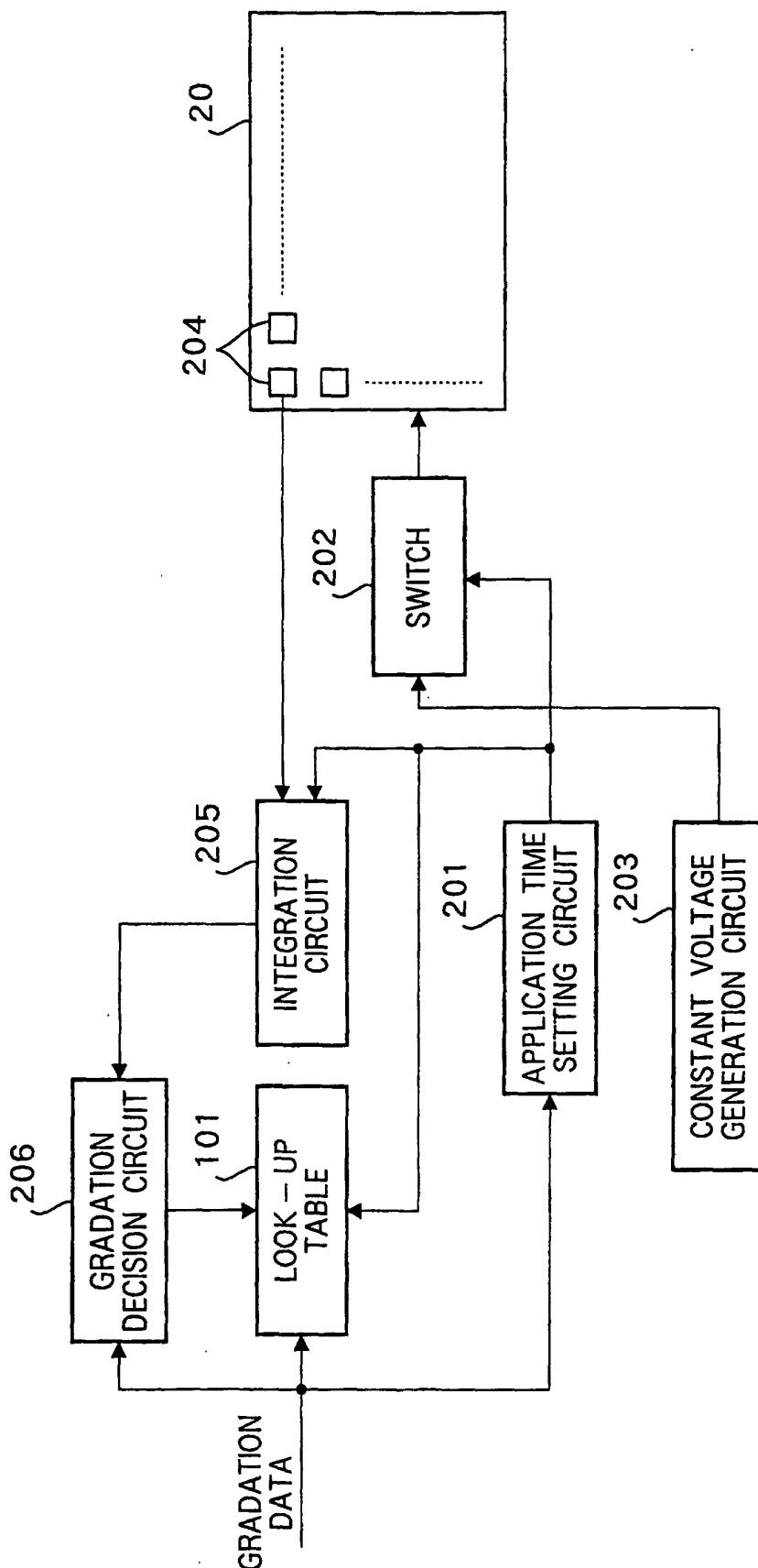
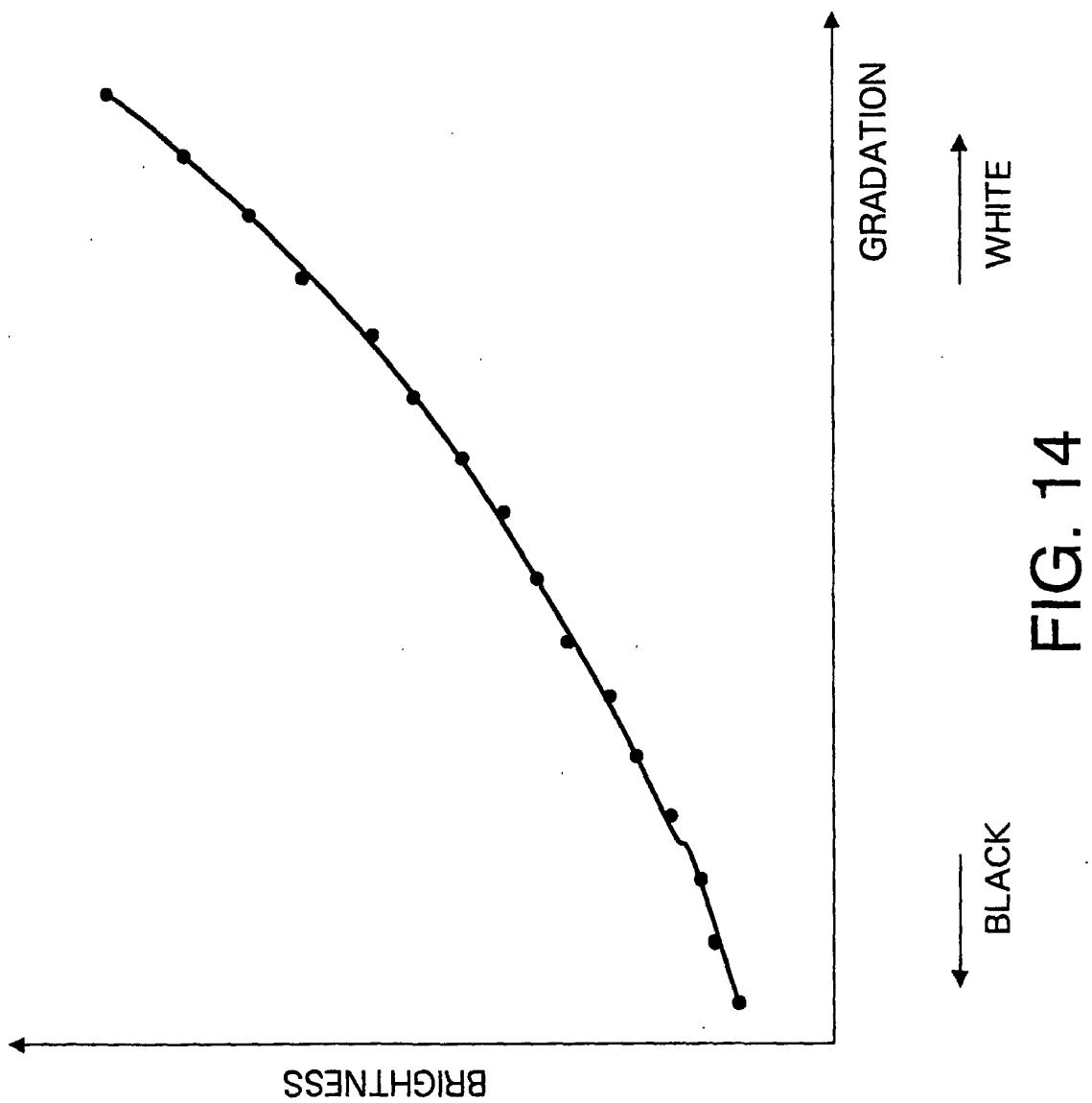


FIG. 13



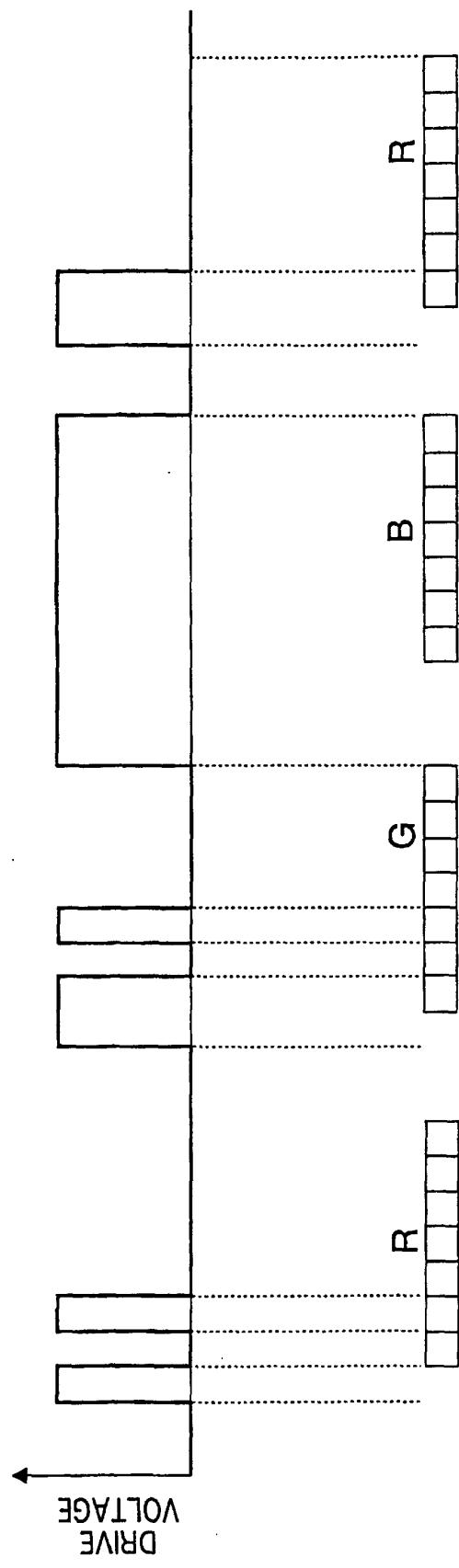


FIG. 15A

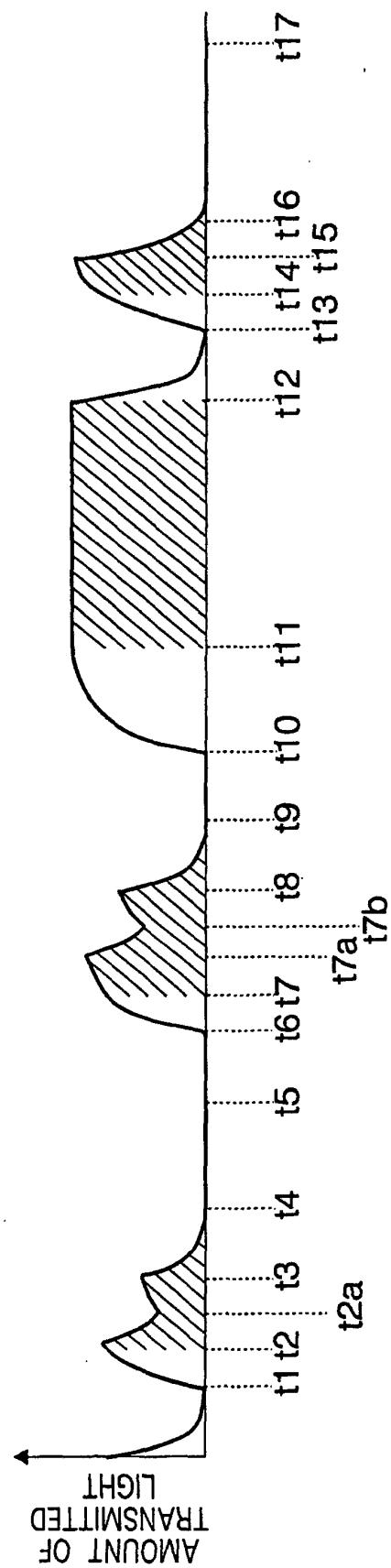


FIG. 15B

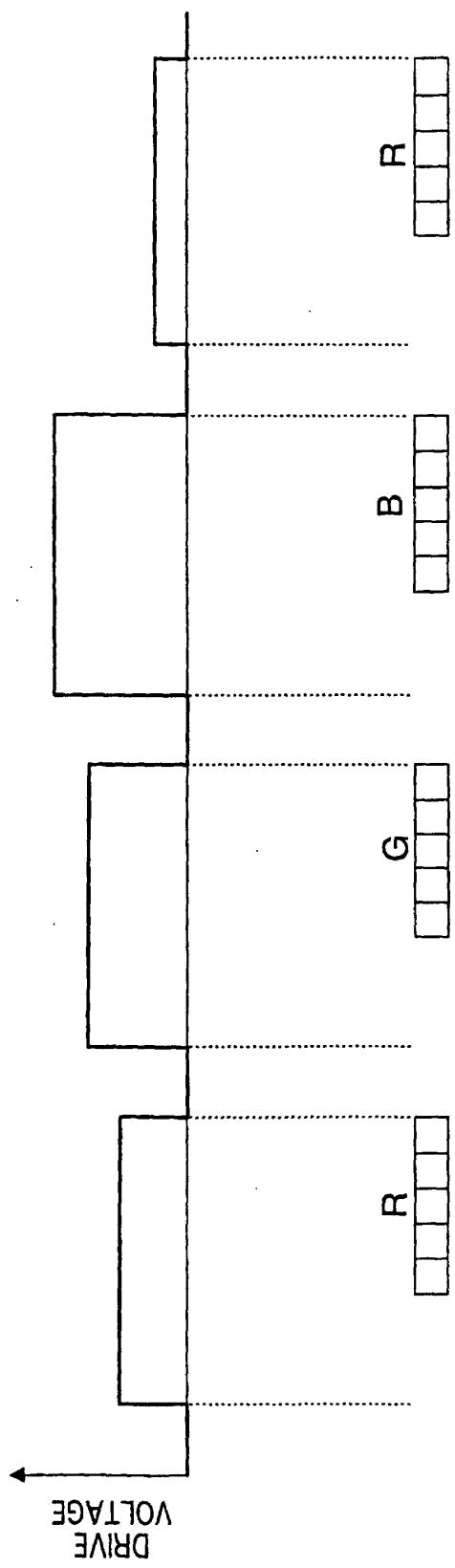


FIG. 16A

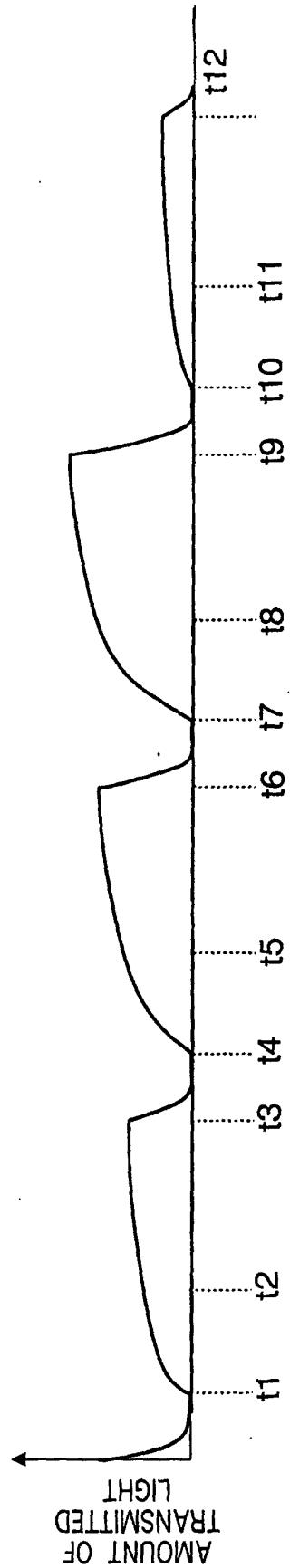


FIG. 16B

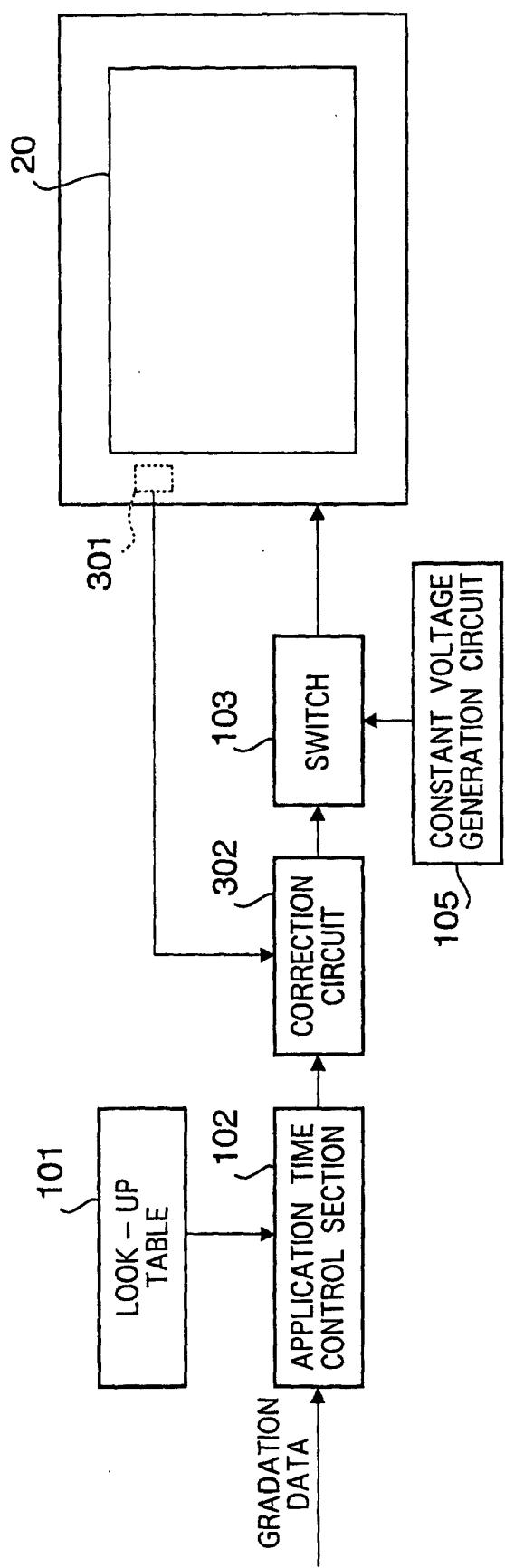


FIG. 17

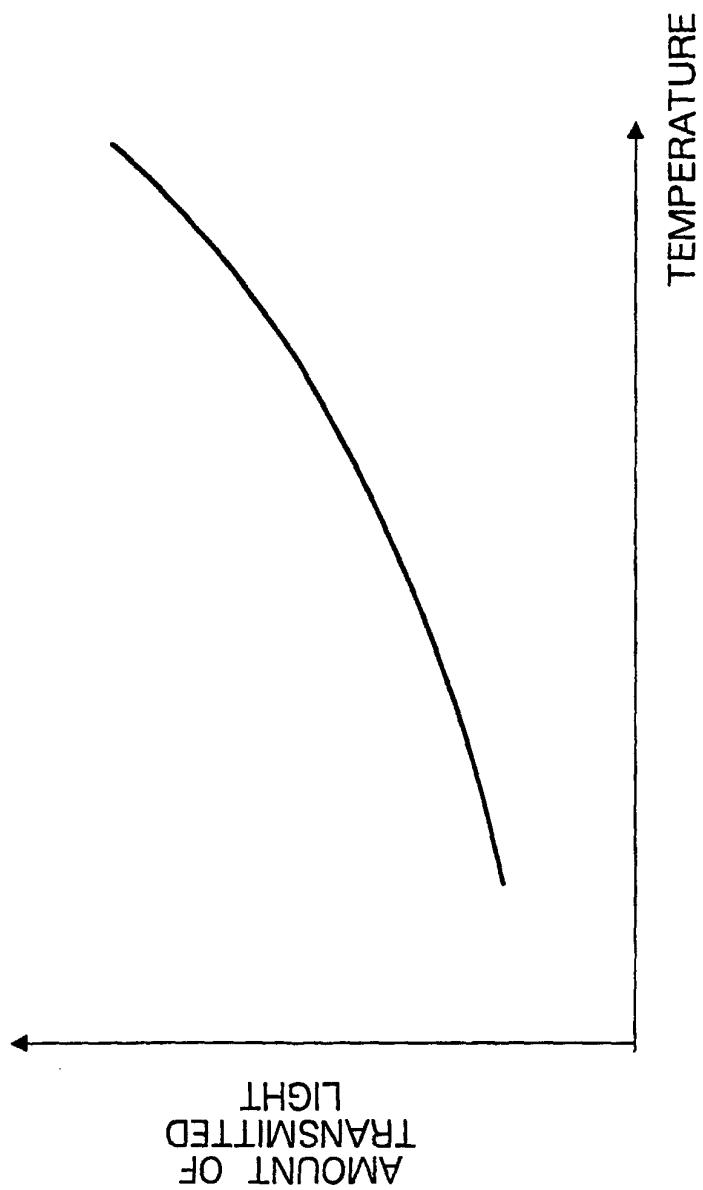


FIG. 18

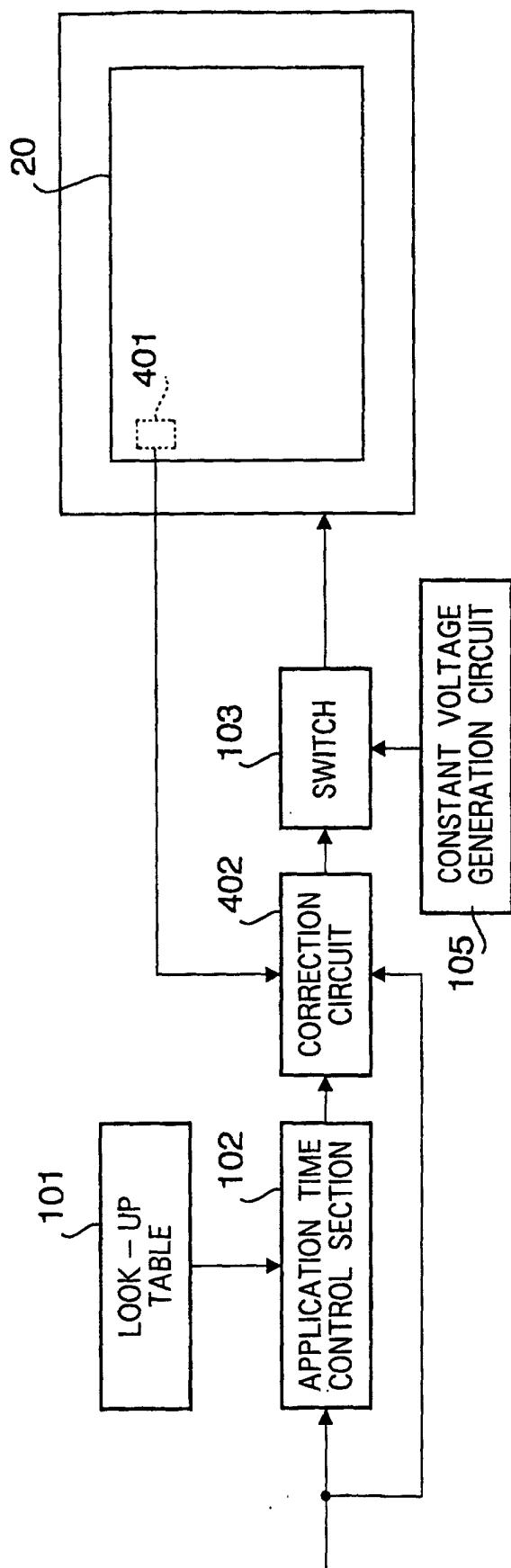


FIG. 19

INTERNATIONAL SEARCH REPORT		International application No. PCT/JP01/11247															
A. CLASSIFICATION OF SUBJECT MATTER Int.Cl ⁷ G09G3/36, G09G3/20, G02F1/133																	
According to International Patent Classification (IPC) or to both national classification and IPC																	
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) Int.Cl ⁷ G09G3/36, G09G3/20, G02F1/133																	
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1922-1996 Toroku Jitsuyo Shinan Koho 1994-2002 Kokai Jitsuyo Shinan Koho 1971-2002 Jitsuyo Shinan Toroku Koho 1996-2002																	
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)																	
C. DOCUMENTS CONSIDERED TO BE RELEVANT <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: left; padding: 2px;">Category*</th> <th style="text-align: left; padding: 2px;">Citation of document, with indication, where appropriate, of the relevant passages</th> <th style="text-align: left; padding: 2px;">Relevant to claim No.</th> </tr> </thead> <tbody> <tr> <td style="text-align: center; padding: 2px;">X</td> <td style="text-align: left; padding: 2px;">JP, 8-54859, A (Casio Computer Co., Ltd.), 27 February, 1996 (27.02.96), Par. Nos. [0068] to [0076]; Figs. 4 to 5</td> <td style="text-align: center; padding: 2px;">1-3</td> </tr> <tr> <td style="text-align: center; padding: 2px;">Y</td> <td style="text-align: left; padding: 2px;">Par. Nos. [0068] to [0076]; Figs. 4 to 5 (Family: none)</td> <td style="text-align: center; padding: 2px;">4-14</td> </tr> <tr> <td style="text-align: center; padding: 2px;">Y</td> <td style="text-align: left; padding: 2px;">JP, 3-134624, A (Matsushita Electronics Corp.), 07 June, 1991 (07.06.91), Full text; Figs. 1 to 5 (Family: none)</td> <td style="text-align: center; padding: 2px;">4-14</td> </tr> <tr> <td style="text-align: center; padding: 2px;">Y</td> <td style="text-align: left; padding: 2px;">JP, 3-134695, A (Matsushita Electronics Corp.), 07 June, 1991 (07.06.91), Full text; Figs. 1 to 5 (Family: none)</td> <td style="text-align: center; padding: 2px;">4-14</td> </tr> </tbody> </table>			Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.	X	JP, 8-54859, A (Casio Computer Co., Ltd.), 27 February, 1996 (27.02.96), Par. Nos. [0068] to [0076]; Figs. 4 to 5	1-3	Y	Par. Nos. [0068] to [0076]; Figs. 4 to 5 (Family: none)	4-14	Y	JP, 3-134624, A (Matsushita Electronics Corp.), 07 June, 1991 (07.06.91), Full text; Figs. 1 to 5 (Family: none)	4-14	Y	JP, 3-134695, A (Matsushita Electronics Corp.), 07 June, 1991 (07.06.91), Full text; Figs. 1 to 5 (Family: none)	4-14
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X	JP, 8-54859, A (Casio Computer Co., Ltd.), 27 February, 1996 (27.02.96), Par. Nos. [0068] to [0076]; Figs. 4 to 5	1-3															
Y	Par. Nos. [0068] to [0076]; Figs. 4 to 5 (Family: none)	4-14															
Y	JP, 3-134624, A (Matsushita Electronics Corp.), 07 June, 1991 (07.06.91), Full text; Figs. 1 to 5 (Family: none)	4-14															
Y	JP, 3-134695, A (Matsushita Electronics Corp.), 07 June, 1991 (07.06.91), Full text; Figs. 1 to 5 (Family: none)	4-14															
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.																	
* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed																	
"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family																	
Date of the actual completion of the international search 25 March, 2002 (25.03.02)		Date of mailing of the international search report 09 April, 2002 (09.04.02)															
Name and mailing address of the ISA/ Japanese Patent Office		Authorized officer															
Facsimile No.		Telephone No.															

INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP01/11247

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP, 63-231423, A (Fujitsu Corp.), 27 September, 1988 (27.09.88), Page 1, right column, line 17 to page 2, upper left column, line 12; Fig. 5	9
Y	Page 2, upper left column, line 13 to page 2, upper right column, line 3; Fig. 6 (Family: none)	10
Y	JP, 5-119733, A (NEC Home Electronics Ltd.), 18 May, 1993 (18.05.93), Par. Nos. [0023] to [0036]; Fig. 1 (Family: none)	9
Y	JP, 11-296150, A (Masaya OKITA), 29 October, 1999 (29.10.99), Par. Nos. [0022] to [0026]; Fig. 3 & EP 949605 A1 & CN 1232243 A & CA 2268357 A1 & TW 440808 A & KR 99081727 A	11-13

Form PCT/ISA/210 (continuation of second sheet) (July 1998)

专利名称(译)	液晶驱动装置和灰度显示方法		
公开(公告)号	EP1345202A1	公开(公告)日	2003-09-17
申请号	EP2001272283	申请日	2001-12-21
[标]申请(专利权)人(译)	株式会社互联 OZAKI YUTAKA		
申请(专利权)人(译)	HUNET INC. OZAKI , YUTAKA		
当前申请(专利权)人(译)	OZAKI , YUTAKA HUNET DISPLAY TECHNOLOGY INC.		
[标]发明人	OZAKI YUTAKA		
发明人	OZAKI, YUTAKA		
IPC分类号	G09G3/20 G09G3/36 G02F1/133		
CPC分类号	G09G3/3688 G09G3/2014 G09G3/2018 G09G2310/027 G09G2320/0252 G09G2320/0285 G09G2320/029 G09G2320/041 G09G2320/0626		
优先权	2000391136 2000-12-22 JP 2001218440 2001-07-18 JP		
其他公开文献	EP1345202A4 EP1345202B1		
外部链接	Espacenet		

摘要(译)

在液晶的单位驱动周期期间，将具有预定图案的电压施加到液晶以驱动液晶，并且考虑通过对透射光的量进行积分而获得的值来设定根据灰度数据的施加图案。当每种施加图案应用于液晶时，在各个时间点的液晶。即使仅通过最大额定电压的ON / OFF驱动液晶，这也允许精细的灰度显示。结果，可以高速驱动液晶并产生多级显示。

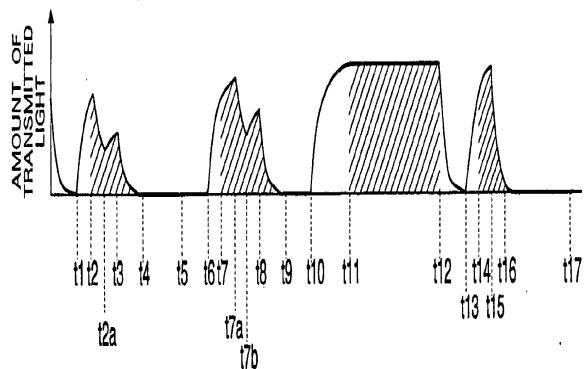


FIG. 15B