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(54) LIQUID CRYSTAL DISPLAY DEVICE

(57) Provided is a liquid crystal display device (10) including: a first substrate (100) having elements configured to drive pixels; a second substrate (200) having color filters (220) corresponding to the pixels; and a sealant (300) configured to bond the first substrate (100) and the

second substrate (200) to each other. The first substrate (100) includes a first protruding structure (370) located in an area in contact with the sealant (300) and the second substrate (200) includes a second protruding structure (360) located in an area in contact with the sealant (300).

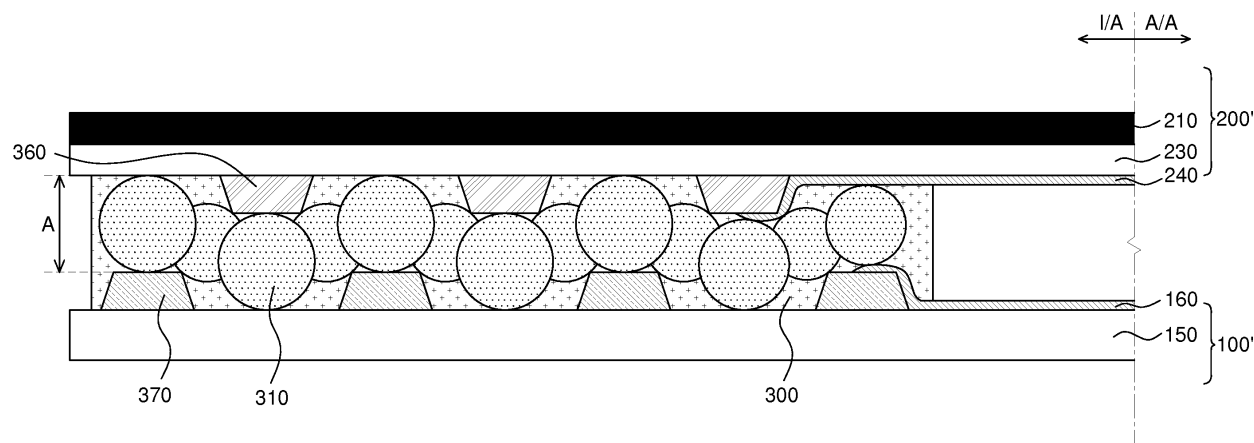


FIG. 6

Description

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] This application claims the priority of Korean Patent Application No. 10-2015-0122812 filed on August 31, 2015, in the Korean Intellectual Property Office, the disclosure of which is incorporated herein by reference.

BACKGROUND

Field

[0002] The present disclosure relates to a liquid crystal display device and a structure thereof.

Description of the Related Art

[0003] A liquid crystal display device has a high contrast ratio and is suitable to display a moving image. Further, since power consumption of the liquid crystal display device is low, the liquid crystal display device is utilized in various fields such as a notebook computer, a monitor, or a TV. A molecular structure of a liquid crystal is thin and long. The liquid crystal has an optical anisotropy having a directivity of an alignment and a polarization property by which when the liquid crystal is located in an electric field, an orientation of the molecules changes depending on an intensity of the electric field. Therefore, the liquid crystal display device implements an image using the optical anisotropy and the polarization property of the liquid crystal.

[0004] Generally, a liquid crystal display device includes a liquid crystal panel formed by interposing a liquid crystal layer between two opposing substrates that are bonded to each other. Electrodes, formed on inner surfaces of two substrates, are used to change an orientation of the liquid crystal molecules by an electric field applied thereto, thereby causing difference of light transmittances. The difference of transmittances by the liquid crystal alignment is reflected to a color combination which is implemented while light supplied from a backlight passes through a color filter to be displayed as a color image.

[0005] A typical liquid crystal display device manufacturing process may be classified into a substrate manufacturing process for forming an array substrate and a color filter substrate, a cell process of completing a liquid crystal panel, and a module process of integrating the liquid crystal panel and the backlight.

[0006] During the process of completing a liquid crystal panel, a sealant is formed at an outer periphery of a substrate to bond the array substrate and the color filter substrate to each other. The sealant also serves to maintain a cell gap. Here, the present inventors recognized that the adhesion of the sealant between both substrates should be reinforced to increase stability and reliability of the liquid crystal panel.

SUMMARY

[0007] An object of the present disclosure is to provide a liquid crystal display device. More specifically, an object of the present disclosure is to provide a structure which reinforces an adhesion of the sealant between both substrates. Further, another object of the present disclosure is to provide the above-mentioned structure through a simple process.

[0008] According to an embodiment of the present disclosure, there is provided a liquid crystal display device. The liquid crystal display device includes: a first substrate having elements configured to driving pixels; a second substrate having color filters corresponding to the pixels; and a sealant configured to bond the first substrate and the second substrate to each other. Herein, the first substrate may include a first protruding structure located in an area in contact with the sealant, and the second substrate may include a second protruding structure located in an area in contact with the sealant.

[0009] According to another embodiment of the present disclosure, there is provided a liquid crystal display device. The liquid crystal display device includes: a pair of substrates including an active area where images are displayed and an inactive area adjacent to or surrounding the active area; and a bonding member located between the pair of substrates, enclosing the active area in a partial area of the inactive area and bonding the pair of substrates to each other. Herein, the pair of substrates may have a structure configured to reinforce an adhesion of the bonding member with respect to the pair of substrates.

[0010] According to an exemplary embodiment of the present disclosure, a liquid crystal display device with a reinforced adhesion between a substrate and a bonding member is provided. Further, according to the exemplary embodiment of the present disclosure, an adhesion between upper and lower substrates may be stably maintained even in a display device which is designed to have a narrow bezel.

BRIEF DESCRIPTION OF THE DRAWINGS

[0011] The above and other aspects, features and other advantages of the present disclosure will be more clearly understood from the following detailed description taken in conjunction with the accompanying drawings, in which:

FIG. 1 is a schematically plane view of a display device according to an exemplary embodiment of the present disclosure;

FIG. 2 is a plane view illustrating a part of an active area of a liquid crystal display device according to an exemplary embodiment of the present disclosure; FIG. 3 is cross-sectional views taken along the line A1-A2 and the line B1-B2 of FIG. 2;

FIG. 4 is a cross-sectional view illustrating a part of

an inactive area of a liquid crystal display device;
 FIG. 5 is a cross-sectional view illustrating a seal area of a liquid crystal display device;
 FIG. 6 is a cross-sectional view illustrating a seal area of a liquid crystal display device according to an exemplary embodiment of the present disclosure; and
 FIG. 7 is a plane view illustrating a seal area of a liquid crystal display device according to an exemplary embodiment of the present disclosure.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

[0012] In describing components of the exemplary embodiment of the present disclosure, terminologies such as first, second, A, B, (a), (b), and the like may be used. The term is used to distinguish a component from the other component but a nature, an order, or the number of the components is not limited by the terminology. If it is described that a component is "connected" or "coupled" to another component, it is understood that the component is directly connected or coupled to the other component but another component may be "connected" or "coupled" between the components. When an element or layer is disposed "on" other element or layer, another layer or another element may be interposed directly on the other element or therebetween. A size and a thickness of each component illustrated in the drawing are illustrated for convenience of description, and the present disclosure is not limited to the size and the thickness of the component illustrated.

[0013] The features of various embodiments of the present disclosure can be partially or entirely bonded to or combined with each other and can be interlocked and operated in technically various ways by those skilled in the art, and the embodiments can be carried out independently of or in association with each other.

[0014] Hereinafter, various exemplary embodiments of the present disclosure will be described in detail with reference to accompanying drawings.

[0015] FIG. 1 is a schematically plane view of a display device according to an exemplary embodiment of the present disclosure.

[0016] A liquid crystal display device will be described as an example of the above-mentioned display device. A liquid crystal display device 10 may include a thin film transistor (TFT) array substrate and a color filter substrate. A liquid crystal layer is interposed between the TFT array substrate and the color filter substrate.

[0017] There is an active area A/A where an image is displayed in a part of an area where the TFT array substrate 100 and the color filter substrate 200 are bonded to each other. In the active area, an array of pixels is disposed. One or more inactive areas I/A may be disposed around the active area. That is, the inactive area(s) I/A may be adjacent to one or more sides of the active area A/A. In FIG. 1, the inactive area encloses a quad-

angular active area. However, a shape of the active area and a shape and an arrangement of the inactive area are not limited to an example illustrated in FIG. 1. The active area and the inactive area may have a shape suitable for a design of an electronic apparatus mounted with the display device 10. An exemplary shape of the active area is a pentagon, a hexagon, a circle, or an oval.

[0018] The inactive area I/A is located at the outside of the active area A/A. In the inactive area I/A, an interface unit, a plurality of wiring lines, or the like, is located. In the interface unit, a plurality of connecting interfaces (for example, a pad, a pin, or the like) which is connected to a wiring line extending to the active area is disposed.

[0019] Each pixel in the active area may be associated with a pixel circuit. The pixel circuit may include one or more transistors. Each pixel circuit may be electrically connected to a gate line and a data line in order to communicate with one or more driving circuits such as a gate driver and a data driver located in the inactive area.

[0020] The driving circuit may be implemented by a thin film transistor TFT in the inactive area. The driving circuit may be referred to as a gate-in-panel (GIP). Further, some components such as a data driver IC is mounted on a divided printed circuit board and is coupled to the connecting interface (a pad, a pin, or the like) disposed in the inactive area using a circuit film such as a flexible printed circuit board (FPCB), a chip-on-film (COF), or a tape-carrier-package (TCP). The printed circuit (COF, PCB, or the like) may be located at a rear side of the display device 10.

[0021] The display device 10 may include various additional elements to generate various signals and/or to drive the pixels in the active area. The additional element related to driving a pixel may include an inverter circuit, a multiplexer, or an electrostatic discharging circuit. The display device 10 may further include additional elements associated with functions other than driving a pixel. For example, the display device 10 may include additional elements which provide a touch sensing function, a user authentication function (for example, finger print recognition), a multilevel touch pressure sensing function, or a tactile feedback function. The above-mentioned additional elements may be located in an external circuit which is connected to the inactive area and/or the connecting interface.

[0022] Red, blue, and green color filters (or color refiners) can be disposed on a so-called color filter substrate corresponding to the active area.

[0023] A TFT array substrate and a color filter substrate may be implemented through processes such as thin film deposition, photo-lithography, or etching. Thereafter, a seal pattern to bond the TFT array substrate and the color filter substrate is formed on any one of the substrates and the two substrates are bonded to each other to interpose a liquid crystal layer therebetween, thereby forming a liquid crystal panel. After attaching a polarizer and/or a driving circuit onto the liquid crystal panel completed as described above, the liquid crystal panel is in-

tegrated with a backlight unit to form a liquid crystal display device.

[0024] In this case, as a sealant 300 which bonds the color filter substrate and the TFT array substrate, a thermosetting resin hardened by heating or an UV curable resin hardened by UV ray may be used. The UV curable resin may include a monomer including a photo initiator or an UV curing agent and glass fiber to maintain a cell gap. After applying the sealant on an edge of one or both substrates, when UV rays are radiated from a mercury UV lamp or a metal halide lamp after or while injecting liquid crystal between both substrates, the sealant is hardened to bond both substrates.

[0025] FIG. 2 is a plane view illustrating a part of an active area of a liquid crystal display device according to an exemplary embodiment of the present disclosure and FIG. 3 is cross-sectional views taken along the line A1-A2 and the line B1-B2 of FIG. 2.

[0026] In FIGS. 2 and 3, some of pixels of the liquid crystal display device and some of a plurality of spacers are illustrated. Referring to FIGS. 2 and 3, the liquid crystal display device includes a first substrate (a TFT array substrate) 100, a second substrate (a color filter substrate) 200 and a liquid crystal layer formed between the first substrate 100 and the second substrate 200. The first substrate 100 may include a thin film transistor 110, a planarization layer 120 and a common electrode 130. The second substrate 200 may include a black matrix 210, a color layer 220, and column spacers 250 and 260. For the convenience of description, only some of components of the liquid crystal display device 10 are illustrated in FIGS. 2 and 3.

[0027] The first substrate 100 is a TFT array substrate and may be formed of an insulating material such as silicon (Si), glass, plastic, or a polymer film. However, the material for the substrate 100 is not limited to the above-mentioned insulating material, but a material which supports a plurality of layers and elements formed on the substrate 100 may be satisfactorily used. A plurality of pixels (simply referred to as pixels or sub pixels) and elements (a transistor, a capacitor, or the like) which drive the pixels are disposed on the first substrate 100. A pixel area may be defined as an area formed when a gate line GL and a data line DL, which transfer signals to the driving element, intersect each other. An aperture area through which light passes to display an image and a shielding area through which light does not pass are formed on the first substrate 100.

[0028] A plurality of pixel areas is defined in the first substrate 100. In each pixel area, at least one thin film transistor (TFT) 110 serving as a switching element is formed. The thin film transistor 110 is disposed corresponding to each sub pixel R, G, or B. The thin film transistor 110 includes a gate electrode, an active layer, a source electrode, and a drain electrode. The active layer between the source electrode and the drain electrode becomes a channel of the thin film transistor 110. The gate electrode, the source electrode, and the drain elec-

trode may be configured by a single layer or a multilayer consisting of a metal material having a low resistive characteristic, for example, aluminum (Al), copper (Cu), molybdenum (Mo), chrome (Cr), gold (Au), titanium (Ti), nickel (Ni), neodymium (Nd), or an alloy thereof. The active layer may be configured by an amorphous silicon layer, a polycrystalline silicon layer obtained by crystallizing amorphous silicon, an oxide semiconductor, an organic semiconductor, or the like.

[0029] When the thin film transistor 110 has a staggered structure, the gate electrode is located on the first substrate 100 and the gate electrode may be electrically connected to the gate line. A gate insulating layer covers the gate electrode. The active layer having the channel of the thin film transistor 110 is located on the gate insulating layer. The drain electrode and the source electrode are located on the active layer, the drain electrode may be electrically connected to the data line and the source electrode may be electrically connected to the pixel electrode.

[0030] When the thin film transistor 110 has a coplanar structure, the active layer is formed on the substrate. In this case, a buffer layer may be further located between the substrate 100 and the active layer. The buffer layer may protect the thin film transistor from impurities such as alkali ion leaked from the substrate 100. A gate insulating layer may be formed of silicon nitride layer (SiN_x), a silicon oxide layer SiO₂, or the like and is formed on the active layer. The gate insulating layer may be formed in the active area and a pad area. That is, the gate insulating layer may be formed on an entire surface of the substrate 100 on which the active layer is formed. However, the present disclosure is not limited thereto. The gate electrode is located on the gate insulating layer. The gate electrode may be formed to overlap the active layer in the active area. In the meantime, the gate electrode and the gate line may be formed to be integrated. An inter insulation layer may be formed of a silicon nitride layer or a silicon oxide layer and is located on the gate electrode. The source/drain electrode is formed on the inter insulation layer. In this case, the inter insulation layer may include a plurality of contact holes. The source electrode and the drain electrode are spaced apart from each other with a predetermined interval to be electrically connected to the active layer. More specifically, a semiconductor layer contact hole which exposes the active layer is formed on the gate insulating layer and the inter insulation layer. The source/drain electrode is electrically connected to the active layer through the semiconductor layer contact hole.

[0031] The planarization layer 120 is located to cover the thin film transistor 110. The planarization layer 120 flattens an upper portion of the thin film transistor 110. The planarization layer 120 is formed of an organic insulating material having a low permittivity such as photo acrylic material, on the entire surface of the first substrate 100. Such a planarization layer 120 removes an unevenness of the surface of the first substrate 100 caused by

the thin film transistor 110.

[0032] The common electrode 130 for supplying common voltage V_{com} to the pixel is located on the planarization layer 120. The common electrode 130 is an electrode to drive the liquid crystal and is formed in a single pattern in an area excepting an area where a contact hole to be electrically connected to the source electrode of the thin film transistor 110 is formed. The common electrode 130 may be electrically connected to the common line which is disposed in parallel to the gate line, through a separate contact hole. The common electrode 130 may be formed of a transparent conductive material such as indium tin oxide (ITO) or indium zinc oxide (IZO). The common electrode 130 may be formed on an entire surface of the first substrate 100.

[0033] A passivation layer 150 covers the common electrode 130. The passivation layer 150 may be formed of an inorganic material such as silicon oxide SiO_2 , silicon nitride $SiNx$, or the like. Further, the passivation layer 150 may be formed not only of the inorganic material, but also of an organic material. The passivation layer 150 protects the common electrode 130 and flattens an upper portion of the common electrode 130. The passivation layer 150 may be formed of the same material as the planarization layer 120 or may be formed of a material different from the planarization layer 120.

[0034] The pixel electrode is located on the passivation layer 150. The pixel electrode is an electrode to drive the liquid crystal and is formed in each pixel to have a particular shape, such as a rectangle or box. Further, the pixel electrode may be formed on the passivation layer 150 to have a plurality of slits or openings. The pixel electrode is electrically connected to the drain electrode of the thin film transistor 110. The pixel electrode may be formed such that a center thereof is bent or angled at least one time. The pixel electrode and the common electrode 130 may be formed of a transparent conductive material. The common electrode 130 may be located above the pixel electrode or the pixel electrode and the common electrode 130 may be located on the same layer.

[0035] A first protruding structure 170 which protrudes toward the color filter substrate 200 is located in an area overlapping the thin film transistor 110 on the passivation layer 150. The first protruding structure may be referred to as a bump spacer (or bump or spacer or the like). The bump spacer 170 has a circular or a bar shaped cross-section. In FIG. 3, the bump spacer 170 having a bar shaped cross-section is disposed in the same direction as a gate line and located over the blue pixel and the red pixel.

[0036] A first alignment layer 160 is located on the passivation layer 150 and the bump spacer 170. The first alignment layer 160 may be formed of polyimide (PI) and acts to adjust an orientation of the liquid crystal.

[0037] The second substrate 200 is a color filter substrate and is opposite to the first substrate 100. The second substrate 200 may include at least one among a

black matrix 210, red (R), green (G), and blue (B) color filters 220, an overcoating layer 230, column spacers 250 and 260, and a second alignment layer 240. The black matrix 210 can be formed to correspond to the shielding area, and the red (R), green (G), and blue (B) color filters 220 are formed to correspond to the aperture area.

[0038] The black matrix 210 corresponds to a boundary of the pixel area and may act to suppress light leakage. The black matrix 180 is formed of an opaque material. Such a black matrix 210 may be formed to overlap the thin film transistor 110 formed on the first substrate 100, to minimize the aperture area of the pixel from being reduced. That is, in an area corresponding to the shielding area, various driving elements and wiring lines such as the thin film transistor 110, the data line, and the gate line are located. In an area defined as the aperture area, the pixel electrode and the common electrode are located.

[0039] The color filters 220 are formed by selectively applying and removing red, green, and blue color pigments using a mask, and such color filters 220 are used to display a color image.

[0040] The overcoating layer 230 covers the black matrix 210 and the color filter 220. The overcoating layer 230 is a layer which allows the black matrix 210 and the red, green, and blue color filters 220 to flatten or planarize their upper portions and is formed of an insulating material. The overcoating layer 230 may be formed of the same material as the planarization layer 120.

[0041] A step (or some sort of uneven portion) is formed on a surface of the first substrate 100 along a profile of the first protruding structure 170. Second protruding structures 250 and 260 are located above the overcoating layer 230 of the second substrate 200 so as to correspond the first protruding structure (e.g. bump spacer) 170 of the first substrate 100. The second protruding structure may be a column spacer. The column spacer may include a first spacer (e.g. gap spacer) 250 which maintains an interval (i.e. cell gap) between two substrates 100 and 200 and a second spacer (e.g. push spacer) 260 which forms a push interval (e.g. push gap) between the first substrate 100 and the second substrate 200. The first protruding structure and the second protruding structure may be located in an area where the sealant and the substrates 100 and 200 are in contact with each other, which will be described below.

[0042] The second alignment layer 240 may covers the second protruding structures 250 and 260. The second alignment layer 240 may be formed of polyimide (PI).

[0043] The second protruding structures (e.g. column spacer) may have a circular or bar shaped cross-section. Further, the column spacers 250 and 260 may have tapered appearances. For example, the gap spacer 250 which is in contact with the overcoating layer 230 may have a tapered shape in which a width of an upper end is 16 μm and a width of the lower end is 12 μm .

[0044] The upper end and the lower end of the push spacer may be formed to have the same width as the

gap spacer. Alternatively, the widths of the upper end and the lower end of the push spacer 260 may be larger or smaller than that of the gap spacer 250. The gap spacer 250 and the push spacer 260 may be simultaneously formed by a single mask process using a half tone mask.

[0045] The gap spacer 250 and the push spacer 260 (vertically or in a stacked manner) overlap the bump spacer 170 of the first substrate 100 and the black matrix 210 of the second substrate 200. The surface of the first substrate 100 protrudes along the profile of the bump spacer 170 formed on the first substrate 100. Further, the gap spacer 250 is formed in an area corresponding to the bump spacer 170 to maintain the cell gap between the first substrate 100 and the second substrate 200. In this case, the cell gap between the first substrate 100 and the second substrate 200 may be 3.0 to 3.3 μm . Here, a width of the upper end of the gap spacer 250 may be smaller than a width of the bump spacer 170. That is, the bump spacer 170 may have a width larger than that of the gap spacer 250.

[0046] The push spacer 260 is formed to have a lower height than that of the gap spacer 250. The surface of the first substrate 100 protrudes along the profile of the bump spacer 170 formed on the first substrate 100. Further, the push spacer 260 is formed in an area corresponding to the bump spacer 170 so that a push gap is formed between the first substrate 100 and the second substrate 200. In this case, a width of the upper end of the push spacer 260 may be smaller than a width of the bump spacer 170. That is, the bump spacer 170 may have a width larger than that of the push spacer 260.

[0047] The gap spacer 250 and the push spacer 260 may be disposed corresponding to the shielding area between the blue pixel and the red pixel. However, the present disclosure is not limited thereto, but the gap spacer 250 and the push spacer 260 may be disposed corresponding to the shielding area between the red pixel and the green pixel.

[0048] A particular push gap (for example, 5000 to 6000 Å) may be formed between the push spacer 260 and the bump spacer 170. The push gap may suppress a liquid crystal panel from being broken or damaged when an external force is applied to the second substrate 200.

[0049] Even though the gap spacer 250 and the push spacer 260 are allowed to freely move around (e.g. to the left and right) if an external force is applied thereto, the bump spacer 170 does not allow the gap spacer 250 and the push spacer 260 to be in contact with the first alignment 160 in the active area. Therefore, the damage (e.g. the alignment direction of the first alignment 160 being twisted or scratches being formed on the first alignment 160) on the first substrate caused by the undesired contact of the first alignment 160 of the active area and the gap spacer 250 and/or the push spacer 260 may be effectively suppressed. Further, when the push spacer 260 is pushed as a result of the external force, friction is increased by the bump spacer 170 so that the gap spacer 250 and the push spacer 260 may move less.

[0050] FIG. 4 is a cross-sectional view illustrating a part of an inactive area of a liquid crystal display device.

[0051] The liquid crystal display device 10 may be configured by a first substrate 100 and a second substrate 200. In this case, the first substrate 100 may be a TFT array substrate and the second substrate 200 may be a color filter substrate. A bonding member 300 which bonds the first substrate 100 and the second substrate 200 to each other is provided between the two substrates. The bonding member 300 may enclose the edges of two substrates.

[0052] Pixel elements are provided in the active area A/A of the first substrate 100. A color filter which implements colors and a black matrix which suppresses undesirable mixture of colors are provided in the active area of the second substrate 200. Various wiring lines and driving elements are disposed at the outer periphery (i.e. inactive area) of the first substrate and the second substrate.

[0053] The bonding member 300 is used to bond two substrates 100 and 200. The bonding member 300 is provided in a seal area. When the seal area is located in the inactive area I/A, a width of the inactive area is reduced, which is advantageous to reduce an overall size of the liquid crystal display device 10. The bonding member 300 may be a sealant that bonds the two substrates 100 and 200 to each other by way of a thermal and/or UV curing process.

[0054] In some cases, the alignment layers 160 and 240 are applied on the seal area (i.e. applied to an area where the sealant and the substrates are in contact with each other) over the active area during the manufacturing process of the substrates 100 and 200. In this case, the alignment layers 160 and 240 and the bonding member 300 partially overlap, which may cause the adhesion between the bonding member (for example, sealant) and the substrate to be weakened. Specifically, in a display device to which a narrow bezel is applied, the sealant 300 and the alignment layer 160 may need to overlap to achieve a minimal bezel width.

[0055] Therefore, the first protruding structure and the second protruding structure described with reference to FIGS. 2 and 3 may also be located in an area where the bonding member 300 and top surfaces of the substrates 100 and 200 are in contact with each other. The purpose is to reinforce the adhesion between the bonding member 300 and a pair of the substrates 100 and 200 by increasing a surface area where the bonding member 300 and top surfaces of the two substrates 100 and 200 are in contact with each other. Further, the first protruding structure and the second protruding structure also serve as a dam (or some other barrier-like structure) which prevents the alignment layer 260 from flowing into the seal area. Therefore, the lowering of the adhesion between the sealant and the substrate due to the undesired overflow of alignment layer may be suppressed. That is, when the first protruding structure and the second protruding structure are provided, the adhesion between the sealant and

the substrate may be further increased as compared with a case that such protruding structure is not provided.

[0056] FIG. 5 is a cross-sectional view illustrating a seal area of a liquid crystal display device.

[0057] The seal area is an area where a bonding member (for example, a sealant) is applied to bond upper and lower substrates. That is, the seal area is an area where the bonding member and the upper/lower substrates are in contact with each other.

[0058] The sealant 300 may include glass fiber 310 to maintain a cell gap. In FIG. 5, a substantially spherical glass fiber is illustrated. The spherical glass fiber 310 may have the substantially the same diameter as the interval between both substrates, that is, a cell gap.

[0059] In the illustrated bonding structure, the adhesion between the sealant and the substrate may be weakened, as described above. That is, the adhesion between the sealant and the substrate is weakened due to an organic layer (e.g. alignment layer) which overflows to the seal area. Further, in a display device which employs a design having a narrow bezel, a seal area (in particular, a line width of the sealant) is also reduced, so that the adhesion is highly likely to be lowered.

[0060] FIG. 6 is a cross-sectional view illustrating a seal area of a liquid crystal display device according to an exemplary embodiment of the present disclosure.

[0061] The seal area is located in a part of an inactive area I/A of a display device. The seal area is an area where a bonding member 300 is in contact with upper/lower substrates 100' and 200'. Here, the lower substrate (i.e. first substrate) 100' may refer to an assembly of a supporting substrate and elements disposed on the supporting substrate. Further, the upper substrate (i.e. second substrate) 200' may refer to an assembly of the supporting substrate and elements disposed on the supporting substrate. The first substrate 100' and the second substrate 200' are divided into an active area where pixels are disposed and an inactive area where pixels are not disposed. The pair of substrates 100' and 200' is equipped with structures (e.g. first protruding structure and second protruding structure) which reinforce the adhesion between a bonding member 300 and one of the pair of substrates. The structure may be a plurality of column spacers.

[0062] In FIG. 6, even though it is illustrated that a top layer of the first substrate 100' is a passivation layer 150 or an alignment layer 160, the top layer of the first substrate 100' may be a different type of layer therefrom according to an implemented example. Similarly, the top layer of the second substrate 200' may be a different layer from the illustrated overcoating layer 230 or the alignment layer 240.

[0063] The first layer 100' is a layer on which elements (e.g. a transistor, a capacitor, or the like) driving pixels are disposed. The first substrate 100' includes a first protruding structure 370 which is located in an area which is in contact with the sealant 300.

[0064] The first protruding structure 370 may be the

same structure as the first protruding structure (for example, a bump spacer) 170 described with reference to FIGS. 2 and 3. That is, the first protruding structure may be located in both an area where the sealant 300 and the first substrate are in contact with each other and the active area A/A. In this case, the first protruding structure 370 in the area where the sealant 300 and the first substrate are in contact with each other and the first protruding structure 170 in the active area may be manufactured through the same process. That is, the first protruding structure 370 may be simply formed on the first substrate without performing an additional process.

[0065] The second substrate 200' is a substrate on which color filters corresponding to pixels are disposed. The second substrate 200' includes a second protruding structure 360 which is located in an area which is in contact with the sealant 300.

[0066] The second protruding structure 360 may be the same structure as the second protruding structure (for example, a push spacer) 260 described with reference to FIGS. 2 and 3. That is, the second protruding structure may be located in both an area where the sealant 300 and the second substrate are in contact with each other and the active area A/A. In this case, the second protruding structure 360 in the area where the sealant 300 and the second substrate are in contact with each other and the second protruding structure 260 in the active area may be manufactured through the same process. That is, the second protruding structure 360 may be simply formed on the second substrate without performing an additional process. The second protruding structure 260 may reduce a gap deviation in the sealant. That is, in a part where the cell gap is maintained only by the spherical glass fiber 310, a gap is highly likely to be changed due to an external force, as compared with a part where the cell gap is maintained by the combination of the protruding structures 360 and 370 and the spherical glass fiber 310. Accordingly, when the protruding structures 360 and 370 are more closely disposed, the deviation of the cell gap may be reduced.

[0067] The structures (i.e. the first protruding structure and the second protruding structure) increase a surface area where the bonding member 300 and a pair of substrates 100' and 200' are in contact with each other as compared with the case where such structures are not provided. Therefore, the adhesion between the bonding member 300 and the pair of substrates 100' and 200' may be reinforced compared to the situation where such structures are not employed.

[0068] The bonding member (for example, a sealant) is located between the first substrate 100' and the second substrate 200' corresponding to the inactive area. In the meantime, the sealant 300 may include a spherical glass fiber 310 which is provided to maintain a cell gap between the first and second substrates, as illustrated in FIG. 6. The spherical glass fibers 310 may have a substantially uniform size. Further, the spherical fibers 310 have a particular elasticity.

[0069] The first protruding structure 370 and the second protruding structure 360 have a height determined based on an interval (e.g. cell gap) between the first substrate and the second substrate and a diameter of the glass fiber 310. For example, the first protruding structure 370 and the second protruding structure 360 have a height which is smaller than a difference between a gap between the first substrate and the second substrate and the diameter of the glass fiber. The purpose is to locate the glass fiber 310 between the first protruding structure 370 and the second substrate 200' or the second protruding structure 360 and the first substrate 100'. In this case, the second protruding structure 360 may have a height which is smaller than a difference between the interval (e.g. cell gap) between the first substrate and the second substrate and the height of the first protruding structure 370. That is, the second protruding structure 360 may have the same height as the push spacer 260 which provides a push gap. Differently in the active area, the first protruding structure 370 and the second protruding structure 360 are not (vertically) opposite to each other.

[0070] In the meantime, the first protruding structure 370 and the second protruding structure 360 may be spaced apart to have a distance in which the spherical glass fiber is located therebetween. That is, a shortest distance between the first protruding structure 370 and the second protruding structure 360 may be equal to or larger than the diameter of the glass fiber.

[0071] The first protruding structure 370 and the second protruding structure 360 may control a range where the alignment layer expands to the outer periphery. That is, as illustrated in FIG. 6, the overflow of the alignment layers 160 and 240 may be suppressed by the first protruding structure 370 and/or the second protruding structure 360 which are located close to the active area.

[0072] As described above, the first protruding structure 370 and the second protruding structure 360 increase a surface area in which the bonding member 300 and top surfaces of two substrates are in contact with each other, thereby reinforcing the adhesion between the bonding member 300 and the pair of substrates 100 and 200. Further, the first protruding structure and the second protruding structure also serve as a dam (or barrier) which restrains the alignment layer 260 from flowing into the seal area. Therefore, the lowering of the adhesion between the sealant and the substrate due to the alignment layer may be avoided.

[0073] FIG. 7 is a plane view illustrating a seal area of a liquid crystal display device according to an exemplary embodiment of the present disclosure; and

[0074] As described above, in the liquid crystal display device, a first protruding structure 370 and a second protruding structure 360 are located in a seal area. A plane arrangement is illustrated in FIG. 7. That is, the first protruding structure 370 and the second protruding structure 360 may be alternately disposed in a part or all of the seal area.

[0075] The first protruding structure 370 and the second protruding structure 360 may be disposed to have various pattern shapes such as a linear shape or an island shape in accordance with a necessary contact surface area.

[0076] When the first protruding structure 370 and the second protruding structure 360 are disposed in a part of the seal area, the first protruding structure 370 and the second protruding structure 360 may be located closer to the active area A/A. If necessary, the first protruding structure 370 and the second protruding structure 360 may be disposed to be farther from the active area.

[0077] A portion in the entire seal area occupied by the first protruding structure 370 and the second protruding structure 360 may vary depending on a design specification such as a bezel width or a required adhesive strength.

[0078] The exemplary embodiments of the present disclosure can also be described as follows:

[0079] According to an aspect of the present disclosure, a liquid crystal display device includes: a first substrate having elements configured to driving pixels; a second substrate having color filters corresponding to the pixels; and a sealant configured to bond the first substrate and the second substrate to each other. Herein, the first substrate may include a first protruding structure located in an area in contact with the sealant, and the second substrate may include a second protruding structure located in an area in contact with the sealant.

[0080] The first substrate and the second substrate may include an active area where the pixels are disposed and an inactive area surrounding the active area and the sealant may be disposed between the first substrate and the second substrate corresponding to the inactive area.

[0081] The sealant may include a spherical glass fiber.

[0082] The first protruding structure and the second protruding structure may have heights determined based on an interval or cell gap between the first substrate and the second substrate and a diameter of the glass fiber.

[0083] The heights of the first protruding structure may be smaller than a difference between the interval or cell gap between the first substrate and the second substrate and the diameter of the glass fiber.

[0084] The heights of the second protruding structure may be smaller than a difference between the interval or cell gap between the first substrate and the second substrate and the height of the first protruding structure.

[0085] A shortest distance between the first protruding structure and the second protruding structure may be equal to or larger than a diameter of the glass fiber.

[0086] The first protruding structure may be located in an area where the sealant and the first substrate are in contact with each other and the active area and the second protruding structure may be located in an area where the sealant and the second substrate are in contact with each other and the active area.

[0087] The first protruding structure located in the area where the sealant and the first substrate are in contact

with each other and the first protruding structure located in the active area may be manufactured through the same process.

[0088] The second protruding structure located in the area where the sealant and the second substrate are in contact with each other and the second protruding structure located in the active area may be manufactured through the same process.

[0089] The first and second substrates may include an active area where images are displayed and an inactive area adjacent to or surrounding the active area; and the sealant may enclose the active area in a partial area of the inactive area, wherein the first and second protruding structures may be configured to reinforce an adhesion of the sealant with respect to the first and second substrates.

[0090] The first and second protruding structures may reinforce an adhesion of the sealant and the first and second substrates by increasing a surface area in which the sealant and the first and second substrates are in contact with each other as compared with a case in which the first and second protruding structures are not provided.

[0091] The sealant may include a spherical glass fiber configured to maintain a cell gap between the first and second substrates.

[0092] A height of the first and second protruding structures may be smaller than a difference between the cell gap and a diameter of the glass fiber.

[0093] The first and second protruding structures may be a plurality of column spacers disposed with an interval that is equal to or larger than the diameter of the glass fiber

[0094] According to another aspect of the present disclosure, a liquid crystal display device includes: a pair of substrates including an active area where images are displayed and an inactive area adjacent to or surrounding the active area; and a bonding member located between the pair of substrates, enclosing the active area in a partial area of the inactive area, and bonding the pair of substrates to each other. Herein, the pair of substrates may have a structure configured to reinforce an adhesion of the bonding member with respect to the pair of substrates.

[0095] The structure may reinforce an adhesion of the bonding member and the pair of substrates by increasing a surface area in which the bonding member and the pair of substrates are in contact with each other as compared with a case in which such structure is not provided.

[0096] The bonding member may include a spherical glass fiber configured to maintain a cell gap between the pair of substrates.

[0097] A height of the structure may be smaller than a difference between the cell gap and a diameter of the glass fiber.

[0098] The structure may be a plurality of column spacers disposed with an interval that is equal to or larger than the diameter of the glass fiber.

[0099] It will be appreciated that technical spirit of the present disclosure have been described herein for purposes of illustration by the above description and the accompanying drawings, and that combination, separation, substitution, and modifications of components may be made by those skilled in the art without departing from the scope and spirit of the present disclosure. Therefore, the exemplary embodiments of the present disclosure are provided for illustrative purposes only but not intended to limit the technical concept of the present disclosure. The scope of the technical concept of the present disclosure is not limited thereto. The protective scope of the present disclosure should be construed based on the following claims, and all the technical concepts in the equivalent scope thereof should be construed as falling within the scope of the present disclosure.

Claims

1. A liquid crystal display device (10), comprising:

a first substrate (100) having elements configured to drive pixels;
a second substrate (200) having color filters (220) corresponding to the pixels; and
a sealant (300) configured to bond the first substrate (100) and the second substrate (200) to each other;

wherein the first substrate (100) includes a first protruding structure (170) located in an area in contact with the sealant (300), and the second substrate (200) includes a second protruding structure (250, 260) located in an area in contact with the sealant (300).

2. The liquid crystal display device (10) according to claim 1, wherein the first substrate (100) and the second substrate (200) include an active area (A/A) where the pixels are disposed and an inactive area (I/A) surrounding the active area (A/A) and the sealant (300) is disposed between the first substrate (100) and the second substrate (200) corresponding to the inactive area (I/A).

3. The liquid crystal display device (10) according to claim 2, wherein the sealant (300) includes a spherical glass fiber.

4. The liquid crystal display device (10) according to claim 3, wherein the first protruding structure (170) and the second protruding structure (250, 260) have heights determined based on an interval or cell gap between the first substrate (100) and the second substrate (200) and a diameter of the glass fiber.

5. The liquid crystal display device (10) according to

claim 4, wherein the height of the first protruding structure (170) is smaller than a difference between the interval or cell gap between the first substrate (100) and the second substrate (200) and the diameter of the glass fiber.

6. The liquid crystal display device (10) according to claim 5, wherein the height of the second protruding structure (250, 260) is smaller than a difference between the interval or cell gap between the first substrate (100) and the second substrate (200) and the height of the first protruding structure (170). 5
7. The liquid crystal display device (10) according to any one of claims 3 to 6, wherein a shortest distance between the first protruding structure (170) and the second protruding structure (250, 260) is equal to or larger than a diameter of the glass fiber. 10
8. The liquid crystal display device (10) according to any one of claims 2 to 7, wherein the first protruding structure (170) is located in an area where the sealant (300) and the first substrate (100) are in contact with each other and the active area (A/A) and the second protruding structure (250, 260) is located in an area where the sealant (300) and the second substrate (200) are in contact with each other and the active area (A/A). 15
9. The liquid crystal display device (10) according to claim 8, wherein the first protruding structure (170) located in the area where the sealant (300) and the first substrate (100) are in contact with each other and the first protruding structure (170) located in the active area (A/A) are manufactured through the same process. 20
10. The liquid crystal display device (10) according to claim 8 or 9, wherein the second protruding structure (250, 260) located in the area where the sealant (300) and the second substrate (200) are in contact with each other and the second protruding structure (250, 260) located in the active area (A/A) are manufactured through the same process. 25
11. The liquid crystal display device (10) of claim 1, wherein: 30

the first and second substrates (100, 200) include an active area (A/A) where images are displayed and an inactive area (I/A) adjacent to or surrounding the active area (A/A); and the sealant (300) encloses the active area (A/A) in a partial area of the inactive area (I/A), 35

wherein the first and second protruding structures (170, 250, 260) are configured to reinforce an adhesion of the sealant (300) with respect to the first and 40

second substrates (100, 200).

12. The liquid crystal display device (10) according to claim 11, wherein the first and second protruding structures (170, 250, 260) reinforce an adhesion of the sealant (300) and the first and second substrates (100, 200) by increasing a surface area in which the sealant (300) and the first and second substrates (100, 200) are in contact with each other as compared with a case in which the first and second protruding structures (170, 250, 260) are not provided. 45
13. The liquid crystal display device (10) according to claim 11 or 12, wherein the sealant (300) includes a spherical glass fiber configured to maintain a cell gap between the first and second substrates (100, 200). 50
14. The liquid crystal display device (10) according to claim 13, wherein a height of the first and second protruding structures (170, 250, 260) is smaller than a difference between the cell gap and a diameter of the glass fiber. 55
15. The liquid crystal display device (10) according to claim 14, wherein the first and second protruding structures (170, 250, 260) is a plurality of column spacers disposed with an interval that is equal to or larger than the diameter of the glass fiber.

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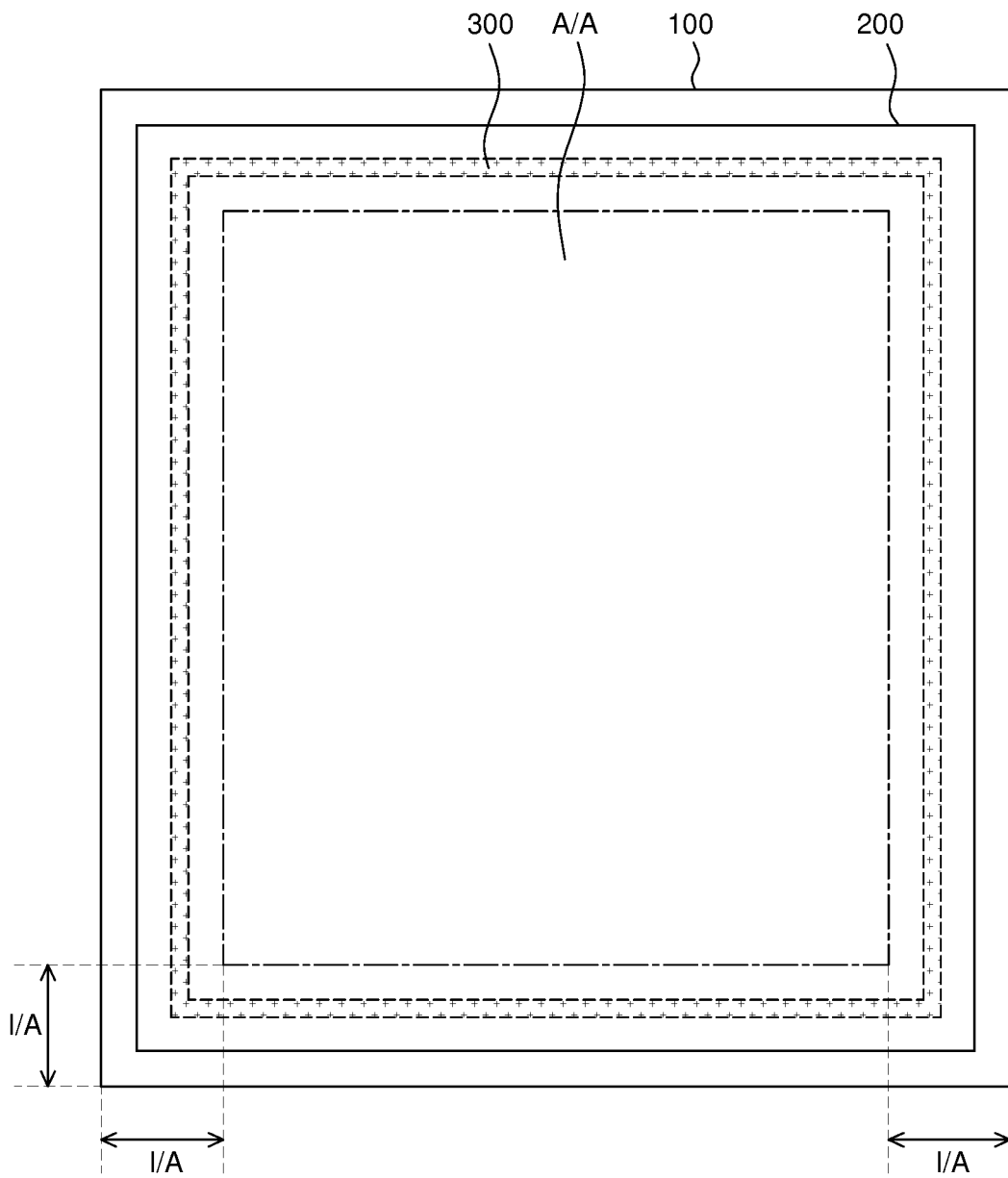


FIG. 1

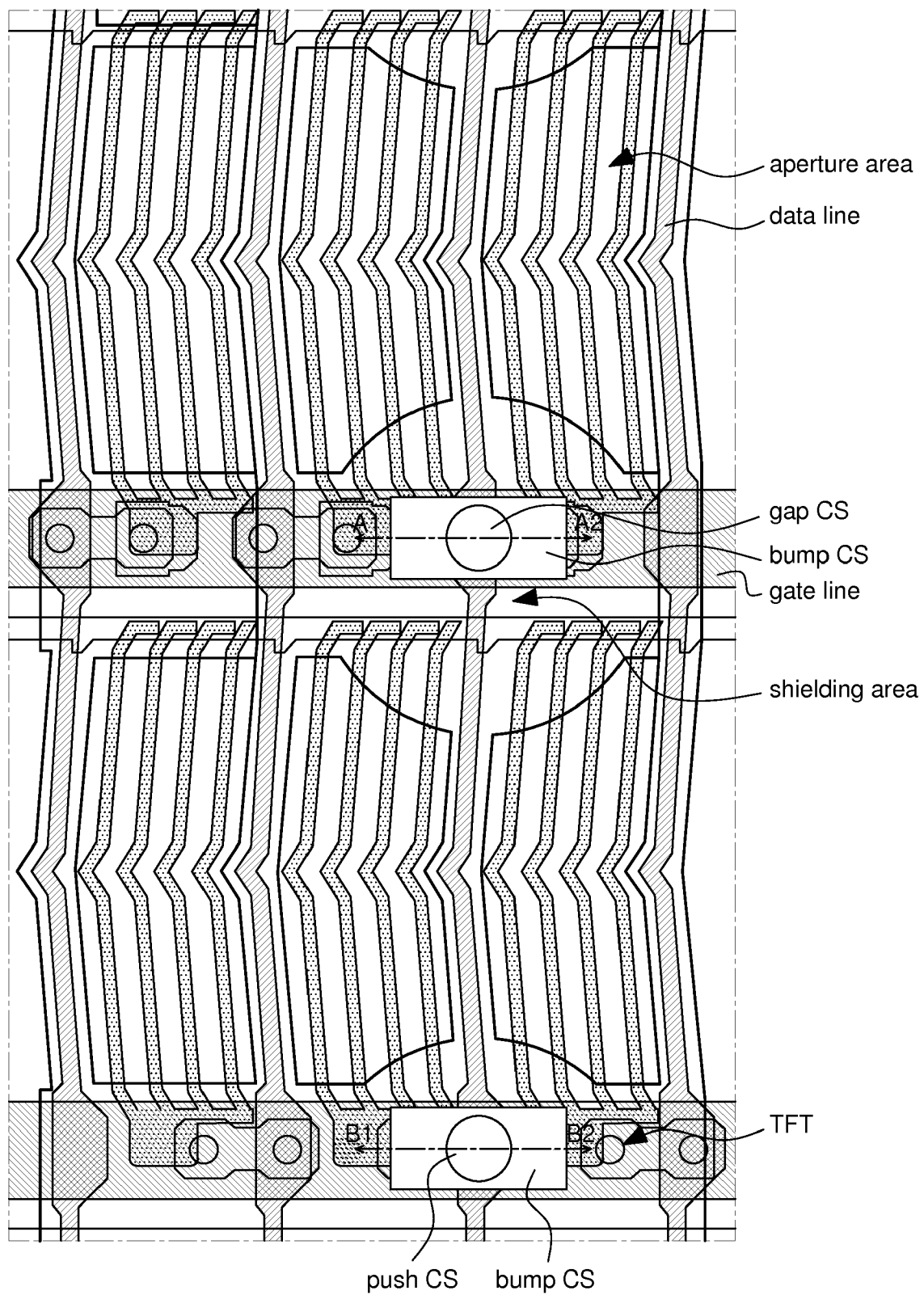


FIG. 2

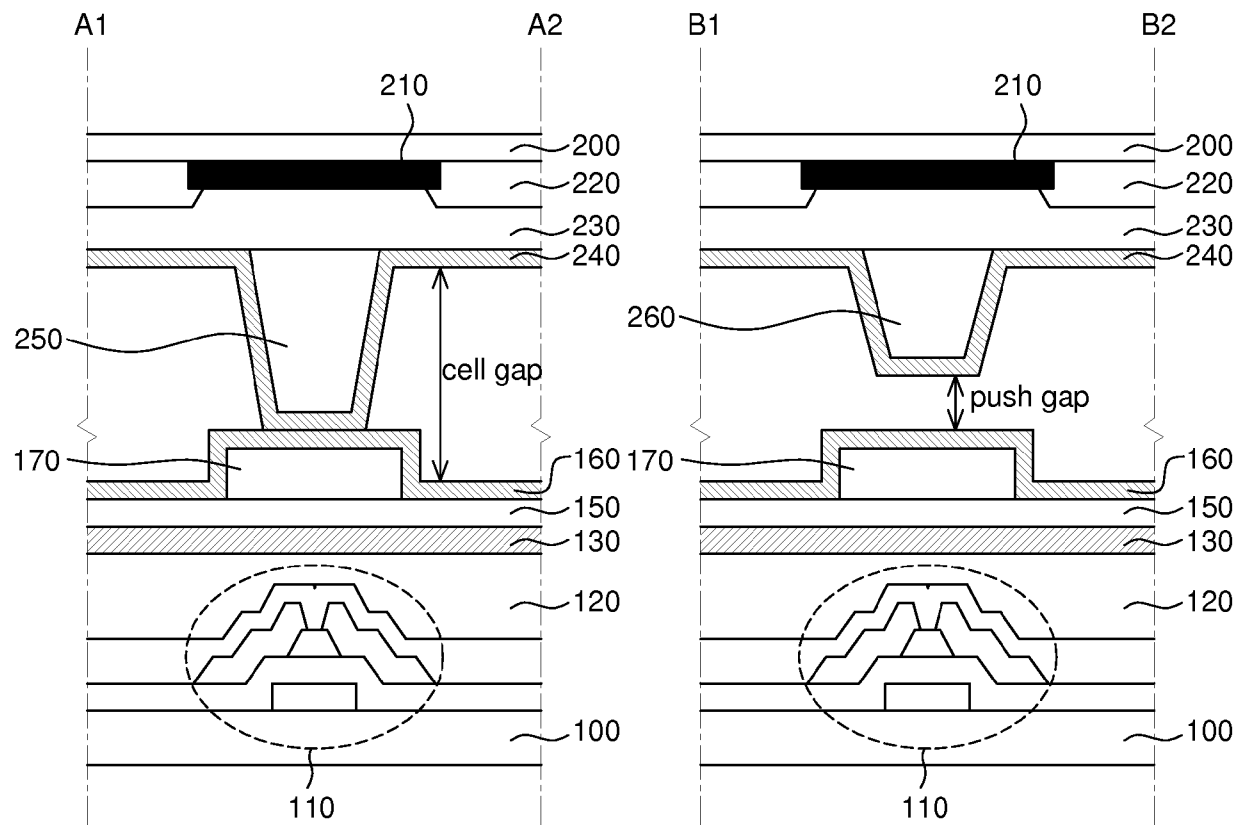


FIG. 3

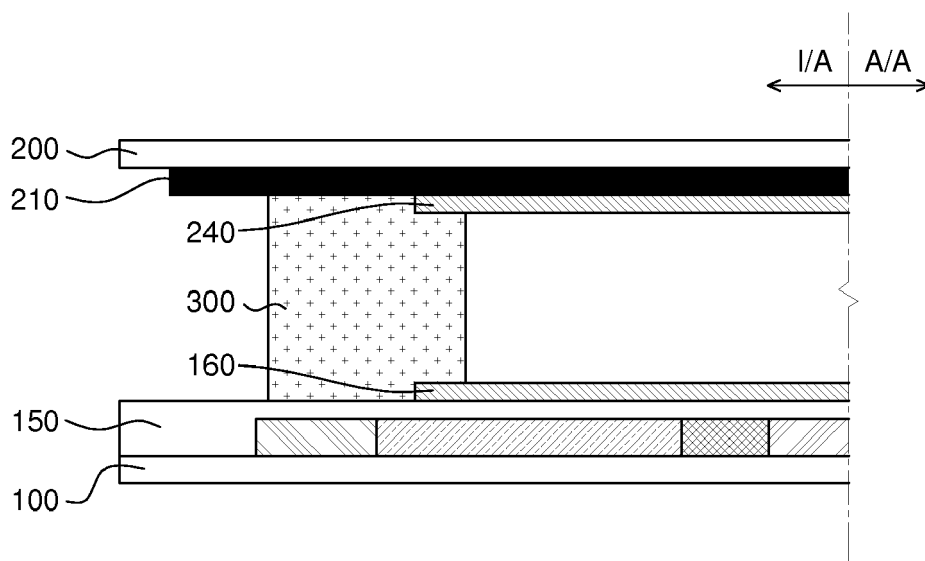


FIG. 4

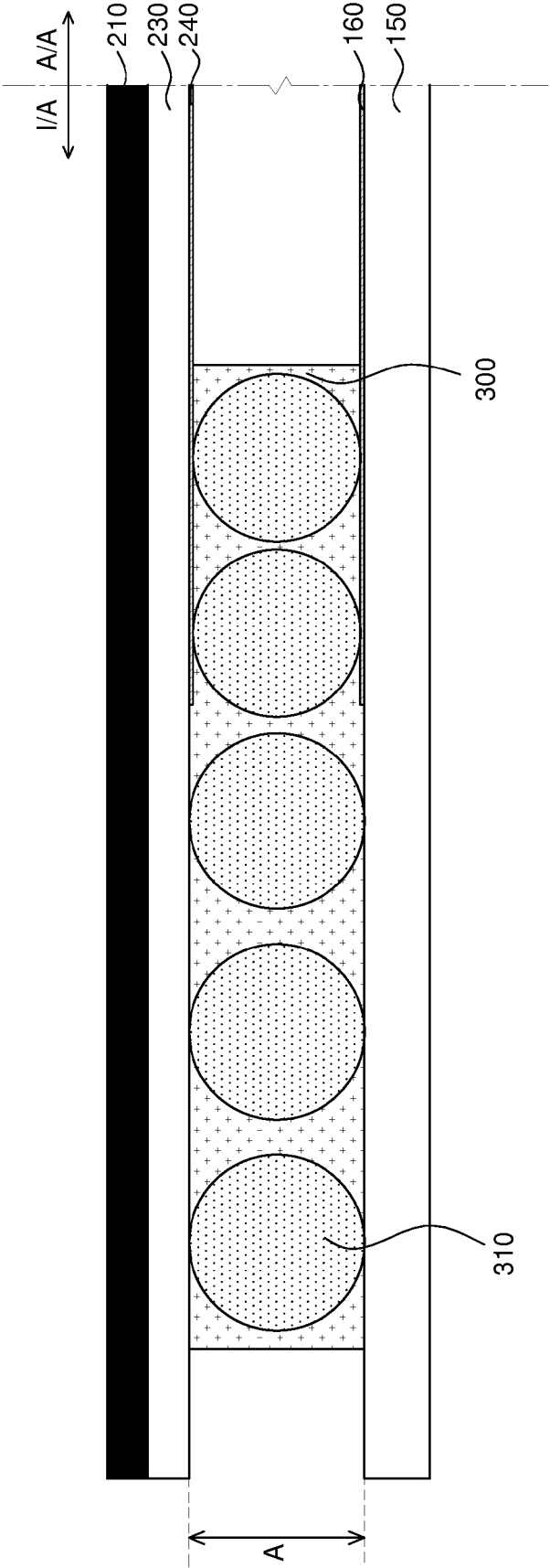


FIG. 5

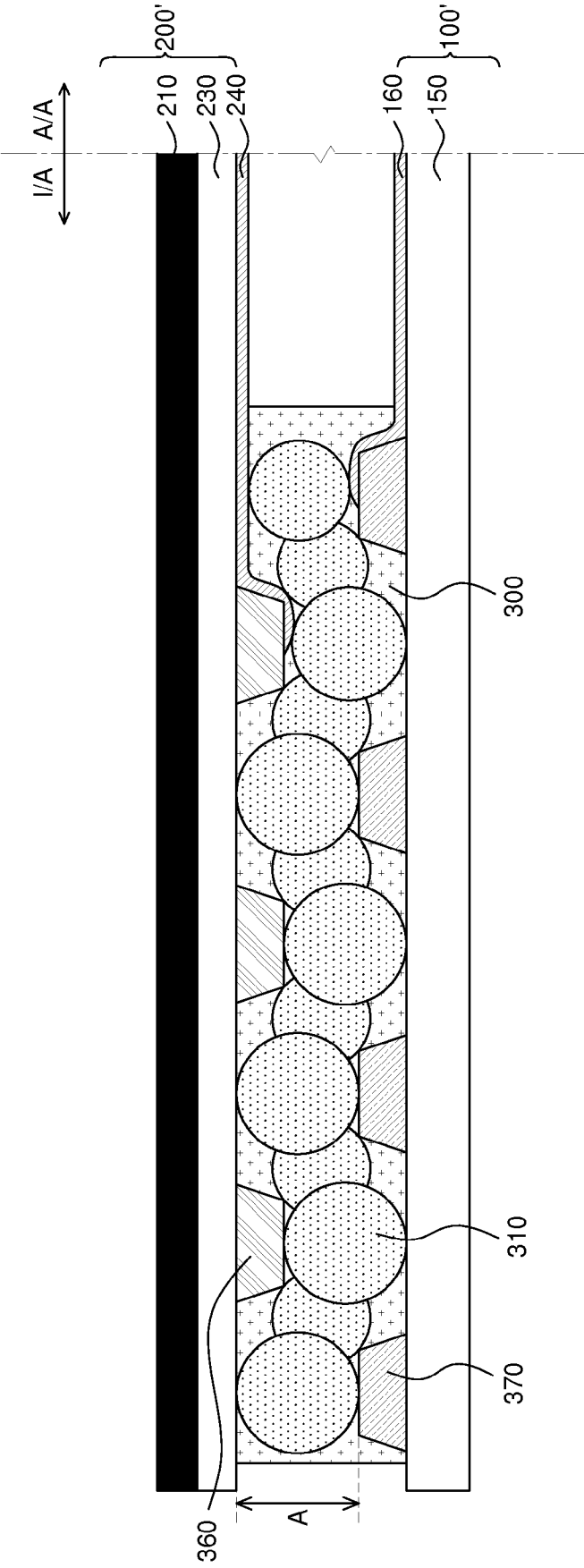


FIG. 6

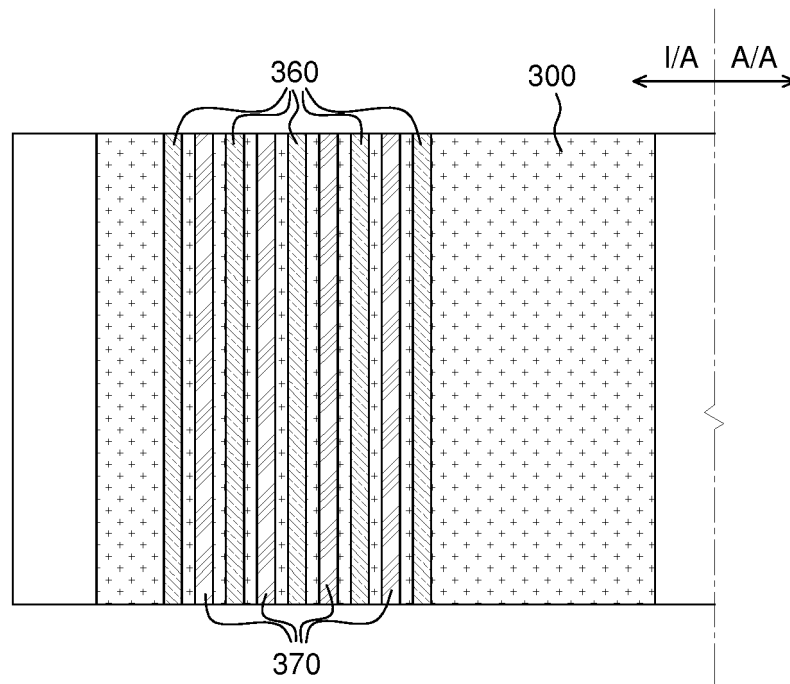


FIG. 7



EUROPEAN SEARCH REPORT

Application Number
EP 16 17 9627

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X	US 2008/116469 A1 (WENG CHIH-HSIUNG [TW] ET AL) 22 May 2008 (2008-05-22) * paragraphs [0033] - [0040]; figures 3,4 *	1,2,11,12 3,4,7,13	
X	US 2015/185539 A1 (SENOKUCHI YUTA [JP] ET AL) 2 July 2015 (2015-07-02) * paragraphs [0001], [0201] - [0208]; figures 20,27,28 *	1,2,8-12 3-7,13,14	
Y	US 6 650 393 B1 (NISHIGUCHI KENJI [JP]) 18 November 2003 (2003-11-18) * column 8, line 19 - line 36 * * column 9, line 58 - column 10, line 3; figure 4 *	3-7,13,14	
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The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 13 January 2017	Examiner Ammerlahn, Dirk
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专利名称(译)	液晶显示装置		
公开(公告)号	EP3136164A1	公开(公告)日	2017-03-01
申请号	EP2016179627	申请日	2016-07-15
[标]申请(专利权)人(译)	乐金显示有限公司		
申请(专利权)人(译)	LG DISPLAY CO. , LTD.		
当前申请(专利权)人(译)	LG DISPLAY CO. , LTD.		
[标]发明人	CHO SUNGHYUN KIM SANGWOON		
发明人	CHO, SUNGHYUN KIM, SANGWOON		
IPC分类号	G02F1/1339		
CPC分类号	G02F1/1339 G02F1/13394 G02F2001/13396 G02F2001/13398		
优先权	1020150122812 2015-08-31 KR		
其他公开文献	EP3136164B1		
外部链接	Espacenet		

摘要(译)

提供一种液晶显示装置 (10) , 包括 : 具有被配置为驱动像素的元件的第一基板 (100) 具有对应于所述像素的滤色器 (220) 的第二基板 (200) 以及构造成将所述第一基板 (100) 和所述第二基板 (200) 彼此接合的密封剂 (300) 。第一基板 (100) 包括位于与密封剂 (300) 接触的区域中的第一突出结构 (370) , 并且第二基板 (200) 包括位于与密封剂接触的区域中的第二突出结构 (360) (300) 。

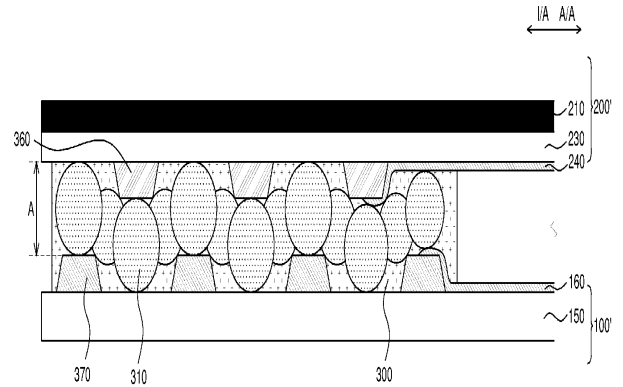


FIG. 6