



(11) **EP 2 015 286 A1**

(12) **EUROPEAN PATENT APPLICATION**  
published in accordance with Art. 153(4) EPC

(43) Date of publication:  
**14.01.2009 Bulletin 2009/03**

(51) Int Cl.:  
**G09G 3/36** <sup>(2006.01)</sup> **G02F 1/133** <sup>(2006.01)</sup>  
**G09G 3/20** <sup>(2006.01)</sup>

(21) Application number: **07741084.3**

(86) International application number:  
**PCT/JP2007/057648**

(22) Date of filing: **05.04.2007**

(87) International publication number:  
**WO 2007/125738 (08.11.2007 Gazette 2007/45)**

(84) Designated Contracting States:  
**AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LI LT LU LV MC MT NL PL PT RO SE SI SK TR**  
Designated Extension States:  
**AL BA HR MK RS**

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(30) Priority: **28.04.2006 JP 2006127054**

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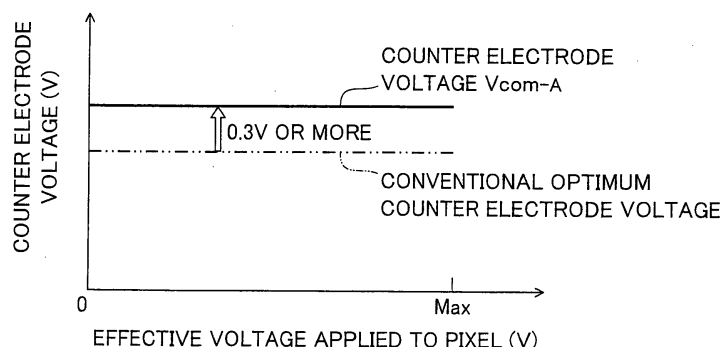
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(54) **LIQUID CRYSTAL DISPLAY APPARATUS AND METHOD FOR DRIVING THE SAME**

(57) A voltage having the positive polarity and a voltage having the negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode. A counter electrode voltage (Vcom) generating circuit or a source signal generating section is provided each of which controls effective voltages to be applied to the pixel for an identical tone so that an effective voltage

having the positive polarity and an effective voltage having the negative polarity are asymmetrically applied to the pixel for all of the tones which can be displayed. The counter electrode voltage (Vcom) generating circuit or the source signal generating section shifts a counter electrode voltage Vcom-A or a source voltage by 0.3 V or more from a voltage causing the effective voltages to be symmetrically applied to the pixel.

FIG. 1 (b)



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## Description

### TECHNICAL FIELD

**[0001]** The present invention relates to a liquid crystal display apparatus driven by a polarity inversion driving in which a voltage having the positive polarity and a voltage having the negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode and to method for driving the same.

### BACKGROUND ART

**[0002]** A conventional liquid crystal display apparatus causes the following problem. Specifically, in a case where, after a fixed pattern, in which a black display includes white square displays as illustrated in Fig. 15 (a), is displayed for a long time, it is changed into another display, for example, an entire half tone display, retentions occur at edge parts of the white square displays as burnings as illustrated in Fig. 15 (b). This allows a degradation in display quality. Note that Fig. 15 (b) shows an initial stage at which changing of displays are carried out and so a burning emerges only on one side of each of the white squares. After elapse of a long period of time, burnings emerge on four sides of the white squares.

**[0003]** To deal with this burning problem, for example, patent document 1 discloses an arrangement in which a counter electrode electric potential is adjusted to be a pixel electrode center electric potential (an electric potential of a center part of a pixel electrode) as illustrated in Fig. 16. Namely, a voltage having the positive polarity and a voltage having the negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode. As such, the voltage having the positive polarity and the voltage having the negative polarity, which have an identical effective voltage for an identical tone value, are applied to the pixel between the counter electrode and the pixel electrode, and so become symmetric with respect to the counter electrode voltage. This is carried out for all of the tones that can be displayed.

**[0004]** For example, patent document 2 discloses another conventional arrangement in which a direct-current component is removed to deal with the burning problem.

Patent Document 1: Japanese Publication for Unexamined Patent Application, *Tokukaihei*, No.5-165431 (date of publication: July 2, 1993)

Patent Document 2: Japanese Publication for Unexamined Patent Application, *Tokukai*, No.2002-251170 (date of publication: September 6, 2002)

**[0005]** However, the conventional liquid crystal display apparatus and methods for driving the same are only for overcoming the problem of a plane burning, and still have a problem that an edge burning which would occur when a display of a fixed pattern, after having been displayed

for a long time, is changed into a half tone display.

**[0006]** The present invention is made in view of the problem and an object of the present invention is to provide a liquid crystal display apparatus and a method for driving the same which can prevent an edge burning which would occur when the display of a fixed pattern, after having been displayed for a long time, is changed into a half tone image.

### 10 DISCLOSURE OF INVENTION

**[0007]** As a result of diligent and various studies, the inventors of the present invention confirmed that this phenomenon was caused because impurities inside a display panel moved in lateral directions in a black display in a normally black mode, for example, and were accumulated at a boundary between the black display and a white display, and accomplished the present invention based on the finding.

**[0008]** Namely, for example, when voltages to be applied to the pixel for the black display are low, an electrical field becomes weak in a cell thickness (vertical) direction. This appears to cause that the impurities move in lateral directions easily in the black display, thereby resulting in the edge burning at the boundary between the black display and the white display which has a strong electric field. In a normally white mode, voltages to be applied to a pixel for a white display are low, and impurities move in lateral directions in a white display and are accumulated at a boundary between a black display and the white display, thereby resulting in the edge burning.

**[0009]** Therefore, it is possible to alleviate the edge burning by increasing the voltages to be applied to the pixel because the electric field becomes strong in the cell thickness direction and the impurities become less likely to move in lateral directions.

**[0010]** To attain the object, a liquid crystal display apparatus of the present invention in which a voltage having the positive polarity and a voltage having the negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode, including: an effective voltage control section that controls effective voltages for an identical tone so that an effective voltage having a positive polarity and an effective voltage having a negative polarity have asymmetrical values to be applied to the pixel for all of tones which can be displayed, wherein the effective voltage control section shifts a counter electrode voltage or a source voltage by 0.3 V or more from a voltage causing the effective voltages in the positive and the negative polarity to be symmetrically applied to the pixel.

**[0011]** To attain the object, a method for driving a liquid crystal display apparatus in which a voltage having a positive polarity and a voltage having a negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode, including the steps of: controlling effective voltages for an identical tone so that an effective voltage having the positive polarity and an effective volt-

age having the negative polarity have asymmetrical values to be applied to the pixel for all of tones which can be displayed; and shifting a counter electrode voltage or a source voltage by 0.3 V or more from a voltage causing the effective voltages in the positive polarity and the negative polarity to be symmetrically applied to the pixel.

**[0012]** In the present invention, the effective voltage is an actual voltage to be applied to the pixel after having been affected by potential fluctuation of a gate bus line. Specifically, in a TFT (Thin Film Transistor), for example, voltages supplied from a circuit or a source driver to a pixel electrode, i.e. an applied voltage is affected by voltage fluctuation of a gate bus line which generates a parasitic capacitance of a pixel. Accordingly, the voltage affected by the voltage fluctuation of the gate bus line becomes the effective voltage to be applied to the pixel.

**[0013]** Therefore, in the present invention, "the effective voltage having the positive polarity and the effective voltage having the negative polarity are symmetrically applied to the pixel" means that (i) an emergence of a flicker (flicker caused by a difference of the effective voltages in the positive and negative polarities to be applied to the pixel) is minimum; (ii) either a luminance or a transmittance is minimum in the normally black mode; or (iii) either a luminance or a transmittance is maximum in the normally white mode.

**[0014]** According to the invention, the effective voltages for an identical tone are controlled so that the effective voltage having the positive polarity and the effective voltage having the negative polarity are asymmetrically applied to the pixel between the counter electrode and the pixel electrode for all of the tones which can be displayed.

**[0015]** With this arrangement, for example, voltages to be applied to the pixel for the black display in the normally black mode become higher than those conventionally applied. This causes the electric field to become strong in the cell thickness (vertical) direction. As a result, it is possible to prevent the movement of the impurities in lateral directions in the liquid crystal display panel, which leads the edge burning to be less likely to occur. In the normally white mode, voltages to be applied to the pixel for the white display become higher than those conventionally applied. Therefore, the edge burning is less likely to occur, like the case of the normally black mode.

**[0016]** Also, a shift amount which less likely to cause the edge burning is 0.3 V or more according to experiments. Therefore, the edge burning surely becomes less likely to occur by shifting either the counter electrode voltage or the source voltage by 0.3 V or more from the voltage causing the effective voltage having the positive polarity and the effective voltage having the negative polarity to be symmetrically applied to the pixel (see the Fig. 4).

**[0017]** As a result, it is possible to provide a liquid crystal display apparatus and a method for driving the same which can prevent the edge burning, which would occur when a fixed pattern, after having been displayed for a long time, is changed to a half tone display.

**[0018]** Further, to attain the object, a liquid crystal display apparatus of the present invention in which a voltage having a positive polarity and a voltage having a negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode, including: an effective voltage control section that controls effective voltages for an identical tone so that an effective voltage having the positive polarity and an effective voltage having the negative polarity have asymmetrical values to be applied to the pixel, for the effective voltages of not more than a first effective voltage which is lower than half of a maximum effective voltage, wherein the effective voltage control section shifts a counter electrode voltage or a source voltage by 0.3 V or more from a voltage causing the effective voltages in the positive polarity and the negative polarity to be symmetrically applied to the pixel, for the effective voltages of not more than the first effective voltage.

**[0019]** Further, to attain the object, a method for driving a liquid crystal display apparatus in which a voltage having a positive polarity and a voltage having a negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode, including the steps of: controlling effective voltages for an identical tone so that an effective voltage having the positive polarity and an effective voltage having the negative polarity have asymmetrical values to be applied to the pixel, for the effective voltages of not more than a first effective voltage which is lower than half of a maximum effective voltage; and shifting a counter electrode voltage or a source voltage by 0.3 V or more from a voltage causing the effective voltages in the positive polarity and the negative polarity to be symmetrically applied to the pixel.

**[0020]** According to the invention, the effective voltage having the positive polarity and the effective voltage having the negative polarity are controlled to have asymmetrical values to be applied to the pixel for the effective voltages of not more than the first effective voltage which is lower than half of the maximum effective voltage. Therefore, for example, voltages to be applied to the pixel for the black display in the normally black mode become higher than those conventionally applied. This causes the electric field to become strong in the cell thickness (vertical) direction. As a result, it is possible to prevent the movement of the impurities in lateral directions in the liquid crystal display panel, which leads the edge burning to be less likely to occur.

**[0021]** Further, to attain the object, a liquid crystal display apparatus of the present invention in which a voltage having a positive polarity and a voltage having a negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode, including: an effective voltage control section that controls effective voltages for an identical tone so that an effective voltage having the positive polarity and an effective voltage having the negative polarity are shifted by 0.3 V or more from each other and are asymmetrically applied to the pixel, for a tone at which a minimum effective voltage is applied to the pixel,

and reduces asymmetry more as a tone is changed from a tone for a lowest effective voltage to a tone corresponding to an effective voltage which is higher than the lowest effective voltage.

**[0022]** Further, to attain the object, a method for driving a liquid crystal display apparatus in which a voltage having a positive polarity and a voltage having a negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode, including the steps of: controlling effective voltages for an identical tone so that an effective voltage having the positive polarity and an effective voltage having the negative polarity are shifted by 0.3 V or more from each other and are asymmetrically applied to the pixel, for a tone at which a minimum effective voltage is applied to the pixel, and reducing asymmetry more as a tone is changed from a tone for a lowest effective voltage to a tone corresponding to an effective voltage which is higher than the lowest effective voltage.

**[0023]** According to the invention, the effective voltage having the positive polarity and the effective voltage having the negative polarity are shifted by 0.3 V or more from each other and are asymmetrically applied to the pixel, for a tone at which the minimum effective voltages are applied to the pixel. Accordingly, voltages to be applied to the pixel for the black display in the normally black mode become higher than those conventionally applied. This causes the electric field to become strong in the cell thickness (vertical) direction. As a result, it is possible to prevent the movement of the impurities in lateral directions in the display panel, which leads the edge burning to be less likely to occur.

**[0024]** Further, in the present invention, the asymmetry is gradually reduced as a tone is changed from the tone for the lowest effective voltage to the tone corresponding to an effective voltage which is higher than the lowest effective voltage. Therefore, it is possible that the relation between the tones and the effective voltages gets close to a conventional relation by reducing the asymmetry when the effective voltages are higher than the lowest voltages.

**[0025]** Additional objectives, features, and strengths of the present invention will be made clear by the description below. Further, the advantages of the present invention will be evident from the following explanation in reference to the drawings.

#### BRIEF DESCRIPTION OF DRAWINGS

**[0026]**

Fig. 1 (a) is a waveform chart of effective voltages for an identical tone are controlled so that the effective voltage having the positive polarity and the effective voltage having the negative polarity are asymmetrically applied to a pixel between a counter electrode and a pixel electrode in an embodiment of the liquid crystal display apparatus in accordance

with the present invention.

Fig. 1 (b) is a graph showing that asymmetry of the effective voltages are within a range of all the tones which can be displayed.

Fig. 2 is a block diagram illustrating an overall structure of the liquid crystal display apparatus.

Fig. 3 is an equivalent circuit diagram illustrating a structure of a pixel of the liquid crystal display apparatus (omitting an auxiliary capacitor).

Fig. 4 is a graph showing a relation between an onset time of an edge burning and a shift amount from an optimum counter electrode voltage.

Fig. 5 is a graph showing a relation between a transition of black-level luminance and a counter electrode voltage.

Fig. 6 is a circuit diagram of a ladder resistor for controlling the effective voltages to be applied to the pixel between the counter electrode and the pixel electrode for an identical tone to be asymmetrical in the positive and negative polarities.

Fig. 7 (a) is a waveform chart showing that the effective voltages to be applied to the pixel are asymmetrical in the positive and negative polarities up to the first effective voltages and then the asymmetry is gradually reduced.

Fig. 7 (b) is a waveform chart showing the effective voltages shown in Fig. 7 (a) in which the shift amount is large for low voltages and a shift occurs for high voltages.

Fig. 8 (a) is a waveform chart showing that the effective voltages to be applied to the pixels have the largest asymmetrical values at the lowest tone and then the asymmetry is gradually reduced.

Fig. 8 (b) is a waveform chart showing the effective voltages shown in Fig. 8 (a) in which the shift amount is large for low voltages and a shift occurs for high voltages.

Fig. 9 (a) is a waveform chart showing that the effective voltages to be applied to the pixel have the largest asymmetrical values for the lowest tone and the asymmetry is gradually reduced up to a second effective voltage.

Fig. 9 (b) a waveform chart showing the effective voltages shown in Fig. 9 (a) in which the shift amount is large for low voltages and the asymmetry is gradually reduced as the effective voltages increase toward high voltages.

Fig. 10 (a) is a waveform chart showing that the effective voltages to be applied to the pixel are asymmetrical both for low voltages and high voltages.

Fig. 10 (b) is a waveform chart of the effective voltages which is obtained by carrying out a parallel shift with respect to the waveform in Fig. 10 (a).

Fig. 11 is a waveform chart showing that the effective voltages to be applied to the pixel (i) have the largest asymmetry for the lowest tone; (ii) the asymmetry is reduced toward the second effective voltage; (iii) an axis of asymmetry is shifted toward the

opposite polarity for a forth effective voltage; (iv) and then the asymmetry is reduced toward high voltages.

Fig. 12 is a waveform chart showing that the effective voltages are controlled by a single polarity when the effective voltages are asymmetrically applied to the pixel.

Fig. 13 (a) is a waveform chart showing that a counter electrode voltage is shifted alternately toward the positive polarity and the negative polarity when the effective voltages are asymmetrically applied to the pixel.

Fig. 13 (b) is a waveform chart showing that the counter electrode voltage has a larger shift amount than that of the counter electrode voltage shown in Fig. 13 (a) and is changed within a single polarity.

Fig. 14 (a) is a waveform chart showing that the source voltage is shifted alternately toward the positive polarity and the negative polarity when the effective voltages are asymmetrically applied to the pixel.

Fig. 14 (b) is a waveform chart showing that the source voltage has a larger shift amount than that of the source voltage shown in Fig. 14 (a) and is changed within a single polarity.

(a) of Fig. 15 is a plan view of a fixed pattern in which a black display includes white square displays; and (b) of Fig. 15 is a plan view of an edge burning, which would occur when a display of the fixed pattern, after having been displayed for a long time, is changed to a half tone display.

**[0027]** Fig. 16 is a waveform chart showing that conventional effective voltages are symmetrically applied to the pixel.

(a) of Fig. 17 is a plan view of a fixed pattern in which a black display includes white square displays when the counter electrode voltage is shifted from an optimum value in a case where a vertical alignment control power is strong; and (b) Fig. 17 is a plan view of a plane burning which would occur when a display of the fixed pattern, after having been displayed for a long time, is changed to a half tone display.

(a) of Fig. 18 is a plan view of a fixed pattern in which a black display includes white square displays when the counter electrode voltage is shifted from an optimum value in a case where a vertical alignment control power is weak; and (b) of Fig. 18 is a plan view of a plane burning which would occur when a display of the fixed pattern, after having been displayed for a long time, is changed to a half tone display.

[Explanation of Reference Numerals]

**[0028]**

- 5        1 liquid crystal display panel
- 4 source signal generating section
- 4e ladder resistor
- 5 counter electrode voltage (Vcom) generating circuit
- 10      6 control section
- 7 pixel electrode substrate
- 8 counter substrate
- 12 pixel electrode
- 13 counter electrode
- 15      14 liquid crystal
- P pixel
- V1 first effective voltage
- V2 second effective voltage
- V3 third effective voltage
- 20      V4 forth effective voltage value
- Vcom counter electrode voltage

**BEST MODE FOR CARRYING OUT THE INVENTION**

25 **[0029]** The following description deals with an embodiment of the present invention with reference to the drawings.

**[0030]** A liquid crystal display apparatus of the present invention includes a liquid crystal display panel 1, a scan line driving circuit 2, a signal line driving circuit 3, a source signal generating section 4, a counter electrode voltage (Vcom) generating circuit 5, and a control section 6 as illustrated in Fig. 2.

**[0031]** The liquid crystal display panel 1 includes a pixel electrode substrate 7, a counter substrate 8 so as to be provided parallel to and face the pixel electrode substrate 7, and a liquid crystal (not illustrated) filled in a space between the pixel electrode substrate 7 and the counter substrate 8. On the pixel electrode substrate 7, a plurality of scan lines G (0)... G (J) and a plurality of signal lines S (0)... S (I) are provided so as to intersect with one another and pixels P ... are provided in matrix, respectively.

**[0032]** As illustrated in Fig. 3, a pixel P is provided in an area surrounded by adjacent scan lines G (j) and G (j-1) and adjacent signal lines S (i) and S (i+1). The pixel P includes a TFT (Thin Film Transistor) 11 serving as a switching element, and a liquid crystal capacitor C<sub>LC</sub>. In the TFT 11, a gate is connected to the scan line G (j) and a source signal is supplied to the signal line S (i). The liquid crystal capacitor C<sub>LC</sub> includes a pixel electrode 12 connected to the TFT 11, a counter electrode 13 facing the pixel electrode 12, and a liquid crystal 14 sandwiched by the pixel electrode 12 and the counter electrode 13. The counter electrode 13 is provided on the counter substrate 8 so as to be shared by all of the pixels P.

**[0033]** In the pixel P, the pixel electrode 12 is connected to the signal line S (i) via a source and a drain of the

TFT 11. The gate of the TFT 11 is connected to the scan line G (j). Also, the counter electrode 13 receives a counter electrode voltage  $V_{com}$  released from the counter electrode voltage ( $V_{com}$ ) generating circuit 5. Since this causes a voltage difference between a signal voltage of a source signal  $V_s$  supplied from the signal line S (i) and a counter electrode voltage  $V_{com}$  to be applied to the liquid crystal capacitor  $C_{LC}$  while the TFT 11 is turned ON, a transmittance or a reflection rate of the liquid crystal is modulated. This allows the pixels P... to display an image corresponding to an inputted image data. Also, electric charge accumulated by the liquid crystal capacitor  $C_{LC}$  is retained in each of the pixels P for a certain period of time. This allows a display of an image to be maintained in accordance with the electric charge even after the TFT is turned OFF.

**[0034]** The following description deals with an example of changing the counter electrode voltage and the source voltage.

**[0035]** Normally, a source voltage is fixed in accordance with a certain tone. For example, in Fig. 6, a high voltage for tone 0 is set to 8.5 V and a low voltage for tone 0 is set to 7.5 V. Also, a counter electrode voltage is fixed to 7.5 V by taking into consideration a lead-in voltage due to voltage fluctuation in gate electrode e.g. 0.5 V. With this arrangement, the effective voltages to be applied to the pixel, which are affected by the voltage fluctuation in the gate electrode, are 8.0 V for the high voltage side and 7.0 V for the low voltage side. As a result, + 0.5 V and - 0.5 V higher and lower than the counter electrode voltage are applied to a pixel as the effective voltages.

**[0036]** In an embodiment in accordance with the present invention, it is intended to further alleviate a plane burning by shifting the counter electrode voltage from an optimum value by a shift amount, for example, of 1 V, which shift amount is changed between +1 V and -1 V alternately. This is also applicable to the source voltage. The plane burning refers to a phenomenon in which, in a case where a state illustrated in Fig. 15 (a) is displayed for a long time, and is then changed into a half tone display, in a normally black mode, for example, a black display is retained brightly in an original area of the black display.

**[0037]** In the case of the normally black mode where a vertical alignment control power such as a photo-alignment is weak, a plane burning (brightness) caused by tilt of liquid crystal molecules may be cancelled out by the plane burning (brightness) caused by shifting the counter electrode voltage from the optimum value. In this case, it is not required to change the shift amount. Specifically, as illustrated in Fig. 17 (a) and Fig. 17 (b), normally, in a case where the vertical alignment control power is strong in the normally black mode, the black display is retained brightly and sharply while the display is changed into a half tone display, when the counter electrode voltage is shifted from the optimum voltage. However, in a case where the vertical alignment control power is weak in the

normally black mode, the black display becomes blur when it is changed into a half tone display. This is because (i) a white display part becomes bright due to the fact that liquid crystal molecules remain tilted in the white display part and (ii) a black display part becomes bright due to the burning in the black display part caused by a counter electrode voltage. Therefore, it is possible to prevent a plane burning only by shifting the counter electrode voltage from the optimum value in a case where the vertical alignment control power is weak in the normally black mode (see Fig. 18 (a) and Fig. 18 (b)).

**[0038]** For a specific changing cycle, a control section 6 includes a counter for counting and determining that the time corresponding to an n-frame has elapsed. When the counter determines that the time corresponding to an n-frame has elapsed, a predetermined source voltage or counter electrode voltage is changed. Instead of the counter, it may be possible to change a counter electrode voltage in sync with a rising edge of a power source.

**[0039]** In the liquid crystal display apparatus, for example, as shown in a conventional explanatory drawing in Fig. 15, the voltage having the positive polarity and the voltage having the negative polarity, which have an identical effective voltage for an identical tone value, are applied to the pixel between the counter electrode and the pixel electrode, and so become symmetric with respect to the counter electrode voltage. Note however that a low voltage having the positive polarity and a low voltage having the negative polarity are applied to a pixel for a black display. This causes an occurrence of the edge burning when a fixed pattern, after having been displayed for a long time, is changed into a half tone display. In this case, when a pixel has the lowest luminance, i.e., when the pixel has a zero tone, voltages of, for example,  $\pm 0.5$  V are applied to the pixel.

**[0040]** The studies about the problem allows a presumption that this phenomenon is caused because impurities inside a liquid crystal display panel 1 move in a certain direction in a black display, and is accumulated at a boundary between the black display and a white display. Namely, when voltages to be applied to the pixel P are low, the impurities become easy to move in lateral directions because the electrical field in a cell thickness (vertical) direction is weak. This appears to cause the edge burning at the boundary, between the black display and the white display, which has a strong electrical field. Therefore, it appears that the edge burning is less likely to occur by increasing the voltages to be applied to the pixel P because the electrical field in the cell thickness direction becomes strong and the impurities are less likely to move in lateral directions.

**[0041]** In view of this, an embodiment of the present invention, as illustrated in Fig. 1 (a), is arranged so that an effective voltage having the positive polarity and an effective voltage having the negative polarity, for an identical tone value, are asymmetrically applied to a pixel P between a counter electrode 13 and a pixel electrode 12. Specifically, according to the present embodiment, a

counter electrode voltage  $V_{com-A}$  is shifted by 0.3 V so as to have higher positive polarity than a conventional counter electrode voltage. This causes the voltage having the negative polarity to have a voltage of - 0.8 V. As a result, the edge burning is less likely to emerge because the electric field in the cell thickness direction becomes strong due to the increase in voltage to be applied to the pixel P.

**[0042]** The following description deals with grounds of the shift amount of 0.3 V with reference to Fig. 4. Fig. 4 shows a relation between (i) an onset time of the edge burning in tone 0, i.e., at the lowest voltage and (ii) a shift amount from the optimum value of the counter electrode voltage. The onset time of the edge burning means how long it would take to recognize the edge burning visually. As is clear from Fig. 4, the onset time of the edge burning is getting longer for the shift amount of 0.3 V or more. Namely, it is clear that the shift amount of 0.3 V or more has an effect on alleviation of the edge burning. Therefore, to prevent the edge burning, it is necessary to shift the counter electrode voltage by 0.3 V or more from the optimum value of the counter electrode voltage  $V_{com}$ , in a case of the lowest luminance, i.e.,  $\pm 0.5$  V in the present embodiment. Namely, it is necessary to apply an effective voltage of 0.8 V or more to the pixel P in a case of tone 0, i.e., at the lowest voltage.

**[0043]** However, as explained above, in a conventional arrangement, the voltage having the positive polarity and the voltage having the negative polarity, which have symmetric effective voltages with respect to the counter electrode voltage, are applied to a pixel. Therefore, in a case of a low tone in which the edge burning occurs outstandingly, especially in a case where asymmetric effective voltages of less than  $\pm 0.8$  V are applied to a pixel, it is impossible to secure symmetric effective voltages of 0.8 V for the positive and negative polarities. Note that for tones in which effective voltages of not less than  $\pm 0.8$  V are applied to a pixel, it is possible to apply to the pixel the effective voltages of 0.8 V or more which are symmetric in the positive and negative polarities. Accordingly, the edge burning is immaterial and presents no problem. However, in an arrangement in which the voltages of 0.8 V or more having the positive and negative polarities are applied to a pixel, decline in contrast becomes large, which is not preferable. Therefore, in view of preventing decline in contrast, it is preferable that either the voltage having the positive polarity or the voltage having the negative polarity is 0.8 V or more.

**[0044]** In a case of low tones in which the edge burning occurs, it is useful that a counter electrode voltage or a source voltage is shifted from the optimum value of the counter electrode voltage  $V_{com}$  so that the effective voltages of 0.8 V or more are applied to a pixel. Specifically, it is possible to apply, even in low tones other than tone 0, to a pixel either a voltage having the negative polarity or a voltage having the positive polarity each of which has an effective voltage of 0.8 V or more, by shifting 0.3 V at least in the lowest luminance (tone 0). Therefore, it

is possible to prevent the edge burning conventionally occur.

**[0045]** As explained above, in the present embodiment, the shift amount from the optimum value of the counter electrode voltage  $V_{com}$  is set to 0.3 V. A shift direction is not limited to either in the positive or negative polarity to attain the object. Note that Fig. 1 and Fig. 7 through Fig. 11 (later described) deals with a case where the optimum counter electrode is shifted toward the positive polarity.

**[0046]** Also, it is preferable that the shift amount from the optimum value of the counter electrode voltage  $V_{com}$  is 2.0 V or less. A shift amount of more than 2.0 V causes a reduction in contrast and an increase in flicker.

**[0047]** In the present embodiment, as illustrated in Fig. 1(b), the counter electrode voltage  $V_{com}$  is shifted by 0.3 V toward the positive polarity from a voltage (an optimum counter electrode voltage  $V_{com}$ ) causing the effective voltage having the positive polarity and the effective voltage having the negative polarity to be symmetrically applied to the pixel P. In other words, the counter electrode voltage  $V_{com}$  is shifted by 0.3 V or more toward the positive polarity from the voltage causing the effective voltage having the positive polarity and the effective voltage having the negative polarity to be symmetrically applied to the pixel P so that the effective voltage having the negative polarity is higher than the effective voltage having the positive polarity, for an identical tone to be applied to the pixel P.

**[0048]** The following description deals with how to calculate the optimum voltage of the counter electrode 13. For example, in bright tones, it is possible to find the optimum voltage of the counter electrode 13 by displaying a flicker pattern. Note that the voltage minimizing the flicker is the optimum voltage. In a case of black display, i.e., tone 0 in the normally black mode, however, no flicker is correctly observed even though the counter electrode 13 is shifted from the optimum voltage. In this case, for example as illustrated in Fig. 5, an optimum voltage of the counter electrode 13 is found based on transition of black-level luminance. According to an example in Fig. 5, the black-level luminance has the lowest voltage of 5.9 V. Therefore, the optimum voltage of the counter electrode 13 is 5.9 V in case of a current source voltage for tone 0. Note that when the source voltage  $V_s$  for tone 0 is shifted, the optimum voltage of the counter electrode 13 is also shifted.

**[0049]** The following description deals with how to shift the effective voltage by 0.3 V toward the positive polarity from the counter electrode voltage causing the effective voltage having the positive polarity and the effective voltage having the negative polarity to be symmetrically applied to the pixel P, with reference to Fig. 6. Fig. 6 illustrates ladder resistor 4e in the source signal generating section 4.

**[0050]** In the ladder resistor 4e,  $VH\ 0$  through  $VH\ 255$  are applied voltages for the positive polarity and  $VL\ 0$  through  $VL\ 255$  are applied voltages for the negative po-

larity. The present embodiment is arranged so that asymmetric voltages are applied to the pixel P only for low voltages, by adjusting resistances of the resistors for the applied voltage  $V_{H0}$  through  $V_{H63}$  and  $V_{L0}$  through  $V_{L63}$  (circled resistances in Fig. 6) without changing the total resistance of  $R_{total}$ . Namely, in this example, an asymmetric source voltage is supplied by the ladder resistor 4e in the source signal generating section 4 serving as the effective voltage control section. Note that the applying of asymmetric voltage is not limited to this. The counter electrode voltage  $V_{com}$  can be set to, for example, 7.5 V independently by switching and/or sliding with the use of changing-over switches in the counter electrode voltage ( $V_{com}$ ) generating circuit 5 serving as the effective voltage control section.

**[0051]** In the present embodiment, a tone falls within a range from 0 to 255. However, the present invention is not limited to this tone range.

**[0052]** Further, in the above explanation, as illustrated in Fig. 1 (b), the counter electrode voltage  $V_{com-A}$  is shifted by 0.3 V toward the positive polarity from the counter electrode voltage causing the effective voltage having the positive polarity and the effective voltage having the negative polarity to be symmetrically applied to the pixel P for all the tones.

**[0053]** With the arrangement, for example, voltages to be applied to the pixel P during the black display in the normally black mode become higher than those conventionally applied. This causes the electric field to become strong in the cell thickness (vertical) direction. As a result, it is possible to prevent the movement of the impurities in the liquid crystal display panel 1 in lateral directions, which leads the edge burning to be less likely to occur. In the normally white mode, voltages to be applied to the pixel during the white display become higher than those conventionally applied. Therefore, the edge burning is less likely to occur, like the case of the normally black mode. As explained above, according to the present embodiment, it is possible to prevent the edge burning both in the normally black mode and in the normally white mode. For convenience of explanation, the following description deals with cases mainly in the normally black mode.

**[0054]** Also, a shift amount which was less likely to cause the edge burning is 0.3 V or more according to experiments. Therefore, in the present embodiment, the edge burning surely becomes less likely to occur by shifting either the counter electrode voltage  $V_{com}$  or the source voltage  $V_s$  by 0.3 V or more from a voltage causing the effective voltage having the positive polarity and the effective voltage having the negative polarity to be symmetrically applied to the pixel P.

**[0055]** The present invention is not limited to this. For example, as illustrated in Fig. 7 (a), even for a first effective voltage  $V_1$  which is lower than half of the maximum effective voltage, more specifically, for voltages of more than  $\pm 0.8$  V, it is possible to control asymmetric effective voltages, for an identical tone, which have the positive

polarity and the negative polarity, respectively, and which are applied to a pixel P between the counter electrodes 12 and the pixel electrodes 13. It is also possible to adjust a source voltage for each tone so that asymmetry is reduced for voltages of higher than the first effective voltage  $V_1$ . Namely, the counter electrode voltage  $V_{com}$  is set to the same voltage as that conventionally set. However, since the source voltage is adjusted, it is possible to have an optimum counter electrode voltage  $V_{com-B}$  which is changed as illustrated in Fig. 7 (a). In Fig. 7 (a), an axis of ordinate shows the optimum counter electrode voltage, i.e., a counter electrode voltage causing the flicker to be minimum, or the luminance to have a minimum or maximum value.

**[0056]** Namely, the edge burning occurs for low effective voltages. Therefore, when the asymmetry is reduced for the effective voltages which are higher than the first effective voltage  $V_1$ , it is possible that the relation between the effective voltages and the tones gets close to a conventional relation in which an effective voltage having the positive polarity and an effective voltage having the negative polarity are symmetric.

**[0057]** In this case, as illustrated in Fig. 7 (b), it may be possible to have another optimum counter electrode voltage  $V_{com-B'}$  in which the shift amount is large for low voltages and a shift occurs for high voltages.

**[0058]** Further, a liquid crystal display apparatus of the present embodiment, for example, as illustrated in Fig. 8(a), effective voltages for an identical tone to be applied to the pixels P between the counter electrodes 13 and the pixel electrodes 12 may be controlled so that an effective voltage having the positive polarity and an effective voltage having the negative polarity are shifted by 0.3 V or more from each other and are asymmetrically applied to the pixel, for a tone at which a minimum effective voltage is applied pixel and the asymmetry may be reduced more as a tone is changed from a tone for a lowest effective voltage to a tone corresponding to an effective voltage which is higher than the lowest effective voltage. Namely, the counter electrode voltage  $V_{com}$  is set to the same voltage as that conventionally set. However, since the source voltage is adjusted, it is possible to have an optimum counter electrode voltage  $V_{com-C}$  which is changed as illustrated in Fig. 8 (a). In the Fig. 8 (a), an axis of ordinate shows the optimum counter electrode voltage, i.e., a counter electrode voltage causing the flicker to be minimum, or the luminance to have a minimum or maximum value.

**[0059]** In this case, as illustrated in Fig. 8 (b), it may be possible to have another optimum counter electrode voltage  $V_{com-C'}$  in which the shift amount is large for low voltages and a shift occurs for high voltages.

**[0060]** This arrangement may be realized by adjusting a certain source voltage. Specifically, the arrangement is realized by a source voltage, for example, which is capable of controlling and adjusting a plurality of tone voltages.

**[0061]** With this arrangement, for example, voltages

to be applied to the pixel P for the black display in the normally black mode are asymmetric so that a voltage become higher than that conventionally applied in one side of the polarities. This causes the electric field to become strong in the cell thickness (vertical) direction. As a result, it is possible to prevent the movement of the impurities in the display panel in lateral directions, which leads the edge burning to be less likely to occur.

**[0062]** In the arrangements, the asymmetry is reduced as the tone is changed from a tone for the lowest effective voltages to a tone for voltages which are higher than the lower lowest effective voltages. This allows the relation between the effective voltage and the tones to get close to a conventional relation in which an effective voltage having the positive polarity and an effective voltage having the negative polarity are symmetric.

**[0063]** Further, a liquid crystal of the present embodiment, for example as illustrated in Fig. 9 (a), it is possible to adjust a source voltage for each of the tones so that the asymmetry is reduced as the effective voltage is changed from the lowest effective voltage to a second effective voltage V2, i.e.  $\pm 0.8$  V, which is higher than the lowest effective voltage. Namely, the counter electrode voltage Vcom is set to the same voltage as that conventionally set. However, since the source voltage is adjusted, it is possible to have an optimum counter electrode voltage Vcom-D which is changed as illustrated in Fig. 9 (a). In the Fig. 9 (a), an axis of ordinate shows the optimum counter electrode voltage, i.e., a counter electrode voltage causing the flicker to be minimum, or the luminance to have a minimum or maximum value.

**[0064]** In this case, as illustrated in Fig. 9 (b), it may be possible to have another optimum counter electrode voltage Vcom-D' in which the shift amount is large for low voltages and a shift occurs for high voltages.

**[0065]** With the arrangement, the asymmetry is gradually reduced up to the second effective voltage V2 which is higher than the lowest effective voltage. Therefore, it is possible to prevent the movement of the impurities in the liquid crystal panel 1 in lateral directions up to the second effective voltage V2 which causes the asymmetry to be maintained, thereby preventing the edge burning.

**[0066]** Further, according to another liquid crystal display apparatus of the present embodiment, for example as illustrated in Fig. 10 (a), it is possible that the asymmetry of an effective voltage having the positive polarity and an effective voltage having the negative polarity which are applied to the pixel becomes greater as the effective voltage gets closer to the maximum effective voltage from a third effective voltage V3 which is higher than the second effective voltage V2 and is also higher than half of the maximum effective voltage. Namely, the counter electrode voltage Vcom is set to the same voltage as that conventionally set. However, since the source voltage is adjusted, it is possible to have an optimum counter electrode Vcom-E which is changed as illustrated in Fig. 10 (a). In the Fig. 10 (a), an axis of ordinate shows

the optimum counter electrode voltage i.e., a counter electrode voltage causing the flicker to be minimum, or the luminance to have a minimum or maximum value.

**[0067]** In this case, as illustrated in Fig. 10 (b), it may also be possible to have another optimum counter electrode voltage Vcom-E' which is obtained by carrying out a parallel shift with respect to the waveform in Fig. 10 (a).

**[0068]** This allows preventing the plane burning for low voltages of not higher than the second effective voltage V2, and for high voltages of higher than the third effective voltages V3, for both of which voltages the flicker is hard to recognize.

**[0069]** Also, in a further liquid crystal display apparatus of the present embodiment, for example as illustrated in Fig. 11, an effective voltage having the positive polarity and an effective voltage having the negative polarity are asymmetrically applied to the pixel P while an axis of symmetry is shifted toward the opposite polarity for a fourth effective voltage V4 which is higher than the second effective voltage V2. According to the liquid crystal display apparatus, it is possible to reduce the asymmetry for voltages of higher than the fourth effective voltage V4.

**[0070]** With the arrangement, when the effective voltage having the positive polarity and the effective voltage having the negative polarity are asymmetrical, an axis of asymmetry is once shifted toward the opposite polarity. This allows the flicker to be prevented more surely, as compared to a case where the axis of asymmetry is in the same polarity without being shifted.

**[0071]** Also, according to the liquid crystal display apparatus of the present embodiment, for example as illustrated in Fig. 1 (a) and Fig. 1 (b), it is preferable that the effective voltage having the positive polarity and the effective voltage having the negative polarity, whose white display luminance is not more than 5% or whose transmittance is not more than 5%, are asymmetrically applied to the pixel P in the normally black mode, whereas the effective voltage having the positive polarity and the effective voltage having the negative polarity, whose white display luminance is not less than about 95% or whose transmittance is not less than about 95%, are asymmetrically applied to the pixel P in the normally white mode.

**[0072]** Namely, the effective voltage having the positive polarity and the effective voltage having the negative polarity, whose white display luminance is not more than about 5% or whose transmittance is not more than about 5 %, are asymmetrically applied to the pixel P in the normally black mode, whereas the effective voltage having the positive polarity and the effective voltage having the negative polarity, whose white display luminance is not less than about 95% or whose transmittance is not less than about 95%, are asymmetrically applied to the pixel P in the normally white mode.

**[0073]** Namely, in the above ranges, the effective voltages to be applied to the pixel P are low. Therefore, it is possible to provide a liquid crystal display apparatus which can prevent the edge burning which would occur

when a display of a fixed pattern, after having been displayed for a long time, is changed to a half tone display.

**[0074]** In the normally black mode, the range that the white display luminance or the transmittance is not more than about 5% is defined because the flicker is hard to recognize. It is more preferable that the white display luminance or the transmittance is not more than 2%.

**[0075]** On the other hand, in the normally white mode, the range that white display luminance or the transmittance is not less than about 95% is defined because the flicker is hard to recognize. It is more preferable that the white display luminance or the transmittance is not less than about 98%.

**[0076]** Further, in a liquid crystal display apparatus of the present embodiment, as illustrated in Fig. 12, the counter electrode voltage (Vcom) generating circuit 5 may control the effective voltages by a single polarity driving instead of a polarity inversion driving when the effective voltages of not less than 0.3 V to be applied to the pixels P are asymmetrical.

**[0077]** Namely, the applied voltage may have a single polarity when the counter electrode voltage Vcom is largely shifted.

**[0078]** Also, a liquid crystal display apparatus of the present embodiment, as illustrated in Fig. 13 (a) and Fig. 13 (b), the counter electrode voltage (Vcom) generating circuit 5 may shift, for every plural frames or for every plural fields, counter electrode voltages Vcom-G and Vcom-G' by 0.3 V or more alternately toward the positive polarity and the negative polarity from an effective voltage causing the voltage having the positive polarity and the voltage having the negative polarity to be asymmetrically applied to the pixel, in a certain pixel voltage range or all the pixel voltage ranges.

**[0079]** In general, when the counter electrode voltage is shifted toward a single polarity for a long time, the plane burning worsens. In this regard, with the arrangement, the polarities toward which the counter electrode voltage is shifted are alternately changed at a certain interval. This allows preventing of the plane burning.

**[0080]** Further, according to a liquid crystal display apparatus of the present embodiment, as illustrated in Fig. 13 (a) and 13 (b), when driving counter electrode voltages Vcom-G and Vcom-G' so that an effective voltage having the positive polarity and an effective voltage having the negative polarity are asymmetrically applied to a pixel, a counter electrode voltage (Vcom) generating circuit 5 may cause each of the counter electrode voltages Vcom-G and Vcom-G' to change to alternately have a shift toward the positive polarity and a shift toward the negative polarity at a certain interval.

**[0081]** With this arrangement, it is possible to carry out the changing of the polarities with a simple circuit because a shift toward the positive polarity and a shift toward the negative polarity are changed alternately at a certain interval.

**[0082]** In the above example, the counter electrode voltage Vcom is changed so as to alternately have a shift

toward the positive polarity and a shift toward the negative polarity. The present embodiment is, however, not limited to this. For example as illustrated in Fig. 14 (a) and Fig. 14 (b), when driving a source voltage Vs so that a voltage having the positive polarity and a voltage having the negative polarity become asymmetrical, the source signal generating section 4 may cause the source voltage Vs to change to alternately have a shift toward the positive polarity and a shift toward the negative polarity at a certain interval.

**[0083]** Further, in the liquid crystal display apparatus of the present embodiment, for example as illustrated in Fig. 13 (a), Fig. 13 (b), Fig. 14 (a), and Fig. 14 (b), when driving the counter electrode voltages Vcom-G and Vcom-G' so that the effective voltage having the positive polarity and the effective voltage having the negative polarity, which are applied to a pixel, become asymmetrical, the counter electrode voltage (Vcom) generating circuit 5 may cause each of the counter electrode voltages Vcom-G and Vcom-G' to change in sync with turning on the liquid crystal display panel so as to alternately or randomly have a shift toward the positive polarity and a shift toward the negative polarity.

**[0084]** With this arrangement, it is possible to carry out the changing of the polarities with a simple circuit because a shift toward the positive polarity and a shift toward the negative polarity are changed alternately or randomly in sync with turning on the liquid crystal display panel 1. Also, it appears to be appropriate to carry out the changing of the polarities in sync with turning on the liquid crystal display panel.

**[0085]** Further, in the liquid crystal display apparatus of the present embodiment, it is preferable the counter electrode voltage (Vcom) generating circuit 5 changes the polarities of the counter electrode voltages Vcom alternately toward the positive polarity and the negative polarity, after the display is changed to the entire black display, entire white display, or entire gray display.

**[0086]** Namely, when the changing of polarities is carried out during a normal display including a moving image display, a concern of display malfunction exists. Therefore, it is preferable to carry out the changing of polarities while displaying an entire black display, an entire white display, or an entire gray display which includes a single color display of red (R), green (G), and blue (B), or a still image display.

**[0087]** Further, in a liquid crystal display apparatus of the present embodiment, the counter electrode voltage (Vcom) generating circuit 5 may change the polarities of the counter electrode voltage Vcom at an interval of about one hour to ten days, so that the effective voltage having the positive polarity and the effective voltage having the negative polarity are asymmetrically applied.

**[0088]** Note that a short-term interval of the changing of polarities causes a reduction in effect of preventing the edge burning. However, a long-term interval of the changing of polarities may cause an increase in plane burning.

**[0089]** Therefore, it is preferable that a shift toward the positive polarity and a shift toward the negative polarity are changed alternately in an interval of about one hour to about ten days.

**[0090]** In the liquid crystal display apparatus of the present embodiment, it is preferable that the counter electrode voltage (Vcom) generating circuit 5 or the source signal generating circuit 4 applies a voltage to the liquid crystal 14 in the pixel P between a pair of substrates, i.e., the pixel electrode substrate 7 and the counter substrate 8 in a liquid crystal panel 1.

**[0091]** This allows providing a general liquid crystal display apparatus including a liquid crystal panel 1 having a pixel electrode substrate 7 and a counter substrate 8 with a feature that can prevent the edge burning which would occur when a display of a fixed pattern, after having been displayed for a long time, is changed into a half tone display.

**[0092]** As explained above, in the liquid crystal display apparatus of the present invention, the effective voltage control section shifts the counter electrode voltage by 0.3 V or more toward the positive polarity from a voltage causing the effective voltage having the positive polarity and the effective voltage having the negative polarity, for an identical tone, to be symmetrically applied to the pixel so that the effective voltage having the negative polarity is higher than the effective voltage having the positive polarity to be applied to the pixel.

**[0093]** Experiments revealed that it is more effective against the burning that the counter electrode voltage is set to cause the effective voltage having the negative polarity to be higher than the effective voltage having the positive polarity to be applied to the pixel for an identical tone. The reason would appear to be as follows. There are many plus ions such as positively charged impurities in the panel and these plus ions are easy to be fixed to the TFT substrate.

**[0094]** Therefore, the edge burning surely becomes less likely to happen, for example, by shifting the counter electrode voltage by 0.3 V or more toward the positive polarity from a counter electrode voltage causing the effective voltage having the positive polarity and the effective voltage having the negative polarity, for an identical tone, to be symmetrically applied to the pixel so that the effective voltage having the negative polarity is higher than the effective voltage having the positive polarity to be applied to the pixel.

**[0095]** Also, in this liquid crystal display apparatus, the effective voltages control section may control the effective voltages for an identical tone so that an effective voltage having the positive polarity and an effective voltage having the negative polarity are asymmetrically applied to the pixels between the counter electrodes and the pixel electrodes, even for effective voltages of higher than the first effective voltage, and may reduce asymmetry for voltages of higher than the first effective voltage.

**[0096]** Namely, the edge burning occurs for low effective.

Therefore, when the asymmetry is reduced for the effective voltages which are higher than the first effective voltages, it is possible that the relation between the effective voltages and the tones gets close to a conventional relation.

**[0097]** Also, in the liquid crystal display apparatus, the effective voltage control section may reduce the asymmetry as the effective voltage is changed from the lowest effective voltage to a second effective voltage which is higher than the lowest effective voltage.

**[0098]** Accordingly, the asymmetry is gradually reduced up to the second effective voltage which is higher than the lowest effective voltage. Therefore, it is possible to prevent the movement of the impurities in the liquid crystal panel in lateral directions up to the second effective voltage which causes the asymmetry to be maintained, thereby preventing the edge burning.

**[0099]** Further, in this liquid crystal display apparatus, the effective voltage control section may cause the effective voltage having the positive polarity and the effective voltage having the negative polarity to be symmetrically applied to the pixel for an effective voltage which is higher than the second effective voltage.

**[0100]** This allows the relation between the effective voltage and the tone to be the same as the conventional relation for the effective voltage is higher than the second effective voltage.

**[0101]** Further, in the liquid crystal display apparatus, the effective voltage control section may cause the asymmetry of the effective voltage having the positive polarity and the effective voltage having the negative polarity which are applied to the pixel to become greater from a third effective voltage, which is higher than the second effective voltage and is also higher than half of the maximum effective voltage, to the maximum effective voltage.

**[0102]** This allows preventing the plane burning for low voltages of not higher than the second effective voltage, and for high voltage of higher than the third effective voltage, for both of which voltages the flicker is hard to recognize.

**[0103]** Further, in the liquid crystal display apparatus, the effective voltage control section may cause the effective voltage having the positive polarity and the effective voltage having the negative polarity to be asymmetrically applied to the pixel while an axis of symmetry is shifted toward the opposite polarity, for a fourth effective voltage which is higher than the second effective voltage, and causes the asymmetry to be small, for an effective voltage which is higher than the fourth effective voltage.

**[0104]** Accordingly, when the effective voltages are asymmetrical in the positive and negative polarities, an axis of asymmetry is once shifted toward the opposite polarity. This allows the flicker to be prevented more surely, as compared to a case where the axis of asymmetry is in the same polarity without being shifted.

**[0105]** Further, in the liquid crystal display apparatus, the effective voltage control section may cause the effective

fective voltage having the positive polarity and the effective voltage having the negative polarity, whose white display luminance is not more than 5% or whose transmittance is not more than 5%, to be asymmetrically applied to the pixel in a normally black mode, whereas the effective voltage having the positive polarity and the effective voltage having the negative polarity, whose white display luminance is not less than about 95% or whose transmittance is not less than about 95%, to be asymmetrically applied to the pixel in a normally white mode.

**[0106]** Namely, the effective voltage having the positive polarity and the effective voltage having the negative polarity, whose white display luminance is not more than about 5% or whose transmittance is not more than about 5 %, are asymmetrically applied to the pixel in the normally black mode, whereas the effective voltage having the positive polarity and the effective voltage having the negative polarity, whose white display luminance is not less than about 95% or whose transmittance is not less than about 95%, are asymmetrically applied to the pixel in the normally white mode.

**[0107]** In the above ranges, the effective voltages to be applied to the pixel P are low. Therefore, it is possible to provide a liquid crystal display apparatus which can prevent the edge burning which would occur when a display of a fixed pattern, after having been displayed for a long time, is changed to a half tone display.

**[0108]** In the normally black mode, the range that the white display luminance or the transmittance is not more than about 5% is defined because the flicker is hard to recognize. It is more preferable that the white display luminance or the transmittance is not more than 2%.

**[0109]** On the other hand, in the normally white mode, the range that white display luminance or the transmittance is not less than about 95% is defined because the flicker is hard to recognize. It is more preferable that the white display luminance or the transmittance is not less than about 98%.

**[0110]** Further, in the liquid crystal display apparatus, the effective voltage control section may control the effective voltage by a single polarity driving instead of a polarity inversion driving for a certain pixel voltage region while the effective voltages to be applied to the pixel are asymmetrical.

**[0111]** Namely, the applied voltage may have a single polarity when the counter electrode voltage or the source voltage is largely shifted.

**[0112]** Further, in the liquid crystal display apparatus, the effective voltage control section may shift, for every plural frames or for every plural fields, the counter electrode voltage or the source voltage by 0.3 V or more alternately toward the positive polarity and the negative polarity from an effective voltage causing the voltage having the positive polarity and the voltage having the negative polarity to be symmetrically applied to the pixel, in a certain pixel voltage range.

**[0113]** With this arrangement, the polarities toward which the counter electrode voltage or the source voltage

is shifted are alternately changed at a certain interval. This allows preventing of the plane burning.

**[0114]** Further, in the liquid crystal display apparatus, the effective voltage control section may shift the counter electrode voltage or the source voltage alternately toward the positive polarity and the negative polarity at a certain cycle, so that the effective voltage having the positive polarity and the effective voltage having the negative polarity are asymmetrically applied.

**[0115]** With this arrangement, it is possible to carry out the changing of the polarities with a simple circuit because a shift toward the positive polarity and a shift toward the negative polarity are changed alternately at a certain interval.

**[0116]** Further, in the liquid crystal display apparatus, the effective voltage control section may change the counter electrode voltage or the source voltage in sync with turning on the liquid crystal display panel so that the counter electrode voltage or the source voltage alternately or randomly has a shift toward the positive polarity and a shift toward the negative polarity, so as to perform the asymmetrical application of the effective voltage having the positive polarity and the effective voltage having the negative polarity.

**[0117]** With this arrangement, it is possible to carry out the changing of the polarities with a simple circuit because a shift toward the positive polarity and a shift toward the negative polarity are changed alternately or randomly in sync with turning on the liquid crystal display panel 1. Also, it appears to be appropriate to carry out the changing of the polarities in sync with turning on the liquid crystal display panel.

**[0118]** Further, in the liquid crystal display apparatus, the effective voltage control section may shift the counter electrode voltage or the source voltage alternately toward the positive polarity and the negative polarity, while displaying an entire black display, an entire white display, or an entire gray display which includes a single color display of red (R), green (G), and blue (B), or a still image.

**[0119]** Namely, when the changing of polarities is carried out during a normal display including a moving image display, a concern of display malfunction exists. Therefore, it is preferable to carry out the changing of polarities while displaying the entire black display, the entire white display, or the entire gray display which includes a single color display of red (R), green (G), and blue (B), or a still image display.

**[0120]** Further, in the liquid crystal display apparatus, the effective voltage control section may shift the counter electrode voltage or the source voltage alternately toward the positive polarity and the negative polarity in an interval of about one hour to ten days, so that the effective voltage having the positive polarity and the effective voltage having the negative polarity are asymmetrically applied.

**[0121]** Note that a short-term interval of the changing of polarities causes a reduction in effect of preventing the edge burning. However, a long-term interval of the changing of polarities may cause an increase in plane

burning.

**[0122]** Therefore, it is preferable that a shift toward the positive polarity and a shift toward the negative polarity are changed alternately in an interval of about one hour to ten days.

**[0123]** Further, in the liquid crystal display apparatus, the effective voltage control section may apply a voltage to a liquid crystal in a pixel between a pair of substrates, i.e., a pixel electrode substrate and a counter substrate in a liquid crystal panel.

**[0124]** This allows providing a general liquid crystal display apparatus including a liquid crystal display panel having a pixel electrode substrate and a counter electrode with a feature that can prevent the edge burning which would occur when a display of a fixed pattern, after having been displayed for a long time, is changed to a half tone display.

**[0125]** The invention being thus described, it will be obvious that the same way may be varied in many ways. Such variations are not to be regarded as a departure from the spirit and scope of the invention, and all such modifications as would be obvious to one skilled in the art are intended to be included within the scope of the following claims.

#### INDUSTRIAL APPLICABILITY

**[0126]** The present invention is applicable to a VA or a TN type liquid crystal display apparatus in which a voltage having the positive polarity and a voltage having the negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode, and is applicable to a method for driving the same.

#### Claims

1. A liquid crystal display apparatus in which a voltage having a positive polarity and a voltage having a negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode, comprising:

an effective voltage control section that controls effective voltages for an identical tone so that an effective voltage having the positive polarity and an effective voltage having the negative polarity have asymmetrical values to be applied to the pixel for all of tones which can be displayed, wherein the effective voltage control section shifts a counter electrode voltage or a source voltage by 0.3 V or more from a voltage causing the effective voltages in the positive and the negative polarity to be symmetrically applied to the pixel.

2. The liquid crystal display apparatus as set forth in Claim 1, wherein:

the effective voltage control section shifts the counter electrode voltage by 0.3 V or more toward the positive polarity from a voltage causing the effective voltage having the positive polarity and the effective voltage having the negative polarity, for an identical tone, to be symmetrically applied to the pixel so that the effective voltage having the negative polarity is higher than the effective voltage having the positive polarity to be applied to the pixel.

3. A liquid crystal display apparatus in which a voltage having a positive polarity and a voltage having a negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode, comprising:

an effective voltage control section that controls effective voltages for an identical tone so that an effective voltage having the positive polarity and an effective voltage having the negative polarity have asymmetrical values to be applied to the pixel, for the effective voltages of not more than a first effective voltage which is lower than half of a maximum effective voltage,

wherein the effective voltage control section shifts a counter electrode voltage or a source voltage by 0.3 V or more from a voltage causing the effective voltages in the positive polarity and the negative polarity to be symmetrically applied to the pixel, for the effective voltages of not more than the first effective voltage.

4. The liquid crystal display apparatus as set forth in Claim 3, wherein:

the effective voltage control section controls the effective voltages for an identical tone so that an effective voltage having the positive polarity and an effective voltage having the negative polarity have asymmetrical values to be applied to the pixel, even for effective voltages of higher than the first effective voltage, and reduces asymmetry for voltages of higher than the first effective voltage.

5. A liquid crystal display apparatus in which a voltage having a positive polarity and a voltage having a negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode, comprising:

an effective voltage control section that controls effective voltages for an identical tone so that an effective voltage having the positive polarity and an effective voltage having the negative polarity are shifted by 0.3 V or more from each

other and are asymmetrically applied to the pixel, for a tone at which a minimum effective voltage is applied to the pixel, and reduces asymmetry more as a tone is changed from a tone for a lowest effective voltage to a tone corresponding to an effective voltage which is higher than the lowest effective voltage.

6. The liquid crystal display apparatus as set forth in Claim 5, wherein:

the effective voltage control section reduces the asymmetry as the effective voltage is changed from the lowest effective voltage to a second effective voltage which is higher than the lowest effective voltage.

7. The liquid crystal display apparatus as set forth in Claim 6, wherein:

the effective voltage control section causes the effective voltage having the positive polarity and the effective voltage having the negative polarity to be symmetrically applied to the pixel for an effective voltage which is higher than the second effective voltage.

8. The liquid crystal display apparatus as set forth in Claim 6, wherein:

the effective voltage control section causes the asymmetry of the effective voltage having the positive polarity and the effective voltage having the negative polarity which are applied to the pixel to become greater from a third effective voltage, which is higher than the second effective voltage and is also higher than half of a maximum effective voltage, to the maximum effective voltage.

9. The liquid crystal display apparatus as set forth in Claim 7, wherein:

the effective voltage control section causes the asymmetry of the effective voltage having the positive polarity and the effective voltage having the negative polarity which are applied to the pixel to become greater from a third effective voltage, which is higher than the second effective voltage and is also higher than half of a maximum effective voltage, to the maximum effective voltage.

10. The liquid crystal display apparatus as set forth in Claim 6, wherein:

the effective voltage control section causes the

effective voltage having the positive polarity and the effective voltage having the negative polarity to be asymmetrically applied to the pixel while an axis of symmetry is shifted toward the opposite polarity, for a fourth effective voltage which is higher than the second effective voltage, and causes the asymmetry to be small, for an effective voltage which is higher than the fourth effective voltage.

11. The liquid crystal display apparatus as set forth in any one of Claims 1 to 10, wherein:

the effective voltage control section causes the effective voltage having the positive polarity and the effective voltage having the negative polarity, whose white display luminance is not more than 5% or whose transmittance is not more than 5%, to be asymmetrically applied to the pixel in a normally black mode, whereas the effective voltage having the positive polarity and the effective voltage having the negative polarity, whose white display luminance is not less than about 95% or whose transmittance is not less than about 95%, to be asymmetrically applied to the pixel in a normally white mode.

12. The liquid crystal display apparatus as set forth in any one of Claims 1 to 10, wherein:

the effective voltage control section controls effective voltage by a single polarity driving instead of a polarity inversion driving for a certain pixel voltage region while the effective voltages to be applied to the pixel are asymmetrical.

13. The liquid crystal display apparatus as set forth in any one of Claims 1 to 9, wherein:

the effective voltage control section shifts, for every plural frames or for every plural fields, the counter electrode voltage or the source voltage by 0.3 V or more alternately toward the positive polarity and the negative polarity from an effective voltage causing the voltage having the positive polarity and the voltage having the negative polarity to be symmetrically applied to the pixel, in a certain pixel voltage range.

14. The liquid crystal display apparatus as set forth in any one of Claims 1, 3, or 5, wherein:

the effective voltage control section shifts the counter electrode voltage or the source voltage alternately toward the positive polarity and the negative polarity at a certain cycle, when the effective voltage having the positive polarity and the effective voltage having the negative polarity

are asymmetrically applied.

15. The liquid crystal display apparatus as set forth in any one of Claims 1, 3, or 5, wherein:

the effective voltage control section changes the counter electrode voltage or the source voltage in sync with turning on the liquid crystal display panel so that the counter electrode voltage or the source voltage alternately or randomly has a shift toward the positive polarity and a shift toward the negative polarity, so as to perform the asymmetrical application of the effective voltage having the positive polarity and the effective voltage having the negative polarity.

16. The liquid crystal display apparatus as set forth in any one of Claims 1, 3, or 5, wherein:

the effective voltage control section shifts the counter electrode voltage or the source voltage alternately toward the positive polarity and the negative polarity, while displaying an entire black display, an entire white display, or an entire gray image which includes a single color display of red (R), green (G), blue (B), or a still image display.

17. The liquid crystal display apparatus as set forth in any one of Claims 1, 3, or 5, wherein:

the effective voltage control section shifts the counter electrode voltage or the source voltage alternately toward the positive polarity and the negative polarity in an interval of about one hour to ten days, so that the effective voltage having the positive polarity and the effective voltage having the negative polarity are asymmetrically applied.

18. The liquid crystal display apparatus as set forth in any one of Claims 1, 3, or 5, wherein:

the effective voltage control section applies a voltage to a liquid crystal in a pixel between a pair of substrates, i.e., a pixel electrode substrate and a counter substrate in a liquid crystal panel.

19. A method for driving a liquid crystal display apparatus in which a voltage having a positive polarity and a voltage having a negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode, comprising the steps of:

controlling effective voltages for an identical tone so that an effective voltage having the positive polarity and an effective voltage having the

negative polarity have asymmetrical values to be applied to the pixel for all of tones which can be displayed; and

shifting a counter electrode voltage or a source voltage by 0.3 V or more from a voltage causing the effective voltages in the positive polarity and the negative polarity to be symmetrically applied to the pixel.

20. A method for driving a liquid crystal display apparatus in which a voltage having the positive polarity and a voltage having the negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode, comprising the steps of:

controlling effective voltages for an identical tone so that an effective voltage having the positive polarity and an effective voltage having the negative polarity have asymmetrical values to be applied to the pixel, for the effective voltages of not more than a first effective voltage which is lower than half of a maximum effective voltage; and

shifting a counter electrode voltage or a source voltage by 0.3 V or more from a voltage causing the effective voltages in the positive polarity and the negative polarity to be symmetrically applied to the pixel.

21. A method for driving a liquid crystal display apparatus in which a voltage having a positive polarity and a voltage having a negative polarity are alternately applied to a pixel between a counter electrode and a pixel electrode, comprising the steps of:

controlling effective voltages for an identical tone so that an effective voltage having the positive polarity and an effective voltage having the negative polarity are shifted by 0.3 V or more from each other and are asymmetrically applied to the pixel, for a tone at which a minimum effective voltage is applied to the pixel; and reducing asymmetry more as a tone is changed from a tone for a lowest effective voltage to a tone corresponding to an effective voltage which is higher than the lowest effective voltage.

FIG. 1 (a)

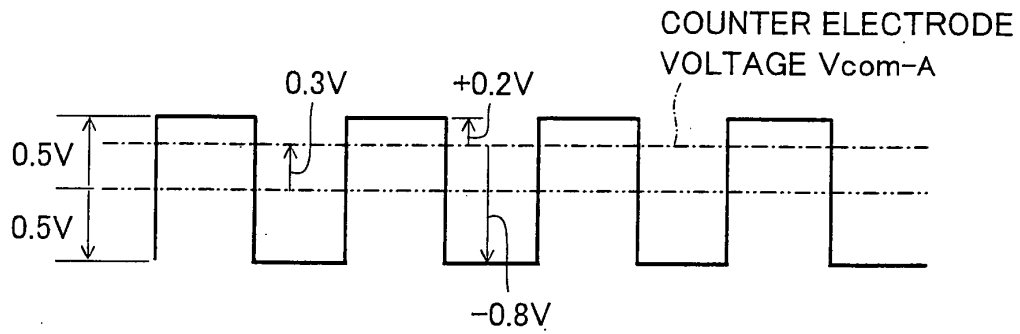


FIG. 1 (b)

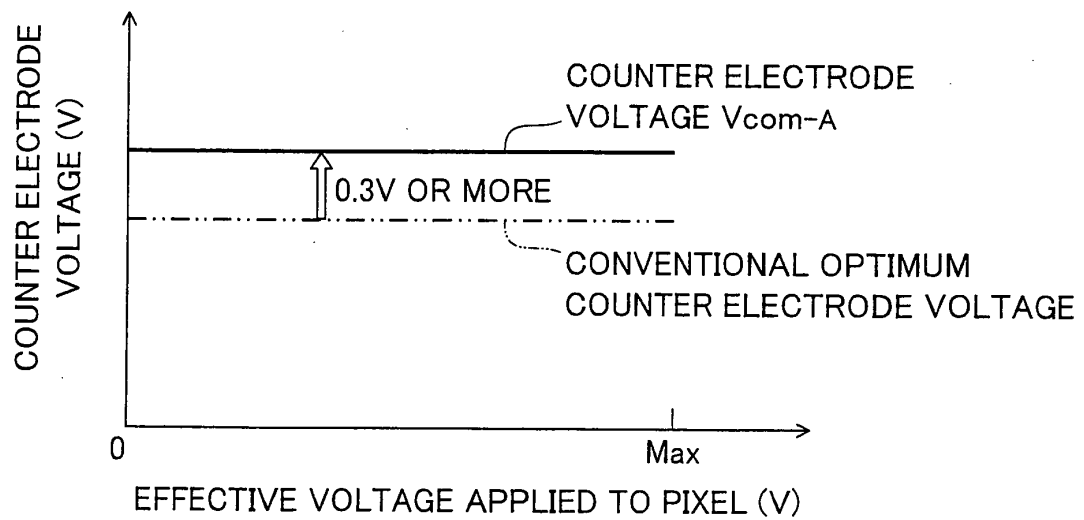


FIG. 2

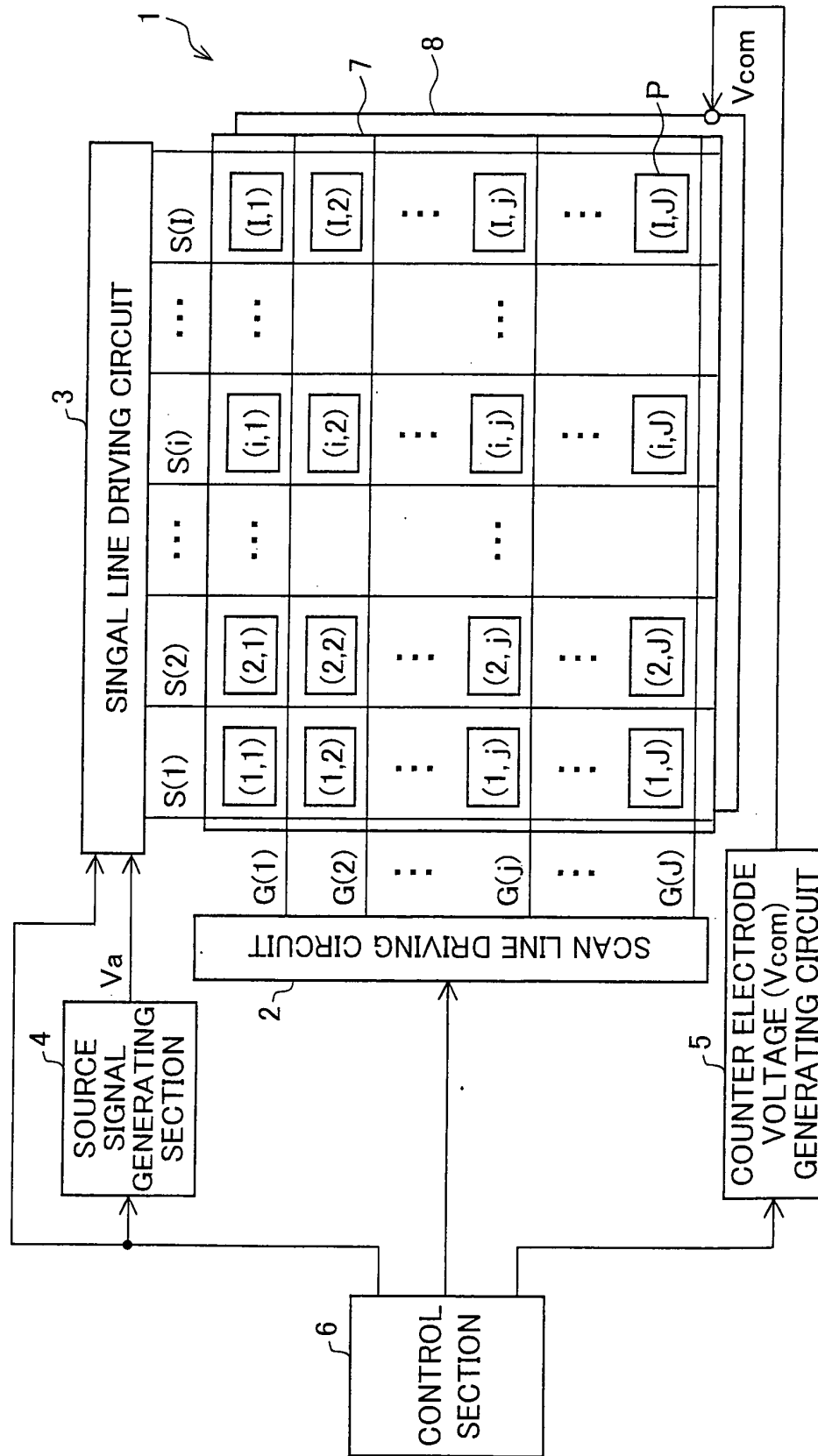


FIG. 3

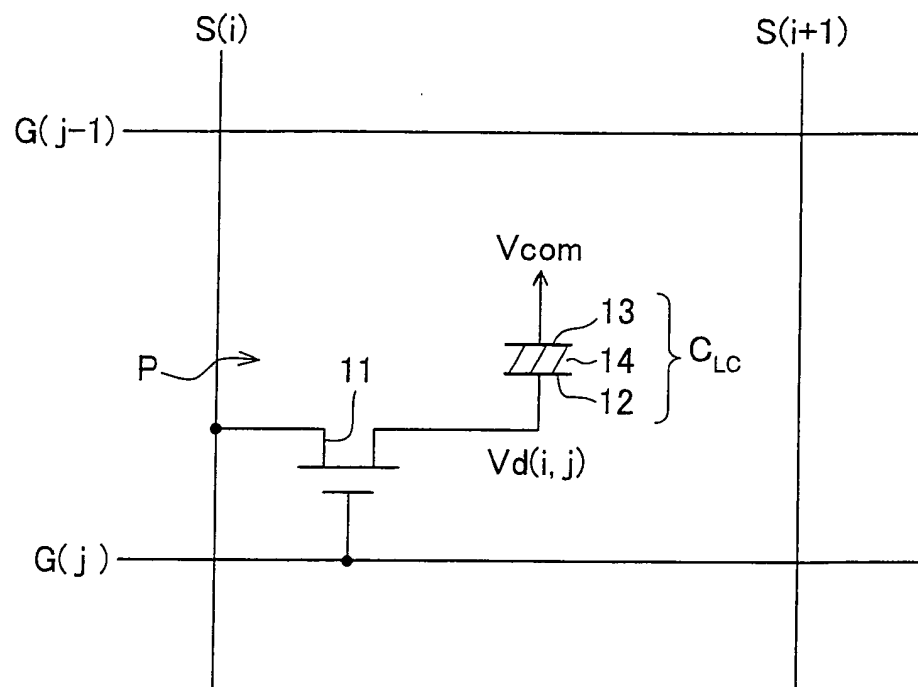


FIG. 4

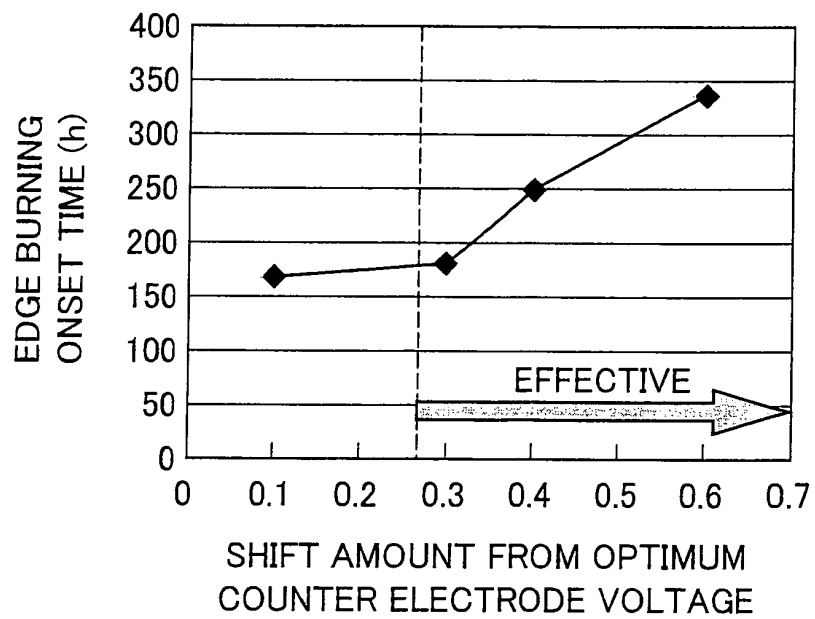


FIG. 5

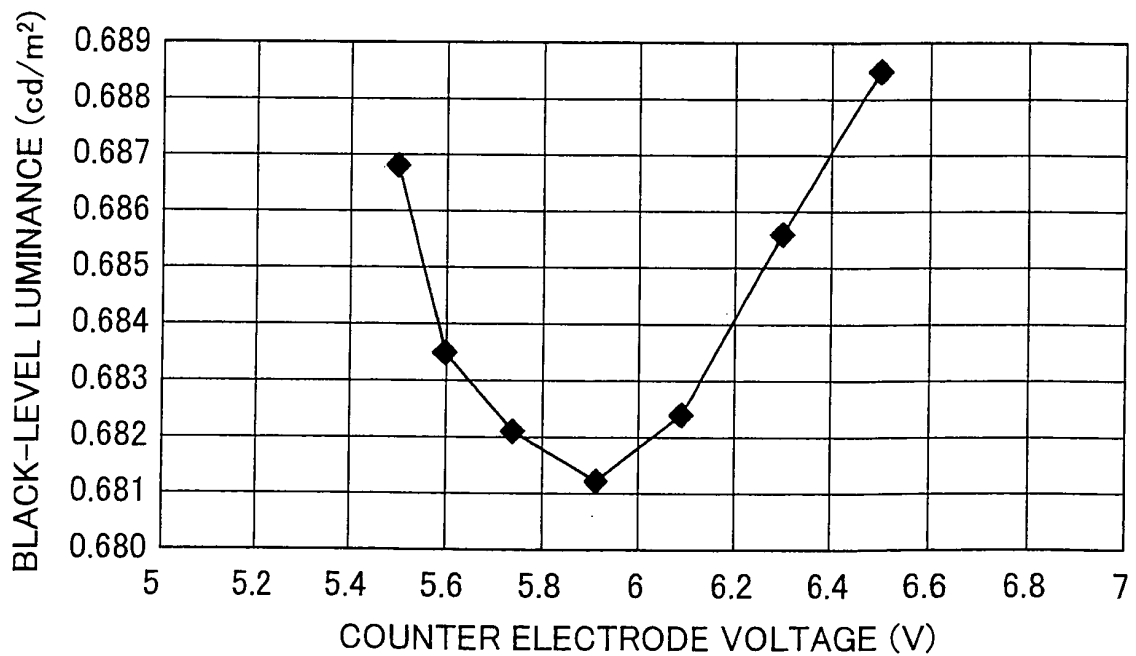


FIG. 6

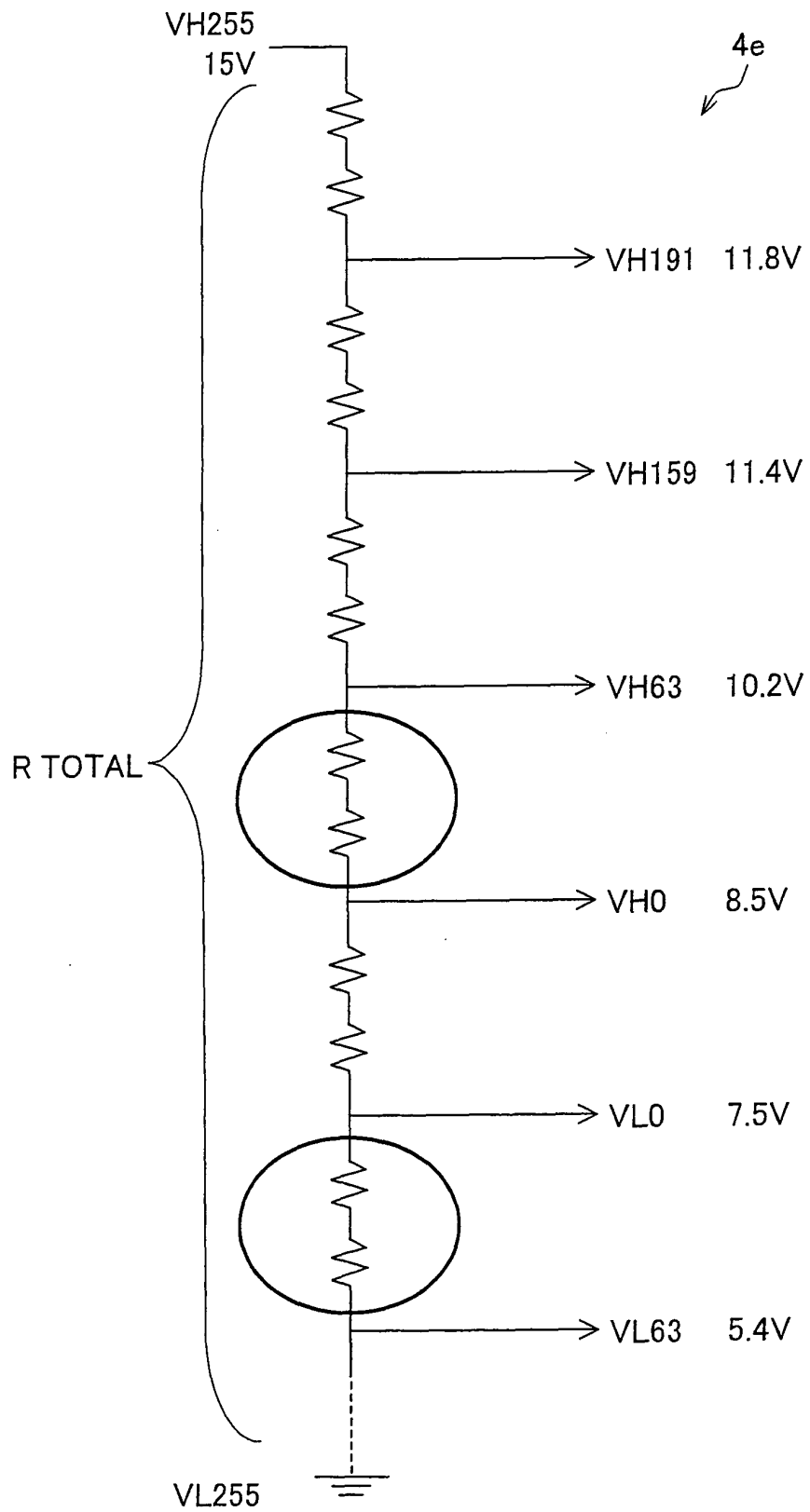


FIG. 7 (a)

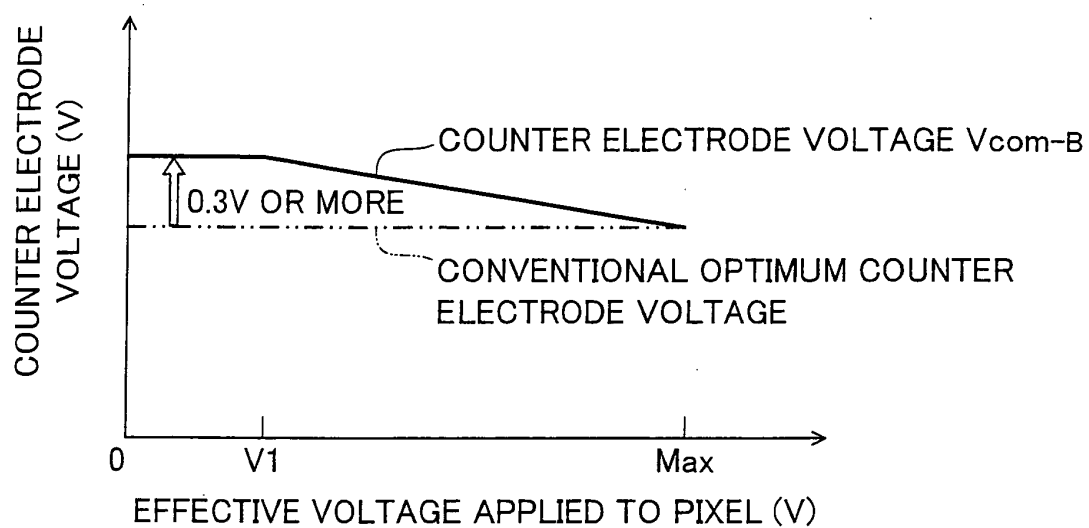


FIG. 7 (b)

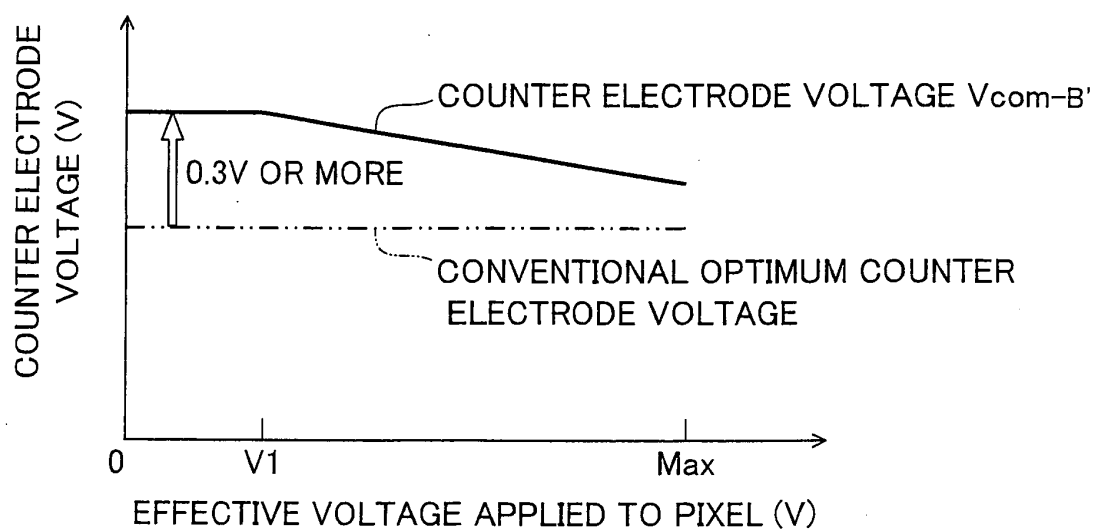


FIG. 8 (a)

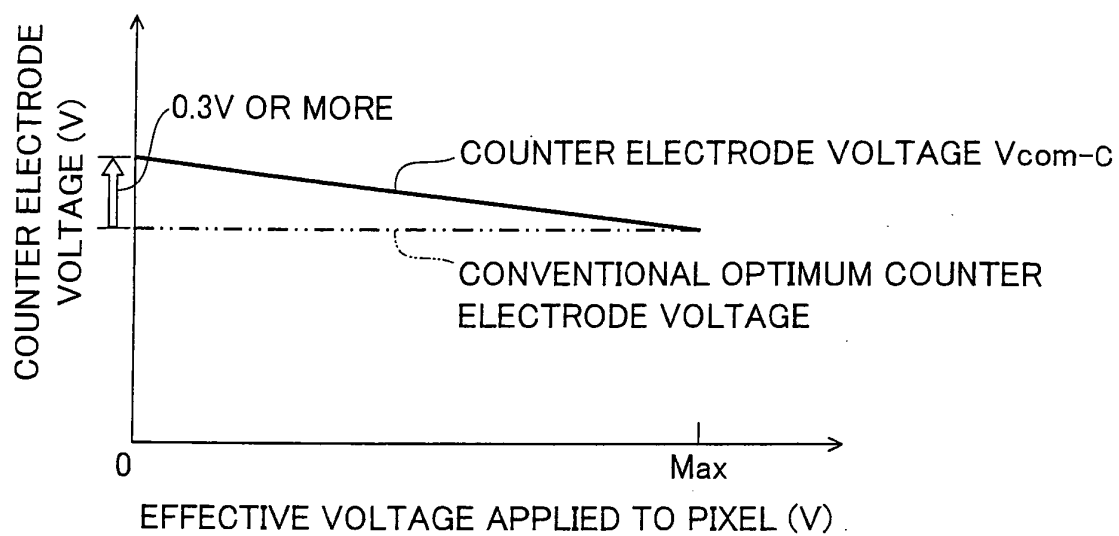


FIG. 8 (b)

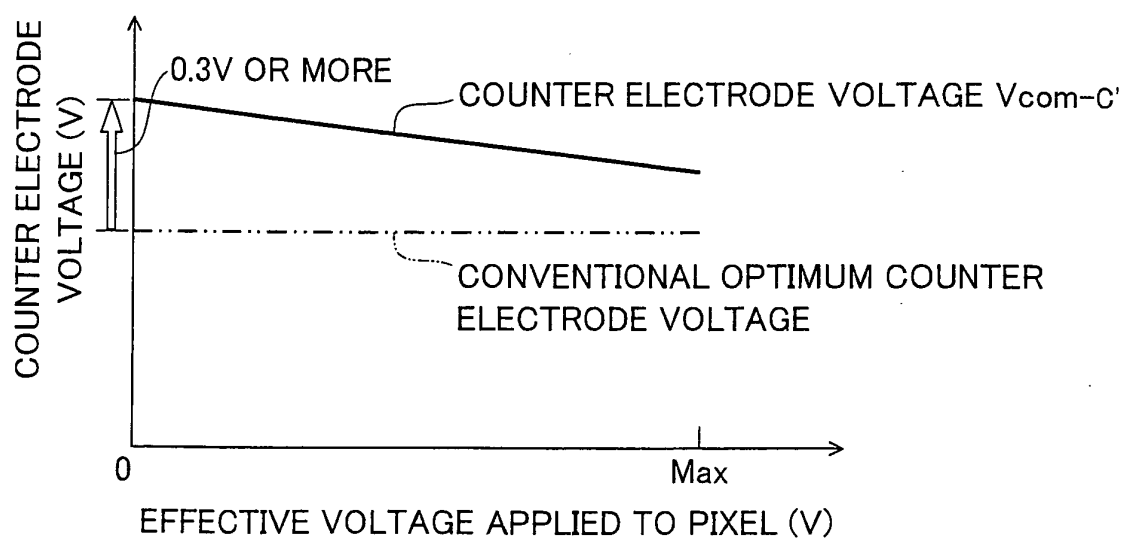


FIG. 9 (a)

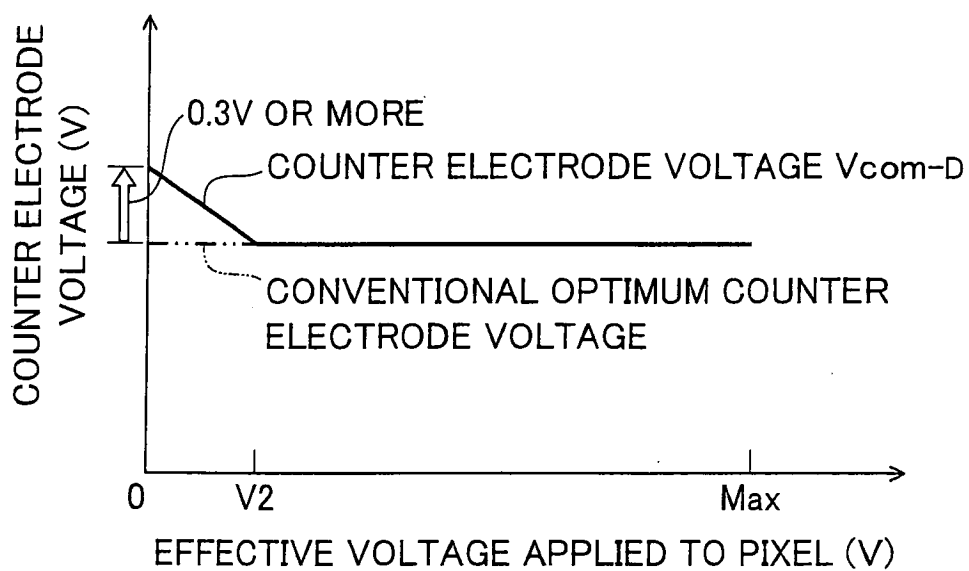


FIG. 9 (b)

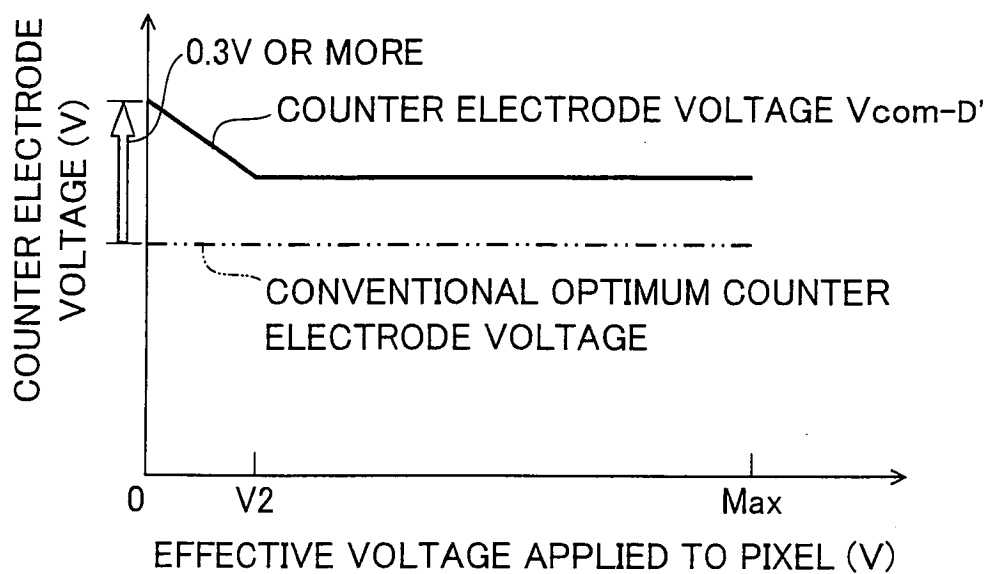


FIG. 10 (a)

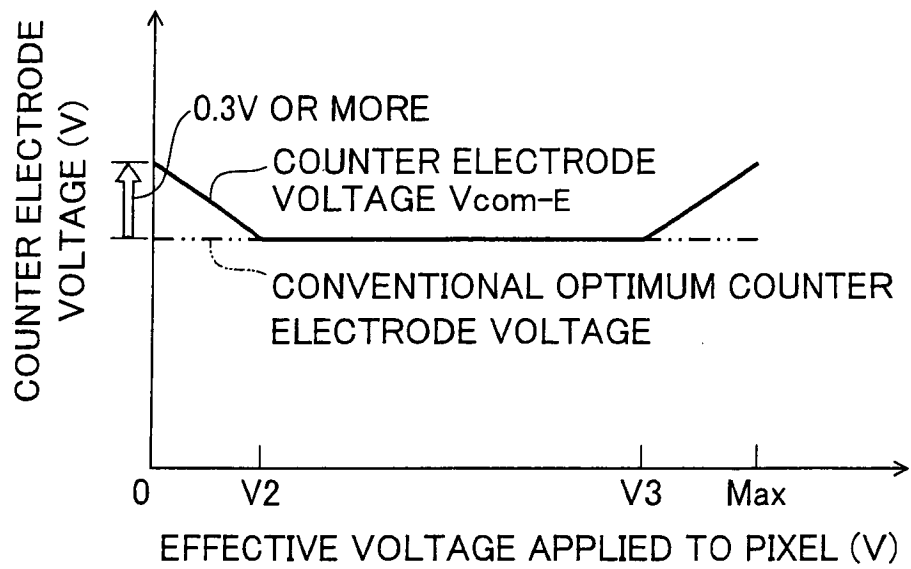


FIG. 10 (b)

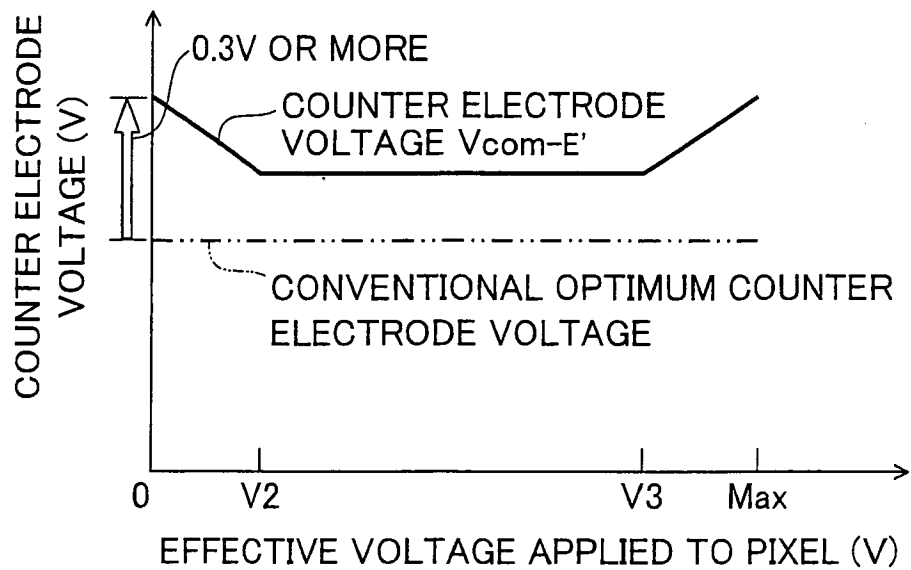


FIG. 11

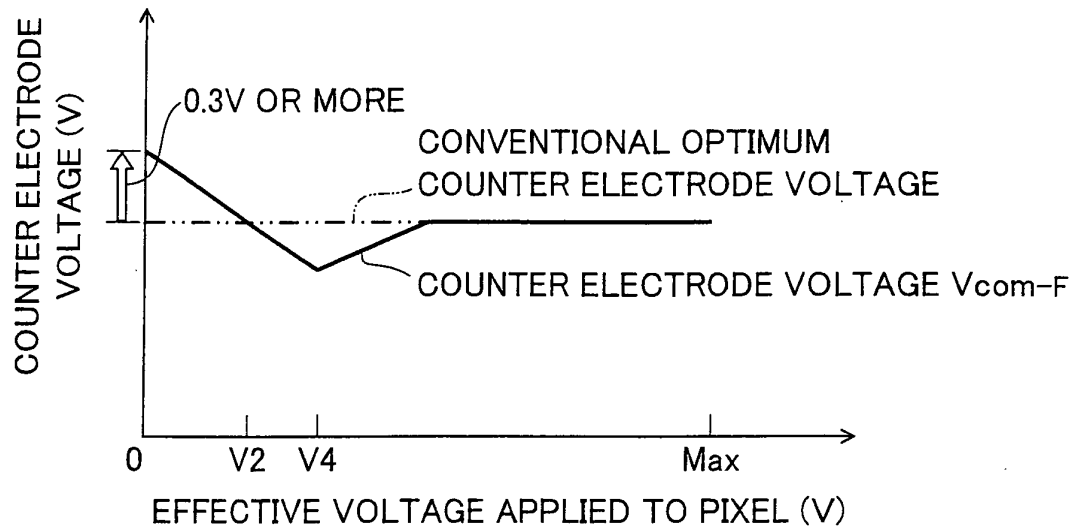


FIG. 12

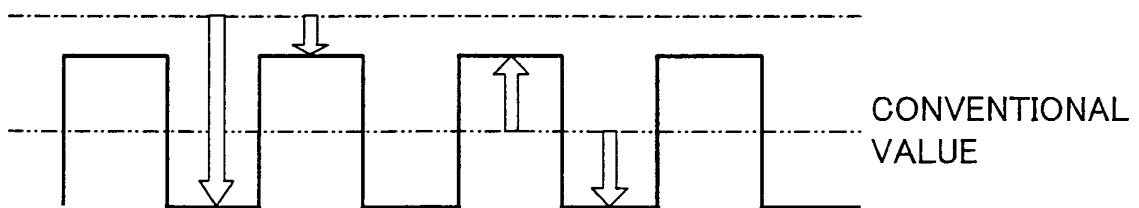


FIG. 13 (a)

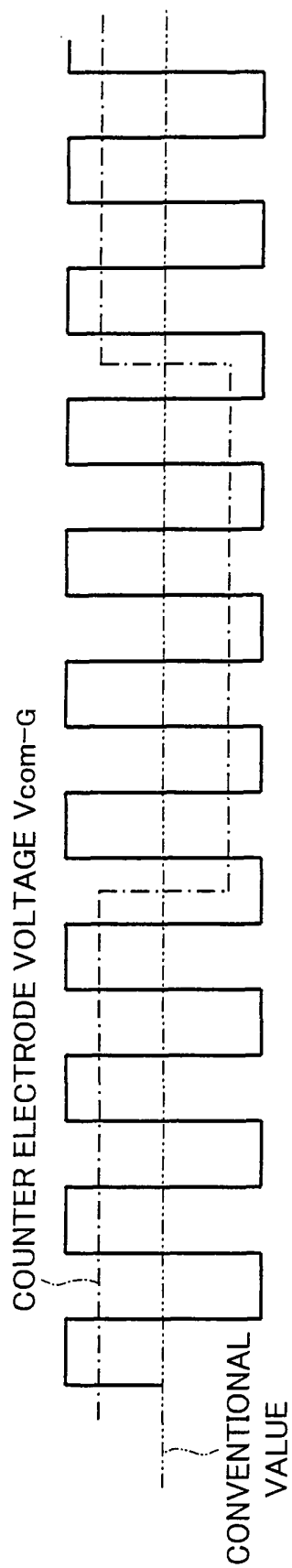


FIG. 13 (b)

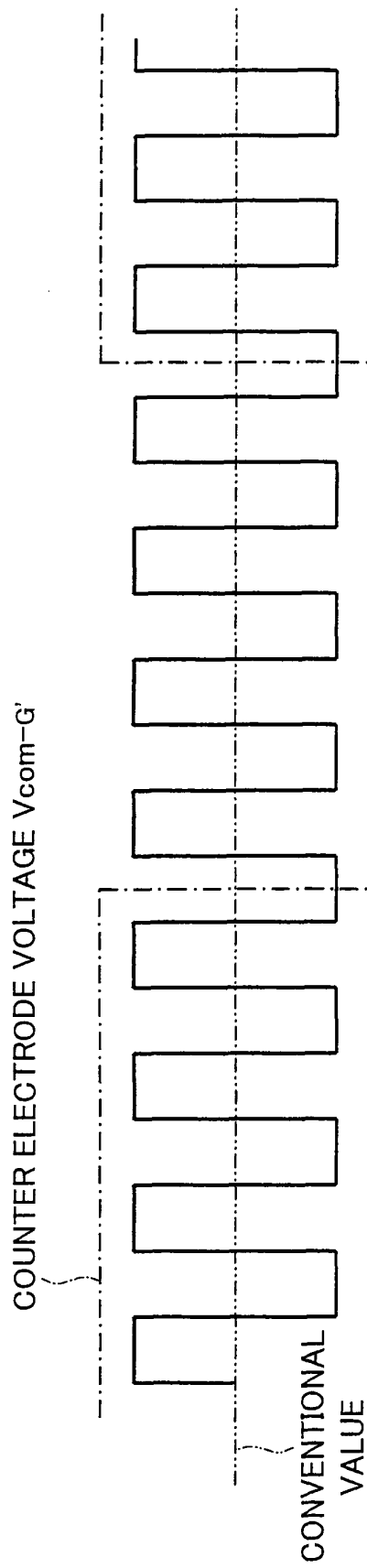


FIG. 14 (a)

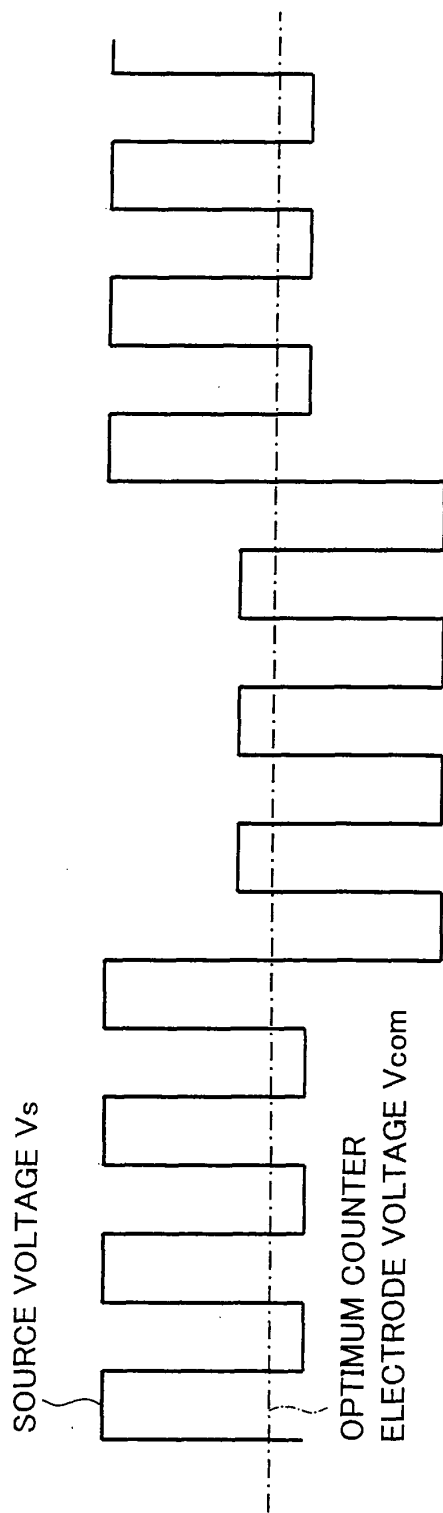


FIG. 14 (b)

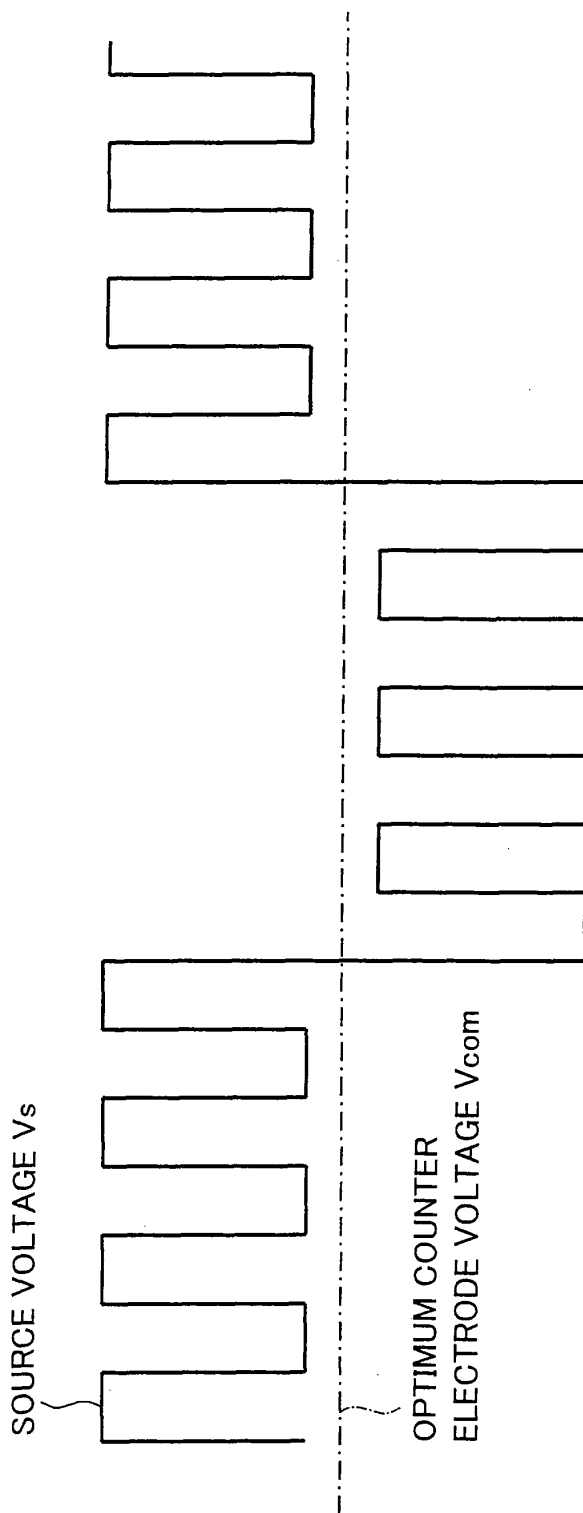


FIG. 15

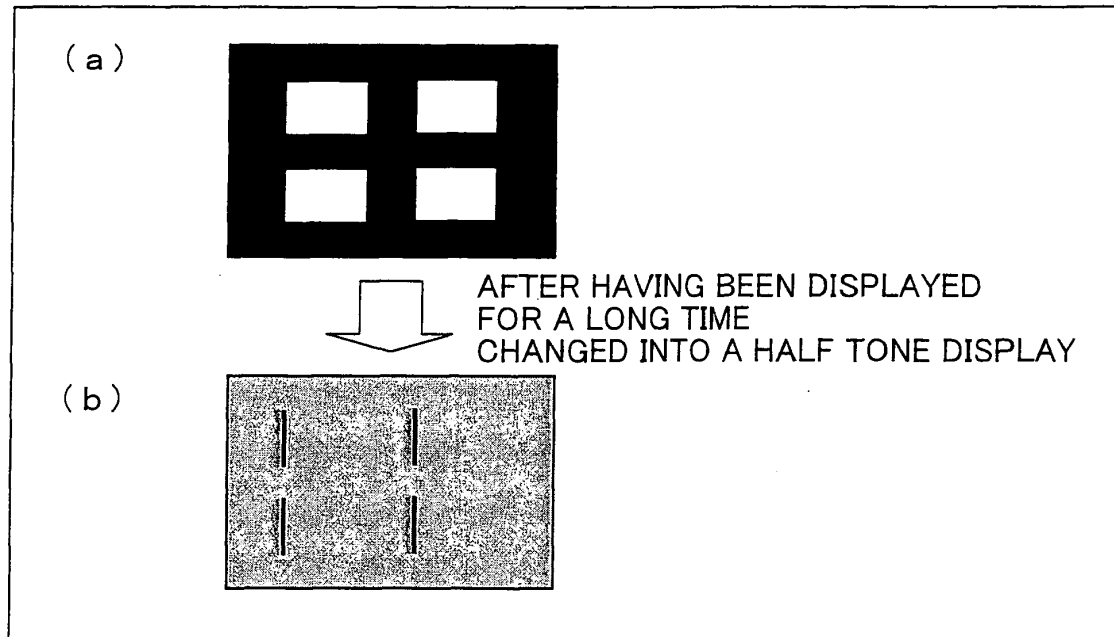


FIG. 16

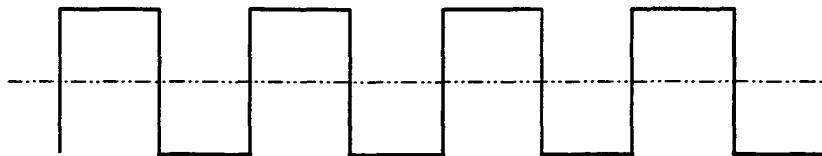


FIG. 17

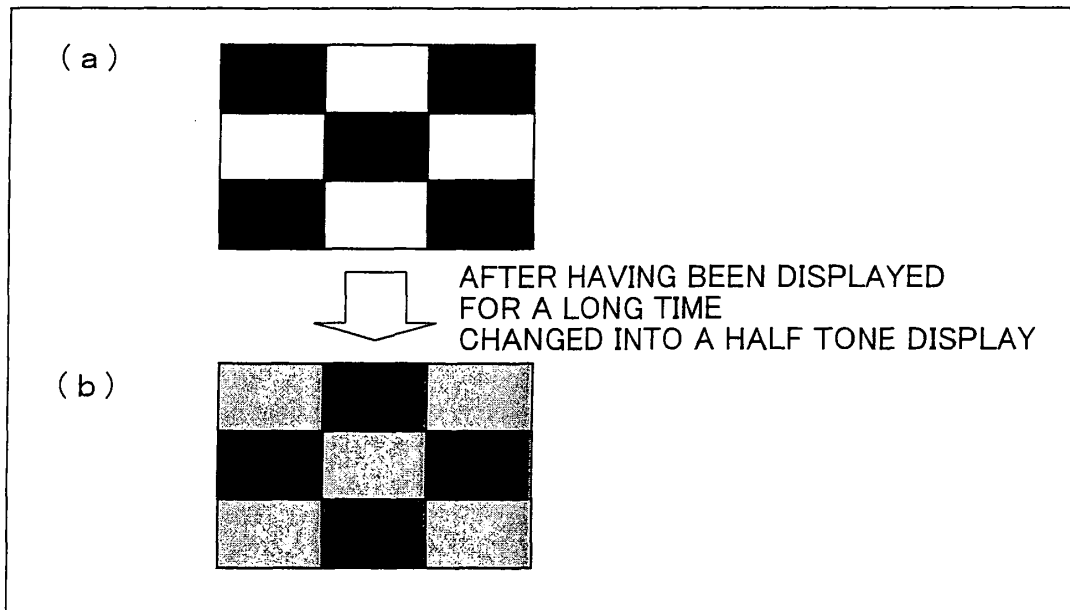
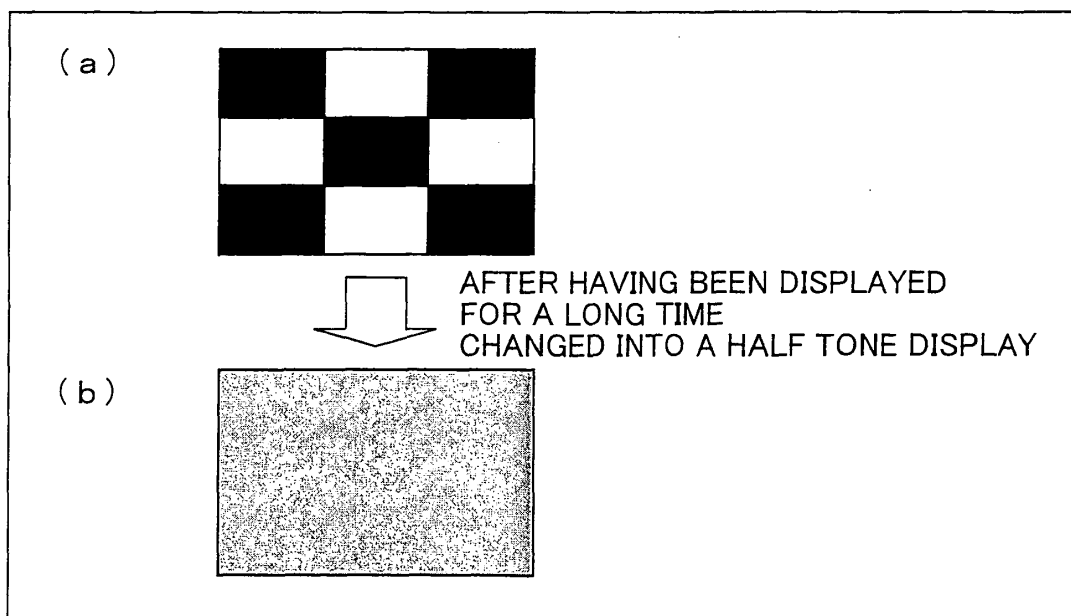


FIG. 18



## INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2007/057648

| <b>A. CLASSIFICATION OF SUBJECT MATTER</b><br><i>G09G3/36(2006.01) i, G02F1/133(2006.01) i, G09G3/20(2006.01) i</i>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                                                                                                                                                                |                                                                               |                            |                  |                                   |                  |                                  |                  |                                   |                  |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------|----------------------------|------------------|-----------------------------------|------------------|----------------------------------|------------------|-----------------------------------|------------------|
| According to International Patent Classification (IPC) or to both national classification and IPC                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |                                                                                                                                                                |                                                                               |                            |                  |                                   |                  |                                  |                  |                                   |                  |
| <b>B. FIELDS SEARCHED</b><br>Minimum documentation searched (classification system followed by classification symbols)<br><i>G09G3/36, G02F1/133, G09G3/20</i>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |                                                                                                                                                                |                                                                               |                            |                  |                                   |                  |                                  |                  |                                   |                  |
| Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched<br><table border="0"> <tr> <td><i>Jitsuyo Shinan Koho</i></td> <td><i>1922-1996</i></td> <td><i>Jitsuyo Shinan Toroku Koho</i></td> <td><i>1996-2007</i></td> </tr> <tr> <td><i>Kokai Jitsuyo Shinan Koho</i></td> <td><i>1971-2007</i></td> <td><i>Toroku Jitsuyo Shinan Koho</i></td> <td><i>1994-2007</i></td> </tr> </table>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |                                                                                                                                                                |                                                                               | <i>Jitsuyo Shinan Koho</i> | <i>1922-1996</i> | <i>Jitsuyo Shinan Toroku Koho</i> | <i>1996-2007</i> | <i>Kokai Jitsuyo Shinan Koho</i> | <i>1971-2007</i> | <i>Toroku Jitsuyo Shinan Koho</i> | <i>1994-2007</i> |
| <i>Jitsuyo Shinan Koho</i>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | <i>1922-1996</i>                                                                                                                                               | <i>Jitsuyo Shinan Toroku Koho</i>                                             | <i>1996-2007</i>           |                  |                                   |                  |                                  |                  |                                   |                  |
| <i>Kokai Jitsuyo Shinan Koho</i>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | <i>1971-2007</i>                                                                                                                                               | <i>Toroku Jitsuyo Shinan Koho</i>                                             | <i>1994-2007</i>           |                  |                                   |                  |                                  |                  |                                   |                  |
| Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |                                                                                                                                                                |                                                                               |                            |                  |                                   |                  |                                  |                  |                                   |                  |
| <b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                                                                                                                                                                |                                                                               |                            |                  |                                   |                  |                                  |                  |                                   |                  |
| Category*                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          | Citation of document, with indication, where appropriate, of the relevant passages                                                                             | Relevant to claim No.                                                         |                            |                  |                                   |                  |                                  |                  |                                   |                  |
| A                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | JP 9-152627 A (Frontec Inc.),<br>10 June, 1997 (10.06.97),<br>Full text; all drawings<br>& US 6344842 B1 & KR 97/0029310 A                                     | 1-21                                                                          |                            |                  |                                   |                  |                                  |                  |                                   |                  |
| A                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | JP 2003-5151 A (Toshiba Corp.),<br>08 January, 2003 (08.01.03),<br>Full text; all drawings<br>& US 2003/0006950 A1 & US 2006/0012554 A1<br>& KR 2002/0097018 A | 1-21                                                                          |                            |                  |                                   |                  |                                  |                  |                                   |                  |
| A                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | JP 2004-69886 A (Canon Inc.),<br>04 March, 2004 (04.03.04),<br>Full text; all drawings<br>(Family: none)                                                       | 1-21                                                                          |                            |                  |                                   |                  |                                  |                  |                                   |                  |
| <input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |                                                                                                                                                                |                                                                               |                            |                  |                                   |                  |                                  |                  |                                   |                  |
| * Special categories of cited documents:<br>"A" document defining the general state of the art which is not considered to be of particular relevance<br>"E" earlier application or patent but published on or after the international filing date<br>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)<br>"O" document referring to an oral disclosure, use, exhibition or other means<br>"P" document published prior to the international filing date but later than the priority date claimed<br>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention<br>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone<br>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art<br>"&" document member of the same patent family |                                                                                                                                                                |                                                                               |                            |                  |                                   |                  |                                  |                  |                                   |                  |
| Date of the actual completion of the international search<br>19 April, 2007 (19.04.07)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |                                                                                                                                                                | Date of mailing of the international search report<br>01 May, 2007 (01.05.07) |                            |                  |                                   |                  |                                  |                  |                                   |                  |
| Name and mailing address of the ISA/<br>Japanese Patent Office                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |                                                                                                                                                                | Authorized officer                                                            |                            |                  |                                   |                  |                                  |                  |                                   |                  |
| Facsimile No.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                                                                                                                                                                | Telephone No.                                                                 |                            |                  |                                   |                  |                                  |                  |                                   |                  |

Form PCT/ISA/210 (second sheet) (April 2005)

## INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2007/057648

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

| Category* | Citation of document, with indication, where appropriate, of the relevant passages                                   | Relevant to claim No. |
|-----------|----------------------------------------------------------------------------------------------------------------------|-----------------------|
| A         | JP 10-268259 A (Advanced Display Inc.),<br>09 October, 1998 (09.10.98),<br>Full text; all drawings<br>(Family: none) | 1-21                  |
| A         | JP 5-119742 A (NEC Corp.),<br>18 May, 1993 (18.05.93),<br>Full text; all drawings<br>(Family: none)                  | 1-21                  |
| A         | JP 5-72997 A (Sanyo Electric Co., Ltd.),<br>26 March, 1993 (26.03.93),<br>Full text; all drawings<br>(Family: none)  | 1-21                  |
| A         | JP 7-199867 A (NEC Corp.),<br>04 August, 1995 (04.08.95),<br>Full text; all drawings<br>(Family: none)               | 1-21                  |

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**REFERENCES CITED IN THE DESCRIPTION**

*This list of references cited by the applicant is for the reader's convenience only. It does not form part of the European patent document. Even though great care has been taken in compiling the references, errors or omissions cannot be excluded and the EPO disclaims all liability in this regard.*

**Patent documents cited in the description**

- JP 5165431 A [0004]
- JP 2002251170 A [0004]

|                |                                                                                                      |         |            |
|----------------|------------------------------------------------------------------------------------------------------|---------|------------|
| 专利名称(译)        | 液晶显示装置及其驱动方法                                                                                         |         |            |
| 公开(公告)号        | <a href="#">EP2015286A4</a>                                                                          | 公开(公告)日 | 2011-07-06 |
| 申请号            | EP2007741084                                                                                         | 申请日     | 2007-04-05 |
| [标]申请(专利权)人(译) | 夏普株式会社                                                                                               |         |            |
| 申请(专利权)人(译)    | 夏普株式会社                                                                                               |         |            |
| 当前申请(专利权)人(译)  | 夏普株式会社                                                                                               |         |            |
| [标]发明人         | INOUE YUICHI<br>KOBAYASHI KATSUTOSHI<br>KOBAYASHI TETSUYA<br>YAMAGUCHI HIDEHIKO<br>SUTOH SEIJI       |         |            |
| 发明人            | INOUE, YUICHI<br>KOBAYASHI, KATSUTOSHI<br>KOBAYASHI, TETSUYA<br>YAMAGUCHI, HIDEHIKO<br>SUTOH, SEIJI  |         |            |
| IPC分类号         | G09G3/36 G02F1/133 G09G3/20                                                                          |         |            |
| CPC分类号         | G09G3/3696 G02F1/133305 G09G3/3614 G09G3/3655 G09G2320/0204 G09G2320/0209 G09G2320/0247 G09G2320/046 |         |            |
| 代理机构(译)        | 津凯, THOMAS                                                                                           |         |            |
| 优先权            | 2006127054 2006-04-28 JP                                                                             |         |            |
| 其他公开文献         | EP2015286A1                                                                                          |         |            |
| 外部链接           | <a href="#">Espacenet</a>                                                                            |         |            |

#### 摘要(译)

具有正极性的电压和具有负极性的电压交替地施加到对电极和像素电极之间的像素。提供对电极电压 ( Vcom ) 产生电路或源信号产生部分，每个控制有效电压施加到用于相同色调的像素，使得具有正极性的有效电压和具有负极性的有效电压对于可以显示的所有音调，不对称地应用于像素。对电极电压 ( Vcom ) 产生电路或源信号产生部分将对电极电压 Vcom-A或源电压从使得有效电压对称地施加到像素的电压移位0.3V或更多。