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(54) **Shift register unit circuit, shift register, array substrate and display apparatus**

(57) Embodiments of the present disclosure relate to a technique of liquid crystal display, and provide a shift register unit circuit, a shift register, an array substrate and a display apparatus. A first TFT is added in the shift register unit circuit and is used to pull down the level of

a node PU when a frame start signal is at a high level and a forward clock signal changes from a low level to the high level, such that a H-line defect generated in an output signal caused by a coupling effect of a coupling circuit can be avoided, which is advantageous for increasing productivity and yield rate of products.

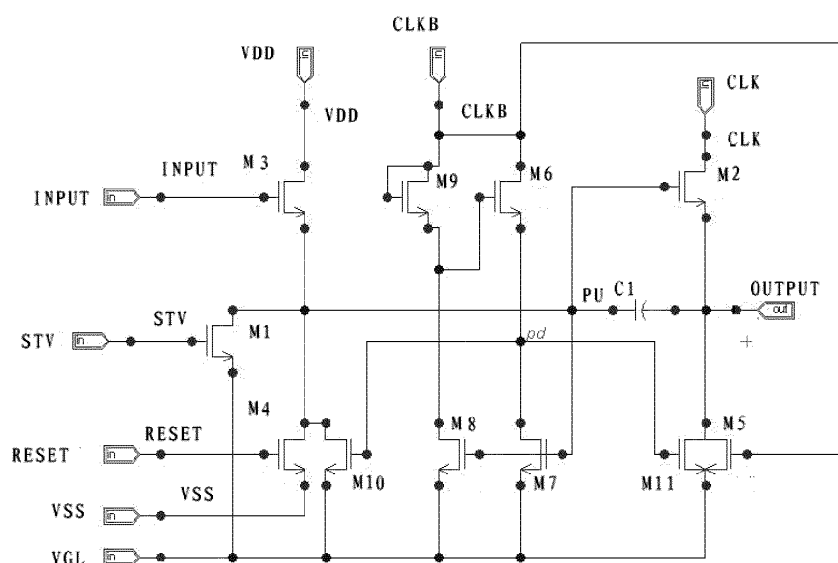


Fig.2a



EUROPEAN SEARCH REPORT

Application Number
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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
Y	CN 102 629 444 A (BEIJING BOE OPTOELECTRONICS TECHNOLOGY) 8 August 2012 (2012-08-08) * abstract *	1-3	INV. G09G3/36 G11C19/28
A	& US 2013/088265 A1 (CHEN XI [CN]) 11 April 2013 (2013-04-11) * paragraphs [0001] - [0004], [0025] - [0052]; figures 2-4 *	4-7	
Y	US 2007/248204 A1 (TOBITA YUICHI [JP]) 25 October 2007 (2007-10-25) * paragraphs [0019] - [0020], [0037], [0118], [0134] - [0135], [0191] - [0221]; figures 7, 24-28 *	1-3	
A	US 2011/222645 A1 (TOBITA YUICHI [JP]) 15 September 2011 (2011-09-15) * paragraphs [0054] - [0056], [0060], [0092] - [0093] *	4-7	TECHNICAL FIELDS SEARCHED (IPC) G09G G11C
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The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 20 February 2014	Examiner Taron, Laurent
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

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EPO FORM 1503 03/82 (P04C01)

**ANNEX TO THE EUROPEAN SEARCH REPORT
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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on
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本发明实施例涉及一种液晶显示技术，提供一种移位寄存器单元电路，移位寄存器，阵列基板和显示装置。在移位寄存器单元电路中添加第一TFT，并且当帧起始信号处于高电平并且正向时钟信号从低电平变为高电平时，用于下拉节点PU的电平，使得可以避免由耦合电路的耦合效应引起的输出信号中产生的H线缺陷，这有利于提高产品的生产率和产率。

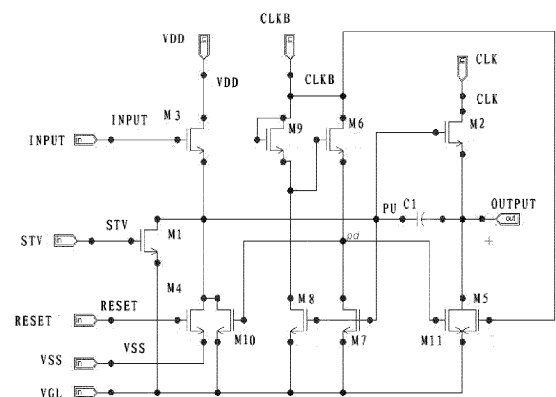


Fig.2a