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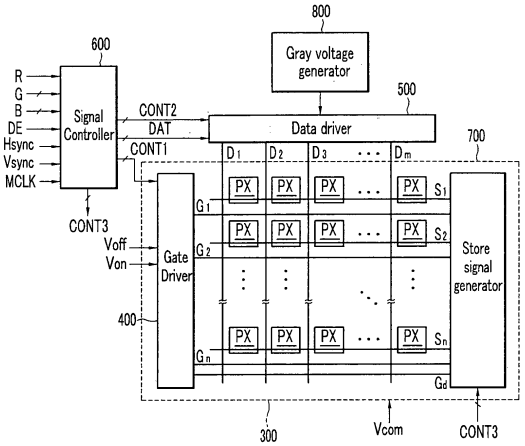
(54) Display device

(57) A display device includes a plurality of gate lines transmitting gate signals wherein each gate signal has a gate-on voltage and a gate-off voltage, a plurality of data lines intersecting the gate lines and transmitting data voltages, a plurality of storage electrode lines extending in parallel to the gate lines and transmitting storage signals, a plurality of pixels arranged in a matrix wherein each pixel includes a switching element connected to a gate line and a data line, a liquid crystal capacitor connected to the switching element and a common voltage, a storage capacitor connected to the switching element and a storage electrode line, and a plurality of storage signal generators generating the storage signals based on the gate signals. The storage signal applied to each pixel has a changed voltage level immediately after the charging the data voltage into the liquid crystal capacitor and the storage capacitor is completed.

FIG.1



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EUROPEAN SEARCH REPORT

Application Number
EP 06 02 5299

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	US 2005/035938 A1 (NODA KAZUHIRO [JP]) 17 February 2005 (2005-02-17) * paragraphs [0037] - [0049], [0055] - [0058]; figures 1-3,8,9 *	1-8, 18-21	INV. G09G3/36
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X	EP 1 536 408 A (SANYO ELECTRIC CO [JP]) 1 June 2005 (2005-06-01) * paragraphs [0013] - [0047], [0078] - [0086]; figures 1-6,13 *	1-8, 18-21	

TECHNICAL FIELDS SEARCHED (IPC)

G09G

The present search report has been drawn up for all claims

Place of search	Date of completion of the search	Examiner
Munich	9 November 2007	Gartlan, Michael

CATEGORY OF CITED DOCUMENTS

X : particularly relevant if taken alone
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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

专利名称(译)	显示设备		
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外部链接	Espacenet		

摘要(译)

一种显示装置，包括传输栅极信号的多条栅极线，其中每个栅极信号具有栅极导通电压和栅极截止电压，多条数据线与栅极线交叉并传输数据电压，多条存储电极线延伸与栅极线平行并传输存储信号，以矩阵排列的多个像素，其中每个像素包括连接到栅极线和数据线的开关元件，连接到开关元件的液晶电容器和公共电压，连接到开关元件和存储电极线的存储电容器，以及基于栅极信号产生存储信号的多个存储信号发生器。在将数据电压充电到液晶电容器中并且存储电容器完成之后，施加到每个像素的存储信号立即具有改变的电压电平。

FIG.1