



DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
X	US 5 453 858 A (YAMAZAKI ET AL) 26 September 1995 (1995-09-26) * figures 1,6,8,9; examples 1,3,4 * -----	1-20	G02F1/1345 G02F1/1362 G02F1/13
			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
			G02F
The supplementary search report has been based on the last set of claims valid and available at the start of the search.			
Place of search <b>Munich</b>		Date of completion of the search <b>2 May 2005</b>	Examiner <b>Frank, W</b>
<p><b>CATEGORY OF CITED DOCUMENTS</b></p> <p>X : particularly relevant if taken alone  Y : particularly relevant if combined with another document of the same category  A : technological background  O : non-written disclosure  P : intermediate document</p> <p>T : theory or principle underlying the invention  E : earlier patent document, but published on, or after the filing date  D : document cited in the application  L : document cited for other reasons  .....  &amp; : member of the same patent family, corresponding document</p>			

**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

EP 01 95 8417

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on  
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02-05-2005

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 5453858 A	26-09-1995	JP 2791422 B2	27-08-1998
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专利名称(译)	液晶显示装置和包括它的电子设备		
公开(公告)号	<a href="#">EP1243965A4</a>	公开(公告)日	2005-06-22
申请号	EP2001958417	申请日	2001-08-23
[标]申请(专利权)人(译)	索尼公司		
申请(专利权)人(译)	索尼公司		
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CPC分类号	G02F1/13452 G02F1/13454 H01L2224/16225		
优先权	2000254335 2000-08-24 JP		
其他公开文献	EP1243965A1		
外部链接	<a href="#">Espacenet</a>		

摘要(译)

提供了一种能够实现更大的厚度，更小的面积和更窄的框架的液晶显示装置以及使用该液晶显示装置的电子设备。为此目的，本发明的液晶显示装置包括第一基板（16），形成在第一基板（16）上并具有以矩阵状排列的像素的像素单元（12），以及第二基板（62）。面对第一基板（16），保持在第一基板（16）和第二基板（62）之间的液晶组合物（63）以及在第一基板（16）上形成的用于将像素信号写入像素单元的外围电路（12），上述外围电路中的至少一部分外围电路（13）由薄膜晶体管形成在第一基板（16）上，上述外围电路的其余部分（23、24、25）外围电路由半导体芯片形成，并且半导体芯片布置在第一基板（16）上，使得半导体芯片的至少一部分与由薄膜晶体管形成的外围电路的区域重叠。

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G02F		
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Place of search	Date of completion of the search	Examiner
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