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(71) Applicant: **SAMSUNG ELECTRONICS CO., LTD.**
Suwon-shi,
Kyungki-do 442-370 (KR)

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(72) Inventor: **Song, Jang-Kun**
Seoul 137-778 (KR)

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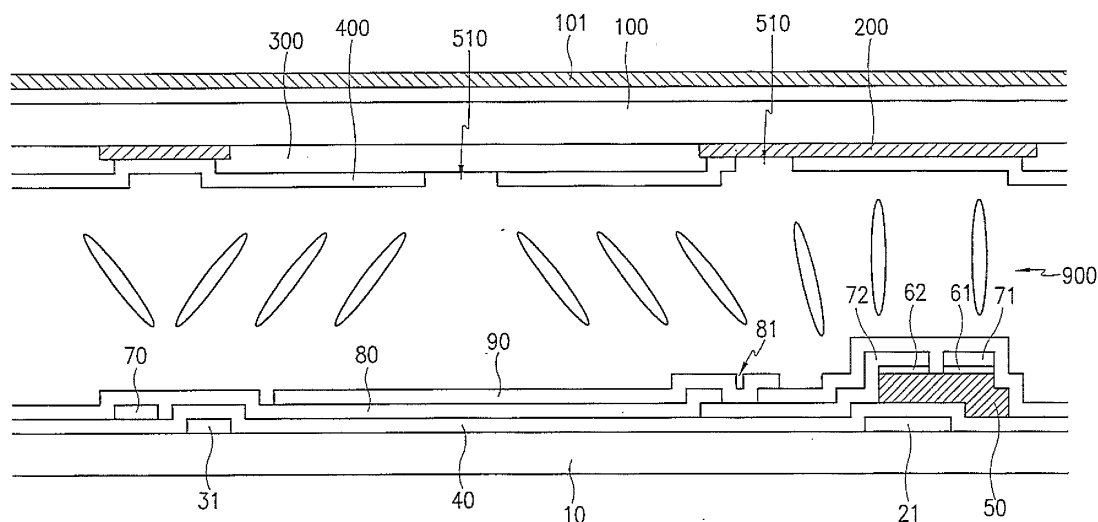
(74) Representative: **Modiano, Micaela Nadia et al**
Modiano & Partners
Thierschstrasse 11
80538 München (DE)

(54) **Micro domain partitioning in an active liquid crystal display**

(57) A pixel electrode (90) bearing a plurality of slits is formed on a first insulating substrate (10). A common electrode (400) is formed on a second insulating substrate (100) with a plurality of opening portions (510,520,530) and a black matrix (200) and a color filter (300). The horizontal opening portions of the pixel electrode and the opening portions of the common electrode (510,520,530) partition the pixel region into left, right, up-

per and lower domains. Liquid crystal material (900) is interposed between the first and second insulating substrates. Source and drain electrodes (71,72) are formed on ohmic contact layers (61,62) which are formed on a semiconductor layer (50) which is formed on a gate insulating layer (40) over the gate electrode (20). Certain pixel electrodes (90) may make capacitor-connection with respect to each other.

FIG.2





EUROPEAN SEARCH REPORT

Application Number
EP 10 16 8932

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
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Y	* the whole document * -----	12,23,28	ADD. G09G3/36
Y	KR 2000 0009518 A (RHO BONG GYU [KR]) 15 February 2000 (2000-02-15) * the whole document * -& US 6 466 288 B1 (RHO BONG KYU [KR]) 15 October 2002 (2002-10-15) * abstract; figures 1,2,5 * * column 5, line 37 - line 39 * -----	12,23,28	
			TECHNICAL FIELDS SEARCHED (IPC)
			G02F
The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 23 February 2011	Examiner Wolfrum, Georg
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

2
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**ANNEX TO THE EUROPEAN SEARCH REPORT
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EP 10 16 8932

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专利名称(译)	在有源液晶显示器中的微域分区		
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[标]申请(专利权)人(译)	三星电子株式会社		
申请(专利权)人(译)	SAMSUNG ELECTRONICS CO. , LTD.		
当前申请(专利权)人(译)	SAMSUNG ELECTRONICS CO. , LTD.		
[标]发明人	SONG JANG KUN		
发明人	SONG, JANG-KUN		
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CPC分类号	G02F1/133707 G02F1/133753 G02F1/136213 G02F1/1393 G02F2001/133746 G02F2001/134345 G02F2001/13712		
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外部链接	Espacenet		

摘要(译)

在第一绝缘基板 (10) 上形成带有多个狭缝的像素电极 (90) 。公共电极 (400) 形成在具有多个开口部分 (510,520,530) 和黑色矩阵 (200) 以及滤色器 (300) 的第二绝缘基板 (100) 上。像素电极的水平开口部分和公共电极 (510,520,530) 的开口部分将像素区域划分为左, 右, 上和下域。液晶材料 (900) 介于第一和第二绝缘基板之间。源电极和漏电极 (71,72) 形成在欧姆接触层 (61,62) 上, 欧姆接触层 (61,62) 形成在半导体层 (50) 上, 半导体层 (50) 形成在栅电极 (20) 上方的栅极绝缘层 (40) 上。某些像素电极 (90) 可以相对于彼此进行电容器连接。

FIG.2

