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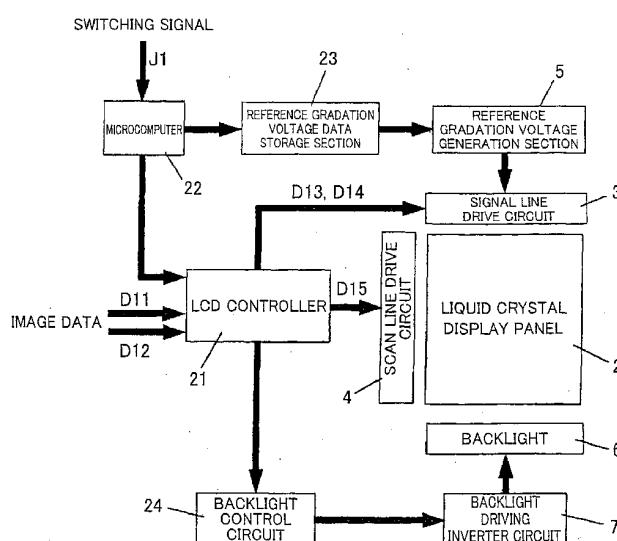
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(54) LIQUID CRYSTAL DISPLAY

(57) A liquid crystal display has an LCD controller, a microcomputer, and a storage section. The LCD controller is capable of setting up an image display period for performing display based on input image data and a black display period for performing display based on black display data, within one field period. The microcomputer switches the mode of the LCD controller between an impulse-drive mode having the image display period and the black display period within the one field

period, and a hold drive mode having only the image display period. The storage section stores sets of reference gradation voltage data that are previously specified. A reference gradation voltage, which is generated at a reference gradation voltage generation section and used for driving the liquid crystal display panel, is variable according to the mode. Thus, it is possible to realize the liquid crystal display that can suppress changes in gamma characteristics so as to prevent deterioration of display quality even when the mode is switched.

FIG. 1



Description**TECHNICAL FIELD**

[0001] The present invention relates to a liquid crystal display which displays images by using a liquid crystal display panel. More particularly, the present invention relates to a liquid crystal display capable of improving deterioration of display quality of moving images, which is caused by response characteristics of the liquid crystal display panel.

BACKGROUND ART

[0002] In these years, there have been needs for reductions in weight and thickness of personal computers and TV sets. According to such needs, flat-panel displays such as liquid crystal displays (LCD) have been developed, as substitutes for cathode-ray tubes (CRT).

[0003] An LCD is a display that applies an electric field to a liquid crystal layer having anisotropic permittivity (anisotropic dielectric constant), the liquid crystal layer being interposed between two substrates. By adjusting the electric field, the LCD controls a volume of light passing through the substrates so as to obtain desired image signals. The LCD is well known among other flat-panel displays convenient for carriage. Especially, a thin film transistor (TFT) LCD utilizing a TFT as a switching element is predominantly used.

[0004] With reference to Figs. 10 through 14, a conventional liquid crystal display is described. Fig. 10 is a block diagram illustrating a schematic structure of the conventional liquid crystal display. Fig. 10 shows an LCD controller 1, a liquid crystal display panel 2, a signal line drive circuit 3, a scan line drive circuit 4, a reference gradation voltage generation section 5, a backlight 6, and a backlight driving inverter circuit 7.

[0005] Image data is input in the LCD controller 1, as gradation data D11 and synchronous data D12. The gradation data D11 is RGB signals, for example. The synchronous data D12 includes a vertical synchronizing signal, a horizontal synchronizing signal, a data enable signal (DE), a clock and others. Based on the input gradation data D11 and synchronous data D12, the LCD controller 1 generates not only gradation data D13 and a signal-side control signal D14 to be output to the signal line drive circuit 3, but also a scan line control signal D15 to be output to the scan line drive circuit 4. With this arrangement, the LCD controller 1 controls image display on the liquid crystal display panel 2.

[0006] With reference to Fig. 11, a structure of the liquid crystal display panel 2 (active matrix type LCD) is described. The liquid crystal display panel 2 has a first and a second glass substrates (not shown). Over the first glass substrate, thin film transistors (TFT) 11 are provided near respective intersections of n scan lines G1 to Gn and m signal lines S1 to Sm. The TFT 11 is a nonlinear element (switching element).

[0007] Gate lines for the TFTs 11 are connected to the scan lines G1 to Gn. Source electrodes are connected to the signal lines S1 to Sm, and drain electrodes are connected to pixel electrodes. The second glass substrate is disposed so as to face the first glass substrate, and common electrodes are formed all over the surface of the glass substrate, using transparent electrodes made of ITO and the like. Each of the common electrodes is connected to a common electrode drive circuit 12, which defines an electric potential. Liquid crystal 13 is interposed between the common electrodes and the pixel electrodes formed on the first glass.

[0008] The scan lines G1 to Gn and the signal lines S1 to Sm are connected to the scan line drive circuit 4 and the signal line drive circuit 3, respectively. The scan line drive circuit 4 applies a high voltage to n scan lines G1 to Gn sequentially, so as to carry out scanning and activate the TFT 11 connected to each of the scan lines G1 to Gn. While the scan line drive circuit 4 scans the scan lines G1 to Gn, the signal line drive circuit 3 outputs gradation voltages corresponding to image data to any of m signal lines S1 to Sm. With this, the gradation voltage is written into the pixel electrodes through the activated TFTs 11. Then, a difference in electric potentials is generated between the common electrodes set at a certain potential and the pixel electrodes where the gradation voltage is applied. With the difference, the volume of light transmission is adjusted.

[0009] The backlight 6 disposed at the back side of the liquid crystal display panel 2 is driven by the inverter circuit 7, so as to emit light with a certain level of brightness. Therefore, according to the aforesaid operating principles of the liquid crystal display panel 2, the volume of transmitted light emitted from the backlight 6 is controlled, so that a desirable image display is realized.

[0010] The reference gradation voltage generation section 5 illustrated in Fig. 10 supplies the signal line drive circuit 3 with a reference gradation voltage. A reference gradation voltage is applied to the liquid crystal display panel 2, in accordance with a gradation level of image data. Fig. 12 is an explanatory view expressing one example of the relationship between the transmittance of the liquid crystal and the applied voltage. In order to carry out image display with the liquid crystal having the characteristics expressed in Fig. 12, a reference gradation voltage is defined so that the relationship between the gradation level of the image data and the transmittance draws a curve of gamma 2.2 as plotted in Fig. 13, for example. With reference to Fig. 12, one-eighth of the maximum reference gradation voltage is defined as a reference gradation, for example. The rest of the gradation voltages are generated by dividing the adjacent reference gradation voltages. More specifically, all display gradation voltages are defined using divided resistors.

[0011] Fig. 14 is an explanatory view illustrating signal waveforms output from the scan line drive circuit 4 and the signal line drive circuit 3 to the scan lines and the

signal lines, respectively, in the conventional liquid crystal display. In Fig. 14, the time is set on the abscissa axis. VG1 to VGn are signals output sequentially to n scan lines, by applying a high electric potential (data electric potential) to a single scan line at a time. VD is a signal waveform output to a single signal line, and Vcom is a signal waveform applied to common electrodes. In an example illustrated in Fig. 14, the VD is a signal that changes in signal intensity corresponding to each image data, and the Vcom is a signal that maintains a certain value and does not change with time.

[0012] The conventional liquid crystal display and its driving method are described above. When movie display is carried out on the conventional liquid crystal display, display quality deteriorates, for example, after-image emerges. The cause of this problem is a slow response of liquid crystal material. When a gradation of input image data changes, the liquid crystal material cannot respond to the change within one field period. Thus, it is assumed that a few field periods are required to respond. To solve this problem, studies about liquid crystal materials and others have been currently implemented.

[0013] Not only the delay of the optical response time, but also an LCD display system itself are pointed out as causes of motion blur in movie display. This issue is brought up at the convention of the institute of electronics information and communication engineers in 1999 (SC-8-1, pp.207-208), for example. Figs. 15 and 16 illustrate results of comparing the response time of display light between a CRT and an LCD, in terms of one pixel. Fig. 15 plots the response time of the CRT, and Fig. 16 plots the response time of the LCD. As shown in Fig. 15, the CRT is a so-called impulse-type display, which illuminates for only a few milliseconds after an electronic beam hits fluorescent material on the surface of a tube. As shown in Fig. 16, the LCD is a so-called hold-type display, which holds display light during one field period from the completion of writing data into a pixel to the start of writing next data.

[0014] When movie display is carried out in the hold-type display, LCD, a currently displayed image and a previously displayed image are viewed as an overlapped image. The overlap is caused by time integration of visual perception and tracking characteristics of the sight line in the direction of movement. Thus, the motion blur occurs. For preventing the motion blur, several techniques have been proposed as follows. Image data and black data are repeatedly written in the liquid crystal display panel within one field period. Accordingly, a period for carrying out black display (black display period) is set up between one field image display and the next field image display. Thus, a hold time for the display light, that is, an image display period is cut down. In this manner, the display status with the hold-type drive is caused to resemble the display with the impulse-type drive such as the CRT.

[0015] Fig. 17 illustrates one example of a so-called

black-writing-type liquid crystal display. After writing input image data in one field into a liquid crystal display panel sequentially, black display data is written into all over the screen at the same time, so that the black display over the entire screen is carried out for a certain period of time. With reference to another example illustrated in Fig. 18, black display data is written into the scan lines sequentially, so that the black display is carried out in part of the screen for a certain period of time.

5 10 **[0016]** Accordingly, the image display period is shortened compared to the one in the conventional hold-type display (disclosed in Japanese Laid-Open Patent Publication No. 127917/1997 and 109921/1999, *Tokukaihei 9-127917 and 11-109921*). Such a liquid crystal display 15 that has display elements and a shutter is disclosed in *Tokukaihei 9-325715*, for example. In the liquid crystal display disclosed in this publication, the display elements transform electric image signals to image-display light while continuing the transforming operation for a 20 certain display hold period. Also, the shutter limits the display hold period to a certain time within one field period, in synchronism with the vertical synchronization of image signals.

[0017] **25** Furthermore, the following is proposed in *Tokukai 2002-123223* and *2002-318569*. Only when moving images are displayed on the liquid crystal display panel, the black display is carried out in at least a part of the screen for a certain period of time, so that the impulse-type display is carried out. As a result, the motion blur emerged in movie display can be restrained. In static display, the hold-type display is carried out without setting the black display period. Thus, deterioration of display quality due to flicker and others is prevented.

30 **[0018]** **35** However, the relationship between the display gradation and the display brightness, that is, so-called gamma characteristics differ between the impulse display mode shown in Figs. 17 and 18 and the normal hold display mode shown in Fig. 19. The impulse-type display is carried out by producing the black display in at 40 least part of the screen for a certain period of time. In Fig. 20, for example, the full line plots gamma characteristic in the hold display mode. The dotted line plots gamma characteristics when the motion blur is prevented in the impulse display mode shown in Fig. 18. That is, the black display period is inserted into one field period, so that the image display period is shortened. With this, it is recognized that display brightness in a low gradation tends to decrease. Therefore, image-display characteristics vary between movie display with the 45 method shown in Figs. 17 and 18 and static display with the method shown in Fig. 19. Thus, display quality significantly deteriorates.

[0019] In Fig. 21, the full line plots changes in display brightness with time in the hold display mode. The dotted line in Fig. 21 plots changes in display brightness with time when the motion blur is prevented in the impulse display mode illustrated in Fig. 18. As Fig. 21 clearly shows, the ratio in attained brightness between

the methods in low gradation display is different from the ratio in attained brightness between the methods in high gradation display. Therefore, it is considered that the different characteristics in display gradation and display brightness (gamma characteristics) between the display methods come from the differences between the response characteristics of the liquid crystal in the low-gradation display and the response characteristics of the liquid crystal in the high-gradation display.

[0019] When image display is carried out with the impulse-type display illustrated in Fig. 18, due to the temperature dependency of the liquid crystal as shown in Fig. 22, display brightness in a low gradation tends to decrease as the temperature of the liquid crystal display panel becomes low. That is, gamma characteristics change depending on temperatures of the liquid crystal display panel, so that display quality varies (deteriorates).

DISCLOSURE OF INVENTION

[0020] An object of the present invention is to provide a liquid crystal display, which can prevent deterioration of display quality by suppressing variations in gamma characteristics, even when a mode for driving a liquid crystal display panel is switched between an impulse drive mode and a hold drive mode.

[0021] To attain the foregoing object, the liquid crystal display of the present invention displaying, using a liquid crystal display panel, an image responsive to input image data, includes driving means for driving the liquid crystal display panel in either an impulse drive mode or a hold drive mode, (i) in the impulse drive mode an image display period for performing display according to the input image data, and a monochrome display period for performing display according to previously-specified certain monochrome display data being generated within an input image data rewriting period (e.g. within one field period) for writing in each pixel of the liquid crystal display panel, while (ii) in the hold drive mode display according to the input image data being always performed within the rewriting period without setting the monochrome display period; switching means for switching between the modes for driving the liquid crystal display panel by the driving means; and means for varying a gradation voltage to be applied to the liquid crystal display panel corresponding to the input image data in accordance with a mode for driving the liquid crystal display panel.

[0022] In addition to the arrangement, it is preferable in the primary invention that the means for varying the gradation voltage varies a reference gradation voltage for driving the liquid crystal display panel.

[0023] Further, in addition to the arrangement, it is preferable that the liquid crystal display panel further includes a storage section storing sets of reference gradation voltage data previously specified.

[0024] In addition to the arrangement, it is preferable

that the liquid crystal display panel further includes means for detecting a temperature in the liquid crystal display; and means for varying a gradation voltage to be applied to the liquid crystal display panel in accordance with the input image data and the detected temperature in the display.

[0025] Further, in addition to the arrangement, it is preferable that the switching means switches between the modes for driving the liquid crystal display panel, in accordance with a user's instruction, or in accordance with a result of detecting an amount of movement of the input image data or a result of detecting a status of connection with a device for inputting the input image data.

[0026] In the liquid crystal display of the present invention, in accordance with a selected drive mode (impulse drive mode or hold drive mode), a gradation voltage to be applied to the liquid crystal display panel varies corresponding to input image data. Therefore, it is possible to prevent variations in gamma characteristics, which is caused by differences in response speed of the liquid crystal between display gradations, when certain monochrome display is carried out. Thus, it is possible to prevent deterioration of display quality, which occurs when the impulse display mode and the hold display mode are switched.

[0027] According to a mode for driving the liquid crystal display panel, the driving means varies a reference gradation voltage itself for driving the liquid crystal display panel. Thus, display capability of the liquid crystal display panel can be maintained and high-quality display can be realized. Further, gamma characteristics due to the temperature dependency of the liquid crystal can be also suppressed, so that high-quality display can be always achieved.

[0028] For a fuller understanding of the nature and advantages of the invention, reference should be made to the ensuing detailed description taken in conjunction with the accompanying drawings.

40 BRIEF DESCRIPTION OF DRAWINGS

[0029]

Fig. 1 is a block diagram illustrating a schematic structure of the liquid crystal display according to the first embodiment of the present invention.

Fig. 2 is an explanatory diagram illustrating one example of drive signal waveforms in the liquid crystal display panel performing the impulse-type display, according to the first embodiment of the present invention.

Fig. 3 is a schematic explanatory view showing specific examples of the reference gradation voltage data storage section in the liquid crystal display, according to the first embodiment of the present invention.

Fig. 4 is a schematic explanatory view expressing response characteristics of the liquid crystal in the

liquid crystal display, according to the first embodiment of the present invention.

Fig. 5 is a block diagram illustrating a schematic structure of the reference gradation voltage generation section in the liquid crystal display, according to the first embodiment of the present invention.

Fig. 6 is a circuit diagram schematically illustrating substantial parts of the signal line drive circuit in the liquid crystal display, according to the first embodiment of the present invention.

Fig. 7 is a schematic explanatory view expressing gamma characteristics of the liquid crystal display performing the hold-type display and the impulse-type display, according to the first embodiment of the present invention.

Fig. 8 is a block diagram illustrating a schematic structure of the liquid crystal display, according to the second embodiment of the present invention.

Fig. 9 is a schematic explanatory view showing specific examples of the reference gradation voltage data storage section in the liquid crystal display, according to the second embodiment of the present invention.

Fig. 10 is a block diagram illustrating a schematic structure of the conventional liquid crystal display.

Fig. 11 is an explanatory diagram (equivalent circuit diagram) illustrating a schematic structure of the liquid crystal display panel.

Fig. 12 is an explanatory view expressing one example of the relationships between the transmittance and the applied voltage of the liquid crystal.

Fig. 13 is a schematic explanatory view expressing gamma characteristics in the liquid crystal display.

Fig. 14 is an explanatory view illustrating one example of drive signal waveforms in the liquid crystal display panel performing the hold-type display.

Fig. 15 is a schematic explanatory view expressing time response of the display light in the CRT.

Fig. 16 is a schematic explanatory view expressing time response of the display light in the LCD.

Fig. 17 is a schematic explanatory view illustrating the principle of display operation in the black-writing-type impulse display mode.

Fig. 18 is a schematic explanatory view illustrating the principle of display operation in the impulse display mode with another method of the black-writing type.

Fig. 19 is a schematic explanatory view illustrating the principle of display operation in the hold display mode.

Fig. 20 is a schematic explanatory view expressing gamma characteristics in the hold display mode and the impulse display mode in the conventional liquid crystal display.

Fig. 21 is a schematic explanatory view expressing changes in display brightness with time in hold display mode and the impulse display mode in the conventional liquid crystal display.

Fig. 22 is a schematic explanatory view expressing gamma characteristics in the impulse display mode under each temperature condition in the conventional liquid crystal display.

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BEST MODE FOR CARRYING OUT THE INVENTION

[0030] The following more specifically describes the present invention through examples and comparative examples. However, the present invention is not limited by the examples and comparative examples.

[0031] With reference to Figs. 1 through 7, a first embodiment of the present invention is described below. The same numbers are used for the same parts with the conventional examples, and their explanations are omitted. Fig. 1 is a block diagram illustrating a schematic structure of the liquid crystal display of the present embodiment. Fig. 2 is an explanatory diagram illustrating one example of drive signal waveforms in the impulse-type display in the liquid crystal display of the present embodiment. Fig. 3 is a schematic explanatory view showing specific examples of the reference gradation voltage data storage section in the liquid crystal display of the present embodiment.

[0032] Fig. 4 is a schematic explanatory view expressing response characteristics of the liquid crystal in the liquid crystal display of the present embodiment. Fig. 5 is a block diagram illustrating a schematic structure of the reference gradation voltage generation section in the liquid crystal display of the present embodiment. Fig. 6 is a circuit diagram illustrating a schematic structure of primary parts of the signal line drive circuit in the liquid crystal display of the present embodiment. Fig. 7 is a schematic explanatory view expressing gamma characteristics in the hold display mode and the impulse display mode in the liquid crystal display according to the present embodiment.

[0033] The liquid crystal display of the present embodiment has a microcomputer 22, a reference gradation voltage data storage section 23, and a backlight control circuit 24 as illustrated in Fig. 1. The microcomputer 22 is supplied with a switching signal J1. The switching signal J1 is used for switching between the hold-type display displaying input image data throughout one field period (e.g. for 16.7 msec when the vertical display cycle is 60 Hz) and the impulse-type display generating, in one field period, a monochrome display period in which display based on certain monochrome display data is carried out. The reference gradation voltage data storage section 23 is, for example, a ROM which stores a plurality of values of reference gradation voltages generated at the reference gradation voltage generation section 5 for each of the display methods (in the impulse-type display or the hold-type display). The backlight control circuit 24 controls lighting of the backlight 6 in synchronism with the writing of monochrome display data.

[0034] The microcomputer 22 executes programs

stored on storage such as memory (not shown), thereby giving instructions to the LCD controller 21 (drive means) to perform the display methods, and performing control operations so that values of reference gradation voltages corresponding to the display methods can be read out from the reference gradation voltage data storage section (means for varying gradation voltages) 23. Therefore, it is possible to allow a microcomputer (computer), which is capable of controlling the LCD controller (drive means) 21 and the reference gradation voltage data storage section 23, to act as the microcomputer 22, by causing the microcomputer to execute the aforesaid methods. The programs executed by the computer are, for instance, distributed in the form of storage mediums storing the programs, or distributed through various wired or wireless channels.

[0035] The switching signal J1 is generated in accordance with user's preference which is manually input using a remote controller (not shown) and others. The display methods may be also automatically switched between the movie display and the static display. Based on the switching signal J1, the microcomputer 22 instructs the LCD controller 21 to insert black display data/ perform display, thereby changing whether to carry out the impulse-type display or the hold-type display on the liquid crystal panel 2.

[0036] The switching signal J1 can be generated through a circuit receiving an instruction by a user's manual input (e.g. light-receptive spot on the remote controller (not shown)), or a circuit detecting an automatic switching trigger (e.g. a circuit detecting the amount of movement of input image data or a circuit detecting a status of connection with another device). Also, based on the switching signal J1 from such a circuit, the microcomputer 22 may control the LCD controller 21 and the reference gradation voltage data storage section 23. Alternatively, the microcomputer 22 may control these members 21 and 23, referring to user's instructions or an automatic switching trigger.

[0037] The circuit that receives the user's instructions or the microcomputer 22 may receive, as the user's instructions, instructions defining whether or not to carry out the impulse-type display, or determine whether or not the impulse-display is carried out, in response to specified instructions defining in advance the relevance of whether or not to carry out the impulse-display. The specified instructions include an instruction for selecting an input image source or for setting an image display mode. Further, the microcomputer 22 or the circuit that detects an automatic switching trigger may define whether or not to carry out the impulse-type display, based on the amount of movement detected in the input image data, or based on the presence or absence of the state of connection with a computer (not shown) that mainly generates static images. Note that, the circuit receiving instructions or the circuit detecting triggers generates the switching signal J1 so as to define whether or not to carry out the impulse-type display. Also, the

microcomputer 22 controls the members 21 and 23 so as to define whether or not to carry out the impulse-type display.

[0038] Regardless of the methods for defining whether or not to carry out the impulse-type display, as illustrated in Fig. 2, when performing the impulse-type display, scanning signals supplied from the scan line drive circuit 4 to the scan lines G1 to Gn on the liquid crystal display panel 2 have an image data select period T1 and a black display select period T2 within one field period. During the image data select period T1, gradation voltages corresponding to image data are written into the pixel electrodes. During the black display select period T2, a voltage for carrying out the black display is written into the pixel electrode. Gradation voltages corresponding to image data and a voltage for carrying out the black display are alternately output to the signal lines S1 to Sm.

[0039] The black display select period T2 is set as about a half of the conventional scan line select period T3 described with reference to Fig. 14. The black display is carried out in a scan line locating above or below with a distance for several lines from a scan line, selected in the image data select period T1. A voltage corresponding to the black display is applied to the signal lines S1 to Sm during the black display select period T2, so that the black display can be carried out in each of the scan lines. A line to which black data is written and a line to which image data is written are selected by suitable control of the scan line drive circuit 4 by the LCD controller 21. As a result, the lines can be scanned sequentially, while the distance for several lines between the image-data written line and the black-data written line is maintained.

[0040] The black display data is inserted between sets of image data for respective fields, inside the LCD controller 21. As to one line, a half of the period for selecting a single line is used for supplying image data to the signal line driver circuit 3, and the rest of the period is used for supplying the black display data to the signal line drive circuit 3. Accordingly, the impulse-type display can be realized with 50 percent of the impulse-ratio (ratio of the image display period in one image display cycle). Note that, Fig. 2 illustrates one example of using the liquid crystal display panel in a normally white mode. However, the present invention is not limited to this, and it is obvious that the liquid crystal display panel in a normally black mode can be also used.

[0041] On the other hand, for the hold-type display having no prevention for the motion blur in static display, the conventional example illustrated in Fig. 14 is applied in similar fashion. The LCD controller 21 controls the scan line drive circuit 4 not only to supply the signal line drive circuit 3 with input image data, but also to perform line-scanning in each field circle. Thus, the normal hold-type display can be realized with 100 percent of the impulse ratio.

[0042] Next, the reference gradation voltage genera-

tion section 5 supplies the signal line drive circuit 3 with reference gradation voltages, based on the reference gradation voltage data stored in the reference gradation voltage data storage section 23. As shown in Fig. 3, the reference gradation voltage data storage section 23 stores sets of reference gradation voltage data corresponding to the hold display mode and the impulse display mode, respectively, onto separate regions of a ROM. One of these sets of reference gradation voltage data is selected and output to the reference gradation voltage generation section 5, according to an instruction from the microcomputer 22. The sets of reference gradation voltage data to be stored on the reference gradation voltage data storage section 23 are set as follows.

[0043] First, reference gradation voltage data corresponding to the hold display mode is set so that the relationship between the display gradation and the display brightness (liquid crystal transmittance) forms gamma 2.2, in accordance with a so-called V-T curve as plotted in Fig. 12. As an example, when the number of display signal levels, that is, the number of displayed data is 256 gradations for eight bits, voltage data V0, V32..., V255 corresponding to 0, 32, 64, 96, 128, 160, 192, 224, 255 gradations have been set or stored. For other gradations but the stored reference gradations, the reference gradation voltages are linearly divided by resistors, so as to obtain voltages for all gradations to be applied to the liquid crystal display panel 2.

[0044] On the other hand, for the impulse-type display, reference gradation voltage data is not determined directly based on the V-T curve plotted in Fig. 12. The reference gradation voltage data is determined by working out the relationship between the intensity integral I and the voltage T applied to the liquid crystal within one field period, while display brightness (transmittance) changes with time during the impulse display mode as plotted in Fig. 4. The intensity integral I changes according to the response speed of the liquid crystal. The response speed of the liquid crystal changes according to a display gradation. Therefore, in the case of the impulse-type display, the relationship between the applied voltage and the liquid crystal transmittance (intensity) shown in Fig. 12 cannot be held. That is, it is not possible to realize desirable gradation display with the gradation voltage for the hold-type display, which is determined in accordance with the V-T curve in Fig. 12.

[0045] As countermeasures, for the impulse-type display, a new relationship between the intensity integral I and the applied voltage within one field period is measured. Then, the reference gradation voltage data, which is different from the one in the hold-type display, is set so that the relationship between the display gradation and the display brightness (liquid crystal transmittance) can form, for example, the relationship of gamma 2.2. As an example, when the number of display signal levels, that is, the number of displayed data is 256 gradations for eight bits, voltage data V0, V32..., V255 corre-

sponding to 0, 32, 64, 96, 128, 160, 192, 224, 255 gradations are set or stored. For other gradations but the stored reference gradations, the reference gradation voltages are linearly divided by resistors, so that voltages for all gradations to be applied to the liquid crystal display panel 2 are obtained.

[0046] As illustrated in Fig. 5, the reference gradation voltage generation section 5 causes a DA converter 5a to convert digital data V0, V32, ..., V255 obtained from the reference gradation voltage data storage section 23 into analog data. Then, the reference gradation voltage generation section 5 causes an amplifier 5b to amplify and adjust the reference gradation voltages VA0, VA32, ..., VA255 as appropriate, thereby supplying them to the signal line drive circuit 3 including a source driver and others. As illustrated in Fig. 6, in the signal line drive circuit 3, input terminals of the reference gradation voltages VA0, VA32, ..., VA255 are connected each other by divided resistors. Voltages for all gradations corresponding to image data are generated, so that display in accordance with image data for eight bits is performed.

[0047] The above description is about a case that gradation voltages corresponding to nine reference gradations for each 32-step gradation, 0, 32, 64, 96, 128, 160, 192, 224, 255 are generated and other gradation voltages are produced with divided resistances. However, regardless of the example, it is obvious that the above description can be applied for a case that gradation voltages corresponding to reference gradations for each 16-step gradation are generated, for example.

[0048] As described above, based on the switching signal J1 supplied to the microcomputer 22, one of the sets of reference gradation voltage data for the hold-type display and the impulse-type display, both of which are stored in the reference gradation voltage data storage section 23, is read out into the reference gradation voltage generation section 5. Then, based on the reference gradation voltage data thus read out, a gradation voltage to be applied to the liquid crystal display panel 2 is determined, corresponding to each gradation level of the input image data.

[0049] As shown in Fig. 7, regardless of the hold display mode or the impulse display mode, it is possible to prevent changes in gamma characteristics caused by differences in response speed of the liquid crystal in each display gradation due to the insertion of the black display. Thus, a desirable display condition can be maintained, so that deterioration of display quality due to changes in gamma characteristics can be prevented.

[0050] For simplicity of the explanation, in the above embodiment, the hold display mode in which a ratio of image display period in one field period (impulse ratio) is 100 percent and the impulse display mode in which the ratio is 50 percent are selected in an alternate manner. However, the present invention is not limited to this. Obviously, it is possible to control switching between a plurality of different impulse ratios by setting a suitable

black display period. Thus, the arrangement allows a reference gradation voltage for driving the liquid crystal display panel to be varied, in accordance with each impulse ratio.

[0051] In the present embodiment, in accordance with the impulse ratio, a reference gradation voltage for driving the liquid crystal display panel is varied. As a result, the gamma characteristics are always maintained at certain levels. However, the present invention is not limited to this. As an example, a gradation conversion section is provided in a stage prior to the LCD controller 21. Then, a gradation level of image data is converted, so that a gradation voltage to be applied to the liquid crystal display panel 2 is varied according to the input image data. In this case the image data supplied to the LCD controller 21 is actually compressed in bits, so that the display performance deteriorates due to the gradation conversion.

[0052] On the contrary, as presented in the present embodiment, a reference gradation voltage to be supplied to the signal line drive circuit 3 is adjusted, so that changes in gamma characteristics can be restrained with the display performance for eight bits maintained. Thus, even when subtle gradation changes are displayed, high-quality display is realized without stripe discontinuity in the display.

[0053] With reference to Figs. 8 and 9, described next is a second embodiment of the present invention. The same numbers are used for the same parts as the first embodiment of the present invention, and their explanations are omitted. Fig. 8 is a block diagram illustrating a schematic structure of the liquid crystal display according to the present embodiment. Fig. 9 is a summary table showing specific example of the reference gradation voltage data storage section in the liquid crystal display of the present embodiment.

[0054] As illustrated in Fig 8, the liquid crystal display of the present embodiment has a temperature sensor 35, a reference gradation voltage data storage section 33, and a microcomputer 32. The temperature sensor 35 detects a temperature inside the display. The reference gradation voltage data storage section 33 is a ROM, for example, which stores plurality of values of the reference gradation voltages generated by the reference gradation voltage generation section 5, corresponding to the respective temperatures in the display and display methods (the impulse display mode and the hold display mode). The microcomputer 32 controls the LCD controller 21 to switch between the display methods (the impulse display mode and the hold display mode), based on the switching signal J1. The microcomputer 32 also selects and reads out any one of the sets of reference gradation voltage data stored in the reference gradation voltage data storage section 33, based on the switching signal J1 and in-device temperature data J2 detected by the temperature sensor 35.

[0055] As shown in Fig. 9, the reference gradation voltage data storage section 33 stores the sets of refer-

ence gradation voltage data corresponding to the hold display mode and the impulse display mode under respective temperature conditions, onto separate regions of a ROM. One of these sets of reference gradation voltage data is selected and output to the reference gradation voltage generation section 5, according to instructions from the microcomputer 32.

[0056] In consideration with the temperature dependency of the liquid crystal, reference gradation voltage data stored in the reference gradation voltage data storage section 33 is set so that gamma characteristics in the hold display mode is matched with those in the impulse display mode, in the following ranges of temperature; below 10 degrees, 10 to 20 degrees, 20 to 30 degrees, 30 to 40 degrees, 40 to 50 degrees, and above 50 degrees in Celsius, in a similar manner shown in the first embodiment. That is, when the number of display signal levels, that is, the number of display data is 256-gradation for eight bits, 12 sets of gradation voltage data corresponding to nine reference gradations for each a 32-step gradation, 0, 32, 64, 96, 128, 160, 192, 224, and 255, have been set and stored.

[0057] The temperature sensor 35 is preferably provided so as to detect a temperature of the liquid crystal display panel 2 itself as much as possible. A single or even a plurality of the temperature sensors 35 may be provided on inner surfaces of different panels. Also, the ranges of temperature for switching a reference gradation voltage are not limited to the above examples, and it is obvious that the ranges can be set as appropriate.

[0058] As described above, based on the switching signal J1 supplied to the microcomputer 22 and the detection signal J2 produced by the temperature sensor 35, any one of plural reference gradation voltage data is read out into the reference gradation voltage generation section 5. The plural reference gradation voltage data are stored on the reference gradation voltage storage section 33. Note that the plural reference gradation voltage data are used for carrying out the hold-type display or impulse-type display under each temperature condition. Then, based on the reference gradation voltage data thus read out, a gradation voltage to be applied to the liquid crystal display panel 2 is determined, corresponding to each gradation level of the input image data.

[0059] In this manner, under any temperature conditions and regardless of the type of display mode, it is possible to prevent changes in gamma characteristics caused by differences in response speed of the liquid crystal in each display gradation due to the insertion of the black display. Thus, a desirable display condition can be maintained, so that deterioration of display quality due to the changes in gamma characteristics can be prevented.

[0060] For simplicity of the explanation, in the above embodiment, the hold display mode in which a ratio of image display period in one field period (impulse ratio) is 100 percent and the impulse display mode in which

the ratio is 50 percent can be selected alternatively. However, the present invention is not limited to this. Obviously, it is possible to control switching between a plurality of different impulse ratios by setting a suitable black display period. Thus, the arrangement allows a reference gradation voltage for driving the liquid crystal display panel to be varied, in accordance with each impulse ratio.

[0061] In the present embodiment, in accordance with a temperature in the display and whether the impulse-type display or the hold-type display to be performed, a reference gradation voltage for driving the liquid crystal display panel is varied. As a result, the gamma characteristics are always maintained at certain levels. However, the present invention is not limited to this. As an example, a gradation conversion section is provided in a stage prior to the LCD controller 21. Then, a gradation level of image data is converted, so that a gradation voltage to be applied to the liquid crystal display panel 2 can be varied according to the input image data.

[0062] In the first and second embodiments, as shown in Fig. 18, descriptions are given about the impulse-type display carrying out the black display for a certain period of time in part of the screen. It is also possible to apply the above embodiments for the impulse-type display carrying out the black (or another single color) display over the entire screen for a certain period of time, as shown in Fig. 17. It is obvious that the timing for supplying the black display data to each of the scan lines of the liquid crystal display panel is not limited to the above.

[0063] As described above, the liquid crystal display of the present invention displays an image corresponding to input image data using the liquid crystal display panel, and has the following three means. The first means is to set, within one field period, an image display period for displaying input image data and a monochrome display period for displaying certain monochrome data. The second means is to switch the ratio of the image display period within one field period. The third means is to vary, in accordance with the ratio of the image display period within one field period, a gradation voltage to be applied to the liquid crystal display panel corresponding to the input image data.

[0064] In addition to the arrangement, the means for varying a gradation voltage may vary a reference gradation voltage for driving the liquid crystal display panel.

[0065] Furthermore, in addition to the arrangement, the liquid crystal display may have a storage section for storing a plurality of the reference gradation voltage data that is previously specified.

[0066] In addition to the arrangement, the liquid crystal display may have still other means for detecting a temperature in display, and means for varying a gradation voltage to be applied to the liquid crystal panel corresponding to input image data, in accordance with the detected in-device temperature.

[0067] In the liquid crystal display panel of the present embodiment, according to the ratio of the image display

period within one field period, a gradation voltage to be applied to the liquid crystal display panel varies corresponding to input image data. Therefore, it is possible to prevent variation in gamma characteristics caused by

5 differences in response speed of the liquid crystal between display gradations, when monochrome display is carried out. Thus, it is possible to prevent deterioration of display quality occurring when the impulse display mode and the hold display mode are switched, for example.

[0068] The reference gradation voltage for driving the liquid crystal display panel can be varied according to the ratio of the image display period within one field period. Therefore, display performance of the liquid crystal 10 display panel can be maintained, so that high-quality display can be realized. Further, by preventing differences in gamma characteristics due to the temperature dependency of the liquid crystal, high-quality display can be always achieved.

[0069] The invention being thus described, it will be obvious that the same way may be varied in many ways. Such variations are not to be regarded as a departure from the spirit and scope of the invention, and all such modifications as would be obvious to one skilled in the 15 art are intended to be included within the scope of the following claims.

INDUSTRIAL APPLICABILITY

[0070] In the liquid crystal display of the present invention, in accordance with a selected drive mode (impulse drive mode or hold drive mode), a gradation voltage to be applied to the liquid crystal display panel varies corresponding to input image data. Therefore, it is 20 possible to prevent variation in gamma characteristics, which is caused by differences in response speed of the liquid crystal between display gradations, when monochrome display is carried out in the impulse drive mode. Thus, high-quality display can be realized, so that the 25 liquid crystal display can be suitably used for various types of devices including liquid crystal TV sets.

Claims

1. A liquid crystal display displaying, using a liquid crystal display panel, an image responsive to input image data, comprising:

50 driving means for driving the liquid crystal display panel in either an impulse drive mode or a hold drive mode, (i) in the impulse drive mode an image display period for performing display according to the input image data, and a monochrome display period for performing display according to previously-specified monochrome display data being generated within an input image data rewriting period for writing in each pix-

el of the liquid crystal display panel, while (ii) in
the hold drive mode display according to the in-
put image data being always performed within
the rewriting period without setting the mono-
chrome display period; 5
switching means for switching between the
modes for driving the liquid crystal display pan-
el by the driving means; and
means for varying a gradation voltage to be ap-
plied to the liquid crystal display panel corre- 10
sponding to the input image data in accordance
with a mode for driving the liquid crystal display
panel.

2. The liquid crystal display of claim 1, wherein 15
the means for varying the gradation voltage varies
a reference gradation voltage for driving the liquid
crystal display panel.

3. The liquid crystal display of claim 2, further compris- 20
ing:
a storage section storing sets of reference gra-
dation voltage data previously specified. 25

4. The liquid crystal display of any one of claims 1
through 3, further comprising:
means for detecting a temperature in the liquid 30
crystal display; and
means for varying a gradation voltage to be ap-
plied to the liquid crystal display panel, in ac-
cordance with the input image data and the de-
tected temperature in the display. 35

5. The liquid crystal display of any one of claims 1
through 3, wherein 40
the switching means switches between the
modes for driving the liquid crystal display panel in
accordance with a user's instruction.

6. The liquid crystal display of any one of claims 1
through 3, wherein 45
the switching means switches between the
modes for driving the liquid crystal display panel, in
accordance with a result of detecting an amount of
movement of the input image data or a result of de-
tecting a status of connection with a device for in-
putting the input image data. 50

FIG. 1

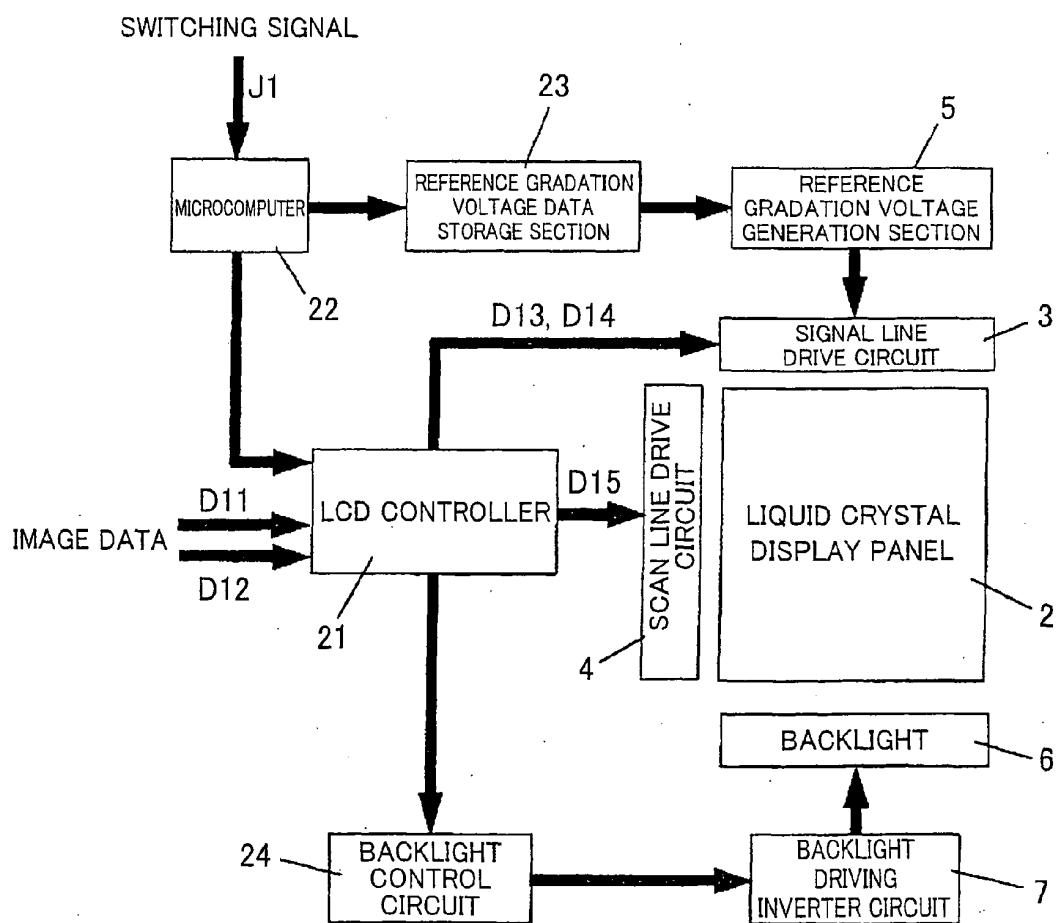


FIG. 2

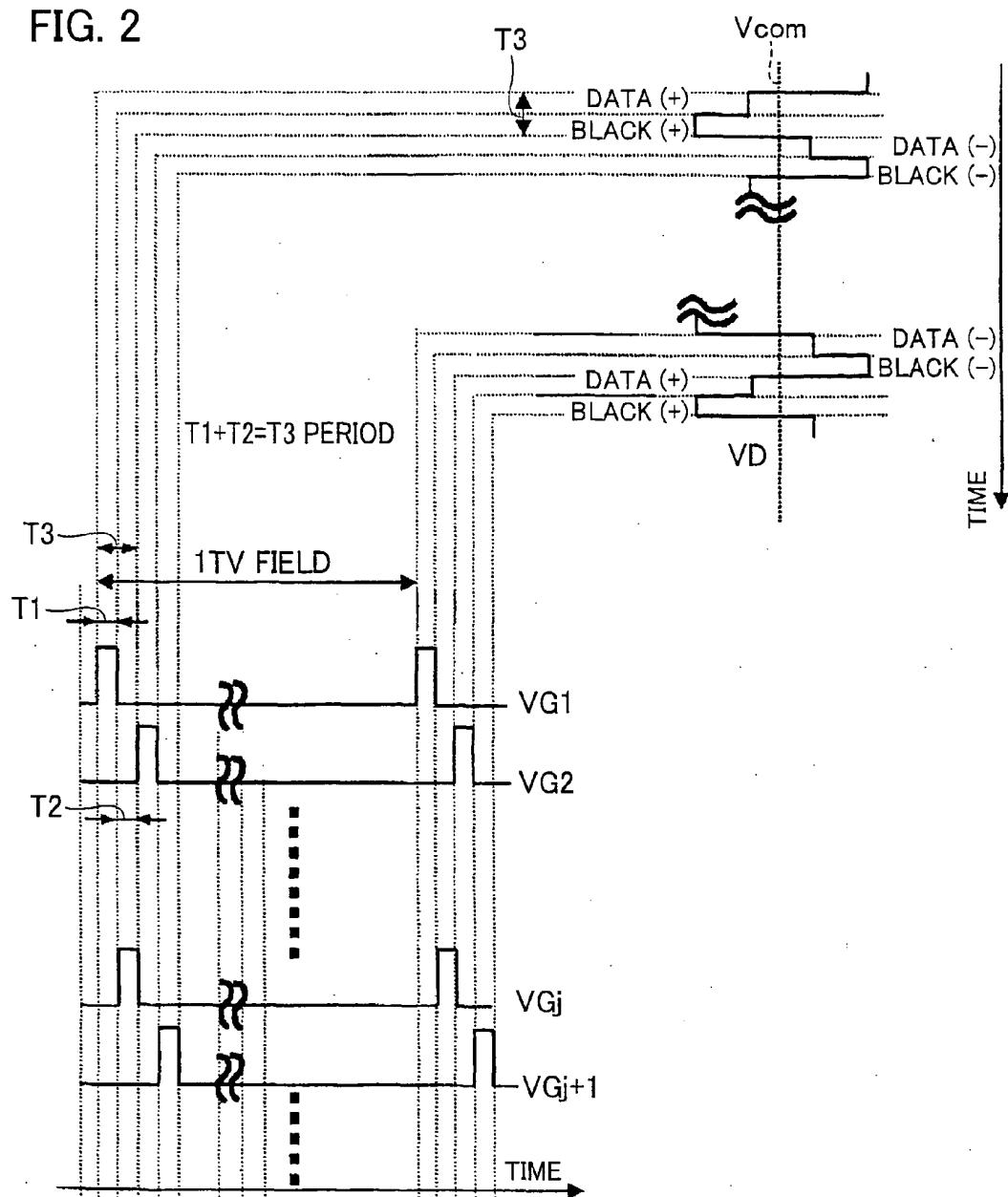


FIG. 3

VOLTAGE DATA	REFERENCE GRADATIONS								
	0	32	64	96	128	160	192	224	255
HOLD-TYPE DISPLAY	V0	V32	V64	V96	V128	V160	V192	V224	V225
IMPULSE-TYPE DISPLAY	V0	V32	V64	V96	V128	V160	V192	V224	V225

FIG. 4

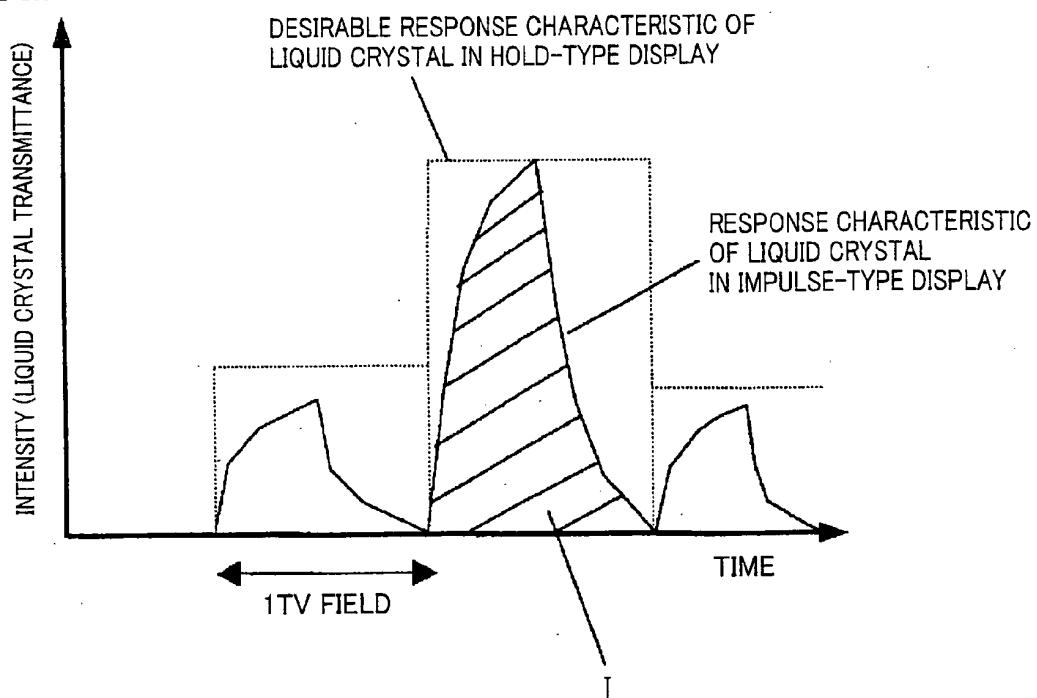


FIG. 5

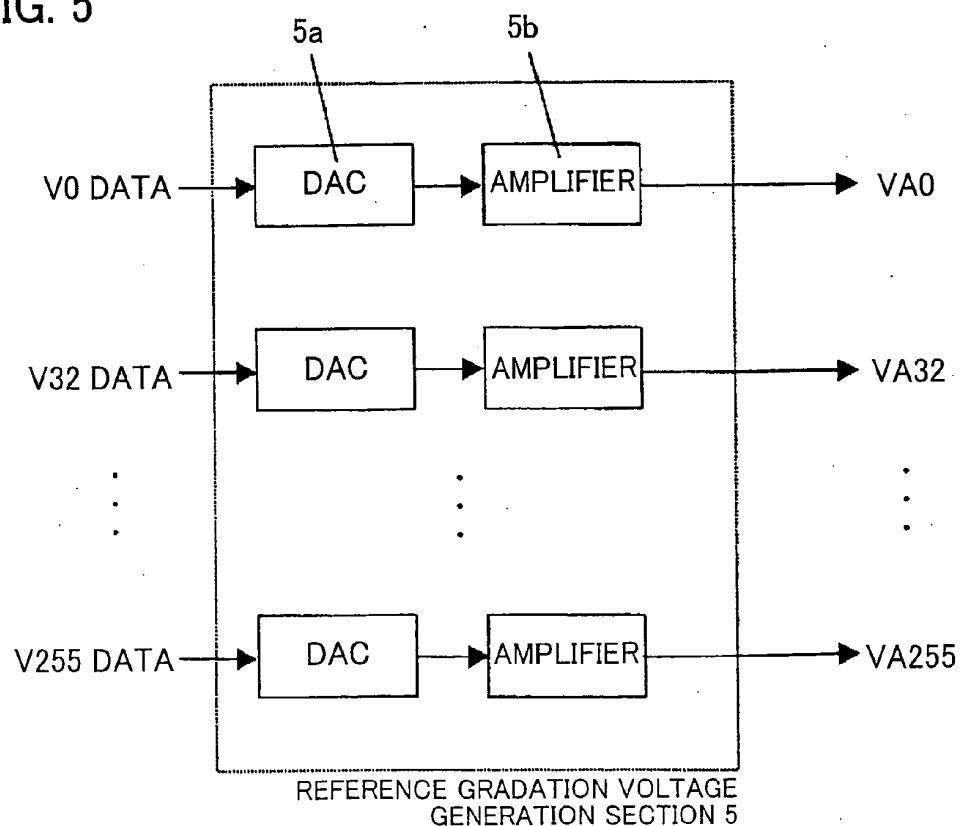


FIG. 6

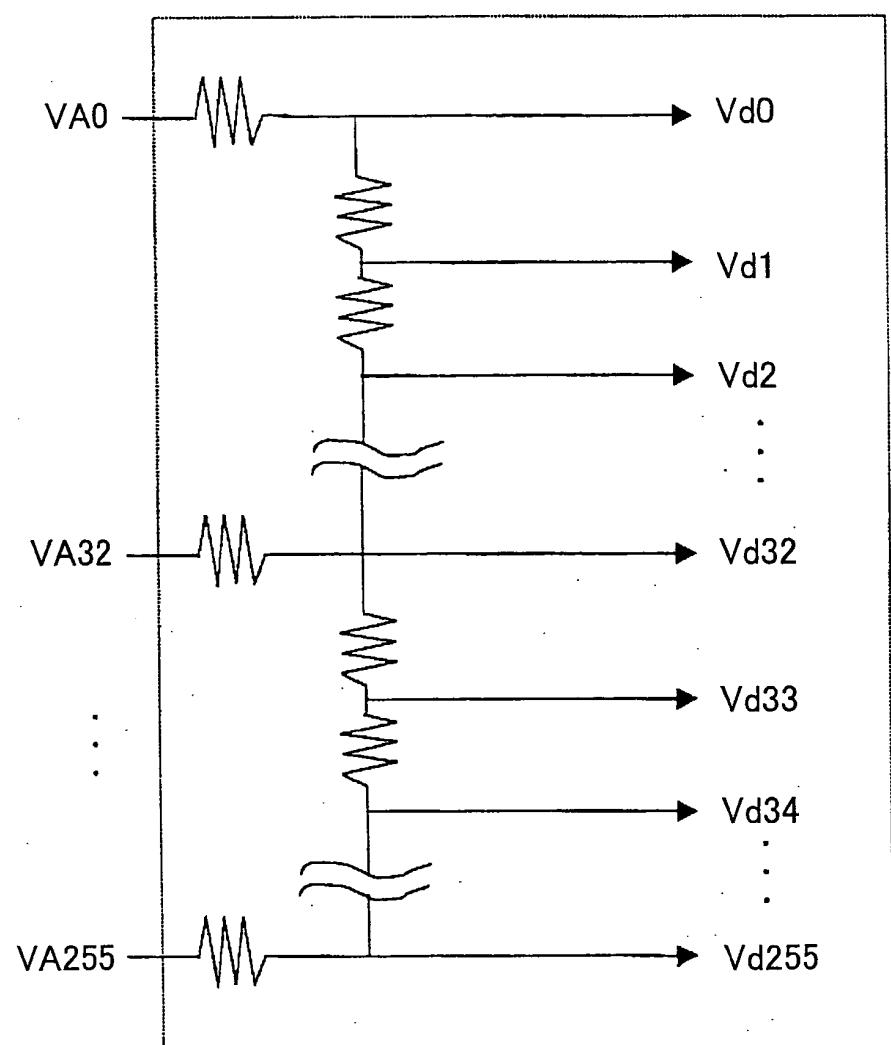


FIG. 7

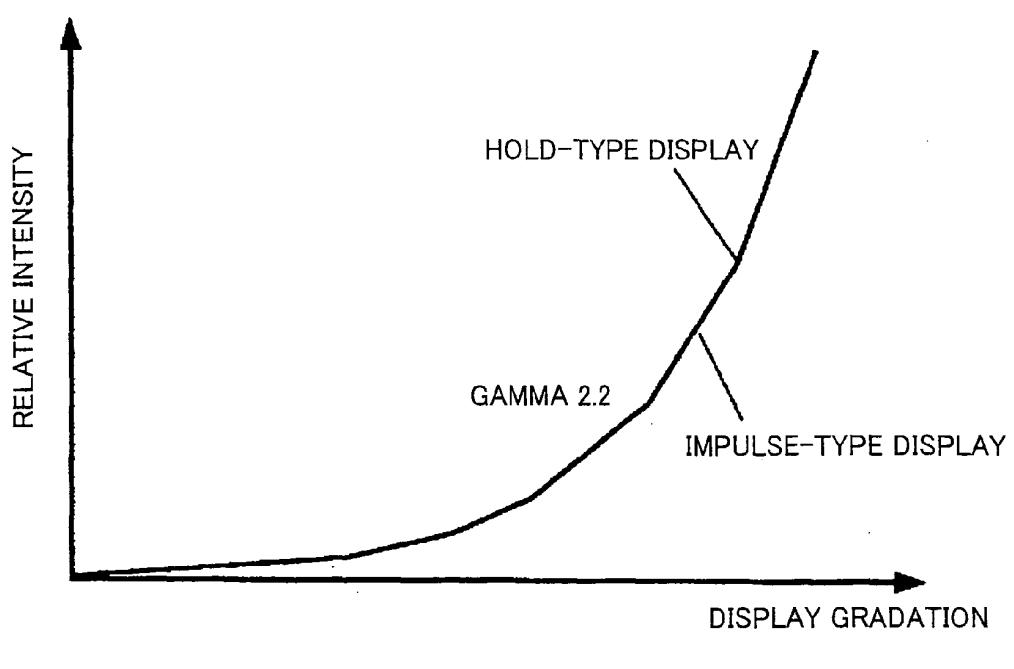


FIG. 8

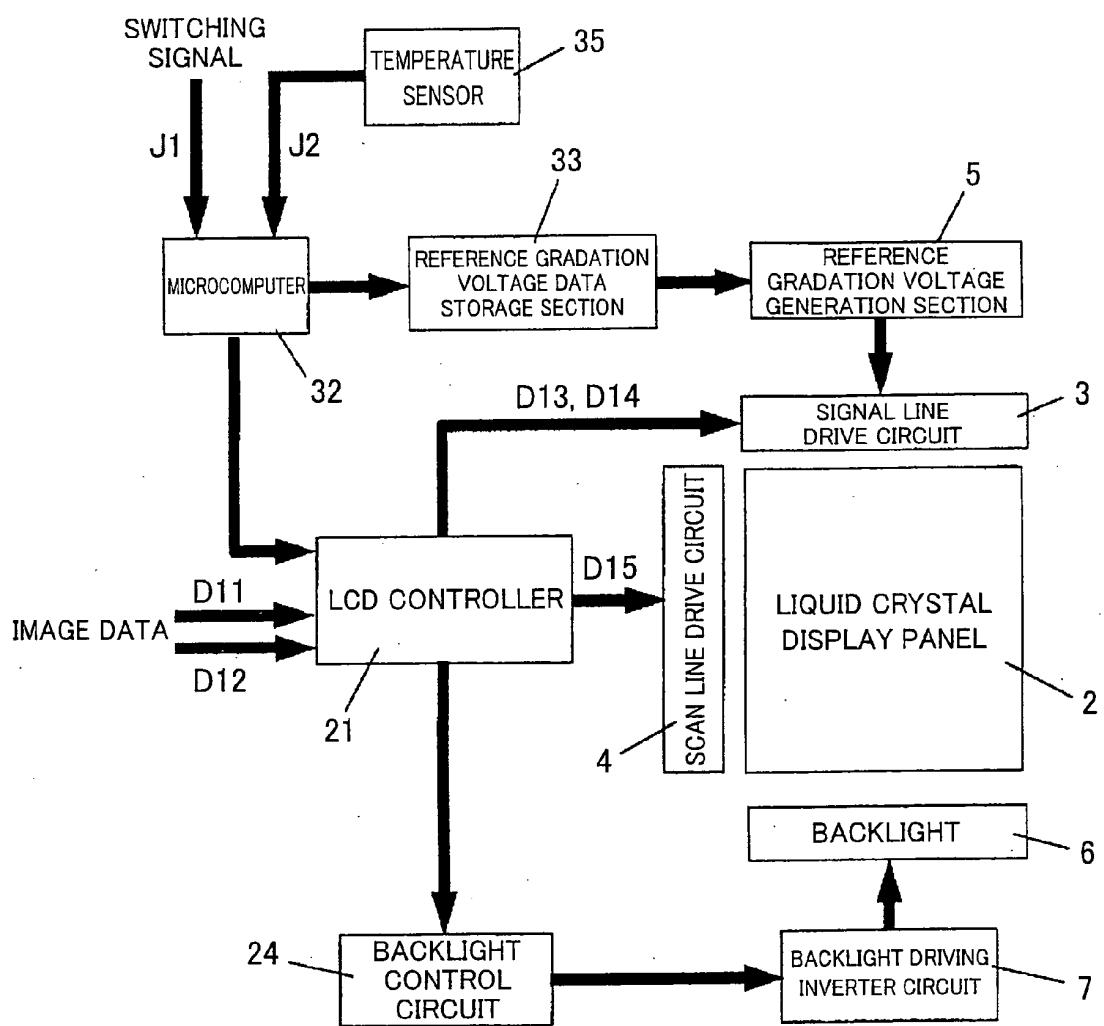


FIG. 9

			REFERENCE GRADATIONS									
			0	32	64	96	128	160	192	224	255	
VOLTAGE DATA	10~20°C	BELLOW 10°C	HOLD-TYPE DISPLAY	V0	V32	V64	V96	V128	V160	V192	V224	V225
	20~30°C		IMPULSE-TYPE DISPLAY	V0	V32	V64	V96	V128	V160	V192	V224	V225
	30~40°C		HOLD-TYPE DISPLAY	V0	V32	V64	V96	V128	V160	V192	V224	V225
	40~50°C		IMPULSE-TYPE DISPLAY	V0	V32	V64	V96	V128	V160	V192	V224	V225
	50~60°C		HOLD-TYPE DISPLAY	V0	V32	V64	V96	V128	V160	V192	V224	V225
			IMPULSE-TYPE DISPLAY	V0	V32	V64	V96	V128	V160	V192	V224	V225
			HOLD-TYPE DISPLAY	V0	V32	V64	V96	V128	V160	V192	V224	V225
			IMPULSE-TYPE DISPLAY	V0	V32	V64	V96	V128	V160	V192	V224	V225
			HOLD-TYPE DISPLAY	V0	V32	V64	V96	V128	V160	V192	V224	V225
			IMPULSE-TYPE DISPLAY	V0	V32	V64	V96	V128	V160	V192	V224	V225

FIG. 10

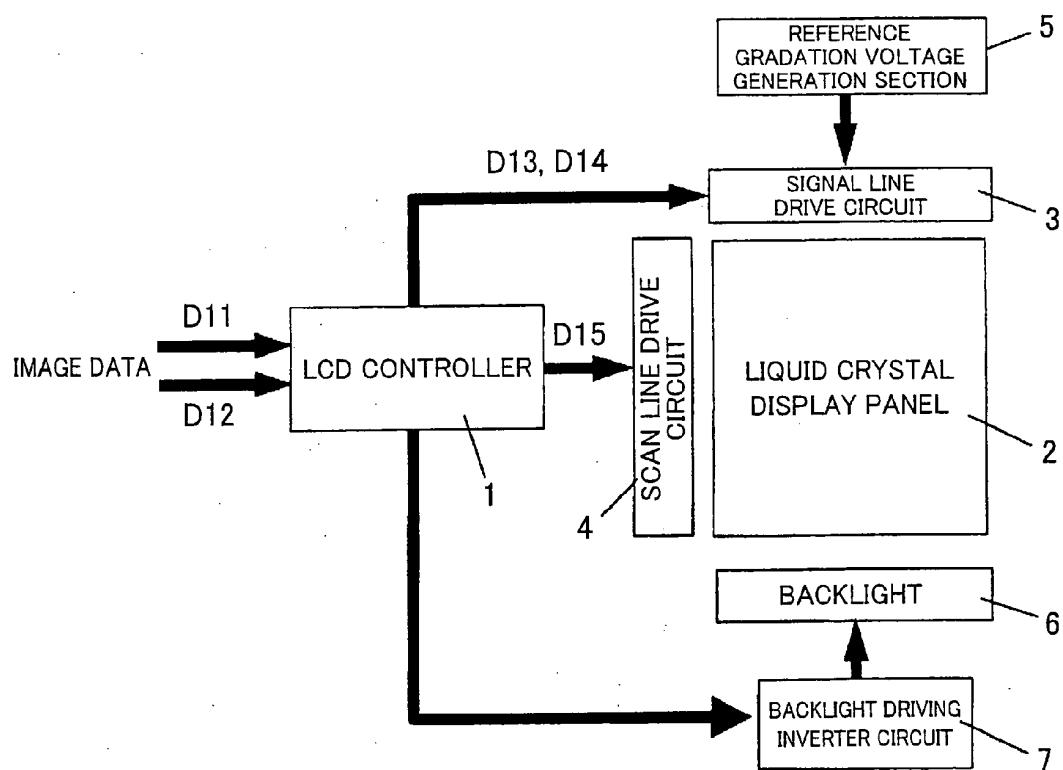


FIG. 11

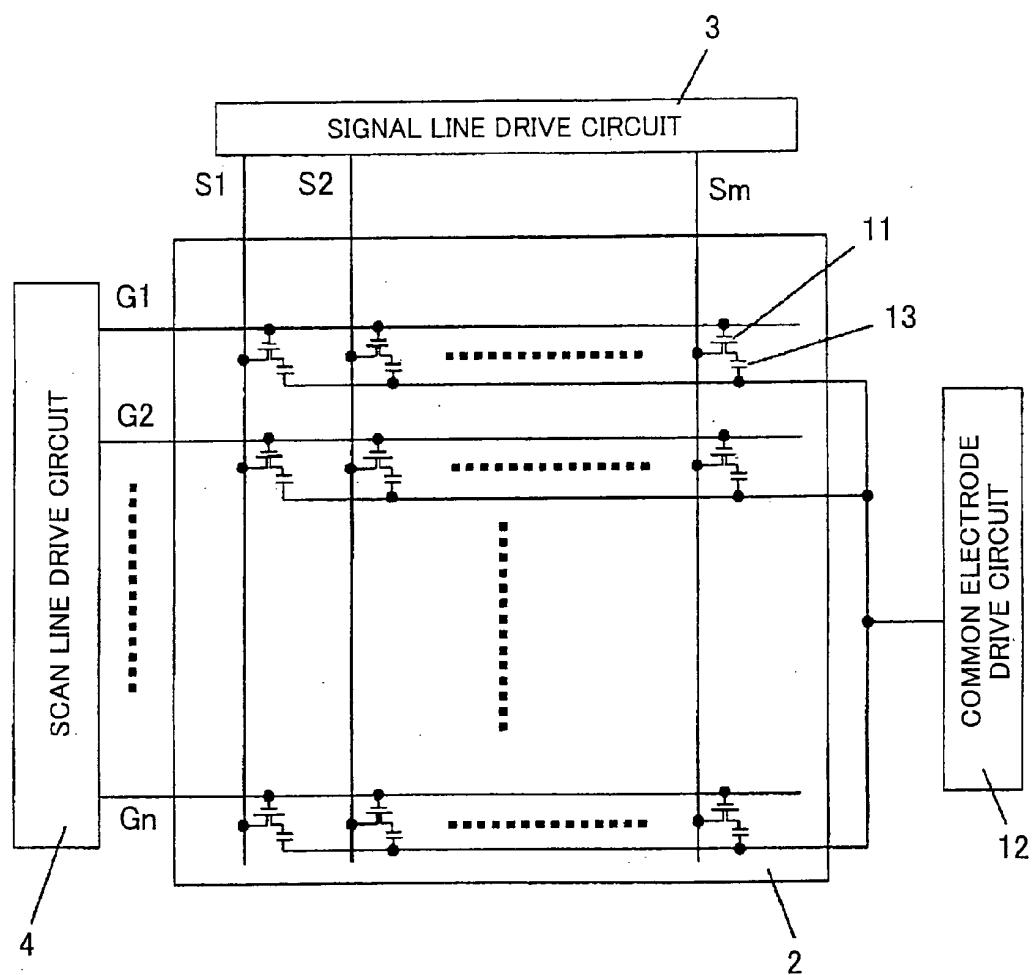
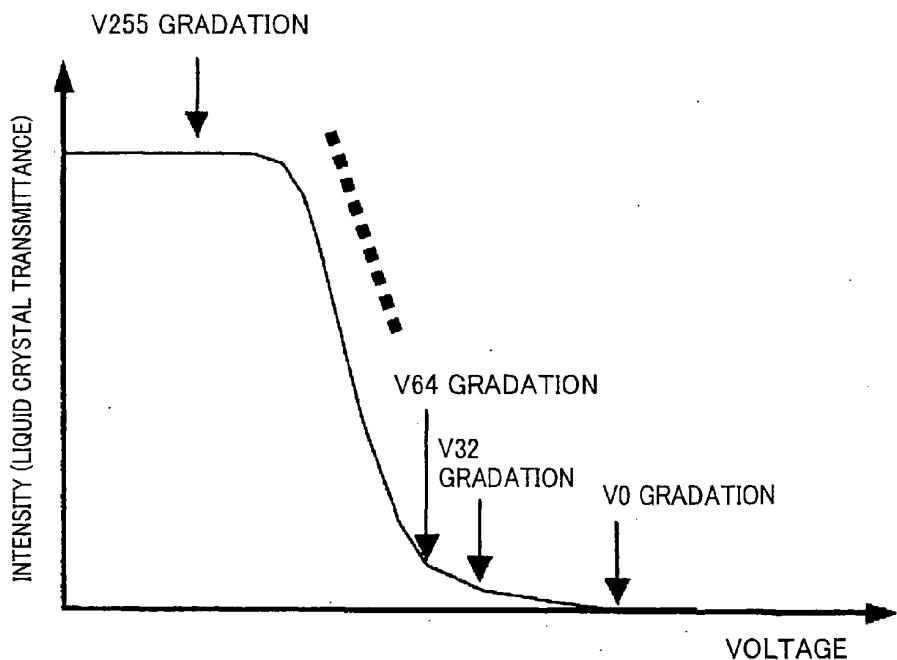
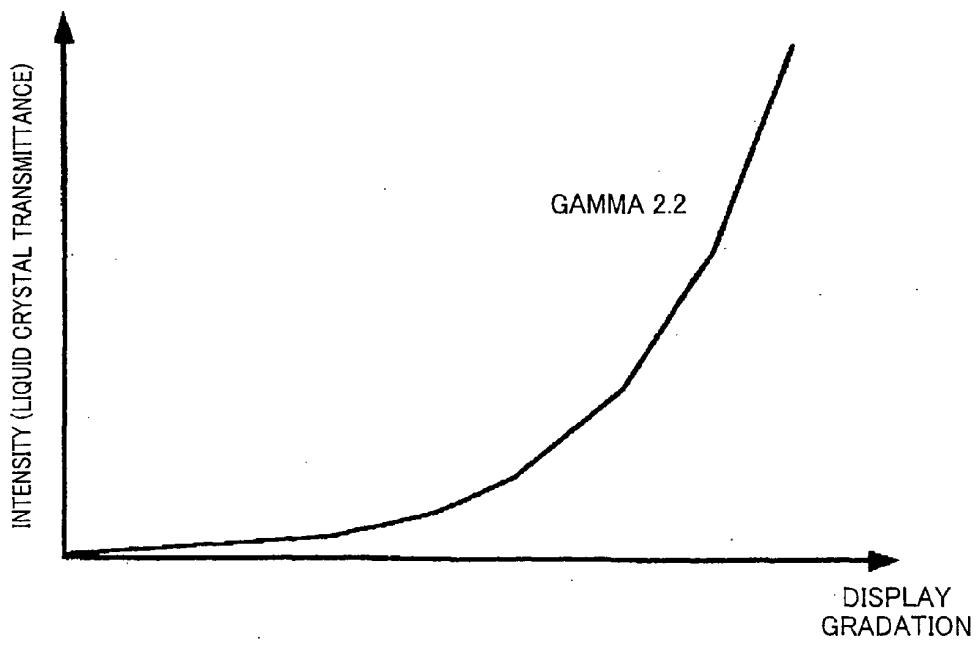


FIG. 12



V-T CHARACTERISTICS OF LIQUID CRYSTAL DISPLAY PANEL AND VOLTAGES

FIG. 13



GAMMA CHARACTERISTICS OF LIQUID CRYSTAL DISPLAY

FIG. 14

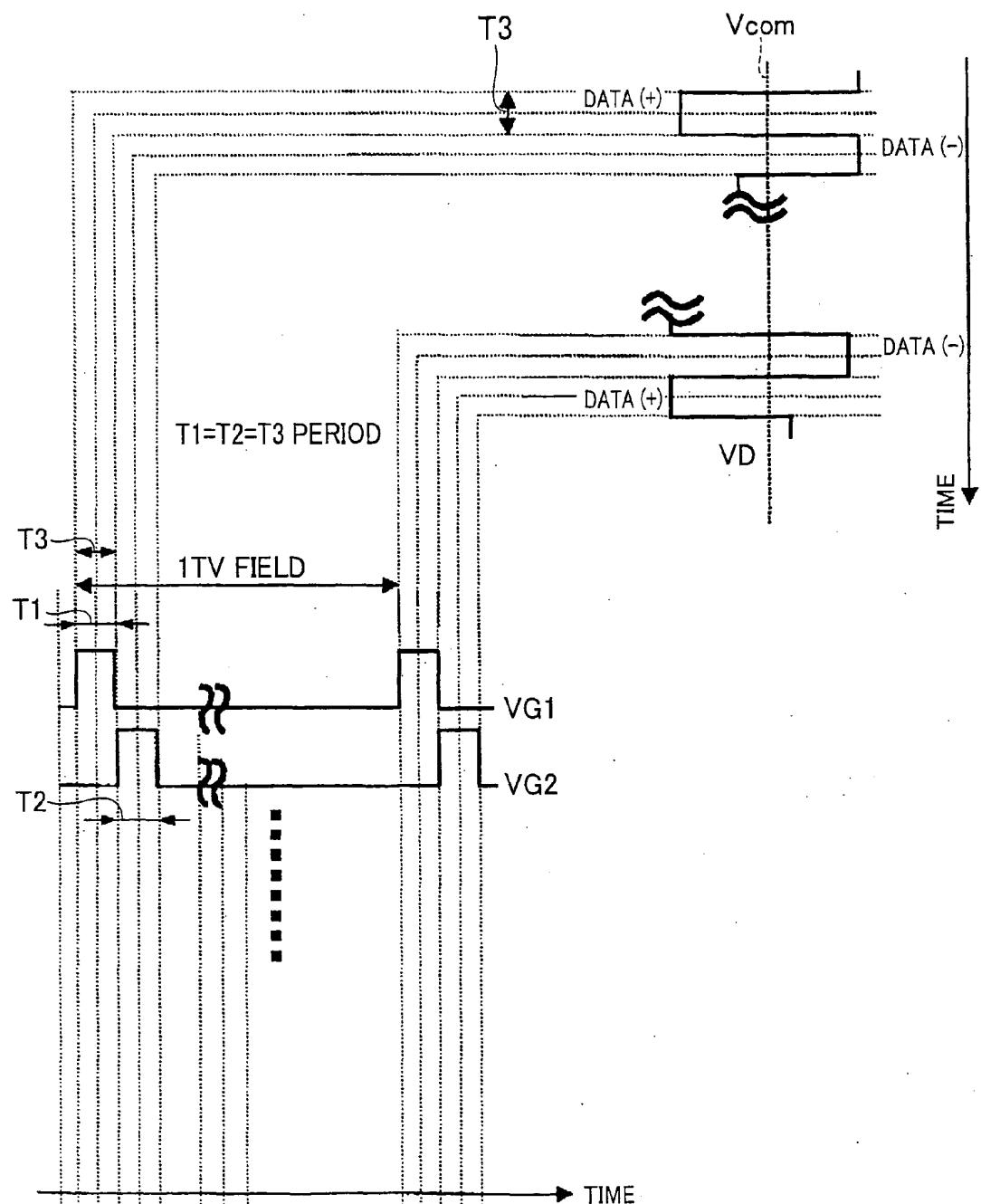


FIG. 15

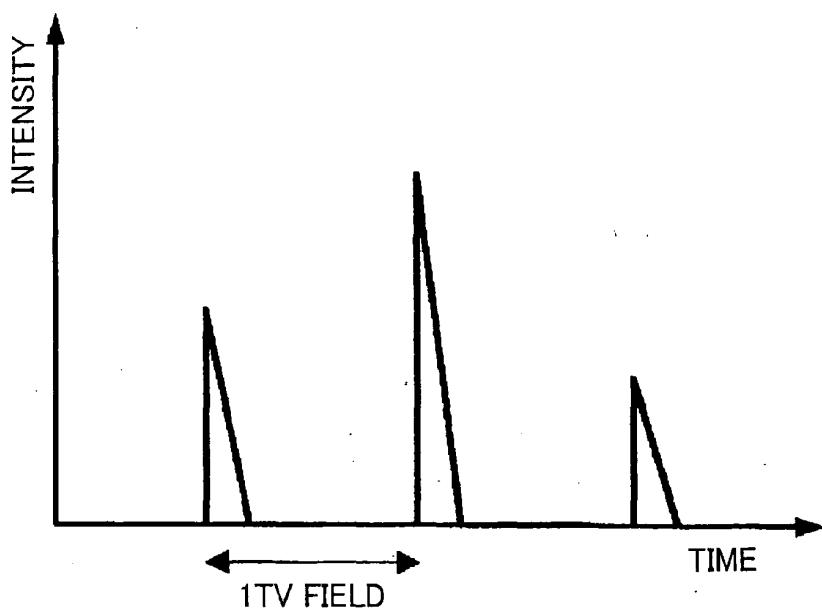


FIG. 16

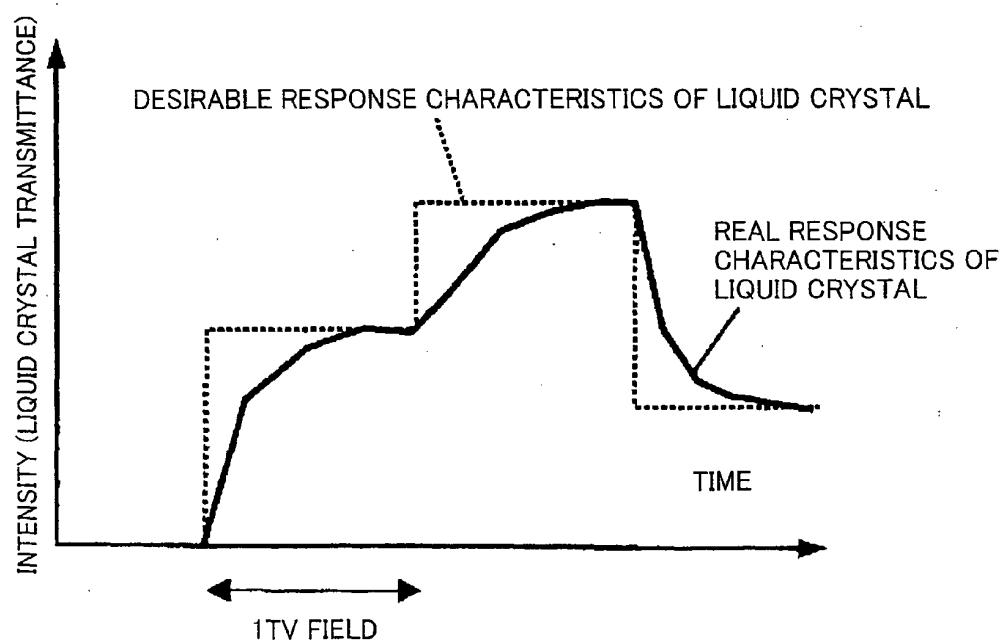


FIG. 17

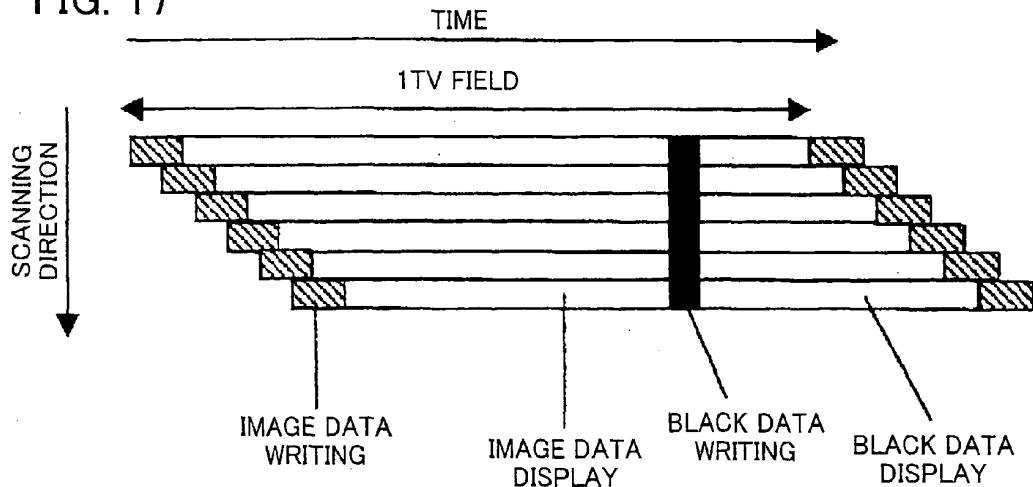


FIG. 18

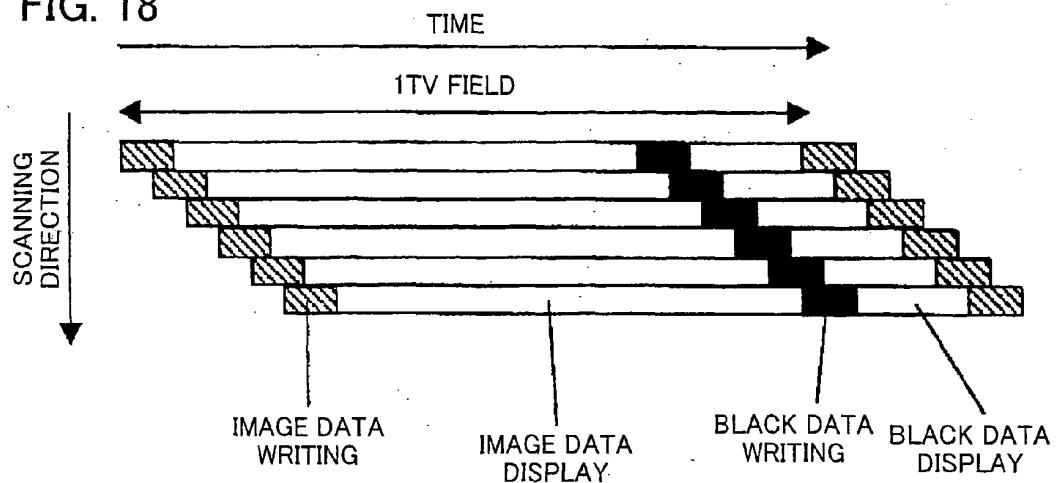


FIG. 19

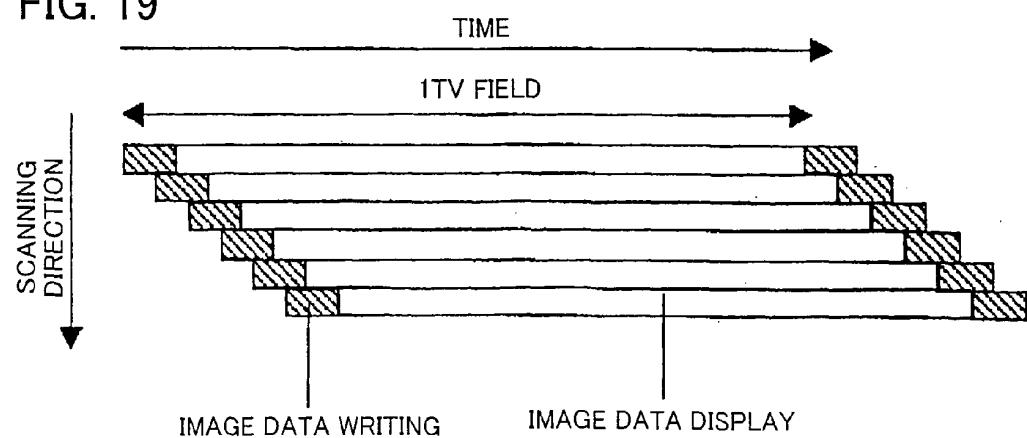


FIG. 20

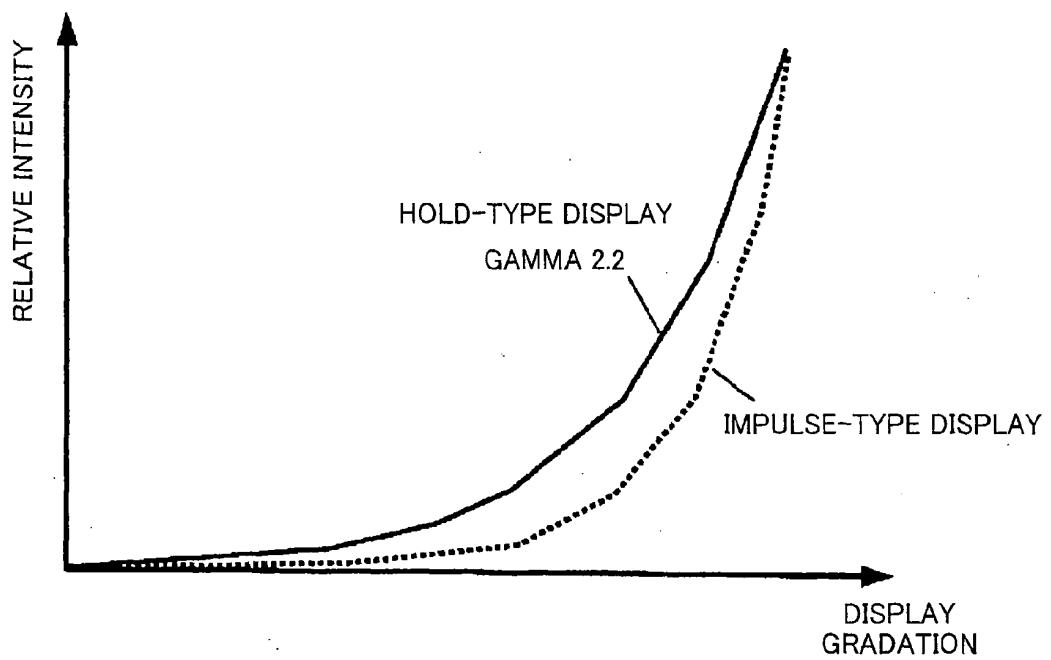


FIG. 21

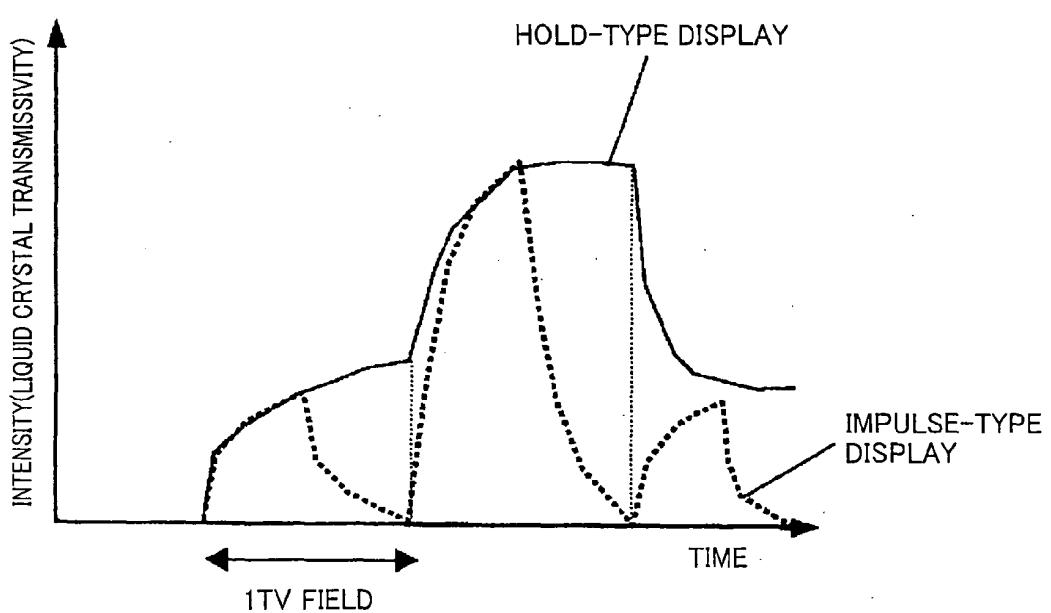
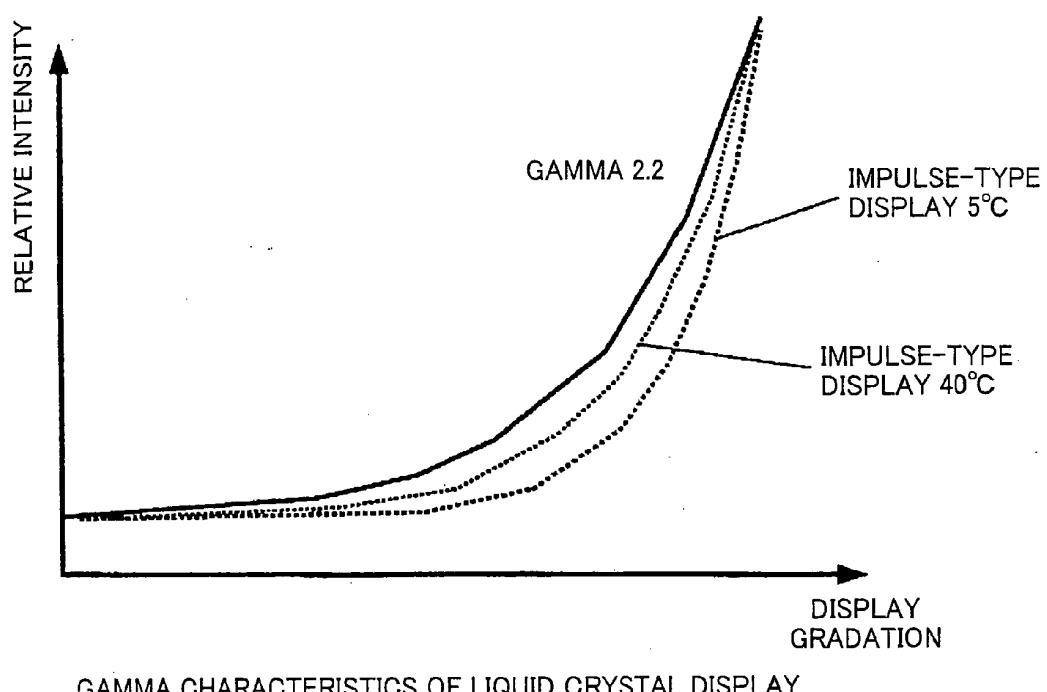


FIG. 22



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2004/000966

A. CLASSIFICATION OF SUBJECT MATTER
Int.Cl⁷ G09G3/36, 3/20, G02F1/133

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
Int.Cl⁷ G09G3/20-3/38, G02F1/133Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
Jitsuyo Shinan Koho 1926-1996 Jitsuyo Shinan Toroku Koho 1996-2004
Kokai Jitsuyo Shinan Koho 1971-2004 Toroku Jitsuyo Shinan Koho 1994-2004

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category ^b	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	JP 2002-323876 A (NEC Corp.), 08 November, 2002 (08.11.02), Par. Nos. [0063] to [0077]; Figs. 4, 7 to 15 & US 2002/0154088 A1 & EP 1255241 A1	1, 6 2-5
Y	JP 10-97227 A (Toshiba Corp.), 14 April, 1998 (14.04.98), Full text; all drawings (Family: none)	2-4
Y	JP 9-281943 A (Fujitsu General Ltd.), 31 October, 1997 (31.10.97), Par. No. [0019] (Family: none)	5

 Further documents are listed in the continuation of Box C. See patent family annex.

* Special categories of cited documents:
 "A" document defining the general state of the art which is not considered to be of particular relevance
 "E" earlier document but published on or after the international filing date
 "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
 "O" document referring to an oral disclosure, use, exhibition or other means
 "P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
 "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
 "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
 "&" document member of the same patent family

Date of the actual completion of the international search
26 February, 2004 (26.02.04)Date of mailing of the international search report
16 March, 2004 (16.03.04)Name and mailing address of the ISA/
Japanese Patent Office

Authorized officer

Facsimile No.

Telephone No.

Form PCT/ISA/210 (second sheet) (July 1998)

INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2004/000966

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP 8-313927 A (Casio Computer Co., Ltd.), 29 November, 1996 (29.11.96), Claim 1 (Family: none)	6
P,X	JP 2003-66918 A (Hitachi, Ltd.), 05 March, 2003 (05.03.03), Par. Nos. [0015] to [0016], Fig. 5 & CN 1410956 A	1,6
A	JP 7-66992 A (Fujitsu Ten Ltd.), 10 March, 1995 (10.03.95), Full text; all drawings (Family: none)	1
A	JP 2002-72969 A (Hitachi, Ltd.), 12 March, 2002 (12.03.02), Full text; all drawings (Family: none)	4
A	JP 2002-244627 A (Kaho Denshi Kofun Yugenkoshi), 30 August, 2002 (30.08.02), Full text; all drawings & US 2002/0105494 A1	4

Form PCT/ISA/210 (continuation of second sheet) (July 1998)

专利名称(译)	液晶显示器		
公开(公告)号	EP1600928A4	公开(公告)日	2006-10-11
申请号	EP2004706818	申请日	2004-01-30
[标]申请(专利权)人(译)	夏普株式会社		
申请(专利权)人(译)	夏普株式会社		
当前申请(专利权)人(译)	夏普株式会社		
[标]发明人	FUJINE TOSHIYUKI		
发明人	FUJINE, TOSHIYUKI		
IPC分类号	G09G3/20 G09G3/34 G09G3/36 G02F1/133		
CPC分类号	G09G3/3611 G09G3/2014 G09G2310/061 G09G2320/0247 G09G2320/0252 G09G2320/0261 G09G2320/0613 G09G2320/0673		
优先权	2003025636 2003-02-03 JP		
其他公开文献	EP1600928A1		
外部链接	Espacenet		

摘要(译)

液晶显示器具有LCD控制器，微计算机和存储部分。LCD控制器能够在一个场周期内设置用于基于输入图像数据执行显示的图像显示时段和用于基于黑色显示数据执行显示的黑色显示时段。微计算机在具有图像显示周期的脉冲驱动模式和一个场周期内的黑色显示周期之间切换LCD控制器的模式，以及仅具有图像显示周期的保持驱动模式。存储部分存储先前指定的多组参考灰度电压数据。在参考灰度电压产生部分产生并用于驱动液晶显示板的参考灰度电压根据模式是可变的。因此，可以实现能够抑制伽马特性的变化的液晶显示器，从而即使在切换模式时也能够防止显示质量的劣化。

