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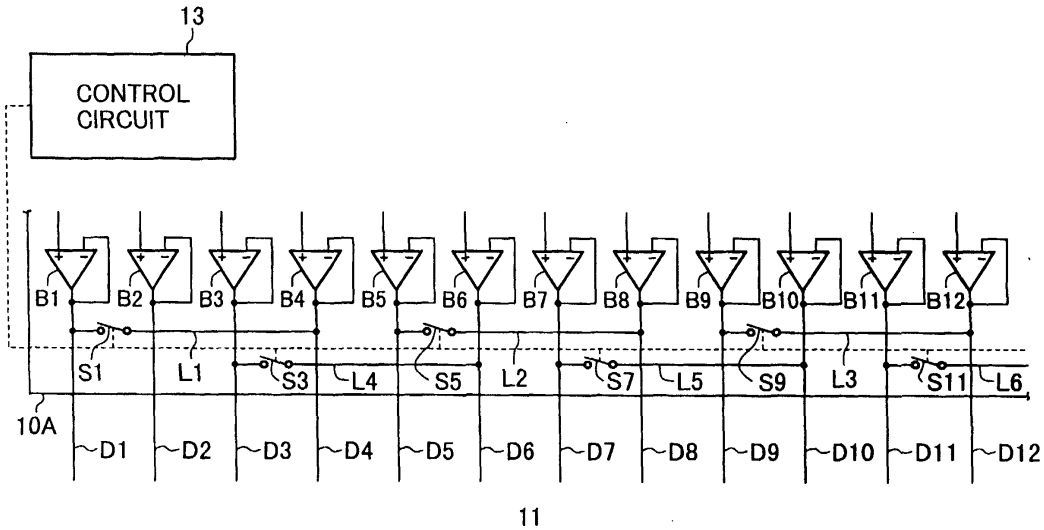
(54) **Dot-inversion data driver for liquid-crystal display device**

(57) In an LCD driven by a data driver 10A of a dot-inversion driving type, the outputs of voltage buffer amplifiers B1 to B12 are connected to respective data-bus lines D1 to D12 of the LC panel. The data-bus lines carry different colour signals; short-circuiting switches S1, S3, S5, S7, S9 and S11 are connected between successive pairs of data-bus lines concerned with the same display colour, and interconnecting lines L1, L2, L4 for the switches are arranged in a staggered configuration on first and second rows. These short-circuiting switches can be formed in a space-saving manner on one side of every other data-bus line, and turned on by a control circuit 13 when the outputs of the voltage buffer amplifier are in a high impedance state, i.e. in a blanking period.

FIG.4



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EUROPEAN SEARCH REPORT

Application Number
EP 01 30 4785

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The present search report has been drawn up for all claims

Place of search	Date of completion of the search	Examiner
THE HAGUE	4 November 2003	Corsi, F

CATEGORY OF CITED DOCUMENTS

X : particularly relevant if taken alone
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A : technological background
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& : member of the same patent family, corresponding document

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EUROPEAN SEARCH REPORT

Application Number
EP 01 30 4785

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The present search report has been drawn up for all claims

Place of search	Date of completion of the search	Examiner
THE HAGUE	4 November 2003	Corsi, F

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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

专利名称(译)	用于液晶显示装置的点反转数据驱动器		
公开(公告)号	EP1202245A3	公开(公告)日	2004-01-07
申请号	EP2001304785	申请日	2001-05-31
[标]申请(专利权)人(译)	富士通株式会社		
申请(专利权)人(译)	FUJITSU LIMITED		
当前申请(专利权)人(译)	富士通半导体有限公司		
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发明人	UDO, SHINYA, C/O FUJITSU LIMITED KOKUBUN, MASATOSHI, C/O FUJITSU LIMITED		
IPC分类号	G02F1/133 G09G3/20 G09G3/36		
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优先权	2000333517 2000-10-31 JP		
其他公开文献	EP1202245B1 EP1202245A2		
外部链接	Espacenet		

摘要(译)

在由点反转驱动型数据驱动器10A驱动的LCD中，电压缓冲放大器B1至B12的输出连接到LC面板的各个数据总线D1至D12。数据总线传输不同的颜色信号;短路开关S1，S3，S5，S7，S9和S11连接在具有相同显示颜色的连续数据总线对之间，并且用于开关的互连线L1，L2，L4以交错配置排列。第一行和第二行。这些短路开关可以在每隔一条数据总线的一侧以节省空间的方式形成，并且当电压缓冲放大器的输出处于高阻抗状态时，即由控制电路13接通。消隐期。

FIG.4