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(54) **Array substrate, touch liquid crystal display panel and manufacturing method thereof**

(57) An array substrate includes a plurality of pixel units, each of the pixel units includes a pixel electrode (202) and a common electrode (204) that are insulated from each other, and a conductive layer (206) that is electrically connected in parallel to the common electrode. A double-layer metal layer is deposited on the common

electrode at an opaque region of the pixel electrode to form a ring structure around a transparent region of the pixel electrode, thus reducing the resistances of the common electrodes. The ring structure can be U-shaped, half-ring shaped, or full-ring shaped.

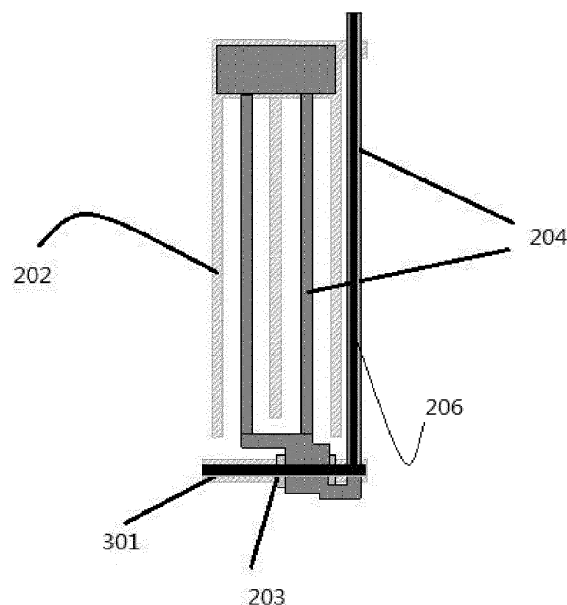


Fig. 5

Description

[0001] The present application claims the priority of Chinese Patent Application No. 201310175657.7, filed with the Chinese Intellectual Property Office on May 13, 2013, entitled as "ARRAY SUBSTRATE, TOUCH LIQUID CRYSTAL DISPLAY PANEL AND MANUFACTURING METHOD THEREOF", the content of which is incorporated by reference herein in its entirety herein.

FIELD OF THE INVENTION

[0002] The present invention refers to the touch screen technology, and in particular to an array substrate, a touch liquid crystal display panel having reduced resistance of common electrodes and a manufacturing method thereof.

BACKGROUND OF THE INVENTION

[0003] At present, in the Thin Film Transistor-Liquid Crystal Display (TFT-LCD) field, a large-scale LCD panel is developing quickly with continuous updating of technologies. A Touch Panel (TP), as an input medium, is integrated with a display screen to form a touch display screen which plays an important role in the display field. Particularly, a capacitance touch panel is popular because of high sensitivity thereof. A mutual capacitance touch panel is commonly employed because of its advantages, such as, high sensitivity and multi-touch detection detection.

[0004] At present, according to combination methods of the touch panel and an LCD device, there are two kinds of the touch panels, i.e., an on-cell touch panel and an in-cell touch panel. The on-cell touch panel and the display device are manufactured separately and then are assembled together. In this way, the thickness of the display screen will be increased. In addition, the transmittance and the contrast of the display will be reduced obviously because multiple layers of transparent glasses or thin films are provided additionally. Further, the manufacturing cost is high with this method. Therefore, current research focuses on in-cell touch panel technology which integrates the TP into a touch liquid crystal display Panel (LCD Panel) because of its low production cost, high transmittance, and thin thickness, etc.

[0005] In a mutual capacitance touch panel, X-direction coordinates of touch points are determined by touch driving electrodes, and Y-direction coordinates of the touch points are determined by touch sensing electrodes. A touch driving voltage is applied to the touch driving electrodes. A constant voltage is applied to the touch sensing electrodes. For detecting a touch point, a progressive scanning is performed on the X-direction touch driving electrodes. When each row of the touch driving electrodes is scanned, the signal on each column of the touch sensing electrodes is read. Intersections of each row and each column can be scanned through a round

of full-screen scanning to obtain X*Y signals. The coordinates of multiple touch points can be determined specifically with this touch location detection method, and thereby the multi-touch detection can be realized.

[0006] Figure 1 shows an equivalent circuit of a mutual capacitance touch panel, which includes a signal source 101, a touch driving electrode resistance 103, a mutual capacitance 102 between the touch driving electrode and the touch sensing electrode, a parasitic capacitance 104 between the touch driving electrode and a common electrode or between the touch sensing electrode and a common electrode, a touch sensing electrode resistance 105, and a touch point detection circuit 106. A part of the current of the circuit flows into a touch object (e.g., a finger) when the touch panel is touched by the touch object, which is equivalent to a change of the value of the mutual capacitance 102 between the touch driving electrode and the touch sensing electrode. The touch point detection circuit detects the weak current changes caused by the mutual capacitance 102 into an output voltage signal Vout.

[0007] The touch driving electrode and the touch sensing electrode are both disposed on the LCD panel, and the distance between the TP and the common electrode of the LCD panel is very short. Thus, the parasitic capacitance 104 between the touch driving electrode and a common electrode or between the touch sensing electrode and the common electrode is very large, such that the TP is affected greatly by noises generated by the LCD panel. In this case, current signals detected by the touch point detection circuit are difficult to be detected due to the noises, and thus touch resolution is poor, as a consequence, the touch panel may not operate accurately.

[0008] In addition, considering conventional touch display panels in the prior art, a signal current is small and easy to flow from the driving electrode to the sensing electrode when the touch signal strength is weak. Thus, a signal delay exists between a peripheral side and the center of the common electrode, which will cause the signal at the center of the TP to be weaker than that at each of the four sides thereof. In this case, the signal detection of the TP is affected, and there is a problem that the touch signal will not be detected.

SUMMARY OF THE INVENTION

[0009] Embodiments of the present invention provide an array substrate, a touch liquid crystal display panel, and a manufacturing method of the array substrate, which reduce resistances of common electrodes, thereby solving the problem of unreliable or no response to a touch signal (i.e. the touch signal cannot be reliably detected) occurring at the center of the touch panel. Embodiments of the present invention provide an array substrate that can solve the above technical problems. The array substrate includes: a plurality of pixel units, each of the pixel units including a pixel electrode and a com-

mon electrode insulated from each other. The array substrate further includes a conductive layer which is connected in parallel to the common electrodes. In a specific embodiment, the conductive layer is adapted to reduce the resistance of the common electrode.

[0010] Optionally, the conductive layer is formed on an opaque region of the pixel electrode. Optionally, the conductive layer is a metal layer.

[0011] Optionally, the metal layer is a composite structure.

[0012] Optionally, the metal layer has a Mo/AlNd double-layer structure.

[0013] Optionally, the conductive layer is formed in a ring structure around a transparent region of the pixel electrode.

[0014] Optionally, the conductive layer is formed on a transparent region of the pixel electrode, and the conductive layer is a transparent conductive layer.

[0015] Embodiments of the present invention provide a touch liquid crystal display panel. The touch liquid crystal display includes a first substrate, a second substrate opposite to the first substrate, and a liquid crystal layer disposed between the first substrate and the second substrate. In an embodiment, the first substrate is an array substrate, and the second substrate is a color film substrate.

[0016] Optionally, the display panel is an In-Plane Switching display panel or a Fringe Field Switching display panel.

[0017] Embodiments of the present invention provide a manufacturing method of the array substrate. The manufacturing method includes providing a substrate; disposing a gate electrode on the substrate; disposing a gate insulating layer on the gate electrode; forming source/drain electrodes, pixel electrodes and common electrodes; and disposing on the common electrodes a conductive layer. The conductive layer and the common electrodes are connected in parallel to each other.

[0018] Optionally, the conductive layer is formed at an opaque region of the pixel electrode.

[0019] Optionally, the conductive layer has a Mo/AlNd double-layer metal structure.

[0020] Optionally, the conductive layer is formed at a transparent region of the pixel electrode, and the conductive layer is a transparent conductive layer.

[0021] The embodiments of the present invention have advantages or beneficial effects as follows.

[0022] In the embodiments of the present invention, a conductive layer is deposited on the common electrode and located at an opaque region of the pixel unit, and also connected in parallel with the common electrodes. Thus, the resistance of the common electrode is reduced, the problem that the touch signal cannot be detected at the center of the touch panel is solved, and the sensitivity of the touch panel is improved.

BRIEF DESCRIPTION OF THE DRAWINGS

[0023] The embodiments will be described more fully hereinafter with reference to the accompanying drawings, in which exemplary embodiments of the invention are shown. As those skilled in the art would realize, the described embodiments may be modified in various different ways without departing from the spirit or scope of the embodiments. Like reference numerals designate like elements throughout the specification.

Figure 1 is a signal equivalent circuit diagram of an existing touch panel circuit in the prior art;

Figure 2 is a cross-sectional view of a layer structure when a touch liquid crystal display panel is an IPS display panel in the prior art;

Figure 3 is a cross-sectional view of a layer structure when a touch liquid crystal display panel is an IPS display panel according to an embodiment of the present invention;

Figure 4 is a top view of electrodes when a touch liquid crystal display panel is an IPS display panel in the prior art;

Figure 5 is a top view of electrodes according to the embodiment of Figure 3;

Figure 6 is a cross-sectional view of a layer structure when a touch liquid crystal display panel is an FFS display panel in the prior art;

Figure 7 is a cross-sectional view of a layer structure when a touch liquid crystal display panel is an FFS display panel according to an embodiment of the present invention;

Figure 8 is a top view of electrodes when a touch liquid crystal display panel is an FFS display panel in the prior art; and

Figure 9 is a top view of electrodes of the embodiment of Figure 7.

DETAILED DESCRIPTION OF THE INVENTION

[0024] Technical solutions and advantages of the disclosure, the technical solutions in the embodiments of the disclosure will be described clearly and completely in conjunction with the drawings in the embodiments of the disclosure hereinafter. Apparently, the described embodiments are only some of but not all the embodiments of the present invention. All the other embodiments obtained by those skilled in the art based on the embodiment in the present disclosure without inventive efforts will fall within the scope of protection of the present disclosure.

[0025] The embodiments of the present invention will be described in combination with the figures.

[0026] Figure 2 is a cross-sectional view of a layer structure when a touch liquid crystal display panel is an In-Plane Switching (IPS) display panel in the prior art. The touch liquid crystal display panel includes: a first substrate 200 including a glass substrate 201, pixel elec-

trodes 202, a first insulating layer 203, common electrodes 204 and a second insulating layer 205; a second substrate 210; and liquid crystal 220 sandwiched between the first substrate 200 and the second substrate 210. The first substrate is an array substrate and the second substrate is a color film substrate.

[0027] It should be noted that, a normal array substrate includes: a substrate; and a gate electrode, a gate insulating layer, a polysilicon layer, a source electrode and a drain electrode, data lines, pixel electrodes and common electrodes all of which are disposed on the substrate in sequence. In the application, a complete structure drawing of the array substrate is not given for convenience of the description.

[0028] Referring to Figure 3, Figure 3 is a cross-sectional view of a layer structure when a touch liquid crystal display panel is an IPS display panel in one embodiment of the present invention. An array substrate includes: pixel electrodes 202, common electrodes 204, and a first insulating layer 203 disposed between the pixel electrode 202 and the common electrode 204 to electrically separate the pixel electrodes 202 and the common electrodes 204. In the embodiment of the present invention, a conductive layer 206 is further deposited on the common electrodes 204 to reduce the resistance of the common electrodes 204. At the same time, in the embodiment of the present invention, the conductive layer 206 is deposited on the common electrodes 204 at an opaque region of the pixel electrode 202 to avoid the reduction of an aperture ratio of the pixel electrode. In an embodiment, the conductive layer is deposited on the outmost common electrode on the array substrate to form a ring structure around a transparent region of the pixel electrode. In another embodiment, the conductive layer may be a U shape structure, or may be a half ring structure, or a whole ring structure. The conductive layer 206 shown in Figure 3 is illustrative only and should not be taken as limitations. Optionally, the conductive layer may be deposited on the common electrodes at the opaque region of the pixel electrode 202 to avoid the reduction of the aperture ratio of the pixel electrodes.

[0029] Because the conductive layer is deposited on the common electrodes 204 at the opaque regions of the pixel electrodes 202, an equivalent resistance of the conductive layer is connected in parallel with the resistances of the common electrodes, and thereby the resistances of the common electrodes can be reduced effectively. In this way, the problem of poor touch (i.e. the touch signal cannot be detected) at center of a touch panel due to a large resistance of the common electrode is solved.

[0030] In addition, it is to be noted that in an alternative embodiment, a transparent conductive layer, such as ITO layer, may be deposited on the common electrode at the transparent region of the pixel electrodes to achieve the advantages of reducing the resistance of the common electrode and avoiding the great reduction of the aperture ratio of the pixel electrodes. Of course, there are also some bad effects on the aperture ratio of the pixel elec-

trodes in the case that the transparent conductive layer is deposited on the common electrode in the transparent region of the pixel electrode. Therefore, in an optimal embodiment, the conductive layer is deposited on the common electrode in the opaque region of the pixel electrode.

[0031] The conductive layer deposited on the common electrode can be a transparent conductive layer or an opaque conductive layer. Particularly, the opaque conductive layer is deposited on the common electrodes at the opaque region of the pixel electrode and may be used as shading stripes, thus improving the luminance of backlight further. In another embodiment, the conductive layer deposited on the common electrode in the opaque region of the pixel electrode is a double metal layer structure. Optionally, the double metal layer has a Mo/AlNd double metal layer structure. The laminating sequence of Mo and AlNd can be interchanged.

[0032] Referring to Figure 4 and Figure 5, Figure 4 is a top view of electrodes of the array substrate of an IPS touch liquid crystal display panel in the prior art, where common electrodes 204 are deposited on pixel electrodes 202 through a first insulating layer 203, and the pixel electrodes 202 are disposed on a gate line 301. Figure 5 is a top view of electrodes of the IPS touch liquid crystal display panel according to an embodiment of the present invention. In the embodiment, a conductive layer 206 is deposited on the common electrode overlapping with the data line spatially. Namely, the conductive layer 206 is deposited on the rightmost vertical stripe in Figure 5. It is to be noted that figure 5 is just shown for convenient explanation but should not be taken as limitations. In principle, the conductive layer 206 is deposited on the common electrodes at the opaque region of the pixel electrode and is connected in parallel to the common electrodes to reduce the resistance of the common electrode.

[0033] Referring to Figure 6 through Figure 9, Figure 6 is a cross-sectional view of a layer structure of an Fringe Field Switching (FFS) touch liquid crystal display panel in the prior art. The touch liquid crystal display panel includes: a first substrate including a substrate 401, pixel electrodes 402, a first insulating layer 403, common electrodes 404 and a second insulating layer 405; a second substrate 410; and liquid crystal 420 sandwiched between the first substrate and the second substrate 410. The pixel electrodes 402 are a whole plate structure. The common electrodes 404 are comb-shape and overlapping with the pixel electrodes 402. The first insulating layer 403 is disposed between the pixel electrodes 402 and the common electrodes 404 to cut off the electric connection therebetween.

[0034] Figure 7 is a cross-sectional view of a layer structure of an FFS touch liquid crystal display panel according to an embodiment of the present invention. The first substrate includes: pixel electrodes 402, common electrodes 404 and a conductive layer 406 deposited on the common electrodes 404. The conductive layer 406 is connected in parallel with the common electrodes 404

to reduce the resistances of the common electrodes 404. In addition, the conductive layer 406 is deposited on the common electrodes at an opaque region of the pixel electrode 402 to avoid the reduction of the aperture ratio of the pixel electrodes 402 in the embodiment. Optionally, the conductive layer is deposited on the outmost common electrode on an array substrate to form a ring structure around the transparent region of the pixel electrode. Namely, the ring structure is deposited around the transparent region of the pixel electrode, and the ring structure is still disposed at the opaque region of the pixel electrode. The ring structure may be U shape, a half ring or a whole ring. The conductive layer 406 shown in Figure 7 is just for convenience of explanation, which should not limit the scope of the invention. Optionally, the conductive layer is deposited on the common electrodes at the opaque region of the pixel electrode 402 to avoid the reduction of the aperture ratio of the pixel electrodes. In an embodiment, the conductive layer 406 may be a transparent conductive layer or an opaque conductive layer. In the case that conductive layer is made of transparent conductive materials such as ITO, the conductive layer 406 may be provided at the transparent region of the pixel electrode when the demand for the aperture ratio is not high. In the case that conductive layer is made of opaque conductive materials, the conductive layer can only be deposited on the common electrode at the opaque region of the pixel electrode.

[0035] It is to be noted that, the conductive layer may be used as a shading stripe when the conductive layer made of the opaque conductive materials is deposited on the common electrode at the opaque region of the pixel electrode. In this way, the resistances are reduced and the luminance of backlight is improved.

[0036] Figure 8 is a top view of electrodes when a touch liquid crystal display panel is an FFS display panel in the prior art, and Figure 9 is a top view of electrodes according to an embodiment of the present invention. Referring to Figure 8 and Figure 9, the common electrodes 404 are arranged in a comb-shaped structure. The pixel electrodes are disposed under the common electrodes to form a whole plate (not shown in the Figure). The common electrode 404 is laminated with the pixel electrode via a first insulating layer 403. The gate line 501 is disposed under the pixel electrode. The data line (not shown in the figure) is perpendicular to the gate line 501 and is in parallel with the common electrode 404. As shown in Figure 9, the conductive layer 406 is deposited on the common electrode 404 at the opaque region of the pixel electrode. Optionally, the conductive layer 406 forms a ring structure around the transparent region of the pixel electrode. Optionally, the conductive layer is a composite metal structure, and preferably the conductive layer may have a Mo/AlNd double-layer structure (not shown).

[0037] The following advantages or beneficial effects are provided according to the embodiments of the present invention:

In the embodiments of the present invention, a metal layer with the double-layer structure is deposited on the common electrode at the opaque region of the pixel electrode to form the ring structure around the pixel electrode. In this way, the resistance of the common electrode is reduced, the problem that the touch signal can't be detected at the center of the touch panel is solved, and the sensitivity of the touch panel is improved. Furthermore, the conductive layer can be used as a shading stripe when the conductive layer is made of opaque materials and deposited on the common electrode at the opaque region of the pixel electrode, thus improving and the luminance of backlight.

[0038] The present invention is not limited to the above-mentioned embodiments, but the invention can also be enforced in other various modified embodiments. It should therefore be appreciated that variations may be made in these embodiments by those skilled in the art without departing from the scope of the present invention as defined by the following claims.

Claims

1. An array substrate, comprising: a plurality of pixel units, wherein each of the pixel units comprises a pixel electrode, a common electrode insulated from the pixel electrode, and a conductive layer electrically connected in parallel to the common electrode.
2. The array substrate according to claim 1, wherein the conductive layer is formed on an opaque region of the pixel electrode.
3. The array substrate according to claim 2, wherein the conductive layer is a metal layer.
4. The array substrate according to claim 3, wherein the conductive layer has a composite structure.
5. The array substrate according to claim 4, wherein the conductive layer has a Mo/AlNd double metal layer structure.
6. The array substrate according to any one of Claims 1-5, wherein the conductive layer is formed in a ring structure around a transparent region of the pixel electrode.
7. The array substrate according to claim 1 or claim 6, wherein the conductive layer is formed on a transparent region of the pixel electrode, and the conductive layer is a transparent conductive layer.
8. A touch liquid crystal display panel, comprising a first substrate, a second substrate, and a liquid crystal

layer disposed between the first substrate and the second substrate, wherein the first substrate is the array substrate according to any one of Claims 1-7, the second substrate is a color film substrate.

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9. The touch liquid crystal display panel according to claim 8, wherein the touch liquid crystal display panel is an In-Plane Switching display panel or a Fringe Field Switching display panel.

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10. A manufacturing method of an array substrate, comprising:

providing a substrate;

disposing a gate electrode on the substrate;

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disposing a gate insulating layer on the gate electrode;

disposing a source electrode, a drain electrode, a pixel electrode and a common electrode on the gate insulating layer;

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depositing a conductive layer on the common electrode, wherein the conductive layer is electrically connected in parallel to the common electrode.

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11. The manufacturing method according to claim 10, wherein the conductive layer is deposited at an opaque region of the pixel electrode.

12. The manufacturing method according to claim 11, wherein the conductive layer has a Mo/AlNd double metal layer structure.

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13. The manufacturing method according to claim 10, wherein the conductive layer is deposited at a transparent region of the pixel electrode, and the conductive layer is a transparent conductive layer.

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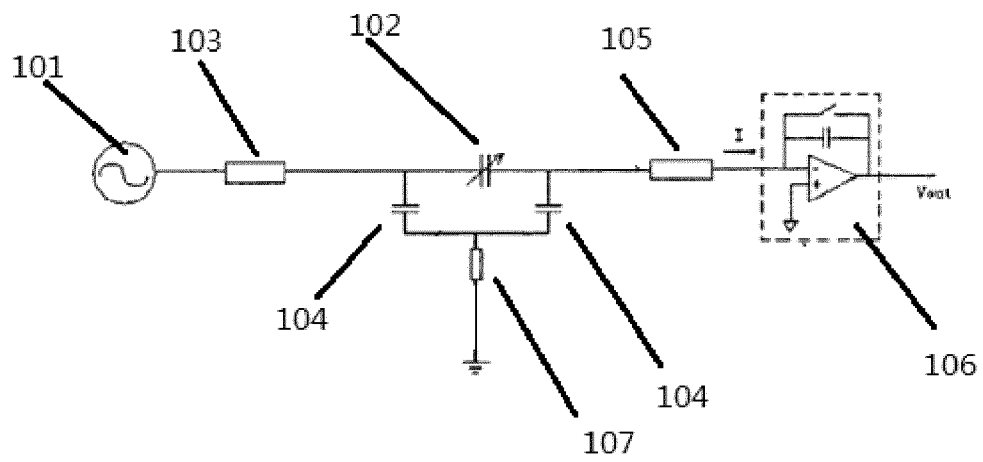


Fig. 1

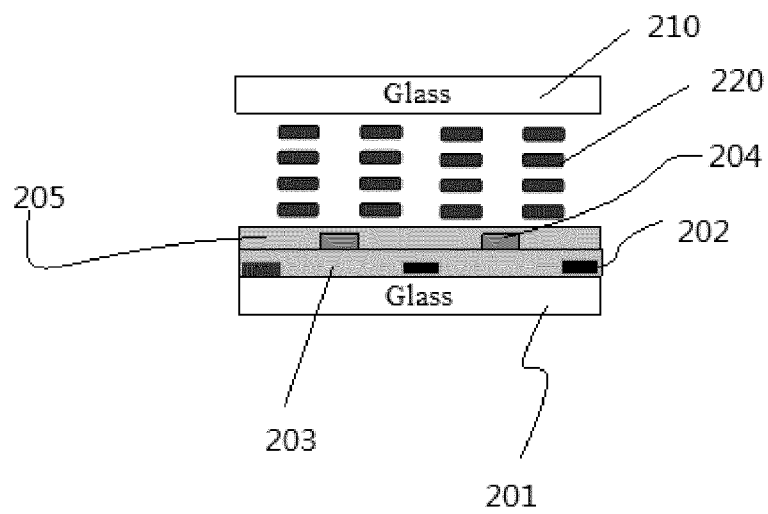


Fig. 2

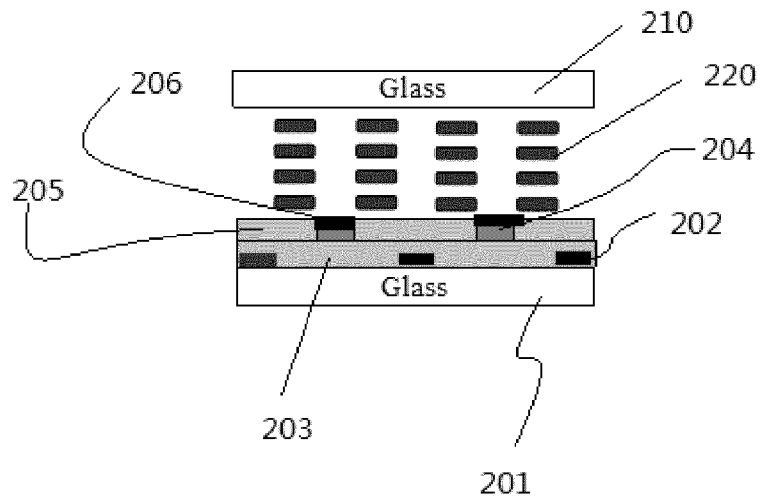


Fig. 3

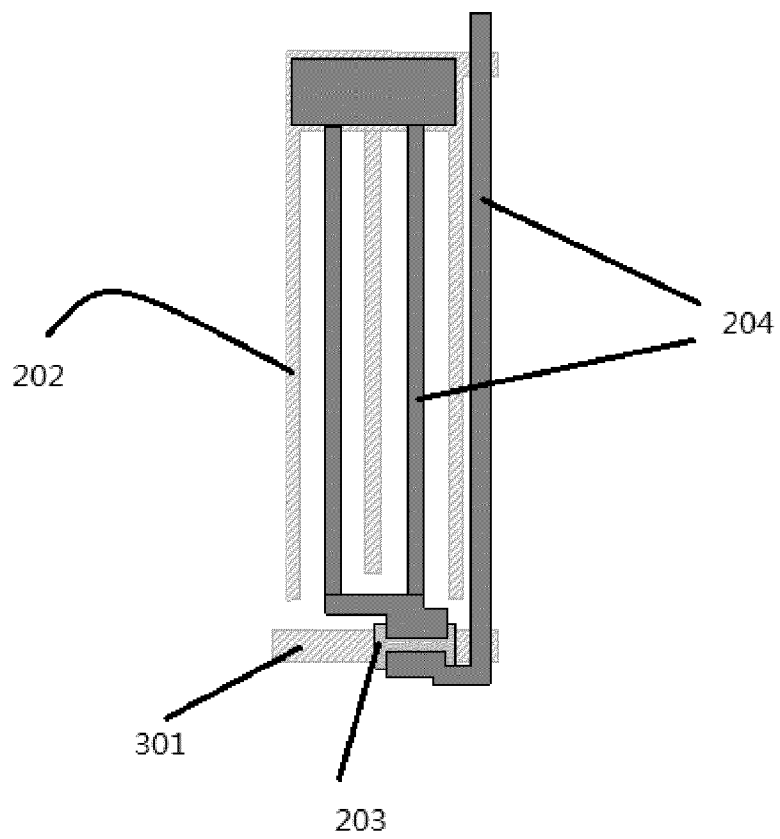


Fig. 4

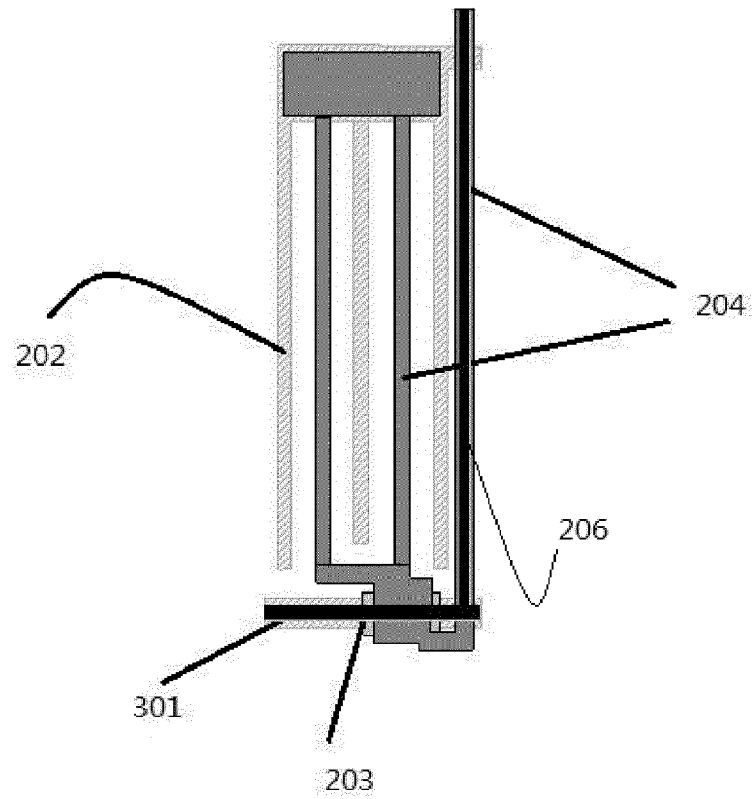


Fig. 5

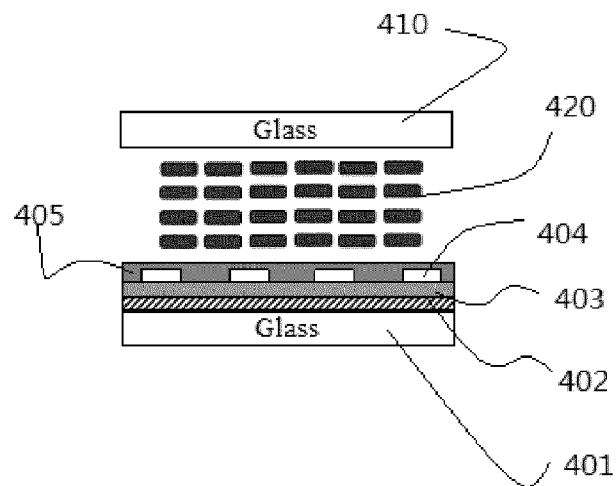


Fig. 6

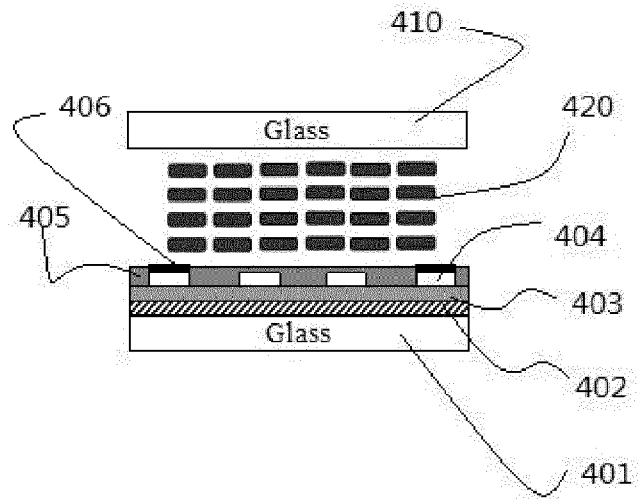


Fig. 7

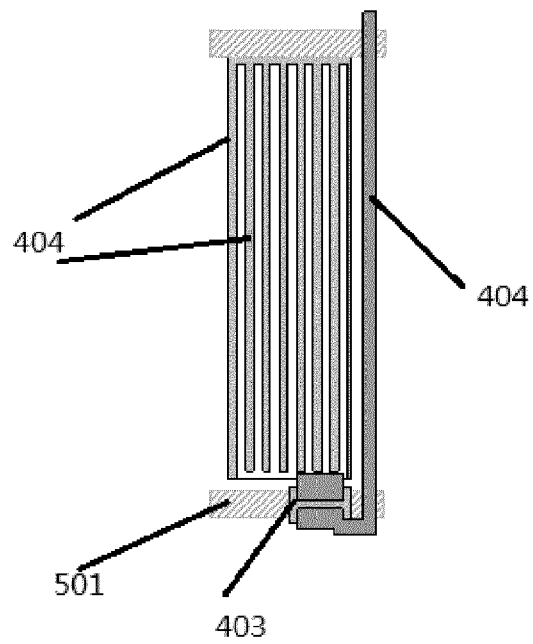


Fig. 8

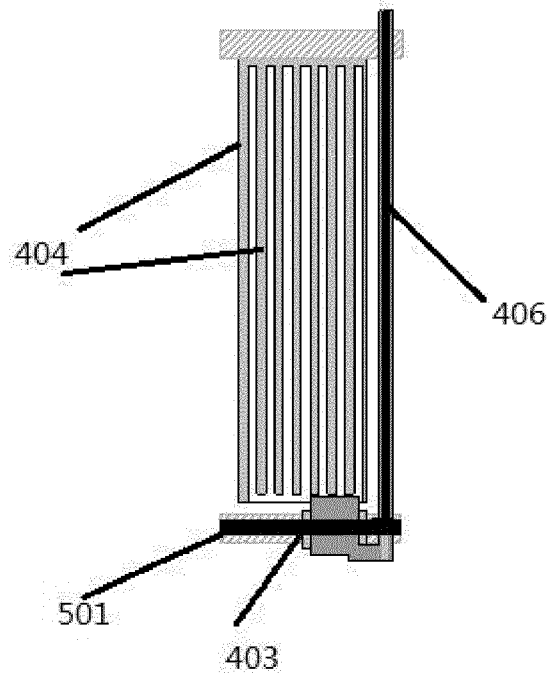


Fig. 9



EUROPEAN SEARCH REPORT

Application Number
EP 13 19 3890

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	US 2012/327353 A1 (DOI TAKASHI [JP] ET AL) 27 December 2012 (2012-12-27)	1-4, 6-11,13	INV. G02F1/1333
Y	* the whole document *	5,12	G02F1/1343 G06F3/041
Y	US 2012/268396 A1 (KIM MIN-SU [KR] ET AL) 25 October 2012 (2012-10-25) * paragraph [0066] *	5,12	ADD. G02F1/1362
E	EP 2 743 752 A1 (LG DISPLAY CO LTD [KR]) 18 June 2014 (2014-06-18) * paragraph [0027] - paragraph [0033]; figures 2,4 * * paragraph [0070] - paragraph [0099]; figures 5-13 *	1-3,8-11	
			TECHNICAL FIELDS SEARCHED (IPC)
			G02F G06F
The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 26 September 2014	Examiner Ammerlahn, Dirk
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			

EPO FORM 1503 03.82 (P04C01)

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 13 19 3890

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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
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26-09-2014

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EPO FORM P0459

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

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Patent documents cited in the description

- CN 201310175657 [0001]

专利名称(译)	阵列基板，触摸液晶显示面板及其制造方法		
公开(公告)号	EP2804043A1	公开(公告)日	2014-11-19
申请号	EP2013193890	申请日	2013-11-21
[标]申请(专利权)人(译)	上海天马微电子有限公司 天马微电子股份有限公司		
申请(专利权)人(译)	上海天马微电子有限公司. 天马微电子股份有限公司.		
当前申请(专利权)人(译)	上海天马微电子有限公司. 天马微电子股份有限公司.		
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CPC分类号	G02F1/13338 G02F1/134363 G02F2001/134372 G02F2001/13629 G02F2201/121 G06F3/0412 G06F3/0445 G06F2203/04103 G02F1/1333 G06F3/044		
优先权	201310175657.7 2013-05-13 CN		
外部链接	Espacenet		

摘要(译)

阵列基板包括多个像素单元，每个像素单元包括彼此绝缘的像素电极（202）和公共电极（204），以及并联电连接的导电层（206）公共电极。双层金属层沉积在公共电极上的像素电极的不透明区域处，以在像素电极的透明区域周围形成环形结构，从而降低公共电极的电阻。环结构可以是U形，半环形或全环形。

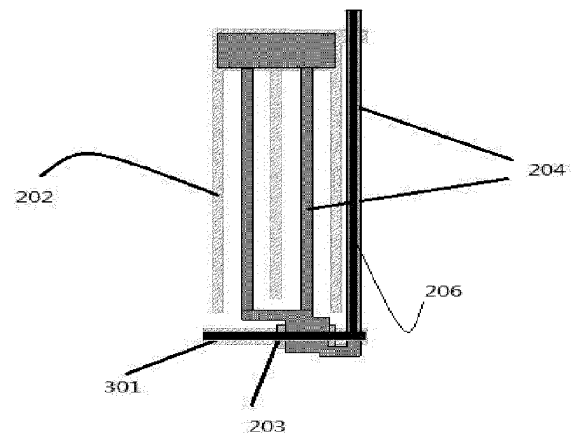


Fig. 5