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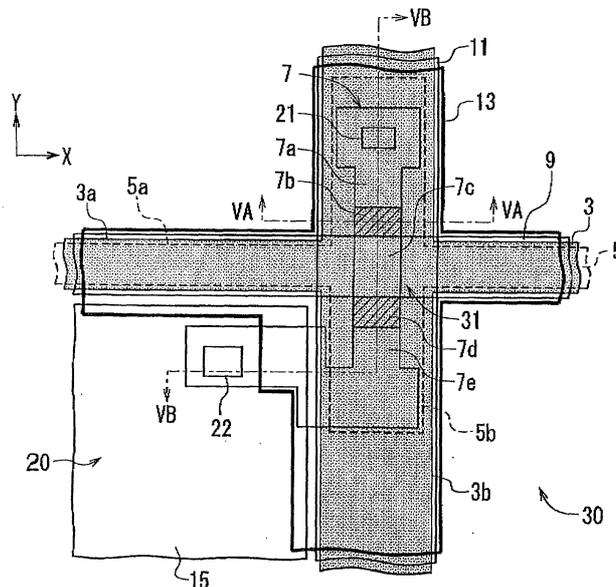
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(54) **TFT array substrate and active-matrix addressed liquid-crystal display device**

(57) A TFT array substrate (30) is provided, which effectively blocks the light traveling toward the active layers of the TFTs. A patterned first light-shielding layer (3) is formed between a transparent plate and TFTs in such a way as to overlap with the active layers of the TFTs. A patterned third light-shielding layer (13) is formed to cover the TFTs on the opposite side to the plate with respect to the TFTs. The third light-shielding layer (13) has the first parts extending along the rows of

the matrix and the second parts extending along the columns thereof. A patterned second light-shielding layer (5) is formed between the first light-shielding layer and the TFTs. The second light-shielding layer (5) has a light-absorbing property, which absorbs the light entering the inside of the substrate. A patterned fourth light-shielding layer, which has a light-absorbing property, may be additionally provided between the TFTs and the third light-shielding layer.

**FIG. 4**



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EUROPEAN SEARCH REPORT

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The present search report has been drawn up for all claims			
Place of search		Date of completion of the search	Examiner
THE HAGUE		3 December 2003	Gill, R
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone                      Y : particularly relevant if combined with another document of the same category                      A : technological background                      O : non-written disclosure                      P : intermediate document</p> <p>T : theory or principle underlying the invention                      E : earlier patent document, but published on, or after the filing date                      D : document cited in the application                      L : document cited for other reasons</p> <p>&amp; : member of the same patent family, corresponding document</p>			

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**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

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外部链接	<a href="#">Espacenet</a>		

摘要(译)

提供TFT阵列基板 ( 30 ) , 其有效地阻挡朝向TFT的有源层行进的光。图案化的第一遮光层 ( 3 ) 形成在透明板和TFT之间, 以便与TFT的有源层重叠。形成图案化的第三遮光层 ( 13 ) 以覆盖相对于TFT的与板相对的一侧上的TFT。第三遮光层 ( 13 ) 具有沿着矩阵的行延伸的第一部分和沿着其列延伸的第二部分。在第一遮光层和TFT之间形成图案化的第二遮光层 ( 5 ) 。第二遮光层 ( 5 ) 具有光吸收特性, 其吸收进入基板内部的光。可以在TFT和第三遮光层之间另外设置具有光吸收特性的图案化的第四遮光层。

