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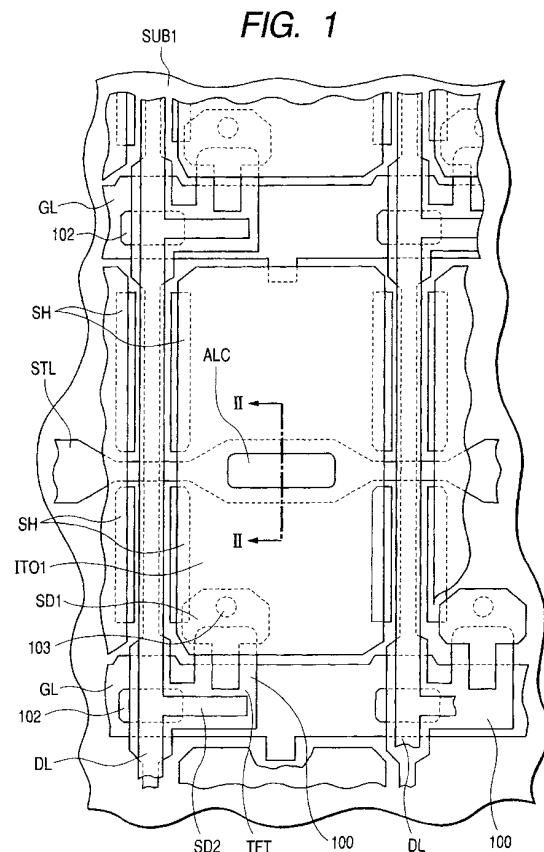
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(54) **Liquid crystal display element having controlled storage capacitance**

(57) A liquid crystal display element includes a pair of substrates (SUB1, SUB2), a liquid crystal layer (CL) sandwiched therebetween, gate lines (GL) and drain lines (DL) disposed to intersect the gate lines disposed on one of the substrates, thin film transistors (TFT) disposed at the intersections of the gate and drain lines, pixel electrodes (ITO1) disposed in an area surrounded by two adjacent gate lines and two adjacent drain lines, a common electrode (ITO2) disposed on the other substrate. The liquid crystal display element is also provided with storage lines (STL) disposed below the pixel electrodes with an insulating layer interposed therebetween to form a capacitance therebetween. Each of the pixel electrodes is formed with an opening (ALC) in a portion thereof facing the storage lines.



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EUROPEAN SEARCH REPORT

Application Number
EP 01 10 5303

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
X	US 5 040 875 A (NOGUCHI KESAO) 20 August 1991 (1991-08-20) * figures 3A,3B,4,5 * * column 3, line 9-30 * * column 4, line 47-67 * * column 5, line 9-21 *	4	G02F1/1362 G02F1/1343
A	---	1-3	
A	US 5 368 915 A (UEDA TOHRU) 29 November 1994 (1994-11-29) * abstract; figures 1-3 * * column 3, line 41-59 * * column 4, line 6-14 * * column 5, line 44-53 *	1-3,5-10	
A	US 6 022 753 A (LEE WON-HEE ET AL) 8 February 2000 (2000-02-08) * figures 1,2 * * column 1, line 10-26 * * column 2, line 61 - column 4, line 6 *	1-3,5-10	
A	PATENT ABSTRACTS OF JAPAN vol. 1999, no. 04, 30 April 1999 (1999-04-30) & JP 11 015022 A (NEC CORP), 22 January 1999 (1999-01-22) * abstract; figures 1,2,16 *	1-3,5-10	TECHNICAL FIELDS SEARCHED (Int.Cl.7) G02F
The present search report has been drawn up for all claims			
Place of search MUNICH		Date of completion of the search 3 July 2003	Examiner Kentischer, F
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

专利名称(译)	具有受控存储电容的液晶显示元件		
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其他公开文献	EP1136873B1 EP1136873A2		
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摘要(译)

液晶显示元件包括一对基板 (SUB1 , SUB2) , 夹在其间的液晶层 (CL) , 设置为与设置在其中一个基板上的栅极线交叉的栅极线 (GL) 和漏极线 (DL) , 薄膜晶体管 (TFT) 设置在栅极和漏极线的交叉处 , 像素电极 (ITO1) 设置在由两个相邻栅极线和两个相邻漏极线围绕的区域中 , 公共电极 (ITO2) 设置在另一个基板上。液晶显示元件还设置有设置在像素电极下方的存储线 (STL) , 其间插入有绝缘层以在它们之间形成电容。每个像素电极在其面对存储线的部分中形成有开口 (ALC) 。

