



EUROPEAN PATENT APPLICATION
published in accordance with Art. 153(4) EPC

(43) Date of publication:
03.06.2015 Bulletin 2015/23

(51) Int Cl.:
G02F 1/1343 ^(2006.01) **G02F 1/13** ^(2006.01)
G09G 3/00 ^(2006.01)

(21) Application number: **12878646.4**

(86) International application number:
PCT/CN2012/086471

(22) Date of filing: **12.12.2012**

(87) International publication number:
WO 2014/015621 (30.01.2014 Gazette 2014/05)

(84) Designated Contracting States:
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR
Designated Extension States:
BA ME

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(30) Priority: **27.07.2012 CN 201210265619**

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(54) **ARRAY SUBSTRATE, LIQUID CRYSTAL DISPLAY PANEL AND LIQUID CRYSTAL DISPLAY**

(57) An array substrate(6), a liquid crystal display panel and a liquid crystal display, and the array substrate (6), corresponding to a plurality of pixel regions, each of which comprises a second electrode(2) and a third electrode(3) on a side close to a liquid crystal-layer(30), the second electrode(2) comprising at least one second electrode strip, the third electrode(3) comprising at least one third electrode strip; each of the pixel regions of the array

substrate(6) further comprising a fourth electrode(4) on the side close to the liquid crystal layer(30), the fourth electrode(4) comprising at least one fourth electrode strip located between the second electrode strip and the third electrode strip that are adjacent; and an electric potential of the fourth electrode(4) falling between an electric potential of the second electrode(2) and an electric potential of the third electrode(3).

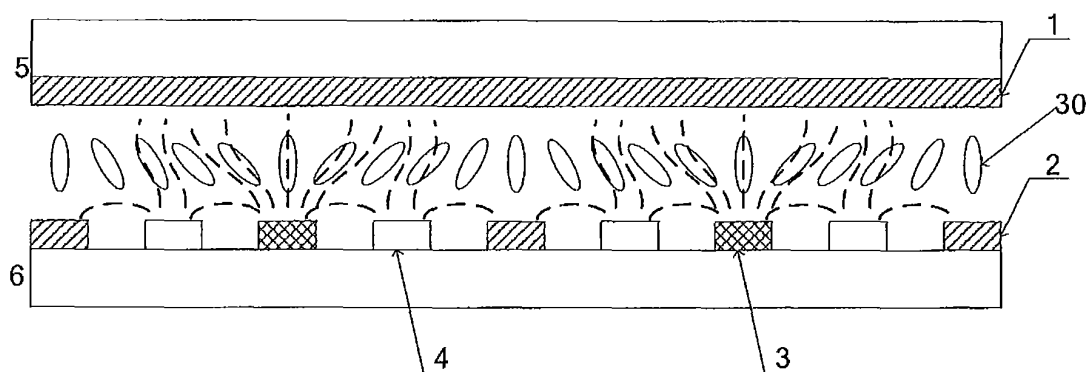


Fig. 3

Description

TECHNICAL FIELD

[0001] Embodiments of the present invention relate to an array substrate, a liquid crystal display panel and a liquid crystal display.

BACKGROUND

[0002] In a thin film transistor liquid crystal display (TFT-LCD), by means of utilizing the intensity variation of an electric field across a liquid crystal layer, the orientation of liquid crystal molecules is changed, and thus the strength of transmitted light is controlled to display an image. Generally speaking, a complete liquid crystal display panel must have a backlight module, a polarizer, an upper substrate and a lower substrate, and a layer of liquid crystal molecules filled in a cell composed of the two substrates.

[0003] An electric field generated in a liquid crystal display is substantially perpendicular to a substrate plane, and is used to control the state of liquid crystal molecules. This liquid crystal display is generally referred to as a vertical electric field type liquid crystal display. In the vertical electric field type liquid crystal display, a first electrode is formed on a lower substrate sandwiching a liquid crystal layer, a second electrode is formed on an upper substrate, and after a voltage is applied to them, an electric field substantially perpendicular to the substrate plane that begins from the first electrode and guides to the second electrode will be formed.

[0004] An electric field generated in a liquid crystal display is substantially parallel to a substrate plane, and is used to control the state of liquid crystal molecules. This liquid crystal display is generally referred to as a transverse electric field type liquid crystal display. In the transverse electric field type liquid crystal display, a first electrode and a second electrode are usually formed on a lower substrate sandwiching a liquid crystal layer, and after a voltage is applied to them, an electric field substantially parallel to the substrate plane that begins from the first electrode and guides to the second electrode will be formed.

[0005] In prior art, there is also a case that a liquid crystal display with a vertical electric field and a transverse electric field is manufactured by combining advantages of the vertical electric field and the transverse electric field. As shown in FIG. 1, the liquid crystal display includes an upper substrate 5 and a lower substrate 6, and a liquid crystal layer 30 filled between the upper substrate 5 and the lower substrate 6. Herein, the upper substrate 5 includes a first electrode (common electrode) 1 on a side close to the liquid crystal layer 30, and the lower substrate 6 includes a second electrode (common electrode) 2 and a third electrode (pixel electrode) 3 that are located on a side close to the liquid crystal layer 30. After a voltage is applied, the first electrode 1 and the second

electrode 2 are equal in electric potential, and no electric field is generated between them. The first electrode 1 and the third electrode 3 are unequal in electric potential, and a vertical electric field perpendicular to the substrate plane is generated. The second electrode 2 and the third electrode 3 are unequal in electric potential, and a transverse electric field parallel to the substrate plane is generated.

[0006] Compared with the transverse electric field type liquid crystal display, the number of bright and dark stripes in the liquid crystal display with the vertical electric field and the transverse electric field can be reduced. However, in a space region between the second electrode 2 and the third electrode 3 in this substrate, the deflection degree of liquid crystal molecules is still inadequate, thereby resulting in a fact that a corresponding zone is not bright enough. Namely, the display quality of the liquid crystal display may be affected. In this case, if an adequate brightness needs to be ensured, then it is necessary for a driving voltage of the second electrode 2 and the third electrode 3 to be increased.

SUMMARY

[0007] According to embodiments of the present invention, there are provided an array substrate, a liquid crystal display panel and a liquid crystal display. There are a vertical electric field and a transverse electric field in the display panel at the same time, so that the vertical electric field distribution is relatively uniform, and the display quality of the liquid crystal display is improved.

[0008] According to one aspect of an embodiment of the invention, there is provided an array substrate, corresponding to a plurality of pixel regions, each of which comprises a second electrode and a third electrode on a side close to a liquid crystal layer, the second electrode comprising at least one second electrode strip, the third electrode comprising at least one third electrode strip; each of the pixel regions of the array substrate further comprising a fourth electrode on the side close to the liquid crystal layer, the fourth electrode comprising at least one fourth electrode strip located between the second electrode strip and the third electrode strip that are adjacent; and an electric potential of the fourth electrode falling between an electric potential of the second electrode and an electric potential of the third electrode.

[0009] In an embodiment of the invention, in each of the pixel regions of the array substrate, there are more than one for each of the second electrode strip, the third electrode strip and the fourth electrode strip, and the second electrode strips are electrically connected with each other, the third electrode strips are electrically connected with each other, and the fourth electrode strips are electrically connected with each other.

[0010] In an embodiment of the invention, the second electrode strip, the third electrode strip and the fourth electrode strip are different from each other in a width.

[0011] In an embodiment of the invention, the second

electrode strip, the third electrode strip and the fourth electrode strip are in the same plane.

[0012] In an embodiment of the invention, the fourth electrode is in an electrically suspended state.

[0013] In an embodiment of the invention, a width of a spacing between the second electrode strip and the fourth electrode strip is different from a width of a spacing between the third electrode strip and the fourth electrode strip.

[0014] In an embodiment of the invention, widths of spacing between any adjacent the second electrode strip and the fourth electrode strip are different from each other, and widths of spacing between any adjacent the third electrode strip and the fourth electrode strip are different from each other.

[0015] In an embodiment of the invention, the second electrode strip, the third electrode strip and the fourth electrode strip each are bent into a V shape.

[0016] According to another aspect of an embodiment of the invention, there is provided a liquid crystal display panel, comprising an upper substrate and a lower substrate cell-assembled, and a liquid crystal layer filled between the upper substrate and the lower substrate, an initial orientation state of liquid crystal molecules in the liquid crystal layer is perpendicular to the upper substrate and the lower substrate; the lower substrate is the above array substrate.

[0017] In an embodiment of the invention, the upper substrate comprises a first electrode on a side close to the liquid crystal layer.

[0018] In an embodiment of the invention, the first electrode and the second electrode are connected to each other.

[0019] In an embodiment of the invention, the upper substrate further comprises: a protective layer formed on the first electrode.

[0020] According to still another aspect of an embodiment of the invention, there is provided a liquid crystal display, comprising the above liquid crystal display panel.

[0021] With respect to the array substrate, the liquid crystal display panel and the liquid crystal display provided by embodiments of the invention, at least one fourth electrode is provided between the second electrode and the third electrode of the array substrate separated from each other, and the fourth electrode is not connected to each of the second electrode and the third electrode. After a voltage is applied, an electric field is generated between the third electrode and the fourth electrodes on two sides of it, and electric fields are generated between the fourth electrode and the second electrode and the third electrode on two sides of it, respectively. Thereby, it is possible that the deflection degree of liquid crystal molecules in the space region between the second electrode and the third electrode is increased without increasing the driving voltage of the second electrode and the third electrode, and accordingly, the luminance of a corresponding region is enhanced. In turn, the display quality of the liquid crystal display panel and the liquid crystal

display can be improved. Further, a first electrode that is connected to a second electrode may be provided on a color filter substrate, so that a vertical electric field can be generated between the first electrode and each of the third electrode and the fourth electrode. Thus, the number of bright and dark stripes in the liquid crystal display can be reduced.

BRIEF DESCRIPTION OF THE DRAWINGS

[0022] In order to clearly illustrate the technical solution of the embodiments of the invention, the drawings of the embodiments will be briefly described in the following; it is obvious that the described drawings are only related to some embodiments of the invention and thus are not limitative of the invention.

FIG. 1 is a schematically sectional view illustrating a structure of a liquid crystal display panel in prior art;

FIG. 2 is a schematically sectional view illustrating a structure of an array substrate provided by an embodiment of the invention;

FIG. 3 is a schematically sectional view illustrating a structure of a liquid crystal display panel provided by another embodiment of the invention;

FIG. 4 is a schematically sectional view illustrating another structure of a liquid crystal display panel provided by another embodiment of the invention;

FIG. 5 is a schematically top view illustrating a structure of a lower substrate of a liquid crystal display panel provided by an embodiment of the invention;

FIG. 6 is a diagram illustrating an equivalent circuit of a lower substrate of a liquid crystal display panel provided by an embodiment of the invention;

FIG. 7 is a schematically top view illustrating a structure of electrode strips of a lower substrate of a liquid crystal display panel provided by an embodiment of the invention; and

FIG. 8 is a schematically top view illustrating a structure of electrode strips of a lower substrate of still another liquid crystal display panel provided by an embodiment of the invention.

DETAILED DESCRIPTION

[0023] In order to make objects, technical details and advantages of the embodiments of the invention apparent, the technical solutions of the embodiment will be described in a clearly and fully understandable way in collection with the drawings related to the embodiments of the invention. It is obvious that the described embodiments are just a part but not all of the embodiments of the invention. Based on the described embodiments herein, those skilled in the art can obtain other embodiment(s), without any inventive work, which should be within the scope of the invention.

[0024] An array substrate provided by an embodiment of the invention, as shown in FIG. 2, corresponds to a

plurality of pixel regions, and the array substrate 6 includes a second electrode 2 and a third electrode 3 that are located on a side close to the liquid crystal layer 30 in correspondence to each of the pixel regions. The second electrode 2 includes at least one second electrode strip, and the third electrode 3 includes at least one third electrode strip. For example, the electrode strip here may be a comb-teeth electrode portion of a comb-shaped electrode. For example, the second electrode strip and the third electrode strip are disposed alternately on the array substrate 6.

[0025] The array substrate 6 further includes a fourth electrode 4 that is located on the side close to the liquid crystal layer 30 in correspondence to each of the pixel regions. The fourth electrode 4 includes at least one fourth electrode strip that is located between the second electrode strip and the third electrode strip that are adjacent; and an electric potential of the fourth electrode falls between an electric potential of the second electrode and an electric potential of the third electrode.

[0026] In an embodiment of the invention, for the array substrate 6, in each of the pixel regions, there are more than one for each of the second electrode strip, the third electrode strip and the fourth electrode strip, and moreover, the second electrode strips are electrically connected to each other, the third electrode strips are electrically connected to each other, and the fourth electrode strips are electrically connected to each other.

[0027] The second electrode strip, the third electrode strip and the fourth electrode strip may be in the same plane.

[0028] In an embodiment of the invention, the fourth electrode is in an electrically suspended state.

[0029] In an embodiment of the invention, the second electrode strip of the second electrode 2, the third electrode strip of the third electrode 3 and the fourth electrode strip of the fourth electrode 4 may have the same width or different widths. With reference to FIG. 7, a width a of the second electrode strip, a width b of the fourth electrode strip, and a width c of the third electrode strip are not the same as each other.

[0030] In an embodiment of the invention, the second electrode strip of the second electrode 2, the third electrode strip of the third electrode 3 and the fourth electrode strip of the fourth electrode 4 on the array substrate 6 may vary continuously in width on the plane. For example, each of the second electrode strip, the third electrode strip and the fourth electrode strip varies continuously in a manner of being wider in the upper part of the picture, and being narrower in the lower part of the picture. Of course, this is merely an example, and continuous variations in other form may also be possible.

[0031] In an embodiment of the invention, further, the second electrode strip of the second electrode 2, the third electrode strip of the third electrode 3 and the fourth electrode strip of the fourth electrode 4 on the array substrate 6 may take a form of a step on the plane. For example, each of the second electrode strip, the third electrode

strip and the fourth electrode strip is narrower in the upper part of the picture, wider in the lower part of the picture, and changed from narrow to wide gradually in steps. Certainly, this is merely an example, and stepwise changes in other forms may also be possible.

[0032] In an embodiment of the invention, a width of a spacing between the second electrode strip and the fourth electrode strip and a width of a spacing between the third electrode strip and the fourth electrode strip may be the same or different. In the embodiment of the invention, for example, as shown in FIG. 8, a space width d between the second electrode strip and the fourth electrode strip and a space width e between the third electrode strip and the fourth electrode strip are different. For example, in one pixel region, if a spacing on the left side of the picture is smaller than a spacing on the right side of the picture, the V-T property will be different, and the viewing angle property is better as well.

[0033] In an embodiment of the invention, widths of spacing between any adjacent the second electrode strip and the fourth electrode strip are different from each other, and widths of spacing between any adjacent the third electrode strip and the fourth electrode strip are different from each other.

[0034] In an embodiment of the invention, the second electrode strip, the third electrode strip and the fourth electrode strip each are bent into a V shape, and other bending shape, the shape of a folded line or the shape of a straight line may also be possible.

[0035] A liquid crystal display panel provided by an embodiment of the invention, as shown in FIG. 3 to FIG. 5, includes an upper substrate (it may also be referred to as a color filter substrate) 5 and a lower substrate (it may also be referred to as an array substrate) 6 that are cell-assembled, and a liquid crystal layer 30 filled between the upper substrate 5 and the lower substrate 6, and an initial orientation state of liquid crystal molecules in the liquid crystal layer is perpendicular to the upper substrate 5 and the lower substrate 6. For example, the liquid crystal molecules in the liquid crystal layer have a positive dielectric anisotropy.

[0036] In an example of the invention, the upper substrate 5 includes a first electrode 1 on a side close to the liquid crystal layer. In the embodiment, the first electrode 1 is set to be a common electrode (Com electrode). However, the first electrode 1 is not necessary, and the first electrode 1 may not be provided on the upper substrate 5. The lower substrate 6 corresponds to a plurality of pixel regions, and the lower substrate 6 includes a second electrode 2 and a third electrode 3 that are located on the side close to the liquid crystal layer 30 in each of the pixel regions. The second electrode 2 includes at least one second electrode strip, and the third electrode 3 includes at least one third electrode strip. For example, the second electrode strip and the third electrode strip may be disposed alternately.

[0037] In an embodiment of the invention, the lower substrate 6 further includes a fourth electrode 4 that is

located on the side close to the liquid crystal layer 30 in each of the pixel regions. The fourth electrode 4 includes at least one fourth electrode strip that is located between the second electrode strip and the third electrode strip that are adjacent; and an electric potential of the fourth electrode falls between an electric potential of the second electrode and an electric potential of the third electrode.

[0038] In an embodiment of the invention, the second electrode 2 is set to be a common electrode (Com electrode) as well, namely, the first electrode 1 and the second electrode 2 are connected to each other; but they may be arranged to not be connected with each other and have different electric potentials, either. The third electrode 3 is set to be a pixel electrode (Pix electrode). Where, when no voltage is applied, an orientation (i.e. an initial orientation) of liquid crystal molecules in the liquid crystal layer 30 is perpendicular to the upper substrate 5 and the lower substrate 6.

[0039] In an embodiment of the invention, for the lower substrate, in each of the pixel regions, there are more than one for each of the second electrode strip, the third electrode strip and the fourth electrode strip, and moreover, the second electrode strips are electrically connected to each, the third electrode strips are electrically connected to each, and the fourth electrode strips are electrically connected to each.

[0040] In an embodiment of the invention, the second electrode strip of the second electrode 2, the third electrode strip of the third electrode 3 and the fourth electrode strip of the fourth electrode 4 may have the same width or different widths. For example, with reference to FIG. 7, a width a of the second electrode strip, a width b of the fourth electrode strip, and a width c of the third electrode strip are not the same as each other.

[0041] In an example of the invention, the fourth electrode 4 is located on the same plane as the second electrode 2 and the third electrode 3. Moreover, between electrode strips of the second electrode 2 and the third electrode 3 that are adjacent, a plurality of electrode strips of the fourth electrode 4 may be provided. And, the fourth electrode 4 is not connected to each of the first electrode 1, the second electrode 2 and the third electrode 3, namely, the fourth electrode 4 is in an electrically suspended state. As such, under an electric field, an electric potential of the fourth electrode 4 falls between an electric potential of the first electrode 1 or the second electrode 2 and an electric potential of the third electrode 3.

[0042] For example, when a voltage is applied to the liquid crystal display panel, it is possible that a voltage V1 applied to the first electrode 1 and the second electrode 2 is set to be smaller than a voltage V2 applied to the fourth electrode 4, and the voltage V2 applied to the fourth electrode 4 is set to be smaller than a voltage V3 applied to the third electrode 3. That is, $V1 < V2 < V3$. An equivalent circuit diagram of a pixel region of the liquid crystal display panel is shown in FIG. 6, and within the pixel region defined by a gate line 11 and a data line 12 that cross over each other, there are applied three volt-

ages, which are V3 conducted to the third electrode 3 from the data line 12 via a TFT 13, V1 conducted to the second electrode 2 from the first electrode 1, and V2 separately conducted to the fourth electrode 4, respectively. The relationship among the three voltages satisfies the above formula $V1 < V2 < V3$.

[0043] After a voltage is applied, as shown in FIG. 3, the first electrode 1 and the second electrode 2 are equal in electric potential, and thus, no electric field is generated between them. A vertical electric field perpendicular to the substrate plane is generated between the third electrode 3 and the first electrode 1, and meanwhile, a transverse electric field parallel to the substrate plane is generated between the third electrode 3 and the fourth electrodes 4 on two sides of it too. Specifically, in the embodiment, still more, a vertical electric field perpendicular to the substrate plane is generated between the fourth electrode 4 and the first electrode 1, and meanwhile, a transverse electric field parallel to the substrate plane is generated between the fourth electrode 4 and the second electrode 2 and the third electrode 3 on two sides of it as well.

[0044] In general, in the liquid crystal panel provided by the embodiment, transverse electric fields are generated between the second electrode 2, the third electrode 3 and the fourth electrode 4, respectively, and it is possible that the deflection degree of liquid crystal molecules in the space region between the second electrode and the third electrode is increased without increasing the driving voltage of the second electrode and the third electrode, so as to enhance the luminance of corresponding regions. In turn, the display quality of the liquid crystal display panel and the liquid crystal display can be improved. A vertical electric field is generated between the third electrode 3 and the first electrode 1, a vertical electric field is generated between the fourth electrode 4 and the first electrode 1, so that the number of bright and dark stripes in the liquid crystal display can be reduced, and the display quality of the liquid crystal display panel can be improved accordingly as well.

[0045] In an embodiment of the invention, the second electrode strip of the second electrode 2, the third electrode strip of the third electrode 3 and the fourth electrode strip of the fourth electrode 4 on the lower substrate 6 may vary continuously in width on the plane. For example, each of the second electrode strip, the third electrode strip and the fourth electrode strip varies continuously in a manner of being wider in the upper part of the picture, and being narrower in the lower part of the picture. Of course, this is merely an example, and continuous variations in other forms may also be possible.

[0046] In an embodiment of the invention, further, the second electrode strip of the second electrode 2, the third electrode strip of the third electrode 3 and the fourth electrode strip of the fourth electrode 4 on the lower substrate 6 may be in a step form on the plane. For example, each of the second electrode strip, the third electrode strip and the fourth electrode strip is narrower in the upper part of

the picture, wider in the lower part of the picture, and changed from narrow to wide gradually in steps. Certainly, this is merely an example, and stepwise changes in other forms may also be possible. For example, the reduction in width of the second electrode strip, the third electrode strip and the fourth electrode strip is in the same direction, namely, each shrinks from the lower part to the upper part of the picture, and it may also be the opposite, namely, each shrinks from the upper part to the lower part of the picture.

[0047] In an embodiment of the invention, a width of a spacing between the second electrode strip of the second electrode 2 and the fourth electrode strip of the fourth electrode 4 and a width of a spacing between the third electrode strip of the third electrode 3 and the fourth electrode strip of the fourth electrode 4 may be the same or different. In the embodiment of the invention, for example, as shown in FIG. 8, a space width d between the second electrode strip of the second electrode 2 and the fourth electrode strip of the fourth electrode 4 and a space width e between the third electrode strip of the third electrode 3 and the fourth electrode strip of the fourth electrode 4 are different. In one pixel region, if a spacing on the left side of the picture is smaller than a spacing on the right side of the picture, the V-T property will be different, and the viewing angle property is better as well.

[0048] In an embodiment of the invention, widths of spacing between any adjacent the second electrode strip of the second electrode 2 and the fourth electrode strip of the fourth electrode 4 are different from each other, and widths of spacing between any adjacent the third electrode strip of the third electrode 3 and the fourth electrode strip of the fourth electrode 4 are different from each other.

[0049] In addition, the second electrode strip of the second electrode 2, the third electrode strip of the third electrode 3 and the fourth electrode strip of the fourth electrode 4 in the embodiment of the invention may each be bent into a V shape, and other bending shape, the shape of a folded line or the shape of a straight line may also be possible.

[0050] In the liquid crystal display panel provided by the embodiment of the invention, as shown in FIG. 4, the upper substrate 5 further includes a protective layer 7 formed on the first electrode 1. As compared with a structure without the protective layer 7, having the protective layer 7 enables a tilt angle of the electric field to be larger, so that the luminance of corresponding regions is enhanced. In turn, the liquid crystal display panel can be improved.

[0051] In addition, it is to be noted that, the lower substrate 6 corresponds to the above array substrate 6, the upper substrate 5 corresponds to the above color filter substrate 5, and thus same reference numerals are used for description.

[0052] A liquid crystal display provided by an embodiment of the invention includes the liquid crystal display panel provided by the above embodiment, and the struc-

ture of the liquid crystal display panel is the same as that in the above embodiment, whereby details are omitted here.

[0053] The embodiment of the invention being thus described, it will be obvious that the same may be varied in many ways. Such variations are not to be regarded as a departure from the spirit and scope of the invention, and all such modifications as would be obvious to those skilled in the art are intended to be included within the scope of the following claims.

Claims

1. An array substrate, corresponding to a plurality of pixel regions, each of which comprises a second electrode and a third electrode on a side close to a liquid crystal layer, the second electrode comprising at least one second electrode strip, the third electrode comprising at least one third electrode strip; each of the pixel regions of the array substrate further comprising a fourth electrode on the side close to the liquid crystal layer, the fourth electrode comprising at least one fourth electrode strip located between the second electrode strip and the third electrode strip that are adjacent; and an electric potential of the fourth electrode falling between an electric potential of the second electrode and an electric potential of the third electrode.
2. The array substrate claimed as claim 1, wherein, in each of the pixel regions of the array substrate, there are more than one for each of the second electrode strip, the third electrode strip and the fourth electrode strip, and the second electrode strips are electrically connected with each other, the third electrode strips are electrically connected with each other, and the fourth electrode strips are electrically connected with each other.
3. The array substrate claimed as claim 1 or 2, wherein, the second electrode strip, the third electrode strip and the fourth electrode strip are different from each other in a width.
4. The array substrate claimed as any one of claims 1 to 3, wherein, the second electrode strip, the third electrode strip and the fourth electrode strip are in the same plane.
5. The array substrate claimed as any one of claims 1 to 4, wherein, the fourth electrode is in an electrically suspended state.
6. The array substrate claimed as any one of claims 1 to 5, wherein, a width of a spacing between the second electrode strip and the fourth electrode strip is different from a width of a spacing between the third

electrode strip and the fourth electrode strip.

7. The array substrate claimed as claim 6, wherein, widths of spacing between any adjacent the second electrode strip and the fourth electrode strip are different from each other, and widths of spacing between any adjacent the third electrode strip and the fourth electrode strip are different from each other. 5
8. The array substrate claimed as claim 1, wherein, the second electrode strip, the third electrode strip and the fourth electrode strip each are bent into a V shape. 10
9. A liquid crystal display panel, comprising an upper substrate and a lower substrate cell-assembled, and a liquid crystal layer filled between the upper substrate and the lower substrate, an initial orientation state of liquid crystal molecules in the liquid crystal layer is perpendicular to the upper substrate and the lower substrate; wherein, the lower substrate is the array substrate claimed as any one of claims 1 to 8. 15
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10. The liquid crystal display panel claimed as claim 9, wherein, the upper substrate comprises a first electrode on a side close to the liquid crystal layer. 25
11. The liquid crystal display panel claimed as claim 10, wherein, the first electrode and the second electrode are connected to each other. 30
12. The liquid crystal display panel claimed as claim 10 or 11, wherein, the upper substrate further comprises:
a protective layer formed on the first electrode. 35
13. A liquid crystal display, comprising the liquid crystal display panel claimed as any one of claims 9 to 12. 40

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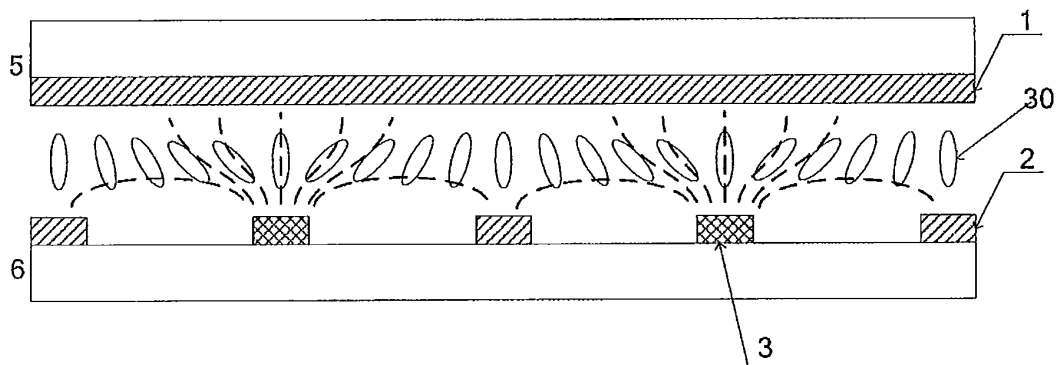


Fig. 1

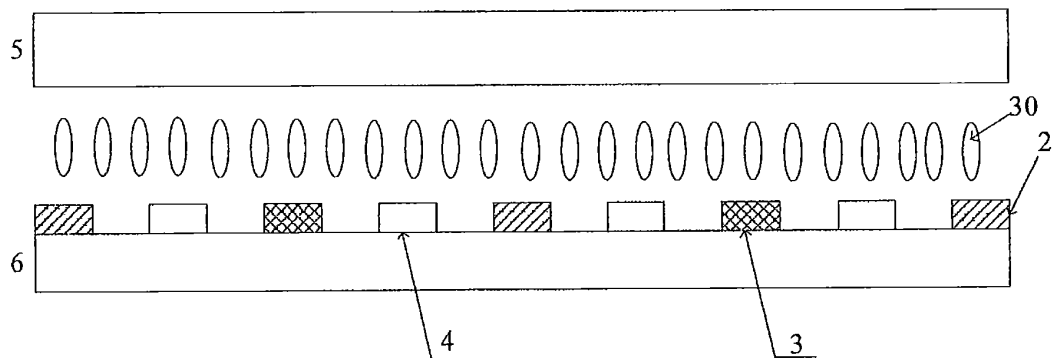


Fig. 2

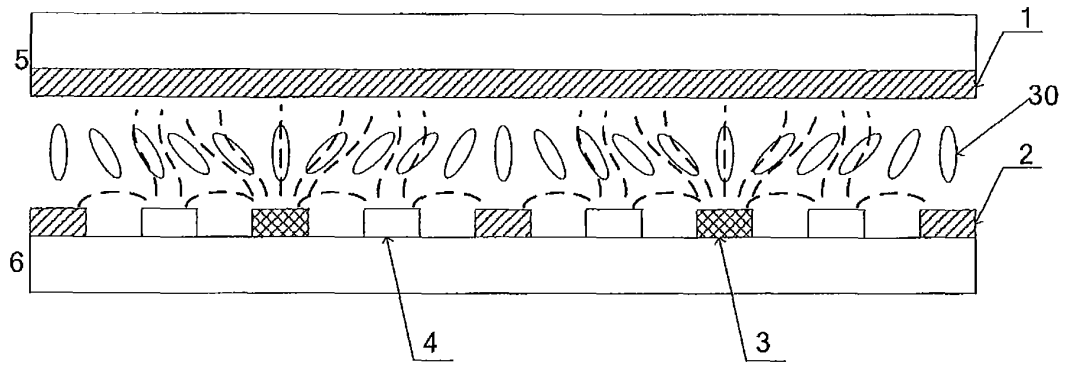


Fig. 3

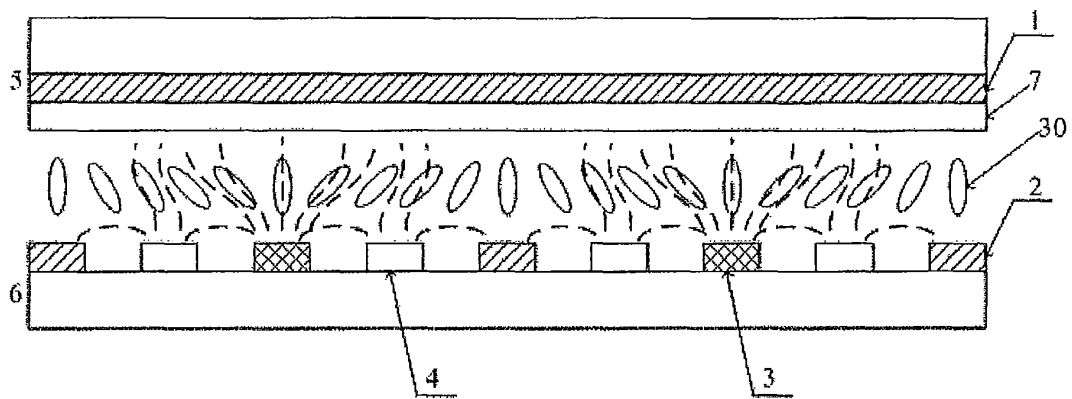


Fig. 4

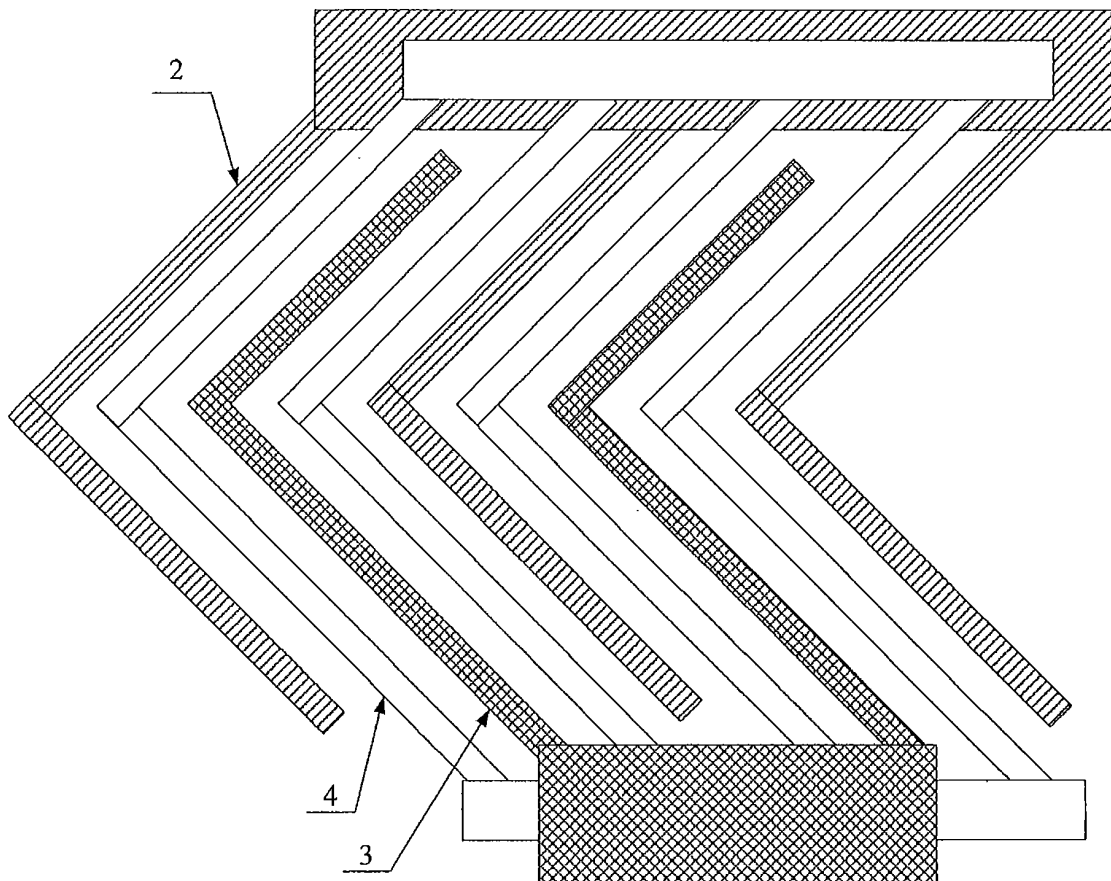


Fig. 5

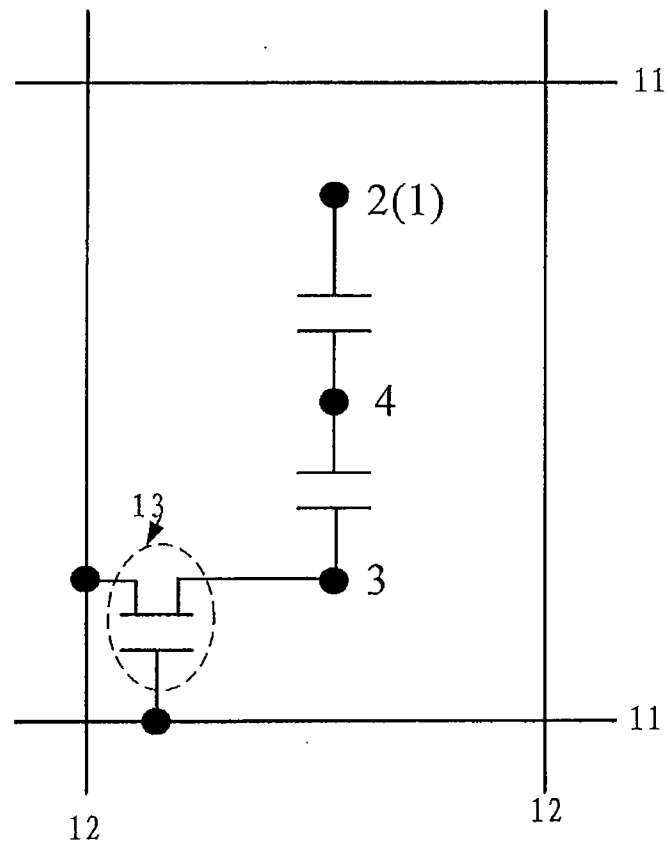


Fig. 6

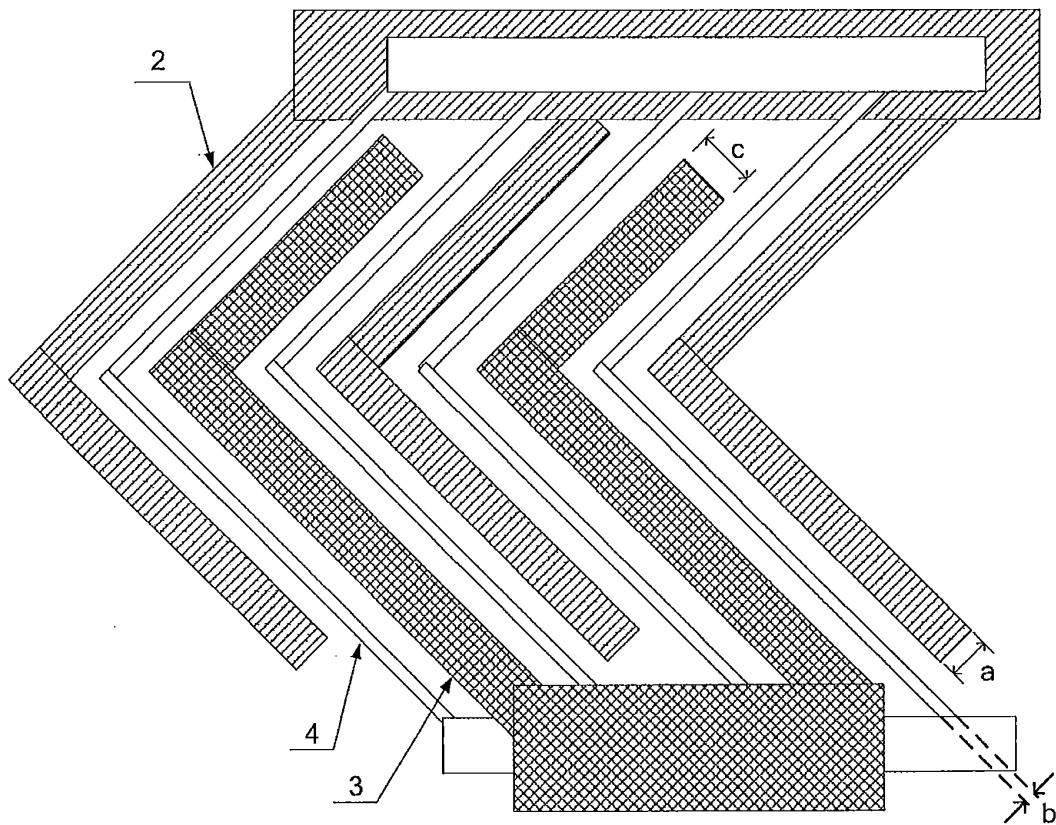


Fig. 7

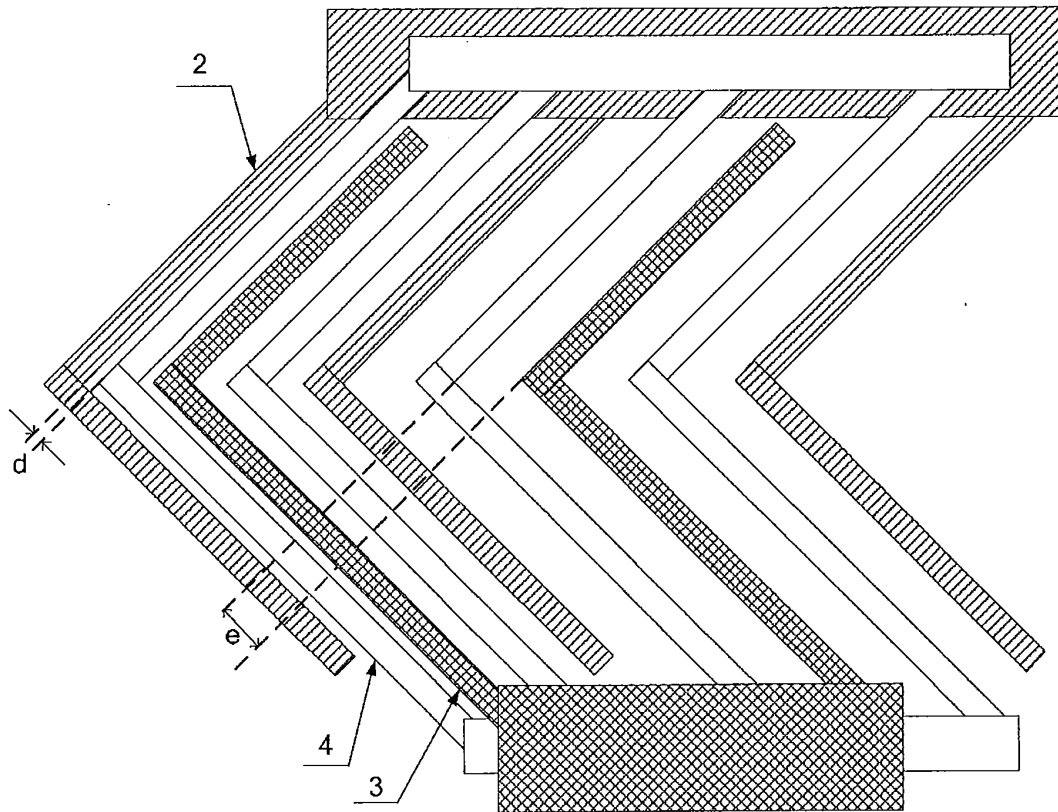


Fig. 8

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2012/086471

A. CLASSIFICATION OF SUBJECT MATTER

See the extra sheet

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC: G09G 3, G02F 1/13+

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

CNABS, CNTXT: electrode, inter-, voltage, potential, level, horizontal, electric field, width

EPODOC: VOLTAGE, POTENTIAL, WIDE, WIDTH, BREADTH, SPAC+, SUBSTRATE, BAS+

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
PX	CN 102789100 A (BOE TECHNOLOGY GROUP CO., LTD.), 21 November 2012 (21.11.2012), description, paragraphs [0033]-[0063], and figures 1-8	1-13
A	WO 2011086743 A1 (SHARP KK), 21 July 2011 (21.07.2011), description, paragraphs [0024]-[0106], and figures 1-6	1-13
A	WO 2011083616 A1 (SHARP KK), 14 July 2011 (14.07.2011), the whole document	1-13
A	CN 2705805 A (HONG FU JIN PRECISION INDUSTRY (SHENZHEN) CO., LTD. et al.), 22 June 2005 (22.06.2005), the whole document	1-13
A	JP 2002139736 A (SANYO ELECTRIC CO., LTD.), 17 May 2002 (17.05.2002), the whole document	1-13

☐ Further documents are listed in the continuation of Box C.☒ See patent family annex.

* Special categories of cited documents:	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"A" document defining the general state of the art which is not considered to be of particular relevance	
"E" earlier application or patent but published on or after the international filing date	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"O" document referring to an oral disclosure, use, exhibition or other means	
"P" document published prior to the international filing date but later than the priority date claimed	"&" document member of the same patent family

Date of the actual completion of the international search

22 April 2013 (22.04.2013)

Date of mailing of the international search report

02 May 2013 (02.05.2013)

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INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.

PCT/CN2012/086471

Patent Documents referred in the Report	Publication Date	Patent Family	Publication Date
CN 102789100 A	21.11.2012	None	
WO 2011086743 A1	21.07.2011	CN 102695983 A	26.09.2012
		EP 2525254 A1	21.11.2012
		US 2012293756 A1	22.11.2012
WO 2011083616 A1	14.07.2011	None	
CN 2705805 Y	22.06.2005	None	
JP 2002139736 A	17.05.2002	JP 3609712 B2	12.01.2005

Form PCT/ISA/210 (patent family annex) (July 2009)

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2012/086471

CONTINUATION (SECOND SHEET) A. CLASSIFICATION OF SUBJECT MATTER

G02F 1/1343 (2006.01) i

G02F 1/13 (2006.01) i

G09G 3/00 (2006.01) n

专利名称(译)	阵列基板，液晶显示面板和液晶显示器		
公开(公告)号	EP2878994A1	公开(公告)日	2015-06-03
申请号	EP2012878646	申请日	2012-12-12
[标]申请(专利权)人(译)	京东方科技集团股份有限公司		
申请(专利权)人(译)	京东方科技集团股份有限公司.		
当前申请(专利权)人(译)	京东方科技集团股份有限公司.		
[标]发明人	SUZUKI TERUAKI		
发明人	SUZUKI, TERUAKI		
IPC分类号	G02F1/1343 G02F1/13 G09G3/00		
CPC分类号	G02F1/134363 G02F2001/133742 G02F2001/134381 G09G3/3648 G09G2300/0426 G09G2300/0447 G09G2300/0852 G09G2320/0233 G02F1/134309 G02F1/13439		
优先权	201210265619.6 2012-07-27 CN		
其他公开文献	EP2878994B1 EP2878994A4		
外部链接	Espacenet		

摘要(译)

阵列基板(6)，液晶显示面板和液晶显示器，以及阵列基板(6)，对应于多个像素区域，每个像素区域包括第二电极(2)和第三电极(3)靠近液晶层(30)的一侧，第二电极(2)包括至少一个第二电极条，第三电极(3)包括至少一个第三电极条；阵列基板(6)的每个像素区域还包括靠近液晶层(30)的一侧的第四电极(4)，第四电极(4)包括位于第二电极之间的至少一个第四电极条电极条和相邻的第三电极条；第四电极(4)的电位落在第二电极(2)的电位和第三电极(3)的电位之间。

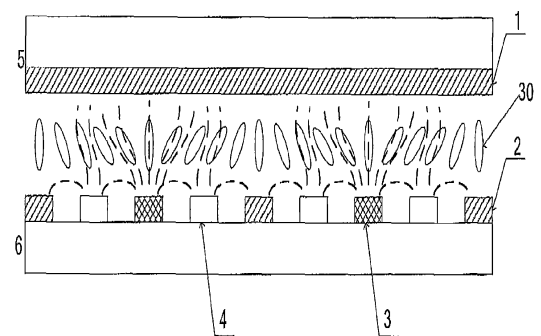


Fig. 3