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(54) **ARRAY SUBSTRATE, LIQUID CRYSTAL DISPLAY DEVICE AND ALIGNMENT RUBBING METHOD**

(57) Embodiments of the present invention provide an array substrate, a liquid crystal display apparatus and an alignment rubbing method. The array substrate comprises a gate line, a data line, and a pixel unit defined by the gate line and the data line intersecting each other, as well as an alignment film formed on the array substrate. The pixel unit each comprises a thin film transistor, a first electrode, and a second electrode provided with slits; and a first non-zero preset angle is present between a slit-direction of the second electrode and a data-line-direction, a second non-zero preset angle is present between a rubbing direction of the alignment film and the slit-direction of the second electrode, and an angle between the rubbing direction of the alignment film and the data-line-direction is greater than the second non-zero preset angle.

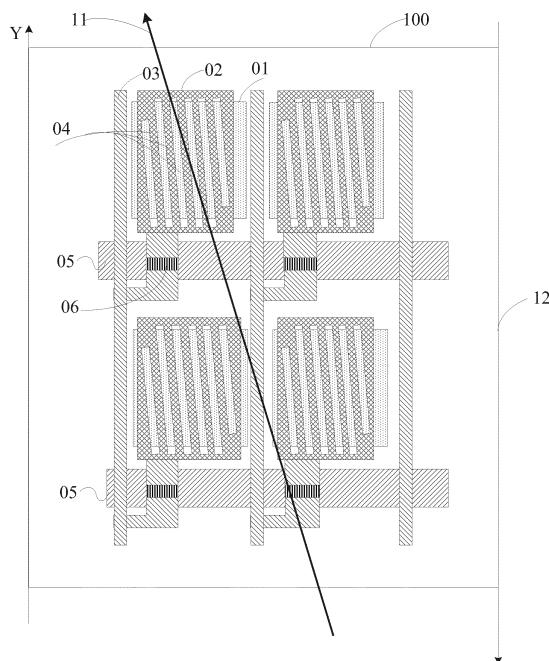


FIG.2

Description

TECHNICAL FIELD

[0001] Embodiments of the present invention relate to an array substrate, a liquid crystal display apparatus and an alignment rubbing method.

BACKGROUND

[0002] At present, liquid crystal displays (LCDs) with wide viewing angles mainly include those of an Advanced Super Dimension Switch (ADS) mode and an In-Plane Switching (IPS) mode. The ADS mode TFT-LCD forms a multi-dimensional electric field both a parallel electric field produced at edges of pixel electrodes on the same plane and a vertical electric field produced between a pixel electrode layer and a common electrode layer, so that liquid crystal molecules at all orientations, which are located directly above the electrodes and between the pixel electrodes in a liquid crystal cell, can be rotated and aligned, which enhances the work efficiency of planar-oriented liquid crystals and increases light transmittance. The Advanced-Super Dimensional Switching technology can improve the picture quality of TFT-LCDs and has advantages of high transmissivity, wide viewing angles, high opening ratio, low chromatic aberration, low response time, no push Mura, etc.

[0003] FIG. 1 shows a schematic illustration of an array substrate of an ADS mode LCD. On the base substrate, there are formed a plurality of pixel units arranged in an array form, and each pixel unit comprises a common electrode 01 (a plate-like electrode) and a pixel electrode 02 (a slit electrode comprising a plurality of slits) therein. The pixel electrode 02 is provided thereon with a plurality of slits 04 parallel to one another and parallel to a data line 03. After the formation of the pixel unit structure, a PI (polyimide) material is deposited on a surface thereof to form an alignment film, and alignment rubbing is required to be performed onto the alignment film to complete the manufacture of the array substrate. In FIG. 1, the data line 03 is taken as a reference line (in a longitudinal direction); in the coordinate system, the direction of the longitudinal coordinate Y is the same as the data-line-direction; the extending direction of the slits 04 of the pixel electrode 02 is the same as the data-line-direction; the forward direction 12 of the array substrate in the rubbing process is opposed to the Y-direction; and the rubbing direction 11 of the alignment film forms a certain angle with the slit-direction of the pixel electrode 02.

[0004] In the technique shown in FIG. 1, there is a comparatively small angle between the rubbing direction 11 of the alignment film and the opposite direction to the forward direction 12 of the array substrate, which results in a rather large relative velocity between the rubbing roller and the array substrate in the Y-direction during the relative movement of the rubbing roller and the array substrate, thus causes mutual damage between the rub-

bing roller and the array substrate, affects rubbing uniformity, and further leading to poor gradation for the prepared display device.

SUMMARY

[0005] Embodiments of the present invention provide an array substrate, a liquid crystal display apparatus and an alignment rubbing method, for enhancing the rubbing uniformity.

[0006] In one aspect of the present invention, there is provided an array substrate, comprising a gate line, a data line, and a pixel unit defined by the gate line and the data line intersecting each other, as well as an alignment film formed on the array substrate; wherein the pixel unit each comprises a thin film transistor, a first electrode, and a second electrode provided with slits; and wherein a first non-zero preset angle is present between a slit-direction of the second electrode and a data-line-direction, a second non-zero preset angle is present between a rubbing direction of the alignment film and the slit-direction of the second electrode, and an angle between the rubbing direction of the alignment film and the data-line-direction is greater than the second non-zero preset angle.

[0007] In another aspect of the present invention, there is provided a liquid crystal display apparatus, comprising: an array substrate and a color filter substrate assembled together to form a cell, wherein the array substrate is an array substrate as described above, and a rubbing direction of an alignment film on the color filter substrate is opposed to a rubbing direction of an alignment film on the array substrate.

[0008] In further another aspect of the present invention, there is provided an alignment rubbing method, the method comprising:

setting a forward speed of an array substrate, as well as an axial direction and an angular velocity of a rubbing roller, wherein a linear direction, along which the forward speed of the array substrate is taken, is opposed to a data-line-direction on the array substrate, and a third non-zero preset angle is present between the axial direction of the rubbing roller and the data-line-direction on the array substrate, and a first non-zero preset angle is present between the data-line-direction and a slit-direction of a second electrode on the array substrate; and making the array substrate moved in accordance with the set forward speed, and come into contact with the rubbing roller, in order to rub an alignment film on the array substrate, wherein a second non-zero preset angle is present between a rubbing direction of the alignment film and the slit-direction of the second electrode; the rubbing direction of the alignment film is perpendicular to the axial direction of the rubbing roller, and an angle between the rubbing direction of the alignment film and the data-line-

direction is greater than the second non-zero preset angle.

[0009] In the array substrate, the liquid crystal display apparatus and the alignment rubbing method provided by the embodiments of the present invention, on one hand, by setting a second non-zero preset angle between a rubbing direction of an alignment film and a slit-direction of a second electrode, the needs of a high aperture ratio and high transmittance are met, and thus the normal display of the liquid crystal display is ensured; on the other hand, by setting a first non-zero preset angle between the slit-direction of the second electrode and a data-line-direction, and letting the angle between the rubbing direction of the alignment film and the data-line-direction greater than the second non-zero preset angle, the relative velocity between the rubbing roller and the array substrate in their relative movement in the data-line-direction is reduced, so that the mutual damage between the rubbing roller and the substrate is reduced, thereby enhancing the rubbing uniformity.

BRIEF DESCRIPTION OF THE DRAWINGS

[0010] In order to clearly illustrate the technical solutions of the embodiments of the invention, the drawings of the embodiments will be briefly described in the following; it is obvious that the described drawings are only related to some embodiments of the invention and thus are not limitative of the invention.

FIG. 1 is a top view of an array substrate in the prior art;

FIG. 2 is a top view of an array substrate provided by an embodiment of the present invention;

FIG. 3 is a schematic illustration of a relationship among a slit-direction, a rubbing direction, and a data-line-direction in the array substrate shown in FIG. 2;

FIG. 4 is a top view of another array substrate provided by an embodiment of the present invention;

FIG. 5 is a schematic illustration of an alignment rubbing method provided by an embodiment of the present invention; and

FIG. 6 is a schematic illustration of a relationship among a slit-direction, a rubbing direction, and a data-line-direction in the alignment rubbing method shown in FIG. 5.

Reference numerals

[0011] 01 - Common electrode; 02 - Pixel electrode; 03 - Data line; 04 - Slit; 05 - Gate line; 06 - Thin film transistor; 11 - Rubbing direction; 12 - Forward direction of a substrate; 13 - Slit-direction; 14 - Axial direction of a rubbing roller.

DETAILED DESCRIPTION

[0012] In order to make objects, technical details and advantages of the embodiments of the invention apparent, the technical solutions of the embodiments will be described in a clearly and fully understandable way in connection with the drawings related to the embodiments of the invention. It is obvious that the described embodiments are just a part but not all of the embodiments of the invention. Based on the described embodiments herein, those skilled in the art can obtain other embodiment(s), without any inventive work, which should be within the scope of the invention.

[0013] An array substrate according to an embodiment of the present invention comprises a plurality of gate lines and a plurality of data lines, and these gate lines and data lines intersect one another and thus define pixel units arranged in a matrix, each pixel unit comprising a thin film transistor as a switching element and a pixel electrode for controlling the alignment of liquid crystal. In the following description, it is mainly directed to a single or a plurality of pixel units, but other pixel units may be formed in the same way.

25 First embodiment

[0014] In all embodiments of the present invention, a data line is taken as a reference line, so that the direction of a longitudinal coordinate Y in a coordinate system is in accordance with the data-line-direction.

[0015] As shown in FIG. 2, an embodiment of the present invention provides an array substrate 100, comprising: gate lines 05, data lines 03, and a plurality of pixel units defined by the gate lines 05 and the data lines 03 intersecting one another. On a surface of the array structure of the array substrate, there is formed an alignment film. The alignment film is formed of, for example, a PI (polyimide) material.

[0016] Each of the pixel units comprises a thin film transistor 06, a plate-like first electrode, and a second electrode provided with slits 04. A first non-zero preset angle $\theta 1$ is present between the slit-direction 13 (i.e., the extending direction of the slits) of the second electrode and the data-line-direction Y, and a second non-zero preset angle $\theta 2$ is present between a rubbing direction 11 of the alignment film and the slit-direction 13 of the second electrode, moreover, the angle between the rubbing direction 11 of the alignment film and the data-line-direction Y is greater than the second non-zero preset angle $\theta 2$.

[0017] In this embodiment, the first electrode is formed from a first transparent electric-conductive layer in a pixel unit of the array substrate 100, and for example, it may typically be a first indium-tin oxide (ITO) layer; the second electrode is formed from a second transparent electric-conductive layer in the pixel unit of the array substrate 100, and for example, it may typically be a second ITO layer. In this embodiment of the present invention, since a multi-dimensional electric field is generated by the first

and second electrodes of the pixel unit of the array substrate 100 to drive the liquid crystal, the second electrode is usually formed into a patterned electrode, and the pattern is typically elongated slits, and these slits may be enclosed on periphery or open at one end. In addition, of the first and second electrodes, the electrode that is connected with a drain of the thin film transistor 06 in the pixel unit is a pixel electrode, and the other is for example a common electrode connected with a common electrode line.

[0018] In one example of the embodiment, the first electrode is electrically connected with a common electrode line of the array substrate; the second electrode is electrically connected with a drain of the thin film transistor 06; as shown in FIG. 2, that is, in this example, the second electrode is a pixel electrode 02, and the first electrode is a common electrode 01. The pixel electrode 02 is provided thereon with a plurality of slits 04; the common electrode 01 has no slit provided thereon and is shaped into a plate. These slits 04, for example, are parallel to each other and enclosed on periphery. The slits 04 extend obliquely with respect to the gate line 05 (or the data lines 03), and hereafter the extending direction of the slits 04 is referred to as the slit-direction 13.

[0019] After the preparation of an array structure, comprising driving elements, of the array substrate, an alignment film is coated on the array structure, and then the alignment film is subject to an alignment rubbing operation. During the alignment film rubbing, as shown in FIG. 3 (a), when an angle formed between the rubbing direction 11 and the Y-direction is greater than an angle formed between the slit-direction 13 of the second electrode and the Y-direction, a first non-zero preset angle θ_1 is present between the slit-direction 13 of the second electrode and the data-line-direction Y, and a second non-zero preset angle θ_2 is present between the rubbing direction 11 of the alignment film and the slit-direction 13 of the second electrode. Thus it can be seen that the value of the angle between the rubbing direction 11 of the alignment film and the data-line-direction Y is the sum of the value of the first non-zero preset angle θ_1 and the value of the second non-zero preset angle θ_2 . This makes the relative velocity between the rubbing roller and the array substrate in their relative movement in the Y-direction reduced, so that the mutual damage between the rubbing roller and the substrate is reduced, thereby enhancing the rubbing uniformity. Preferably, the first non-zero preset angle θ_1 is in a range of 20-30 degrees, such an angle is easy to be achieved in the manufacturing process and makes the mutual damage between the rubbing roller and the substrate reduced, thereby enhancing the rubbing uniformity; preferably, the second non-zero preset angle θ_2 is in a range of 5-9 degrees, and such an angle meets the needs of a high aperture ratio and high transmittance, and therefore the normal display of the liquid crystal display is ensured.

[0020] As shown in FIG. 3(b), when an angle formed between the rubbing direction 11 and the Y-direction is

less than an angle formed between the slit-direction 13 of the second electrode and the Y-direction, a first non-zero preset angle θ_1 is present between the slit-direction 13 of the second electrode and the data-line-direction Y, and a second non-zero preset angle θ_2 is present between the rubbing direction 11 of the alignment film and the slit-direction 13 of the second electrode. Thus it can be seen that, the value of the angle between the rubbing direction 11 of the alignment film and the data-line-direction is the difference of the value of the first non-zero preset angle θ_1 minus the value of the second non-zero preset angle θ_2 . On condition that the angle between the rubbing direction 11 of the alignment film and the data-line-direction Y is ensured to be greater than the second non-zero preset angle θ_2 , the relative velocity between the rubbing roller and the array substrate in their relative movement in the Y-direction can be reduced likewise, so that the mutual damage between the rubbing roller and the substrate is reduced, thereby enhancing the rubbing uniformity. Preferably, the first non-zero preset angle θ_1 is in a range of 20-30 degrees, such an angle is easy to be achieved in the manufacturing process, and makes the mutual damage between the rubbing roller and the substrate reduced, thereby enhancing the rubbing uniformity; preferably, the second non-zero preset angle θ_2 is in a range of 5-9 degrees, and such an angle meets the needs of a high aperture ratio and high transmittance, and therefore the normal display of the liquid crystal display is ensured.

[0021] This embodiment provides an array substrate, which, on one hand, is provided with a second non-zero preset angle between a rubbing direction of an alignment film and a slit-direction of a second electrode so as to meet the needs of a high aperture ratio and high transmittance, thus ensuring the normal display of the liquid crystal display, and on the other hand, is provided with a first non-zero preset angle between the slit-direction of the second electrode and a data-line-direction, making the angle between the rubbing direction of the alignment film and the data-line-direction greater than the second non-zero preset angle, thus reducing the relative velocity between the rubbing roller and the array substrate in their relative movement in the Y-direction, so that the mutual damage between the rubbing roller and the substrate is reduced, thereby enhancing the rubbing uniformity.

Second Embodiment

[0022] As shown in FIG. 4, this embodiment provides an array substrate 100, comprising: gate lines 05, data lines 03, and a plurality of pixel units defined by the gate lines 05 and the data lines 03 intersecting one another. On a surface of the array structure of the array substrate, there is an alignment film. The alignment film is formed of, for example, a PI (polyimide) material.

[0023] Each of the pixel units comprises a thin film transistor, a plate-like first electrode, and a second electrode provided with slits 04; a first non-zero preset angle θ_1 is

present between the slit-direction 13 (i.e., the extending direction of the slits) of the second electrode and the data-line-direction, and a second non-zero preset angle θ_2 is present between a rubbing direction 11 of the alignment film and the slit-direction of the second electrode; moreover, the angle between the rubbing direction 11 of the alignment film and the data-line-direction Y is greater than the second non-zero preset angle θ_2 .

[0024] In this embodiment, the first electrode is formed from a first transparent electric-conductive layer formed in a pixel unit of the array substrate 100, for example, it may typically be a first indium-tin oxide (ITO) layer; the second electrode is formed from a second transparent electric-conductive layer formed in the pixel unit of the array substrate 100, for example, it may typically be a second ITO layer. In this embodiment of the present invention, since a multi-dimensional electric field is generated by the first and second electrodes of the pixel unit of the array substrate 100 to drive the liquid crystal, the second electrode is usually formed into a patterned electrode, and the pattern is typically elongated slits, and these slits may be enclosed on periphery or open at one end. In addition, in the first and second electrodes, the electrode that is connected with a drain of the thin film transistor 06 in the pixel unit is a pixel electrode, and the other is for example a common electrode connected with a common electrode line.

[0025] In one example of the embodiment, the first electrode is electrically connected with a drain of the thin film transistor 06; the second electrode is electrically connected with a common electrode line of the array substrate; as shown in FIG. 4, that is, in this example, the second electrode is a common electrode 01, and the first electrode is a pixel electrode 02. The common electrode 01 is provided thereon with a plurality of slits 04, while the pixel electrode 02 has no slit provided thereon and is shaped into a plate. These slits 04, for example, are parallel to each other, and enclosed on periphery. The slit 04 extends obliquely with respect to the gate line 05 (or the data lines 03), and hereafter the extending direction of the slits 04 is referred to as the slit-direction 13.

[0026] After the preparation of an array structure, comprising driving elements, of the array substrate, an alignment film is coated on the array structure, and then the alignment film is subject to an alignment rubbing operation. During the alignment film rubbing, as shown in FIG. 3 (a), when an angle formed between the rubbing direction 11 and the Y-direction is greater than an angle formed between the slit-direction 13 of the second electrode and the Y-direction, a first non-zero preset angle θ_1 is present between the slit-direction 13 of the second electrode and the data-line-direction Y, and a second non-zero preset angle θ_2 is present between the rubbing direction 11 of the alignment film and the slit-direction 13 of the second electrode. Thus it can be seen that the value of the angle between the rubbing direction 11 of the alignment film and the data-line-direction Y is the sum of the value of the first non-zero preset angle θ_1 and the

value of the second non-zero preset angle θ_2 . This makes the relative velocity between the rubbing roller and the array substrate in their relative movement in the Y-direction reduced. Thus the mutual damage between the rubbing roller and the substrate is reduced, thereby enhancing the rubbing uniformity. Preferably, the first non-zero preset angle θ_1 is in a range of 20-30 degrees, such an angle is easy to be achieved in the manufacturing process, and makes the mutual damage between the rubbing roller and the substrate reduced, thereby enhancing the rubbing uniformity; preferably, the second non-zero preset angle θ_2 is in a range of 5-9 degrees, and such an angle meets the needs of a high aperture ratio and high transmittance, and therefore the normal display of the liquid crystal display is ensured.

[0027] As shown in FIG. 3(b), when an angle formed between the rubbing direction 11 and the Y-direction is less than an angle formed between the slit-direction 13 of the second electrode and the Y-direction, a first non-zero preset angle θ_1 is present between the slit-direction 13 of the second electrode and the data-line-direction Y, and a second non-zero preset angle θ_2 is present between the rubbing direction 11 of the alignment film and the slit-direction 13 of the second electrode. Thus it can be seen that, the value of the angle between the rubbing direction 11 of the alignment film and the data-line-direction is the difference of the value of the first non-zero preset angle θ_1 minus the value of the second non-zero preset angle θ_2 . On condition that the angle between the rubbing direction 11 of the alignment film and the data-line-direction Y is ensured to be greater than the second non-zero preset angle θ_2 , this makes the relative velocity between the rubbing roller and the array substrate in their relative movement in the Y-direction reduced likewise, so that the mutual damage between the rubbing roller and the substrate is reduced, thereby enhancing the rubbing uniformity. Preferably, the first non-zero preset angle θ_1 is in a range of 20-30 degrees, such an angle is easy to be achieved in the manufacturing process, and makes the mutual damage between the rubbing roller and the substrate reduced, thereby enhancing the rubbing uniformity; preferably, the second non-zero preset angle θ_2 is in a range of 5-9 degrees, and such an angle meets the needs of a high aperture ratio and high transmittance, and therefore the normal display of the liquid crystal display is ensured.

[0028] This embodiment provides an array substrate, which, on one hand, is provided with a second non-zero preset angle between a rubbing direction of an alignment film and a slit-direction of a second electrode so as to meet the needs of a high aperture ratio and high transmittance, thus ensuring the normal display of the liquid crystal display, and on the other hand, is provided with a first non-zero preset angle between the slit-direction of the second electrode and a data-line-direction, making the angle between the rubbing direction of the alignment film and the data-line-direction greater than the second non-zero preset angle, thus reducing the relative velocity

between the rubbing roller and the array substrate in their relative movement in the Y-direction, so that the mutual damage between the rubbing roller and the substrate is reduced, thereby enhancing the rubbing uniformity.

Third Embodiment

[0029] This embodiment provides an alignment rubbing method used for an alignment film of an array substrate, the method comprising the steps as follows.

[0030] A schematic illustration of the operation of the rubbing method is shown as FIG. 5. First is to set a forward speed of an array substrate, as well as both an axial direction 14 and a rotating angular velocity of a rubbing roller; the linear direction 12, along which the forward speed of the array substrate is taken, is opposed to a data-line-direction 03 on the array substrate. A third non-zero preset angle θ_3 is present between the axial direction 14 of the rubbing roller and the data-line-direction 03 on the array substrate, and a first non-zero preset angle θ_1 is present between the data-line-direction Y and a slit-direction 13 of a second electrode on the array substrate.

[0031] The array substrate is moved in accordance with the set forward speed, and come into contact with the rubbing roller, and thus the rubbing roller rubs an alignment film formed on the surface of the array structure of the array substrate, so that a second non-zero preset angle θ_2 is present between the rubbing direction 11 of the alignment film and the slit-direction 13 of the second electrode; the rubbing direction 11 of the alignment film is perpendicular to the axial direction 14 of the rubbing roller, and moreover the angle between the rubbing direction 11 of the alignment film and the data-line-direction is greater than the second non-zero preset angle.

[0032] As shown in FIG. 6(a), when an angle formed between the rubbing direction 11 and the Y-direction is greater than an angle formed between the slit-direction 13 of the second electrode and the Y-direction, since the axial direction 14 of the rubbing roller is perpendicular to the rubbing direction 11 of the alignment film, the value of the complementary angle θ_4 of the third non-zero preset angle θ_3 is the sum of the value of the first non-zero preset angle θ_1 and the value of the second non-zero preset angle θ_2 . The value of the complementary angle of the third non-zero preset angle θ_3 is exactly the value of the angle between the rubbing direction of the alignment film and the data-line-direction. Thus it can be seen that, the value of the angle between the rubbing direction of the alignment film and the data-line-direction is the sum of the value of the first non-zero preset angle θ_1 and the value of the second non-zero preset angle θ_2 .

[0033] As shown in FIG. 6(b), when an angle formed between the rubbing direction 11 and the Y-direction is less than an angle formed between the slit-direction 13 of the second electrode and the Y-direction, since the axial direction 14 of the rubbing roller is perpendicular to the rubbing direction 11 of the alignment film, the value of the complementary angle θ_4 of the third non-zero pre-

set angle θ_3 is the difference of the value of the first non-zero preset angle θ_1 minus the value of the second non-zero preset angle θ_2 . The value of the complementary angle of the third non-zero preset angle is exactly the value of the angle between the rubbing direction of the alignment film and the data-line-direction. Thus it can be seen that, the value of the angle between the rubbing direction of the alignment film and the data-line-direction is the difference of the value of the first non-zero preset angle θ_1 minus the value of the second non-zero preset angle θ_2 .

[0034] The alignment film, for example, formed of a PI (polyimide) material, is coated on a surface of the array structure of the array substrate.

[0035] This embodiment provides an alignment rubbing method, comprising setting a forward speed of an array substrate and an axial direction of a rubbing roller, letting the rubbing direction of the alignment film perpendicular to the axial direction of the rubbing roller, and the method, on one hand by setting a second non-zero preset angle to be present between a rubbing direction of an alignment film and a slit-direction of a second electrode, meets the needs of a high aperture ratio and high transmittance, thus ensuring the normal display of a liquid crystal display, and on the other hand, by setting a first non-zero preset angle to be present between the slit-direction of the second electrode and a data-line-direction, and letting the angle between the rubbing direction of the alignment film and the data-line-direction greater than the second non-zero preset angle, so that the relative velocity between the rubbing roller and the array substrate is reduced in their relative movement in the Y-direction, reduces the mutual damage between the rubbing roller and the substrate, thereby enhancing the rubbing uniformity.

[0036] An embodiment of the present invention further provides a liquid crystal display apparatus, comprising an array substrate and a color filter substrate, which are disposed opposite to each other, and the array substrate is any one of the array substrates described in the above embodiments. A rubbing direction of an alignment film on the color filter substrate is opposed to a rubbing direction of an alignment film on the array substrate. Liquid crystal is filled in the space formed between the array substrate and the color film substrate, and the alignment films both on the array substrate and on the color filter substrate are used for initial alignment of the liquid crystal molecules in contact with them, by means of micro grooves and the like formed by a rubbing operation.

[0037] The array substrate comprises gate lines, data lines, and pixel units defined by the gate lines and the data lines intersecting one another. On the array substrate, there is formed an alignment film. The pixel unit comprises a thin film transistor, a first electrode, and a second electrode provided with slits; a second non-zero preset angle is present between a rubbing direction of the alignment film and the slit-direction of the second electrode, thus the needs of a high aperture ratio and

high transmittance are met, and therefore the normal display of the liquid crystal display is ensured; a first non-zero preset angle is present between the slit-direction of the second electrode and the data-line-direction, and moreover, the angle between the rubbing direction of the alignment film and the data-line-direction is greater than the second non-zero preset angle, thus the relative velocity between the rubbing roller and the array substrate in their relative movement in the data-line-direction is reduced, so that the mutual damage between the rubbing roller and the substrate is reduced, thereby enhancing the rubbing uniformity.

[0038] In the disclosed technical scope of the present invention, variations or modifications, which can be easily conceived by any artisan familiar with ordinary technology in the art, should be encompassed by the protection scope of the present invention. Therefore, the protection scope of the present invention should be defined by the scope of the claims.

Claims

1. An array substrate, comprising a gate line, a data line, and a pixel unit defined by the gate line and the data line intersecting each other, as well as an alignment film formed on the array substrate; wherein the pixel unit each comprises a thin film transistor, a first electrode, and a second electrode provided with slits; and wherein a first non-zero preset angle is present between a slit-direction of the second electrode and a data-line-direction, a second non-zero preset angle is present between a rubbing direction of the alignment film and the slit-direction of the second electrode, and an angle between the rubbing direction of the alignment film and the data-line-direction is greater than the second non-zero preset angle.
2. The array substrate according to Claim 1, wherein a value of an angle between the rubbing direction of the alignment film and the data-line-direction is the sum of a value of the first non-zero preset angle and a value of the second non-zero preset angle, or a difference of the value of the first non-zero preset angle minus the value of the second non-zero preset angle.
3. The array substrate according to Claim 1 or 2, wherein the first electrode is electrically connected with a common electrode line of the array substrate; and the second electrode is electrically connected with a drain of the thin film transistor.
4. The array substrate according to Claim 1 or 2, wherein the first electrode is electrically connected with a drain of the thin film transistor; and the second electrode is electrically connected with a common elec-

trode line of the array substrate.

5. The array substrate according to any one of Claims 1-4, wherein the first non-zero preset angle is in a range of 20-30 degrees.
6. The array substrate according to any one of Claims 1-4, wherein the second non-zero preset angle is in a range of 5-9 degrees.
7. A liquid crystal display apparatus, comprising: an array substrate and a color filter substrate assembled together to form a cell, wherein the array substrate is in accordance with any one of Claims 1-6, and a rubbing direction of an alignment film on the color filter substrate is opposed to a rubbing direction of an alignment film on the array substrate.
8. A method for alignment rubbing of an array substrate, comprising:

setting a forward speed of an array substrate, as well as an axial direction and an angular velocity of a rubbing roller, wherein a linear direction, along which the forward speed of the array substrate is taken, is opposed to a data-line-direction on the array substrate, and a third non-zero preset angle is present between the axial direction of the rubbing roller and the data-line-direction on the array substrate, and a first non-zero preset angle is present between the data-line-direction and a slit-direction of a second electrode on the array substrate; and making the array substrate moved in accordance with the set forward speed, and come into contact with the rubbing roller, in order to rub an alignment film on the array substrate, wherein a second non-zero preset angle is present between a rubbing direction of the alignment film and the slit-direction of the second electrode; the rubbing direction of the alignment film is perpendicular to the axial direction of the rubbing roller, and an angle between the rubbing direction of the alignment film and the data-line-direction is greater than the second non-zero preset angle.

9. The method according to Claim 8, wherein a value of a complementary angle of the third non-zero preset angle is a sum of a value of the first non-zero preset angle and a value of the second non-zero preset angle; or a value of a complementary angle of the third non-zero preset angle is a difference of a value of the first non-zero preset angle minus a value of the second non-zero preset angle.

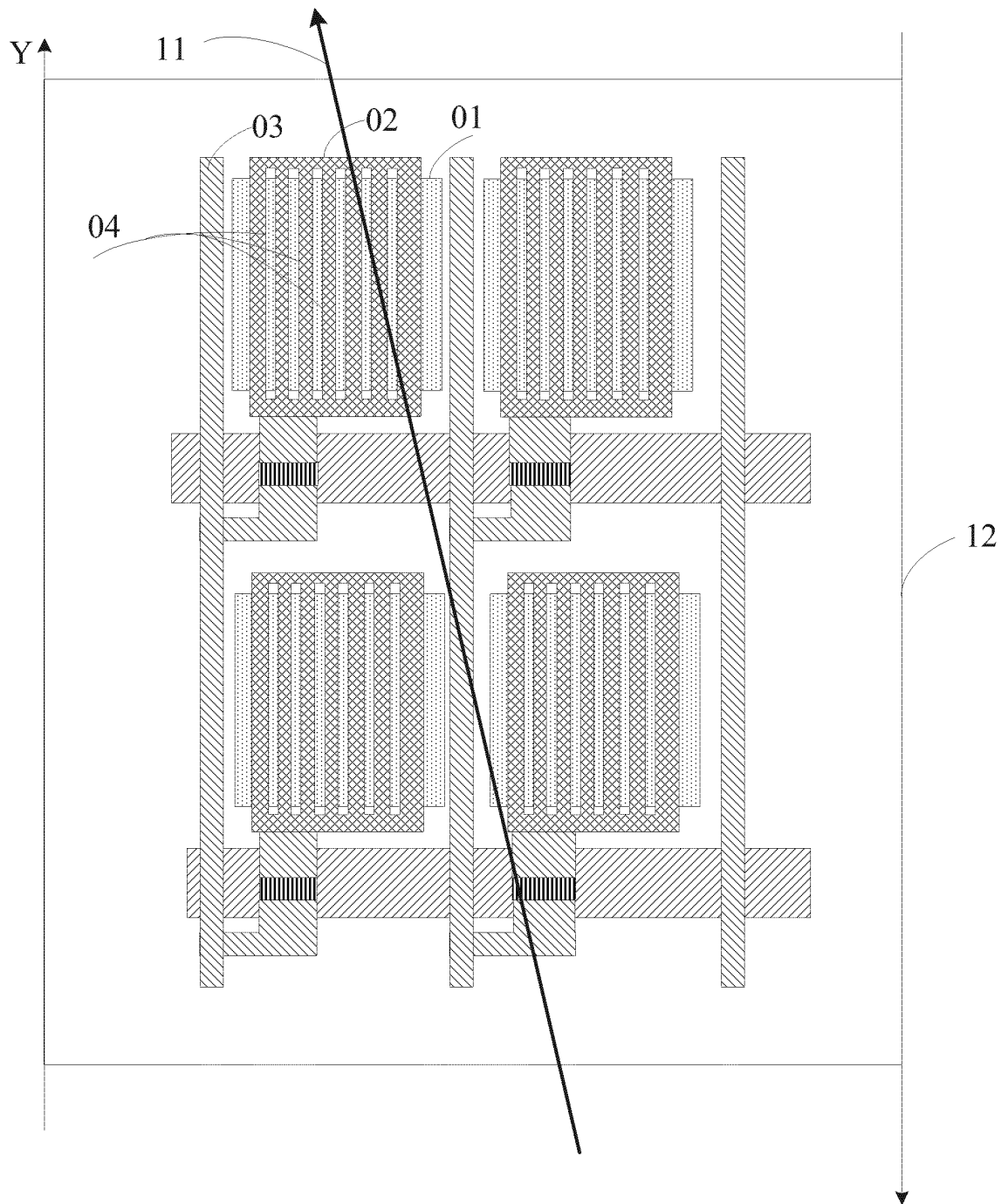


FIG.1

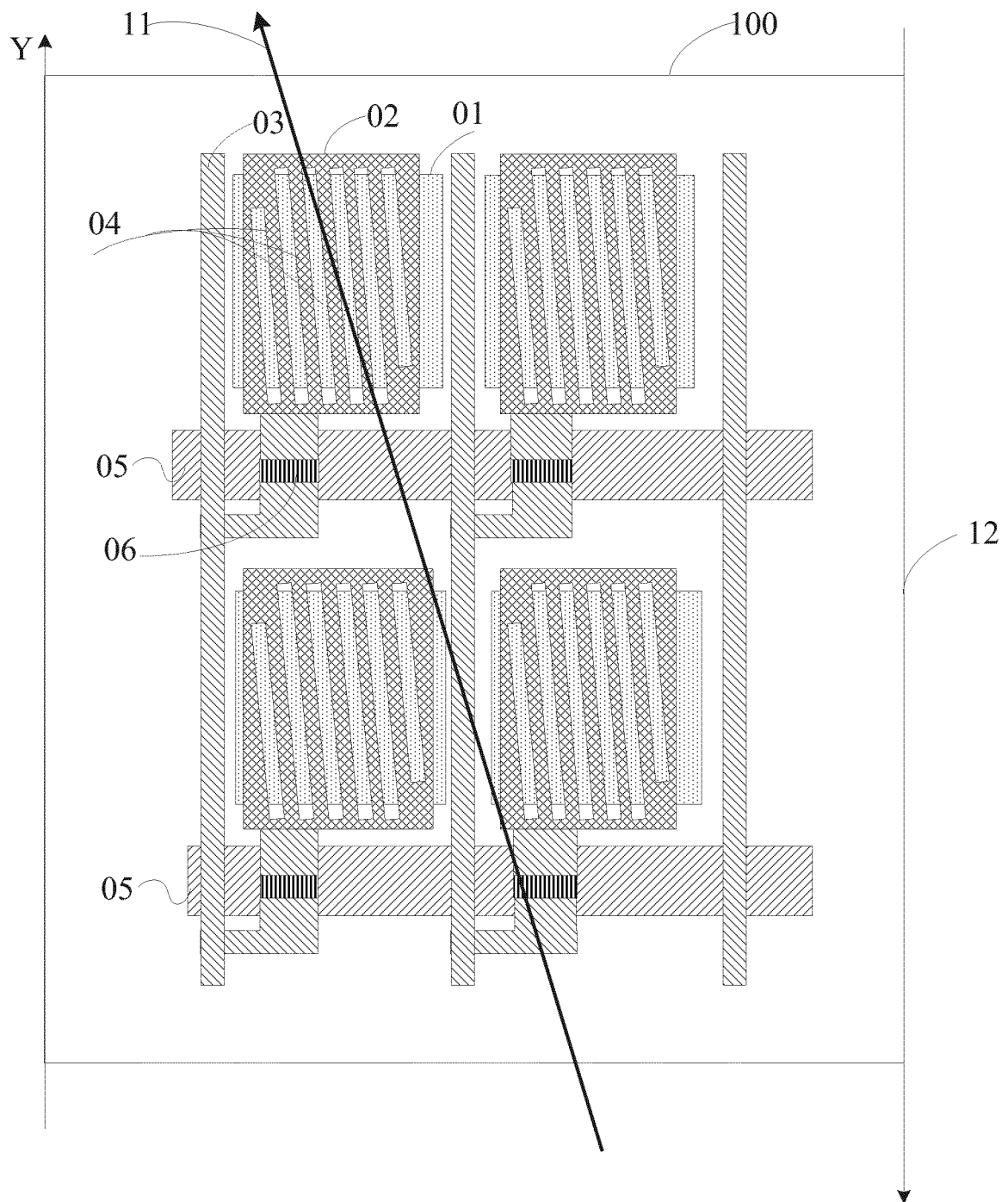


FIG.2

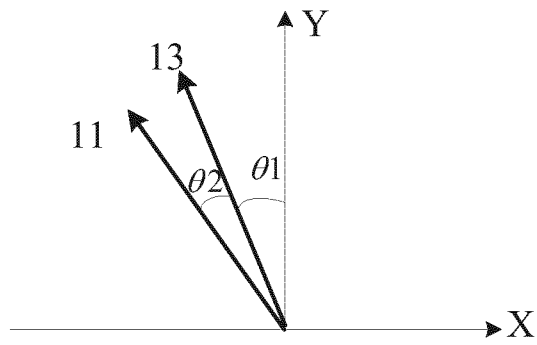


FIG.3 (a)

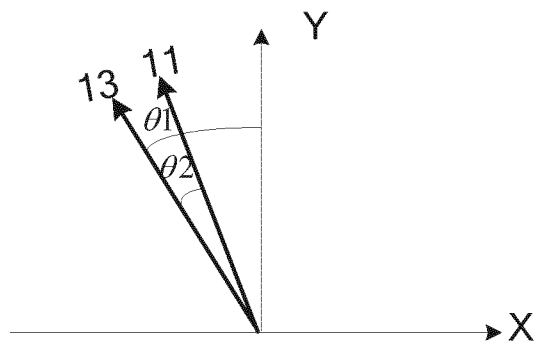


FIG.3 (b)

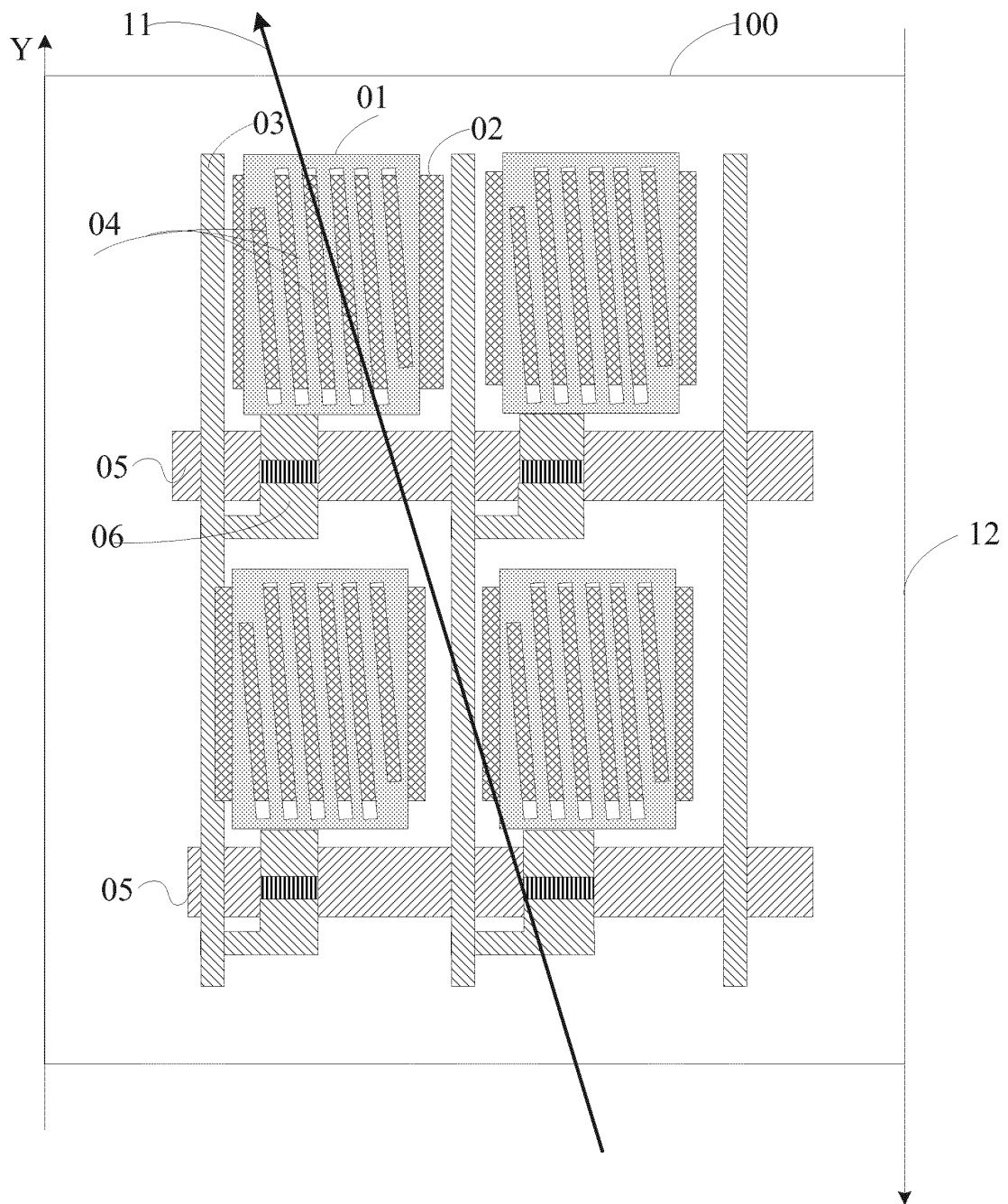


FIG.4

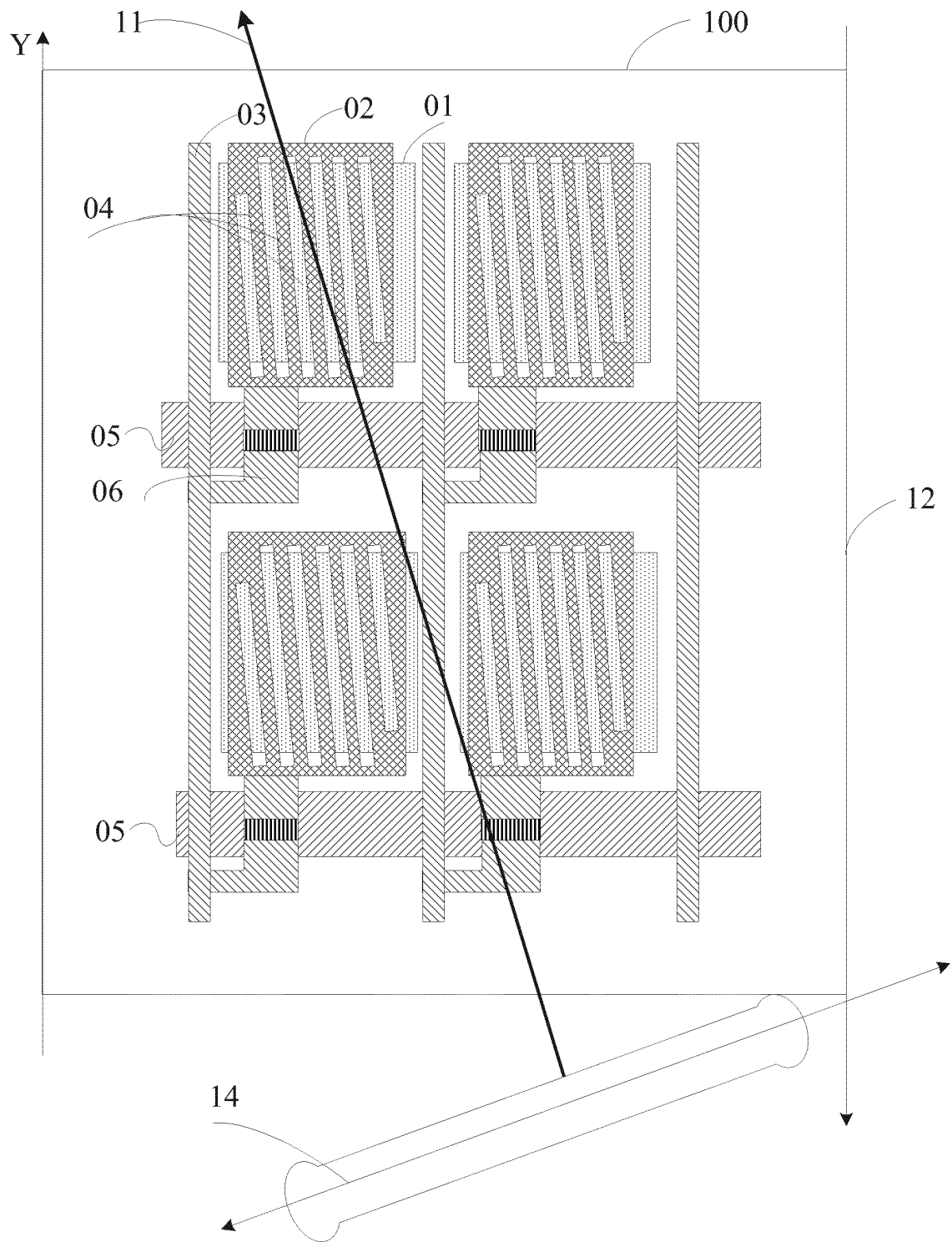


FIG.5

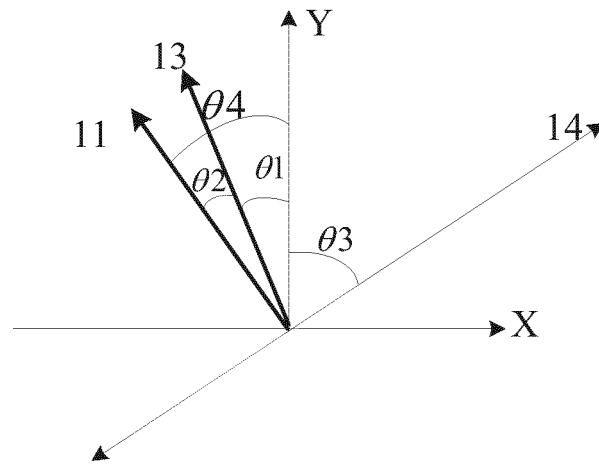


FIG.6 (a)

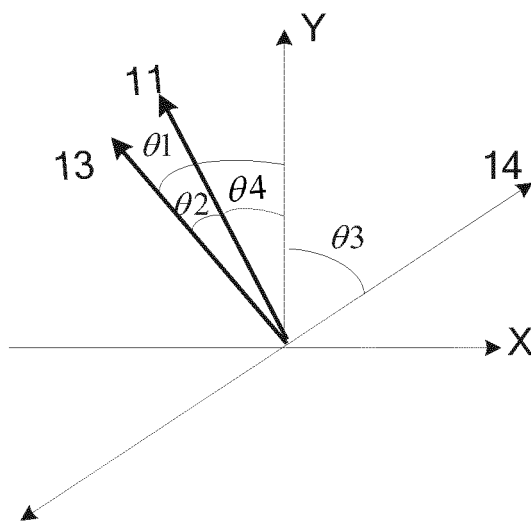


FIG.6 (b)

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2012/082723

A. CLASSIFICATION OF SUBJECT MATTER

G02F 1/1337 (2006.01) i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC: G02F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

VEN, CNABS, CNTXT: rub+, open+, slit+, (pixel w electrode), (picture w element w electrode), substrate, mov+, march+, speed, direction, orientation, data line, digital line, drain line, source line/wiring, angle

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X Y	CN 101441369 A (EPSON IMAGING DEVICES CORP.) 27 May 2009 (27.05.2009) description, page 7, line 20 to page 8, line 26 and page 13, line 7 to page 14, line 28 and figures 1, 2, 6	1-7 8, 9
Y	CN 1501145 A (NIPPON PETROCHEMICALS CO., LTD.) 02 June 2004 (02.06.2004) description, page 2, lines 8 to 10, page 4, line 20 to page 5, line 1 and figure 2	8, 9
X Y	CN 101236340 A (BOE DISPLAY TECHNOLOGY CO., LTD.) 06 August 2008 (06.08.2008) description, page 6, line 18 to page 8, line 18 and figures 1-2D	1-4, 7 8, 9
X Y	CN 102062976 A (HYDIS TECHNOLOGIES CO., LTD.) 18 May 2011 (18.05.2011) description, paragraphs [0027]-[0034] and figures 1, 2	1-4, 7 8, 9

☒ Further documents are listed in the continuation of Box C.
 ☒ See patent family annex.

* Special categories of cited documents:	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"A" document defining the general state of the art which is not considered to be of particular relevance	
"E" earlier application or patent but published on or after the international filing date	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"O" document referring to an oral disclosure, use, exhibition or other means	
"P" document published prior to the international filing date but later than the priority date claimed	"&" document member of the same patent family

Date of the actual completion of the international search 06 January 2013 (06.01.2013)	Date of mailing of the international search report 24 January 2013 (24.01.2013)
Name and mailing address of the ISA State Intellectual Property Office of the P. R. China No. 6, Xitucheng Road, Jimenqiao Haidian District, Beijing 100088, China Facsimile No. (86-10) 62019451	Authorized officer LI, Qingqing Telephone No. (86-10) 62084144

Form PCT/ISA/210 (second sheet) (July 2009)

INTERNATIONAL SEARCH REPORT

International application No.
PCT/CN2012/082723

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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Y	description, paragraphs [0042]-[0083] and figures 3, 7	8, 9
PX	CN 102629058 A (BOE DISPLAY TECHNOLOGY CO., LTD. et al.) 08 August 2012 (08.08.2012) the whole document	1-9
A	CN 1607424 A (LG PHILIPS LCD CO., LTD.) 20 April 2005 (20.04.2005) the whole document	1-9

Form PCT/ISA/210 (continuation of second sheet) (July 2009)

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/CN2012/082723

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International application No.

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Form PCT/ISA/210 (extra sheet) (July 2009)

专利名称(译)	阵列基板，液晶显示装置和对准摩擦方法		
公开(公告)号	EP2799926A1	公开(公告)日	2014-11-05
申请号	EP2012788103	申请日	2012-10-10
[标]申请(专利权)人(译)	京东方科技集团股份有限公司 成都京东方光电科技有限公司		
申请(专利权)人(译)	京东方科技集团有限公司. 成都京东方光电科技有限公司.		
当前申请(专利权)人(译)	京东方科技集团有限公司. 成都京东方光电科技有限公司.		
[标]发明人	MO ZAILONG YANG YUQING SHI TIANLEI PARK SEUNG YIK		
发明人	MO, ZAILONG YANG, YUQING SHI, TIANLEI PARK, SEUNG YIK		
IPC分类号	G02F1/1337		
CPC分类号	G02F1/133707 G02F1/133784 G02F2001/134372		
优先权	201110460678.4 2011-12-31 CN		
其他公开文献	EP2799926B1 EP2799926A4		
外部链接	Espacenet		

摘要(译)

本发明的实施例提供一种阵列基板，液晶显示装置和对准摩擦方法。阵列基板包括栅极线，数据线和由栅极线和数据线彼此交叉限定的像素单元，以及形成在阵列基板上的取向膜。每个像素单元包括薄膜晶体管，第一电极和设置有狭缝的第二电极；在第二电极的狭缝方向与数据线方向之间存在第一非零预设角度，在取向膜的摩擦方向与狭缝方向之间存在第二非零预设角度。第二电极，取向膜的摩擦方向与数据线方向之间的角度大于第二非零预设角度。

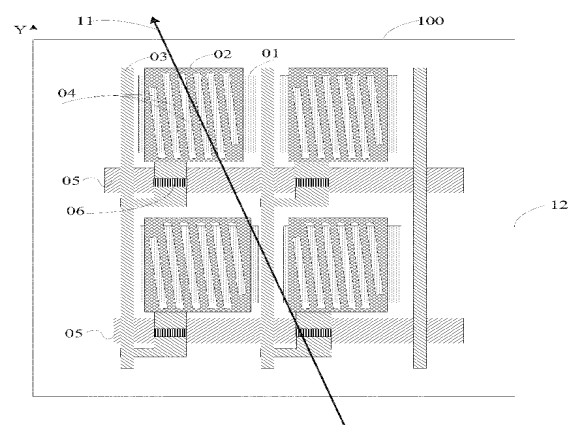


FIG. 2