



(12) **EUROPEAN PATENT APPLICATION**

(43) Date of publication:  
**26.02.2003 Bulletin 2003/09**

(51) Int Cl.<sup>7</sup>: **G02F 1/1335**, G02F 1/1333,  
G02F 1/13363B

(21) Application number: **02018278.8**

(22) Date of filing: **22.08.2002**

(84) Designated Contracting States:  
**AT BE BG CH CY CZ DE DK EE ES FI FR GB GR**  
**IE IT LI LU MC NL PT SE SK TR**  
Designated Extension States:  
**AL LT LV MK RO SI**

(72) Inventors:  
• **Ikeno, Hidenori**  
**Minato-ku, Tokyo (JP)**  
• **Suzuki, Masayoshi**  
**Minato-ku, Tokyo (JP)**

(30) Priority: **22.08.2001 JP 2001251089**

(74) Representative: **Betten & Resch**  
**Patentanwälte,**  
**Theatinerstrasse 8**  
**80333 München (DE)**

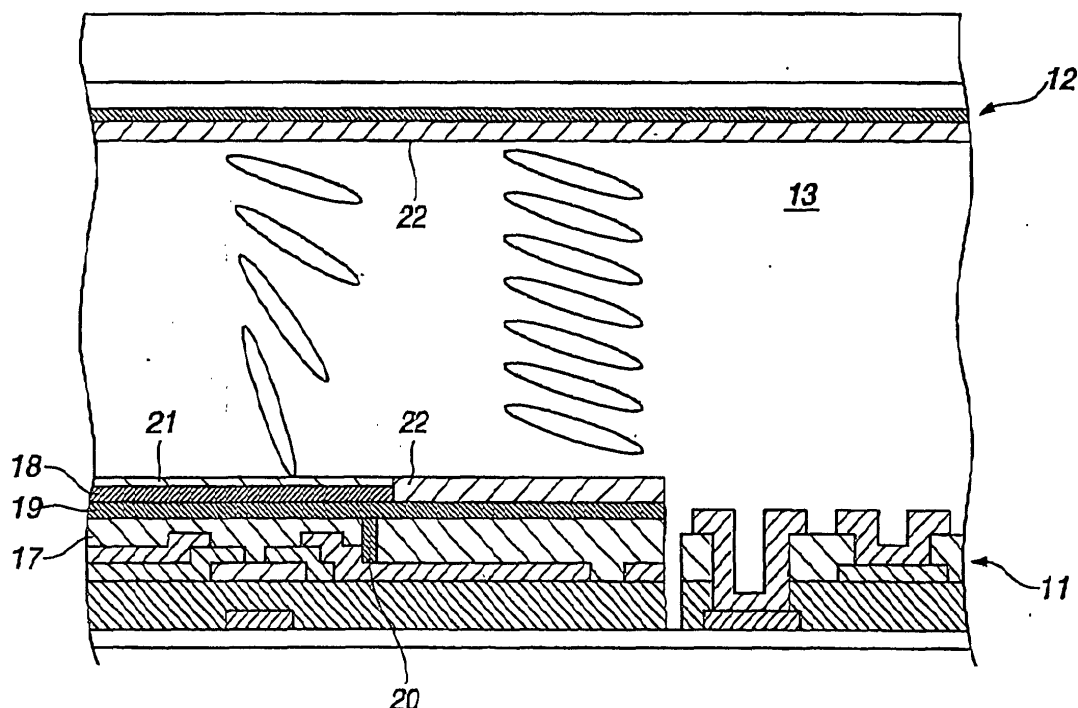
(71) Applicant: **NEC CORPORATION**  
**Tokyo (JP)**

(54) **Transflective liquid crystal display device**

(57) A liquid crystal display device comprises a first substrate (11) including a thin film transistor, a second substrate (12), and a liquid crystal layer (13) of liquid crystal molecules. The LC layer (13) is interposed by the first and second substrates (11,12). The first substrate (11) includes a reflective electrode (18) in a reflective

region and a transmissive electrode (19) in a transmissive region. The LC layer (13) includes a first group of liquid crystal molecules aligned in the reflective region to provide a first retardation and a second group of liquid crystal molecules aligned in the transmissive region to provide a second retardation. The second retardation is different from the first retardation.

**FIG.3**



## Description

## BACKGROUND OF THE INVENTION

## 1. Field of the Invention

[0001] The present invention relates to liquid crystal display (LCD) devices and more particularly to transfective LCD devices, which use the reflected and transmitted brightness.

## 2. Description of the Related Art

[0002] Reflective LCD devices use the reflected brightness from room light, while transmissive LCD devices use the transmitted brightness from an internal light source, such as a backlight.

[0003] The reflective LCD devices, which are used as display devices of portable information terminals, are advantageous over the transmissive LCD devices, in power consumption, thickness and weight. Such advantages derive mainly from the elimination of a backlight. However, the transmissive LCD devices are advantageous in visibility in dark environment.

[0004] Commonly, the LCD devices include a layer of liquid crystal (LC) molecules. Examples of modes are a twisted nematic (TN) mode, a single polarizing plate mode, a super twisted nematic (STN) mode, a guest host (GH) mode, a polymer dispersed liquid crystal (PDLC) mode, and a cholesteric mode. A switching element is provided per pixel to drive the liquid crystal layer. A reflector or a backlight is provided within or outside of the LC cell. To produce fine and high visibility in image, the LCD devices employ an active matrix drive system, in which switching elements, such as, thin film transistors (TFTs) or metal/insulator/metal (MIM) diodes, are attached to each pixel to switch one "on" or "off". In such LCD devices, a reflector or a backlight accompanies the active matrix drive system.

[0005] One example of known transfective LCD devices is illustrated in Figures 14 and 15. The same transfective LCD device is found in Kubo et al. (US 6,195,140 B1 issued Feb. 27, 2001) and JP 2955277 B2. Kubo et al. illustrates the same structure in Figures 1 and 29 and provides a description on Figure 1 from line 49 of column 8 to line 11 of column 11 and a description on Figure 29 in lines 53 to 63 of column 27. JP 2955277 B2 illustrates the same structure in Figures 1 and 10. Both JP 2955277 B2 and Kubo et al. claim priority based on JP patent application No. 9-201176 filed Jul. 28, 1997.

[0006] Figure 14 is a plan view of one pixel portion of an active matrix substrate, illustrating gate lines 4 and source lines 5 that are disposed along the peripheries of pixel electrodes 3 and cross each other at right angles. TFTs 6 are formed in the vicinity of the respective crossings of the gate and source lines 4 and 5. A gate electrode and a source electrode of each TFT 6 are connected to the corresponding one gate line 4 and the corresponding one source line 5, respectively. Each of the pixel electrodes 3 includes a reflective region 7 formed of a metal film and a transmissive region 8 formed of indium/tin oxide (ITO).

[0007] In the reflective mode, the room light passes through the LC layer to the reflective regions 7 of the pixel electrodes 3. At the reflective regions 7, this light is reflected and returns through the LC layer to a viewer. In the transmissive mode, the light from a backlight passes through the transmissive regions 8 of the pixel electrodes 3 and the LC layer to the viewer.

[0008] In the reflective regions 7, the room light and the returned light pass through the LC layer in the opposite directions before reaching the viewer. In the transmissive regions 8, the light from the backlight passes through the LC layer once before reaching the viewer. When both reflective and transmissive modes are used simultaneously, the difference in optical path makes it difficult to optimize the output, such as brightness and contrast. One approach to this problem is found in the structure shown in Figure 15. According to this structure, the thickness of the LC layer  $d_r$  in the reflective regions 7 is different from the thickness of the LC layer  $d_t$  in the transmissive region 8. In the reflective regions 7, the thickness  $d_r$  is adjusted by adjusting the thickness of an insulating layer 17, which lies between a transmissive electrode 19 and on a reflective electrode 1. In Figure 15, the reference numeral 25 indicates a counter electrode.

[0009] To eliminate the difference in optical path, the setting is such that the ratio between the thickness  $d_r$  and the thickness  $d_t$  is about 1 : 2. This requires that the insulating layer 17 be thick almost as much as half the thickness of the LC layer. Thus, in the reflective regions 7, the insulating layer 17 with several micron meters thick is required, resulting in an increased fabrication processes. Besides, the provision of such insulating layer 17 in each reflective region 7 prevents the overlying transmissive electrode 19 from having surface flatness. The surface of the transmissive electrode 19 is coated with material to form an alignment film. The surface of the alignment film is not flat. With such alignment film, rubbing method may not provide a degree of alignment of LC molecules as high as expected.

[0010] An object of the present invention is to provide a liquid crystal device in which the difference in optical path between the reflective mode and transmissive mode is reduced with the plane surface of a substrate kept.

## SUMMARY OF THE INVENTION

[0011] According to one exemplary implementation of the present invention, a liquid crystal display (LCD) device comprises a first substrate including a thin film transistor (TFT), a second substrate, and a liquid crystal (LC) layer of liquid crystal molecules. The LC layer is interposed by the first and second substrates. The first substrate includes a reflective electrode in a reflective region and a transmissive electrode in a transmissive region. The LC layer includes a first group of liquid crystal molecules aligned in the reflective region to provide a first retardation and a second group of liquid crystal molecules aligned in the transmissive region to provide a second retardation. The second retardation is different from the first retardation.

## BRIEF DESCRIPTION OF THE DRAWINGS

[0012] The foregoing and other objects, features and advantages of the invention will be apparent from the following more particular description of exemplary implementations of the invention as illustrated in the accompanying drawings. The drawings are not necessarily scale, emphasis instead being placed upon illustrating the principles of the invention.

Figure 1 is a sectional view of one pixel portion of a transfective LCD device, illustrating a first exemplary implementation of the present invention.

Figures 2(a) to 2(f) are views illustrating fabrication steps for the first implementation.

Figure 3 is a similar view to Figure 1 illustrating different alignment modes of LC molecules in the first implementation, one within each of reflective regions and the other within each of transmissive regions.

Figure 4 is a sectional view of one pixel portion of a transfective LCD device, illustrating a second exemplary implementation of the present invention.

Figures 5(a) to 5(f) are views illustrating fabrication steps for the second implementation.

Figure 6 is a sectional view of one pixel portion of a transfective LCD device, illustrating a third exemplary implementation of the present invention.

Figures 7(a) to 7(f) are views illustrating fabrication steps for the third implementation.

Figure 8 is a very simplified view of one pixel portion of a transfective LCD device, illustrating a fourth exemplary implementation of the present invention.

Figures 9(a) to 9(i) are oversimplified views illustrating other possible arrangements of polarizers and quarter-wave plates.

Figure 10 is a very simplified view of one pixel portion of a transfective LCD device, illustrating a fifth exemplary implementation of the present invention.

Figure 11 is a graphical representation of varying of the strength of output light  $I_{\lambda}$  versus different values of thickness of LC layer, and varying of the strength of output light  $I_P$  versus different values of thickness of the LC layer.

Figures 12(a) and 12(b) are very simplified views of one pixel portion of a transfective LCD device, illustrating two embodiments of a sixth exemplary implementation of the present invention.

Figure 13 is a very simplified view of one pixel portion of a transfective LCD device, illustrating a seventh exemplary implementation of the present invention.

Figures 14 and 15 are views illustrating the discussed prior art.

## DESCRIPTION OF THE EXEMPLARY IMPLEMENTATIONS

[0013] Referring to the accompanying drawings, the same reference numerals are used to designate same or similar parts or portions throughout each view of Figures 1 to 13 for the sake of brevity of description.

[First Implementation of the Invention]

[0014] With reference to Figures 1 to 3, the first implementation of a transfective LCD device of the invention is described. As shown in Figure 1, this LCD device includes, within the device, a lower side (or TFT) substrate 11, a counter substrate 12, and a LC layer 13 interposed between the substrates 11 and 12. The LCD device employs an active matrix drive system, in which switching elements, such as, TFTs or MIM diodes, are attached to each pixel.

[0015] Within one pixel portion illustrated in Figure 1, the lower side substrate 11 includes an insulating substrate 14, a protective insulating film 15, a TFT 16, an insulating layer 17, a reflective electrode 18, and a transmissive electrode 19. The substrate 14 is coated with the protective film 15. Formed on the protective film 15 is the TFT 16. The TFT 16 includes a gate electrode 16a formed on the substrate 14, a drain electrode 16b formed on the protective film 15, a semiconductor layer 16c, and a source electrode 16d. The protective film 15 covers the gate electrode 16a and provides insulation from the drain electrode 16b.

**[0016]** The insulating layer 17 covers the TFT 16. The insulating layer 17 is formed with a contact hole 20 exposing a surface portion of the underlying source electrode 16d. The transmissive electrode 19 is formed over the insulating layer 17. A reflective electrode 18 is formed over a surface portion of the transmissive electrode 19. The reflective electrode 18 is connected to the transmissive electrode 19, which in turn is connected via the contact hole 20 to the source electrode 16d. The reflective electrode 18 and the transmissive electrode 19 are a pixel electrode.

**[0017]** Material, such as polyimide, covers the reflective and transmissive electrodes 18 and 19 to deposit an alignment film. The alignment film is dividable into and consists of a vertical alignment film portion 21 and a horizontal alignment film portion 22, within a reflective mode region, the vertical alignment film 21 coextends with the reflective electrode 18. Within a transmissive mode region, the horizontal alignment film 22 coextends with the exposed surface portion of the transmissive electrode 19. Another alignment film covers the surface of the counter substrate 12, which faces the LC layer 13. In this implementation, the mode of alignment of LC molecules within the reflective mode region including the reflective electrode 18 is a HAN alignment mode. The mode of alignment of LC molecules within the transmissive mode region including the exposed transmissive electrode 19 is a homogeneous alignment mode or a TN alignment mode.

**[0018]** Within a terminal region on the peripheral portions of the lower side substrate 11, a gate terminal 23 is formed on the insulating substrate 14, and a drain terminal 24 is formed on the protective insulating film 15.

**[0019]** Viewing from the nearest side to the LC layer 13, the counter substrate 12 includes a counter electrode 25, a color filter 26, and an insulating substrate 27, which are formed one on another in this order. The incident light to the insulating substrate 27 of the counter substrate 12 passes through the LC layer 13 to the reflective region of each pixel. At the reflective region, this light is reflected by the reflective electrode 18 and returns through the LC layer 13, counter electrode 25 and counter substrate 12 to a viewer.

**[0020]** A backlight 28 is attached to the remote side of the underside substrate 11 from the LC layer 13. The light from the backlight 28 passes through the substrate 14, protective film 15, insulating layer 17 and transmissive electrode 19 of the transmissive region of each pixel to the LC layer 13, and this light passes through the LC layer 13, transmissive electrode 25 and counter substrate 12 to the viewer.

**[0021]** With reference to the fabrication steps illustrated in Figures 2(a) to 2(f), the first implementation of the invention is further described.

**[0022]** Referring to Figure 2(a), a TFT 16 is completed by forming a gate electrode 16a on an insulating substrate 14, coating the gate electrode 16a and insulating substrate 14 with a protective insulating film 15, and forming a drain electrode 16b, a semiconductor layer 16c, and a source electrode 16d on the protective film 15.

**[0023]** Referring to Figure 2(b), the TFT 16 is covered with an insulating layer 17. The insulating layer 17 is formed with a contact hole 20 reaching the underlying source electrode 16d. It is to be noted that the TFT 16 is one of various examples of a switching element, which may be used in an active matrix drive system. Another example of the switching element is a MIM diode.

**[0024]** Referring to Figure 2(c), a transmissive electrode 19 of ITO is formed over the insulating layer 17. The material of the transmissive electrode 19 fills the contact hole 20 to establish electrical connection between the transmissive electrode 19 and the source electrode 16d.

**[0025]** Referring to Figure 2(d), with a portion being masked, a film of an electrically conductive material such as aluminum (Al) is formed over the unmasked portion of the transmissive electrode 19 to form a reflective electrode 18. Another method is to form an electrically conductive film over the whole surface area of the transmissive electrode 19. With a portion being masked, the electrically conductive material within the unmasked portion may be removed by etching.

**[0026]** Referring to Figure 2(e), a polyimide material containing long-alkyl side chain is coated onto the reflective electrode 18 and transmissive electrode 19 and dried by heating to form a polyimide alignment film. The presence of long-alkyl side chain is known to induce a pretilt angle of LC almost as large as 90 degrees. Using a mask 29 to cover the alignment film overlying the reflective electrode 18, an ultraviolet (UV) light is applied to the alignment film as indicated by arrows. Due to exposure to the UV light, separation of long-alkyl side chain takes place within the unmasked portion overlying the transmissive electrode 19, resulting in a considerable reduction in pretilt angle in the LC.

**[0027]** Referring to Figure 2(f), after exposure to UV light, the mask 29 is removed, and the polyimide alignment film is rubbed in a direction for the LC molecules to align. That portion of the polyimide alignment film that coats the reflective electrode 18 becomes a vertical alignment film 21 because it was not exposed to the UV light. The remaining portion of the polyimide alignment film that coats the transmissive electrode 19 becomes a horizontal alignment film 22. In the implementation, the rubbing treatment follows the exposure to UV light. The same result occurs by exposing the polyimide alignment film to UV light after rubbing treatment. This is because the separation of long-alkyl side chain takes place by the UV light exposure treatment in the same manner before or after the rubbing treatment.

**[0028]** The polyimide material coated on the counter substrate 12 becomes a horizontal alignment film 22 due to the exposure to UV light and the rubbing treatment. As a result, as shown in Figure 3, the mode of alignment of LC molecules within the reflective mode region including the reflective electrode 18 becomes a HAN alignment mode, and the mode

of alignment of LC molecules within the transmissive mode region including the exposed transmissive electrode 19 becomes a homogeneous alignment mode or a TN alignment mode.

**[0029]** Different orientations of alignment of the LC molecules provide different reflective indexes. The fabrication process described in connection with Figures 2(a) to 2(f) has provided different modes of alignment of LC molecules in the reflective mode region above the reflective electrode 18 and in the transmissive mode region above the transmissive electrode 19. Using different indexes of refraction provided by different modes of alignment of LC molecules, different values in retardation ( $\Delta n \times d$ ) are provided in the reflective mode region and in the transmissive mode region. Thus, with the same thickness of the LC cell, sufficiently high brightness is provided in the reflective mode as well as in the transmissive mode.

**[0030]** In the present application, the expression that "a difference in mode of alignment of LC molecules" is intended to mean a difference in twist angle of the same alignment mode, for example, TN mode, which causes a difference in retardation. Such difference in twist angle of the same alignment mode can be obtained by rubbing the counter substrate in one direction and subjecting the alignment film of the lower side or TFT substrate to optimal treatment. First step of the treatment is to expose the alignment film on the TFT substrate to linear polarized light such that the twist angle of LC molecules as much as about 70 degrees takes place. This step is followed by a second step of exposing the laminated film. With its reflective region masked, to linear polarized light such that the twist angle of about zero degree takes place in the unmasked transmissive region. In this manner, the twist angle of LC molecules becomes about 70 degrees in the reflective region, and the twist angle of the LC molecules in the transmissive region becomes about zero degree. In this manner, a difference in retardation between the reflective region and the transmissive region is accomplished.

#### [Second Implementation of the Invention]

**[0031]** With reference to Figures 4 to 5(f), the second implementation of a transfective LCD device of the invention is described. The second implementation is substantially the same as the first implementation except the provision of a color filter 30 instead of the insulating layer 17.

**[0032]** Figure 4 is the same view as Figure 1, illustrating that a lower side substrate 11 includes the color filter 30 instead of the insulating layer 17. The color filter 30 covers a TFT 16 and it is formed with a contact hole 20 exposing a surface portion of a source electrode 16d.

**[0033]** With reference to the fabrication steps illustrated in Figures 5(a) to 5(f), the second implementation of the invention is further described. The fabrication steps illustrated in Figures 5(a) to 5(f) are substantially the same as those illustrated in Figures 2(a) to 2(f), respectively.

**[0034]** Referring to Figure 5(b), the TFT 16 is covered with a color filter 30. The color filter 30 is formed with a contact hole 20 reaching the underlying source electrode 16d. The color filter 30 is fabricated by color-resist method. In the color-resist method, a photolithography technique is used to form the color patterns. The color-resist is made by diffusing pigment (red or green or blue or black) in a photosensitive acrylic polymer resin.

**[0035]** The second implementation is different in optical path from the first implementation. In the second implementation, in the transmissive mode, light from a backlight 28 passes through the color filter 30 of the under side or TFT substrate 11 before passing through a color filter 26 of a counter substrate 12. In the reflective mode, the incident light to the counter substrate 12 passes through the color filter 26, and the reflected return light passes again through the color filter 26. In each of the modes, light passes through one color filter and then through the same or another color filter, making color correction easy for eliminating a difference in color expression between the two modes or for independent color correction in each of the modes.

#### [Third Implementation of the Invention]

**[0036]** With reference to Figures 6 to 7(e), the third implementation of a transfective LCD device of the invention is described. The third implementation is similar to the second implementation in that a color filter 30 is used instead of the insulating layer 17 used in the first implementation. Comparing Figure 6 to Figure 4 clearly reveals that, in the third implementation, a counter substrate 12 does not have a color filter, and a lower side or TFT substrate 11 has a reflective electrode 18 formed on the surface of a protective insulating film 15. The reflective electrode 18 is in contact with a source electrode 16d of a TFT 16 and connected to a transmissive electrode 19 via a contact hole 20 formed through the color filter 30. The color filter 30 underlying the transmissive electrode 19. As readily seen from Figure 6, the thickness of the color filter 30 overlying the reflective electrode 18 in the reflective region is less than the thickness of the color filter 30 in the transmissive region. This difference in the thickness of color filter 30 can be adjusted by altering the thickness of the reflective electrode 18.

**[0037]** The third implementation is different in optical path from the second implementation. In the third implementation, in the transmissive mode, light from a backlight 28 passes through the color filter 30 of the under side or TFT

substrate 11 once. In the reflective mode, the incident light to the counter substrate 12 passes through the color filter 30, and the reflected return light passes again through the color filter 30. As mentioned above, altering the thickness of the reflective electrode 18 can provide a desired difference in the thickness of color filter 30, making color correction easy for eliminating a difference in color expression between the two modes. Beside, there is no need to provide a color filter in the counter substrate 12, leading to a reduction in fabrication cost. An error inherent with positioning a color filter within the counter substrate no longer exists, providing enhanced display.

**[0038]** With reference to the fabrication steps illustrated in Figures 7(a) to 7(e), the third implementation of the invention is further described. Figures 7(a) to 7(c) are substantially the same as Figures 5(a) to 5(c), respectively, except the formation of the reflective electrode 18 in Figure 7(a) in the third implementation. In the second implementation, the reflective electrode 18 is formed on the transmissive electrode 19 in Figure 5(d). Accordingly, this implementation does not have a fabrication step corresponding to the fabrication step illustrated in Figure 5(d). Figures 7(d) and 7(e) are substantially the same as Figures 5(e) and 5(f), respectively.

**[0039]** Referring to Figure 7(a), a gate electrode 16a is formed on an insulating substrate 14. The gate electrode 16a and the insulating substrate 14 are coated with a protective insulating film 15. A reflective electrode 18 is formed on the protective film 15 in contact with a source electrode 16d. Forming a drain electrode 16b and a semiconductor layer 16c completes a TFT 16.

**[0040]** Referring to Figure 7(b), the TFT 16 and reflective electrode 18 are covered with a color filter 30. The color filter 30 is formed with a contact hole 20 reaching the underlying reflective electrode 18.

**[0041]** Referring to Figure 7(c), a transmissive electrode 19 of ITO is formed over the color filter 30. The material of the transmissive electrode 19 fills the contact hole 20 to establish electrical connection between the transmissive electrode 19 and the reflective electrode 18.

**[0042]** The fabrication steps of Figures 7(d) and 7(e) are substantially the same as the fabrication steps of Figures 5(e) and 5(f), which in turn are substantially the same as the fabrication steps of Figures 2(e) and 2(f). Accordingly, a description on Figures 7(d) and 7(e) is hereby omitted for brevity.

#### [Fourth Implementation of the Invention]

**[0043]** With reference to Figure 8, the fourth implementation of a transfective LCD device of the invention is described. This implementation is applicable to any one of the first, second and third implementations if the mode of a LC layer 13 is of the TN type. According to the fourth implementation, a lower side substrate 11 includes a polarizer 31 and a quarter-wave plate 32, and a counter substrate 12 includes another polarizer 31 and another quarter-wave plate 32.

**[0044]** In the lower side substrate 11 shown in Figure 8, the polarizer 31 is positioned between an insulating substrate 14 and a backlight 28. The quarter-wave plate 32 is positioned between the insulating substrate 14 and an insulating layer 17 (or a color filter 30, see Figures 4 and 6). In the counter substrate 12 shown in Figure 8, the quarter-wave plate 32 is positioned on the nearest side of an insulating substrate 27 to the LC layer 13. The polarizer 31 is positioned on the remote side of the insulating substrate 24 from the LC layer 13.

**[0045]** The polarizers 31 are oriented orthogonally to each other. Each of the polarizers 31 has a transmission axis. In the lower side substrate 11, the fast and slow axes of the quarter-wave plate 32 should each be oriented at an angle of substantially 45 degrees relative to the orientation of the transmission axis of the polarizer 31. In the counter substrate 12, the fast and slow axes of the quarter-wave plate 32 should each be oriented at an angle of substantially 45 degrees relative to the orientation of the transmission axis of the polarizer 31.

**[0046]** In the reflective mode, when the LC layer 13 is twisted, the polarizer 31 of the counter substrate 12 receives ambient light and linearly polarizes the light. The quarter-wave plate 32 converts the linearly polarized light into right handed circularly polarized light. The twisted LC layer 13 converts the right handed circularly polarized light into linearly polarized light. At the reflective electrode 18, the linearly polarized light is reflected and returns to the twisted LC layer 13. The twisted LC layer 13 converts the linearly polarized return light to right handed circularly polarized return light. The quarter-wave plate 32 of the counter substrate 12 converts the right handed circularly polarized return light to linearly polarized return light. The linearly polarized return light passes through the polarizer 31 of the counter substrate 12 to a viewer.

**[0047]** In the reflective mode, when the LC layer 13 is vertically aligned, the polarizer 31 of the counter substrate 12 receives ambient light and linearly polarizes the light. The quarter-wave plate 32 converts the linearly polarized light into right handed circularly polarized light. The right handed circularly polarized light passes through the vertically aligned LC layer 13, is reflected at the reflective electrode 18 to become left handed circularly polarized return light. The left handed circularly polarized return light passes through the vertically aligned LC layer 13. The quarter-wave plate 32 of the counter substrate 12 converts the left handed circularly polarized return light to linearly polarized return light. This linearly polarized return light cannot pass through the polarizer 31 of the counter substrate 12.

**[0048]** In the transmissive mode, when the LC layer 13 is twisted, the polarizer 31 of the lower side substrate 11

receives light from the backlight 28 and linearly polarises the light. The quarter-wave plate 32 converts the linearly polarised light into left handed circularly polarized light. The twisted LC layer 13 converts the left handed circularly polarized light into right handed circularly polarized light. The quarter-wave plate 32 of the counter substrate 12 converts the right handed circularly polarized light to linearly polarized light. The linearly polarized light passes through the polarizer 31 of the counter substrate 12 to the viewer.

**[0049]** In the transmissive mode, when the LC layer 13 is vertically aligned, the polarizer 31 of the lower side substrate 11 receives light from the backlight 28 and linearly polarizes the light. The quarter-wave plate 32 converts the linearly polarized light into left handed circularly polarized light. The left handed circularly polarized light passes through the vertically aligned LC layer 13. The quarter-wave plate 32 of the counter substrate 12 converts the left handed circularly polarized light to linearly polarized light. This linearly polarized light cannot pass through the polarizer 31 of the counter substrate 12.

**[0050]** As shown in Figure 8, the fourth implementation has inside quarter-wave plates 32, which are protected against UV radiation and humidity by the insulating substrates 14 and 27 and the polarizers 31.

**[0051]** In the fourth implementation, the quarter-wave plates 32 are separated from the respective polarizers 31 and positioned on the near sides of the insulating substrates 14 and 27 to the LC layer 13. This arrangement no longer requires adhesive that was used between the polarizer and the quarter-wave plate, providing greater freedom in selecting an appropriate material of the quarter-wave plate.

**[0052]** The quarter-wave plate is formed of material exhibiting liquid crystal property, which will induce alignment of LCs. Thus, if the quarter-wave plates are positioned in place of alignment films, the provision of alignment films and rubbing treatment may be eliminated. In the case LC cells are in the form of 90 degrees twist structure, it is no longer needed to subject the underside and counter substrates to alignment treatment. In the case LC cells are of the HAN type, the rubbing treatment may be eliminated.

**[0053]** It is possible to arrange quarter-wave plates in a manner to interpose LC cells. This arrangement is effective in preventing light from the adjacent pixels to pass through the relatively thin insulating substrates, each having 500 to 700  $\mu\text{m}$  thick.

**[0054]** Figures 9(a) to 9(i) are oversimplified views illustrating other possible arrangements of polarizers 31 and quarter-wave plates 32.

#### [Fifth Implementation of the Invention]

**[0055]** With reference to Figure 10, the fifth implementation of a transmissive LCD device of the invention is described. This implementation is applicable to any one of the first, second and third implementations if the mode of a LC layer 13 is of the TN type. According to the fifth implementation, a lower side substrate 11 includes a polarizer 31 and a quarter-wave plate 32, and a counter substrate 12 includes another polarizer 31 and another quarter-wave plate 32. However, the quarter-wave plate 32 of the counter substrate 12 is not provided in the transmissive region.

**[0056]** In the lower side substrate 11 shown in Figure 8, the polarizer 31 is positioned between an insulating substrate 14 and a backlight 28. The quarter-wave plate 32 is positioned between the insulating substrate 14 and an insulating layer 17 (or a color filter 30, see Figures 4 and 6). In the counter substrate 12 shown in Figure 8, the quarter-wave plate 32 is positioned on the nearest side of an insulating substrate 27 to the LC layer 13. The polarizer 31 is positioned on the remote side of the insulating substrate 24 from the LC layer 13.

**[0057]** A photoresist and etching technique is used to remove the portion of the quarter-wave plate 32 of the counter substrate 12 to form an opening in the transmissive region.

**[0058]** The following two paragraphs provide a description on the relationship between the strength of output light and quarter-wave plate in the transmissive mode.

**[0059]** In the transmissive mode, light from a backlight 28 passes through the polarizer 31 and quarter-wave plate 32 of the lower side substrate 11 to a LC layer 13. After passing through the LC layer 13, light passes through the quarter-wave plate 32 and the polarizer 31 of the counter electrode 12. The strength of output light  $I_\lambda$  is expressed as,

$$I_\lambda = (1/2)[(\Gamma/2) \times (1/X) \times \sin X]^2 \quad \text{Eq. 1}$$

where,

$\lambda$  = Wavelength of light ( $\Delta n \times d$ ) = Retardation of LC layer 13

$\phi$  = Twist angle of LC molecules

$\Gamma = 2\pi(\Delta n \times d)/\lambda$

$X = [\phi^2 + (\Gamma/2)^2]^{1/2}$

In the transmissive mode, light from the backlight passes through the polarizer 31 of the underside substrate 11 to the LC layer 13, and after passing through the LC layer 13, light passes through the polarizer 31 of the counter substrate 12. In this case, the light does not pass any one of the quarter-wave plates 32. The strength of output light  $I_p$  is expressed as,

$$I_p = (1/2) [(1/X) \times \sin X]^2 [\phi^2 \times \cos^2 \phi + \sin^2 \phi \times (\Gamma/2)^2] + \sin^2 \phi \cos^2 X - \phi \sin 2\phi \cos X [(1/X) \times \sin X] \quad \text{Eq. 2}$$

**[0060]** Figure 11 provides a curve interconnecting the plotted values of  $I_\lambda$  calculated for different values of thickness of LC layer, and another curve interconnecting the plotted values of  $I_p$  calculated for different values of thickness of the LC layer. In the reflective mode, the thickness  $d_r$  of the LC layer when the strength of output light is the maximum is about 3  $\mu\text{m}$ . When a LCD device is designed with the thickness of LC layer around 3  $\mu\text{m}$ , the strength of output light  $I_p$  exceeds the strength of output light  $I_\lambda$ .

**[0061]** Apparently, the above consideration suggests that, in the fifth implementation of Figure 10, removing the quarter-wave plate 32 of the lower side substrate 11 will increase the strength of output light in transmissive mode as well as in the reflective mode.

#### [Sixth Implementation of the Invention]

**[0062]** With reference to Figures 12(a) and 12(b), the sixth implementation of a transfective LCD device of the invention is described. This implementation features the provision of three layer filter/reflector 33 of cholesteric liquid crystal (CLC) layers which reflect left or right handed circularly polarized blue, green and red light.

**[0063]** The three-layer filter/reflector 33 is positioned between an insulating substrate 14 of a lower side substrate 11 and a backlight 28. In a counter substrate 12, a quarter-wave plate 32 is positioned on the near side of an insulating substrate 27 to a LC layer 13.

**[0064]** In the sixth implementation, the three-layer filter/reflector 33 of CLC layers have replaced a quarter-wave plate and a polarizer in the lower side substrate 11. In the transmissive mode, the reflected circularly polarized light passes to the viewer. To prevent this, two quarter-wave plates 32 are positioned on the near side of the three-layer filter/reflector 33 of CLC layers to the LC layer 13 as illustrated in Figure 12(a). As illustrated in Figure 12(b), a single quarter-wave plate 32 may be positioned on the near side of the three-layer filter/reflector 33 of CLC layers to the LC layer 13. In this case, a quarter-wave plate may be removed from counter substrate 12.

#### [Seventh Implementation of the Invention]

**[0065]** With reference to Figures 13, the seventh implementation of a transfective LCD device of the invention is described. This implementation features the provision of two quarter-wave plates 32 and three-layer filter/reflector 33 of CLC layers, and the elimination of a quarter-wave 32 of a counter substrate 12 in the transmissive region.

**[0066]** The three-layer filter/reflector 33 is positioned between an insulating substrate 14 of a lower side substrate 11 and a backlight 28. The quarter-wave plate 32 is positioned between an insulating substrate 14 and the three-layer filter/reflector 33. In a counter substrate 12, a quarter-wave plate 32 is positioned on the near side of an insulating substrate 27 to a LC layer 13.

**[0067]** A photoresist and etching technique is used to remove the portion of the quarter-wave plate 32 of the counter substrate 12 to form an opening in the transmissive region.

**[0068]** While the present invention has been particularly described, in conjunction with exemplary implementations, it is evident that many alternatives, modifications and variations will be apparent to those skilled in the art in light of the foregoing description. It is therefore contemplated that the appended claims will embrace any such alternatives, modifications and variations as falling within the true scope and spirit of the present invention.

**[0069]** This application claims the priority of Japanese Patent Application No. P2001-251089, filed August 22, 2001, the disclosure of which is hereby incorporated by reference in its entirety.

## Claims

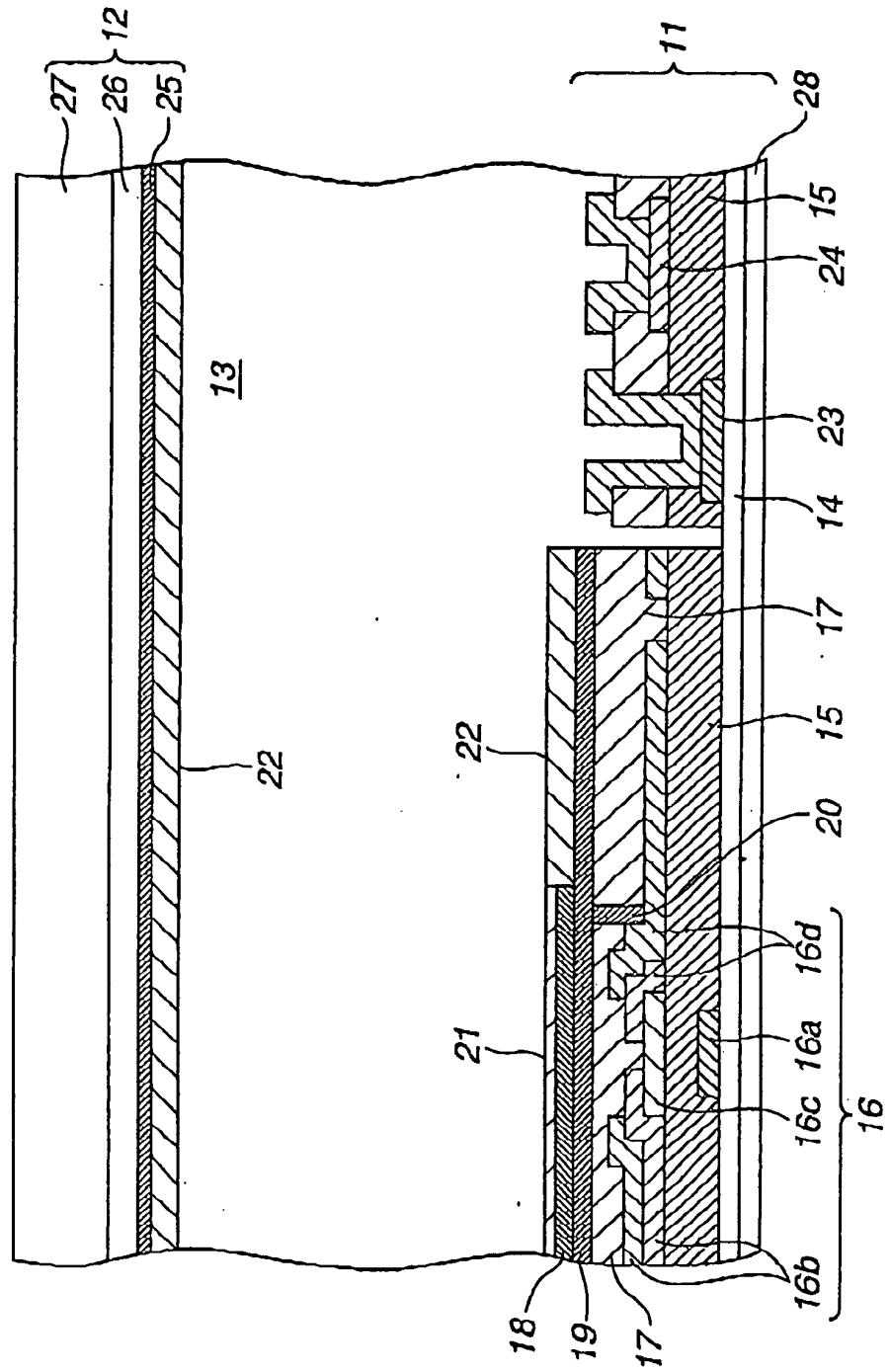
1. A liquid crystal display (LCD) device, comprising:



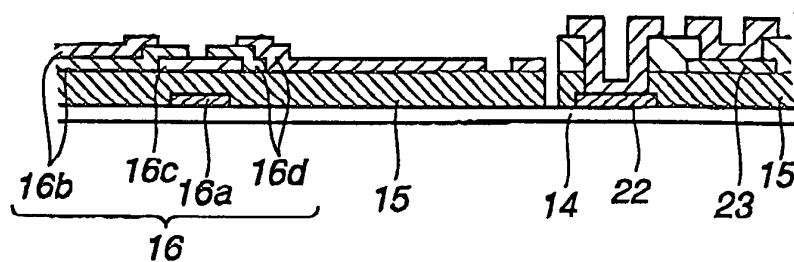
a first substrate including a thin film transistor (TFT);  
a second substrate; and  
a liquid crystal (LC) layer of liquid crystal molecules, the LC layer being interposed by the first and second  
substrates;  
the first substrate including a reflective electrode in a reflective region and a transmissive electrode in a trans-  
missive region,  
the LC layer including a first group of liquid crystal molecules aligned in the reflective region to provide a first  
retardation and a second group of liquid crystal molecules aligned in the transmissive region to provide a  
second retardation that is different from the first retardation.

2. The LCD device as claimed in claim 1, wherein the first substrate further includes an insulating layer formed over the TFT, and the reflective electrode is formed on the insulating layer, and wherein the second substrate includes a color filter.
3. The LCD device as claimed in claim 1, wherein the first substrate further includes a color filter formed over the reflective electrode and the TFT, the color filter having a first thickness in the reflective region and a second thickness in the transmissive region, the first thickness being less than the second thickness.
4. The LCD device as claimed in claim 1, wherein the first substrate further includes a color filter formed over the TFT, and the reflective electrode is formed on the color filter, and wherein the second substrate includes another color filter.
5. The LCD as claimed in one of claims 1 to 4, wherein said transmissive electrode is electrically connected to the reflective electrode.
6. The LCD device as claimed in one of claims 1 to 5, wherein the liquid crystal molecules are aligned in at least one mode selected from homogeneous alignment mode, homeotropic alignment mode, TN alignment mode, HAN alignment mode, and OCB alignment mode.
7. The LCD device as claimed in one of claims 1 to 6, wherein the first group of liquid crystal molecules is aligned in HAN alignment mode, and the second group of liquid crystal molecules is aligned in one of homogeneous alignment mode, TN alignment mode, and OCB alignment mode.
8. The LCD device as claimed in one of claims 1 to 7, wherein the second substrate includes a quarter-wave plate.
9. The LCD device as claimed in claim 8, wherein the quarter-wave plate has an opening in the transmissive region.
10. The LCD device as claimed in claim 8 or 9, further comprising cholesteric liquid crystal (CLC) layer on the remote side of the second substrate from the LC layer.
11. The LCD device as claimed in claim 8, 9 or 10, wherein the first substrate includes a second quarter-wave plate.
12. The LCD device as claimed in claim 9, further comprising cholesteric liquid crystal (CLC) layer on the remote side of the second substrate from the LC layer, and a second quarter-wave plate between the CLC layer and the first substrate.

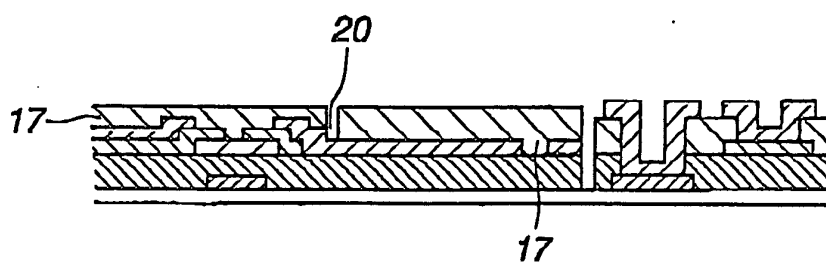
FIG.1



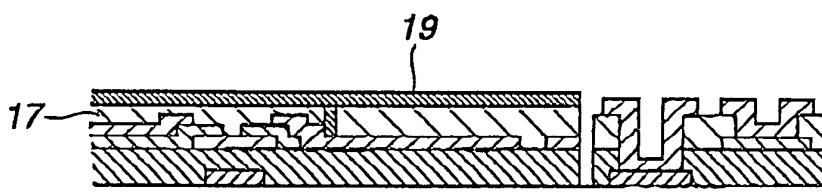
**FIG.2(a)**



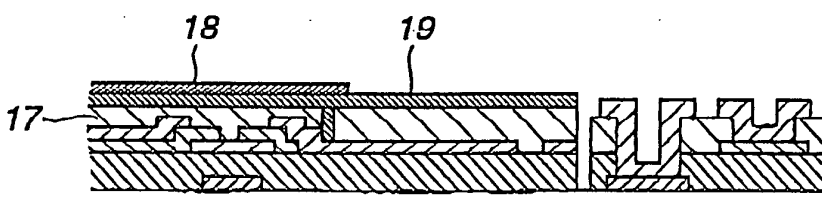
**FIG.2(b)**



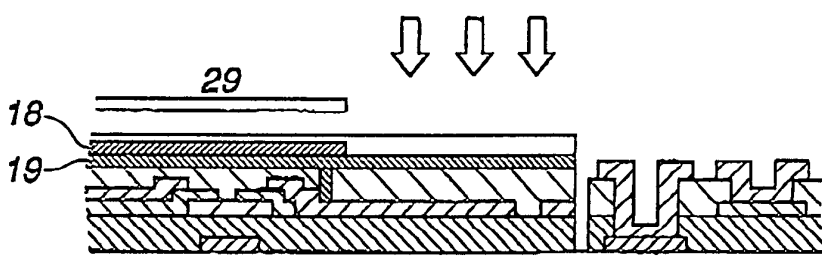
**FIG.2(c)**



**FIG.2(d)**



**FIG.2(e)**



**FIG.2(f)**

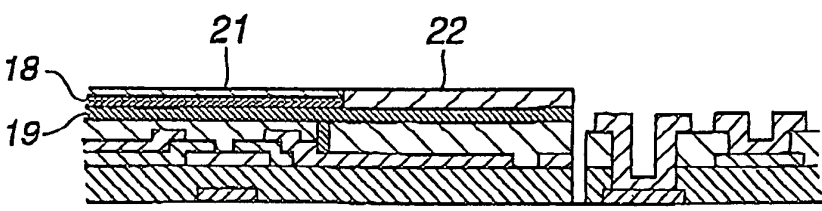
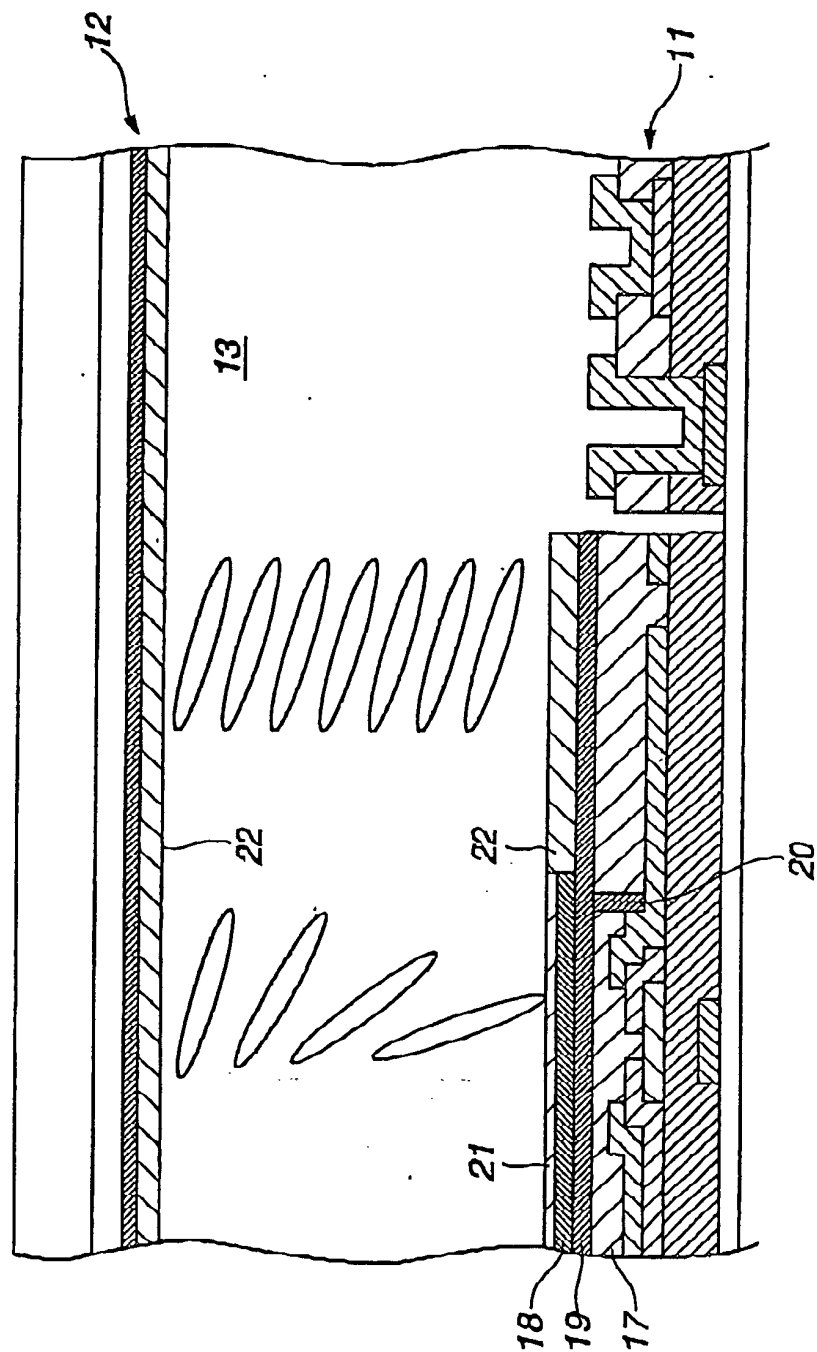
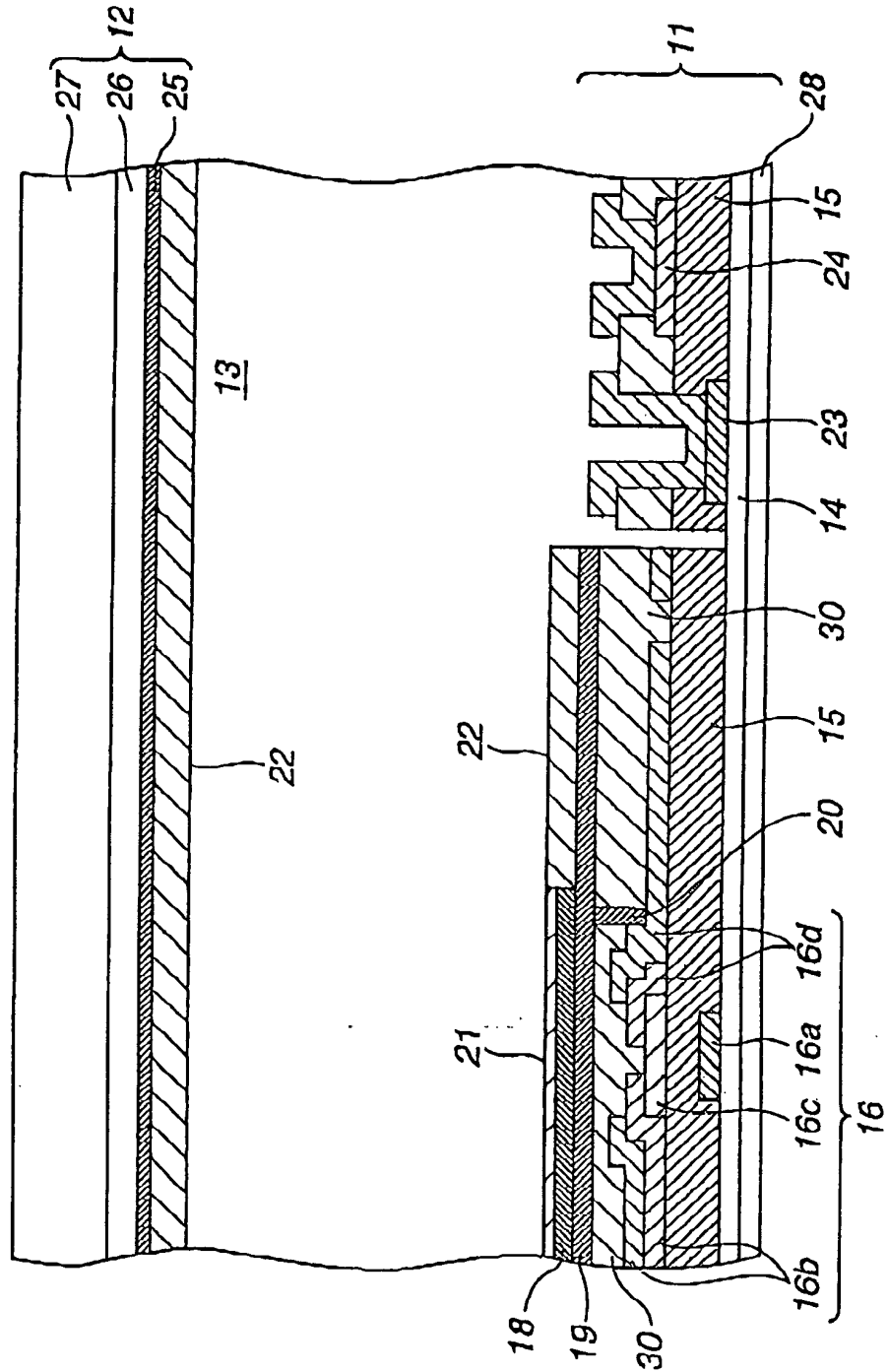


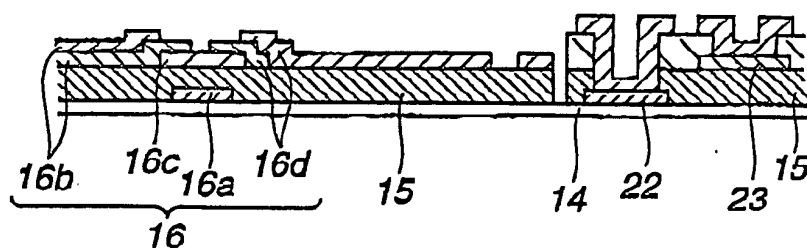
FIG.3



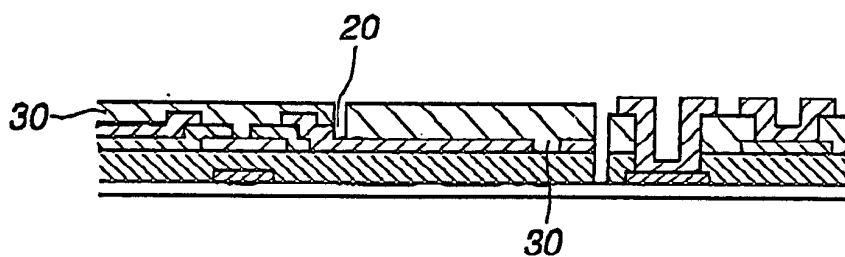
**FIG. 4**



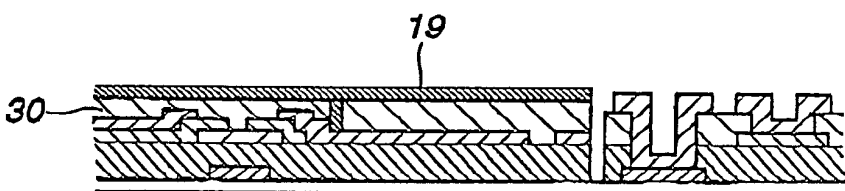
**FIG.5(a)**



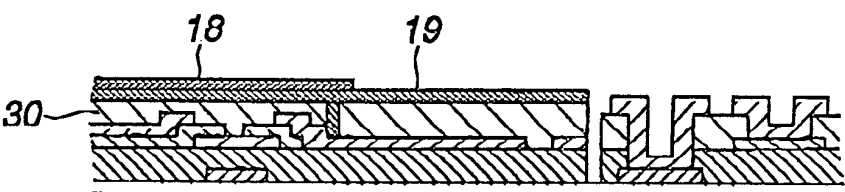
**FIG.5(b)**



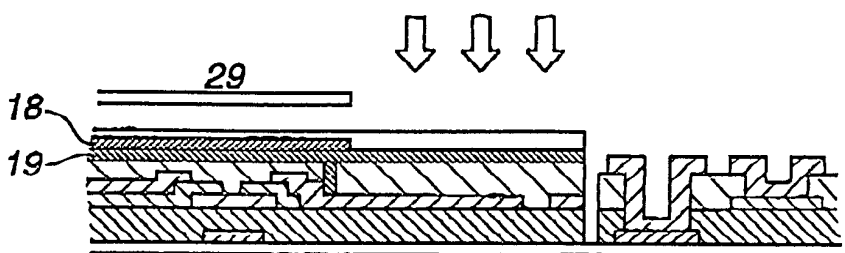
**FIG.5(c)**



**FIG.5(d)**



**FIG.5(e)**



**FIG.5(f)**

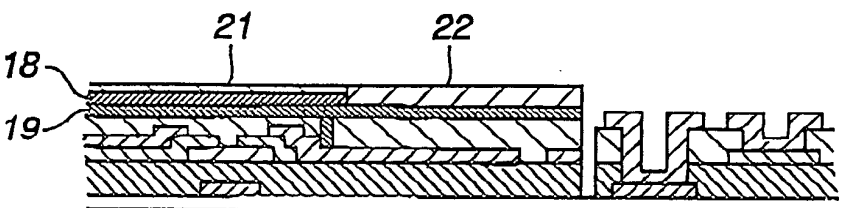
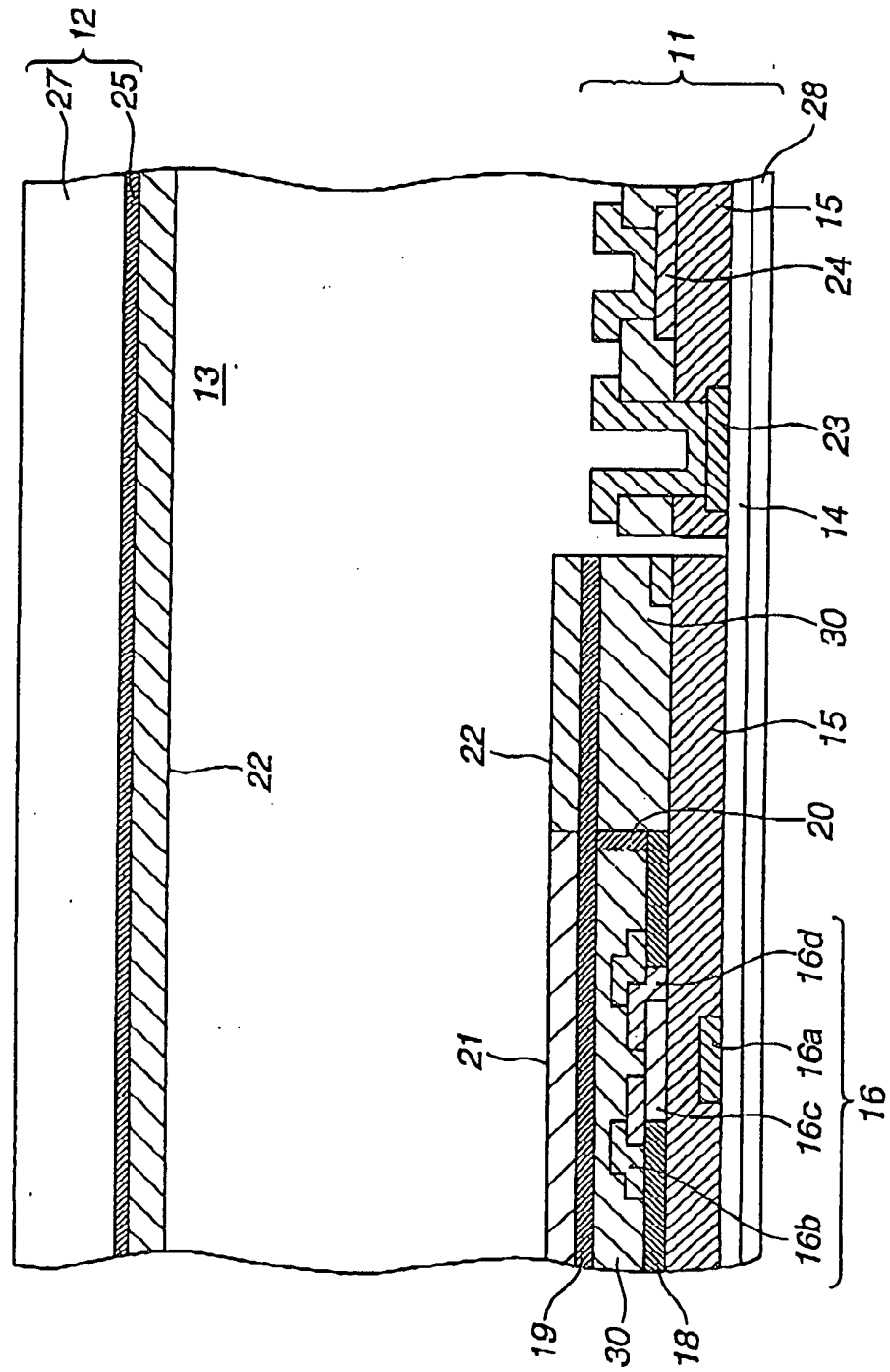
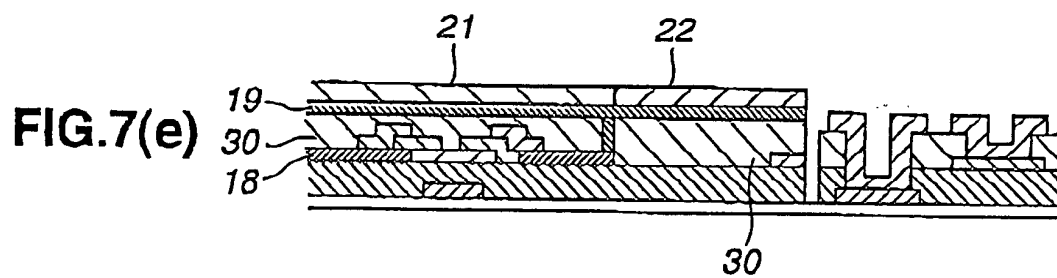
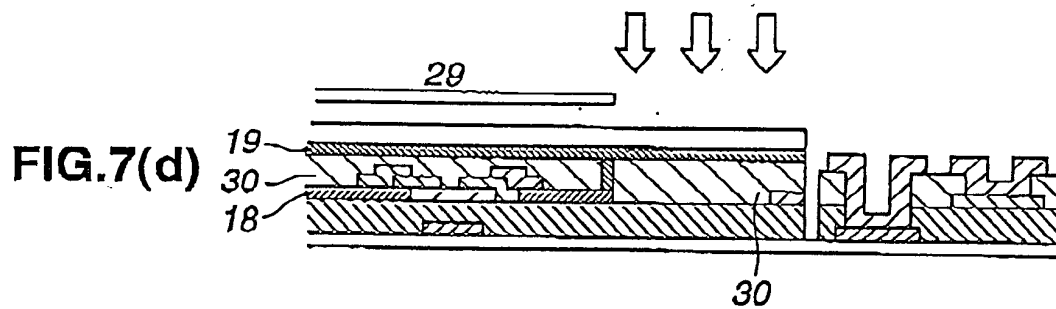
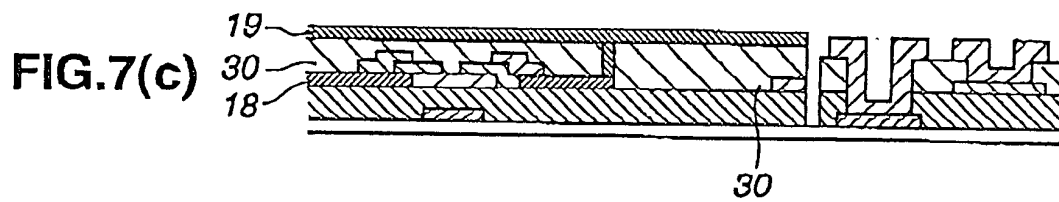
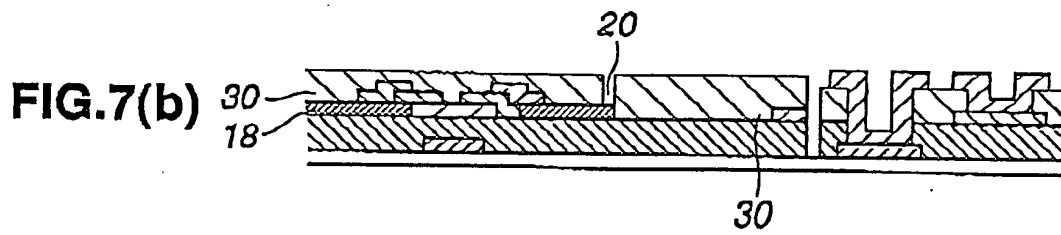
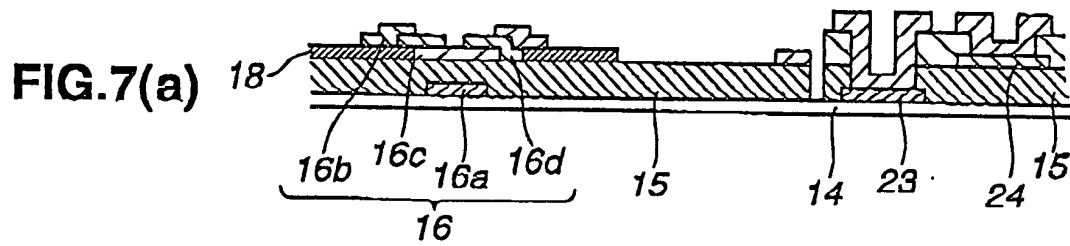


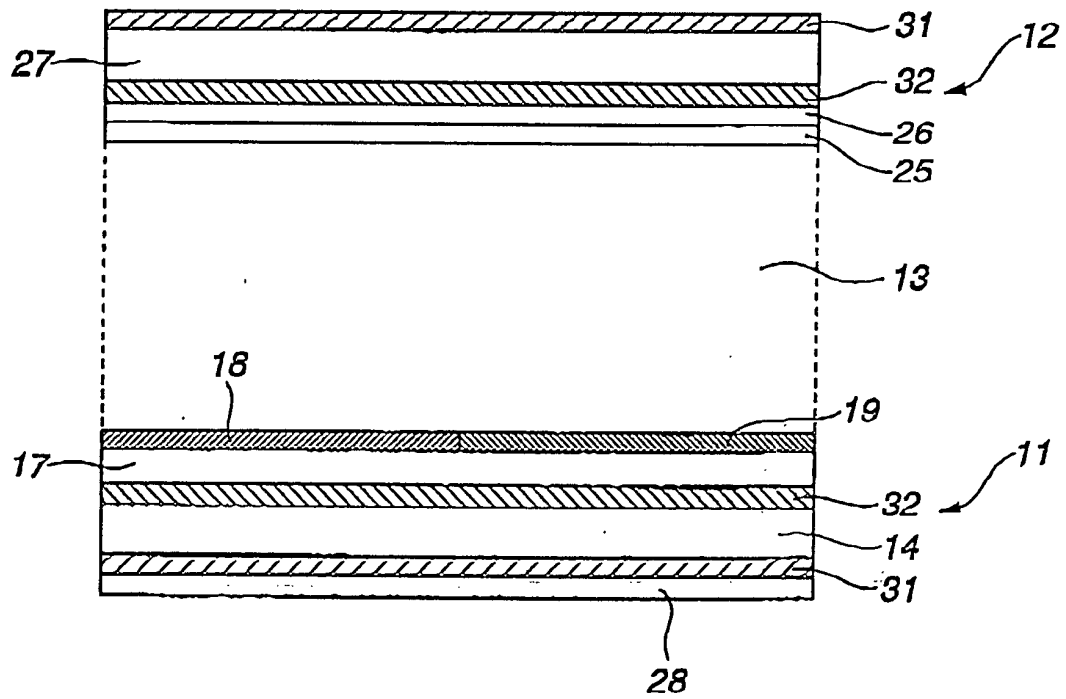
FIG.6

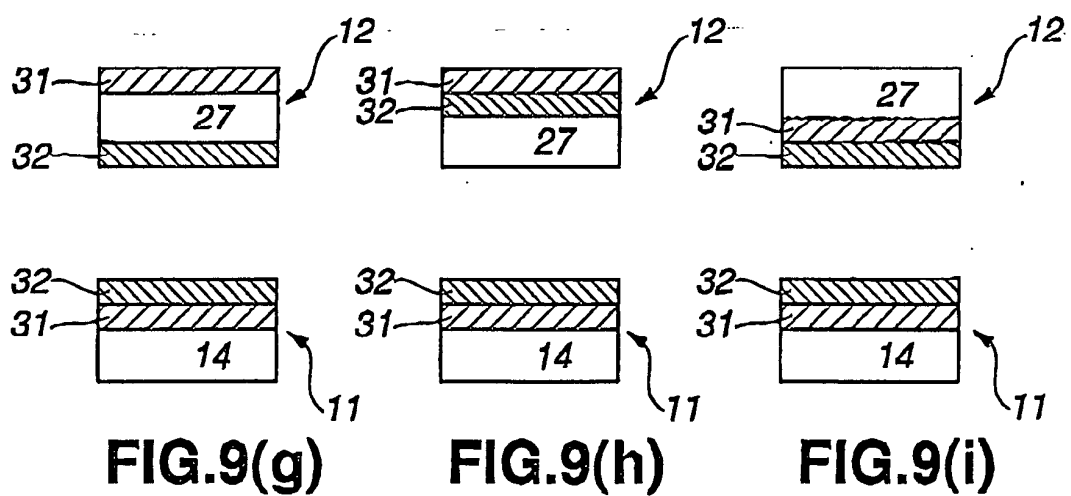
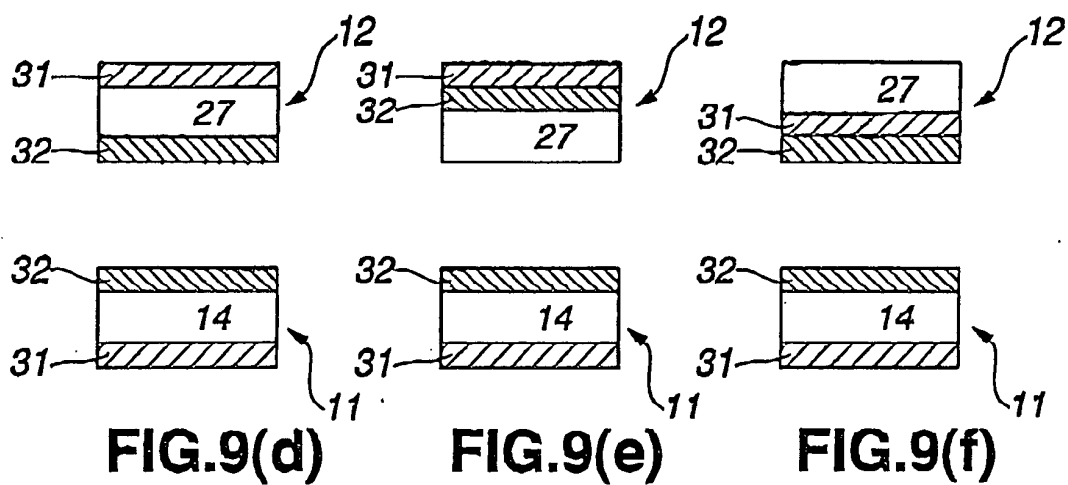
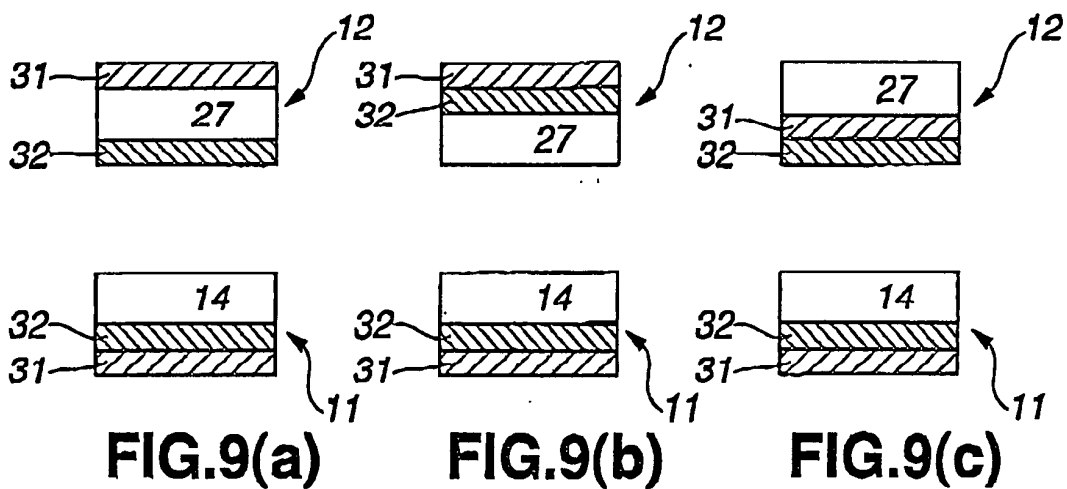






**FIG.8**





**FIG.10**

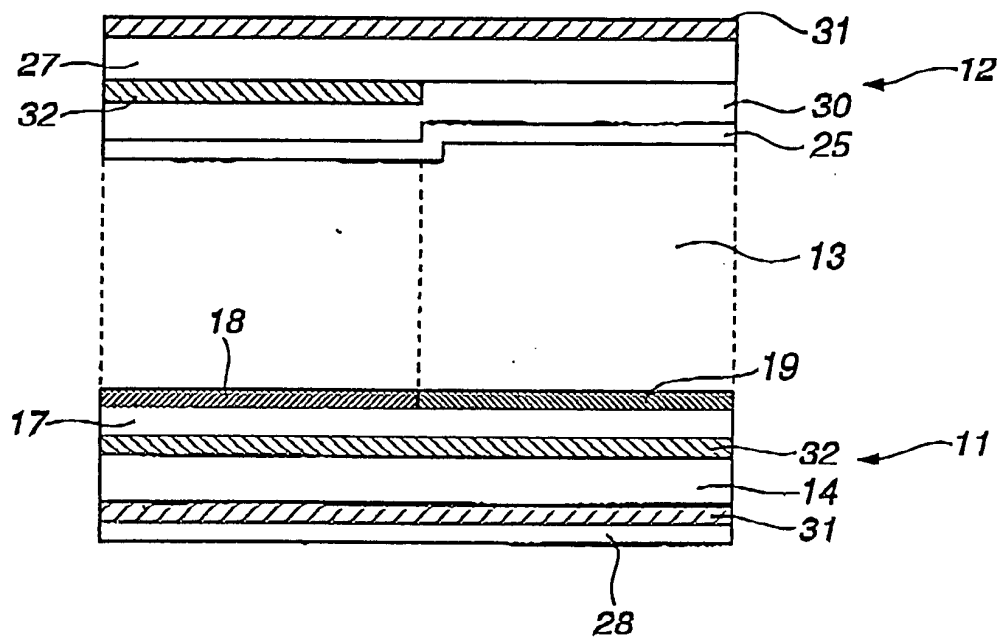
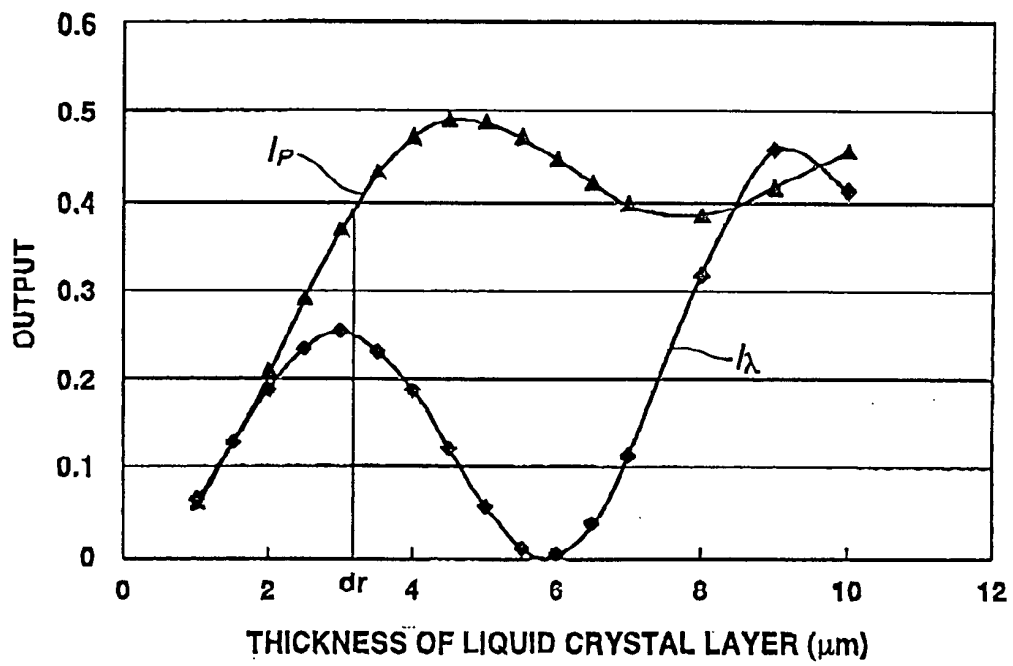
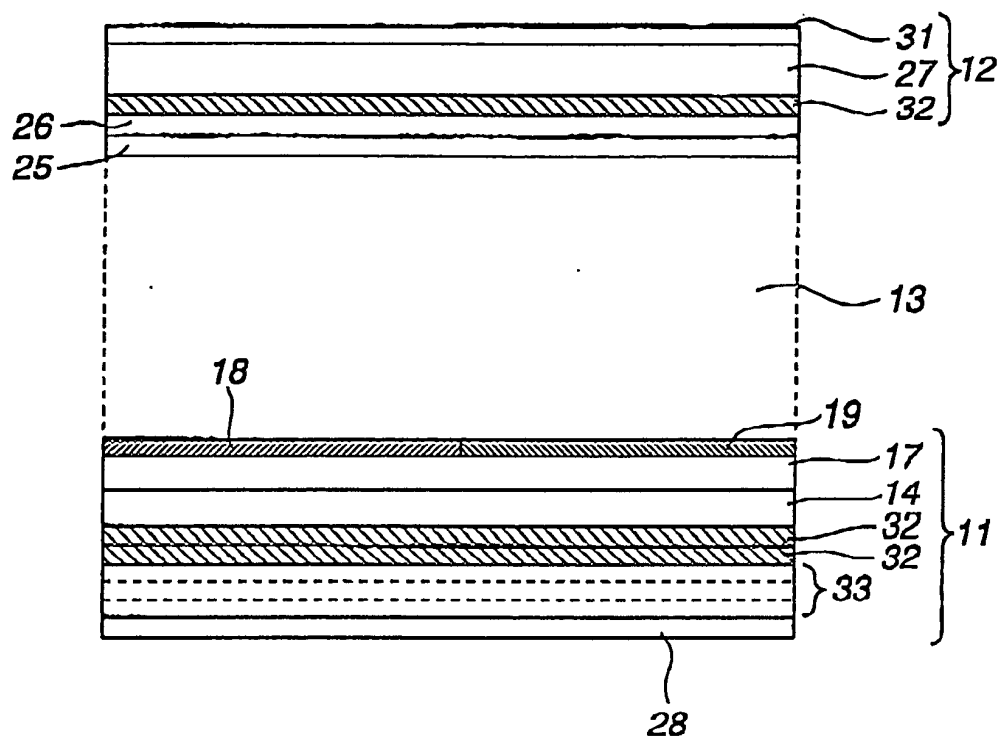


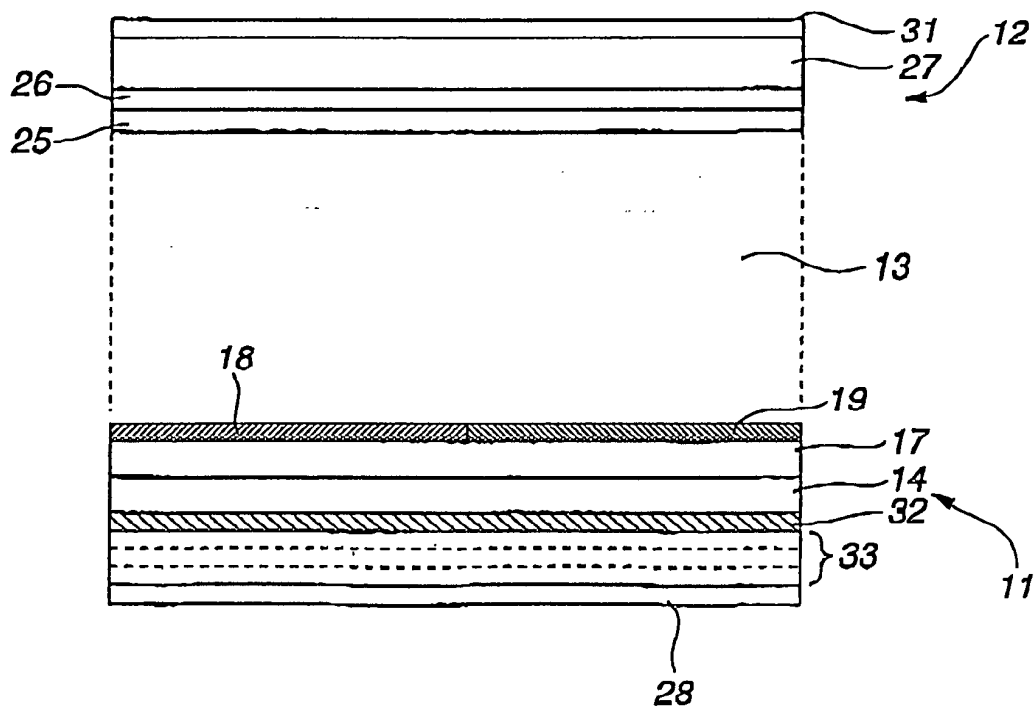
FIG.11



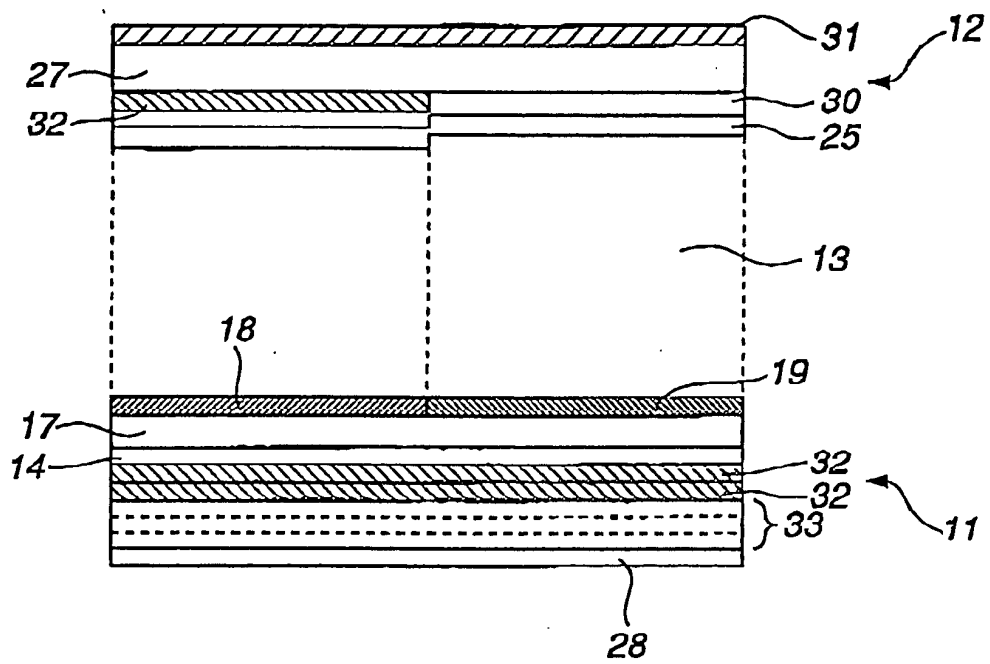
**FIG.12(a)**



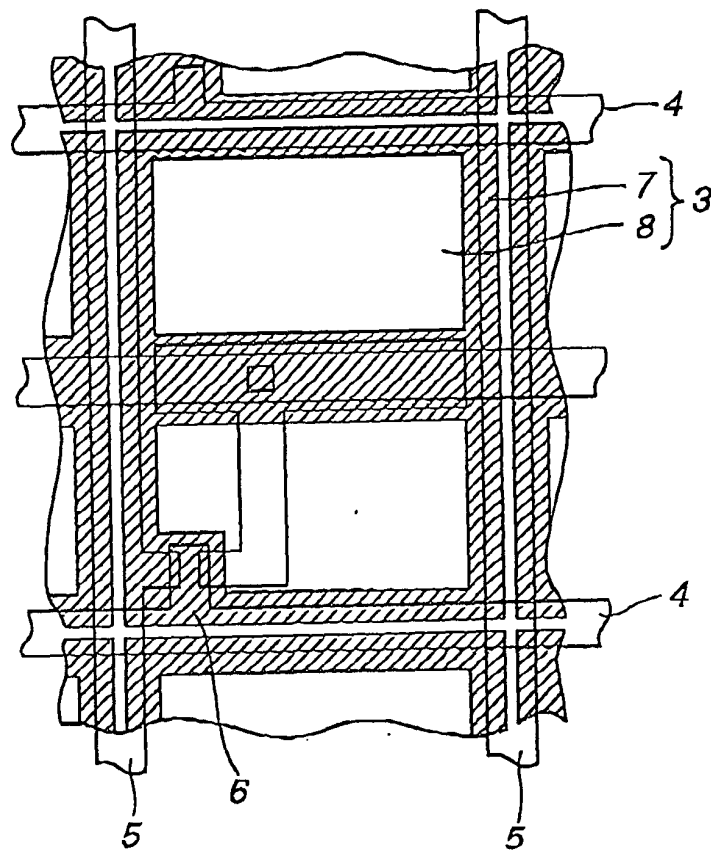
**FIG.12(b)**



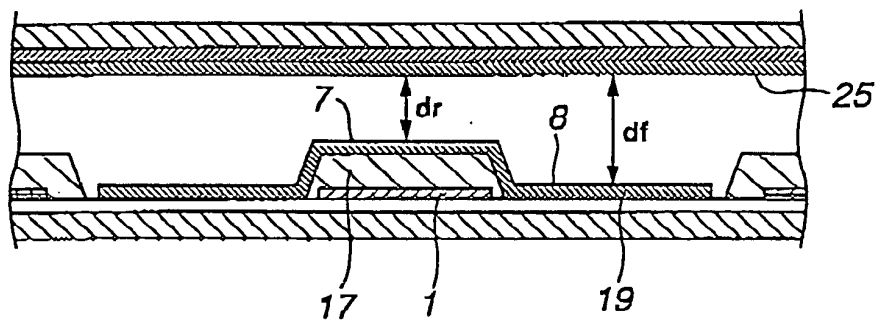
**FIG.13**



**FIG.14**  
**(PRIOR ART)**



**FIG.15**  
**(PRIOR ART)**





专利名称(译)	透反液晶显示装置		
公开(公告)号	<a href="#">EP1286204A2</a>	公开(公告)日	2003-02-26
申请号	EP2002018278	申请日	2002-08-22
申请(专利权)人(译)	NEC公司		
当前申请(专利权)人(译)	NLT科技有限公司.		
[标]发明人	IKENO HIDENORI SUZUKI MASAYOSHI		
发明人	IKENO, HIDENORI SUZUKI, MASAYOSHI		
IPC分类号	G02F1/1337 G02F1/1335 G02F1/13363 G02F1/1343 G02F1/1368 G02F1/1333		
CPC分类号	G02F1/133555 G02F1/133514 G02F1/133753 G02F2001/133638		
代理机构(译)	贝滕 & RESCH		
优先权	2001251089 2001-08-22 JP		
其他公开文献	EP1286204A3		
外部链接	<a href="#">Espacenet</a>		

#### 摘要(译)

一种液晶显示装置，包括：第一基板（11），包括薄膜晶体管；第二基板（12）；以及液晶分子的液晶层（13）。LC层（13）插入第一和第二基板（11,12）。第一基板（11）包括反射区域中的反射电极（18）和透射区域中的透射电极（19）。LC层（13）包括在反射区域中排列以提供第一延迟的第一组液晶分子和在透射区域中排列的第二组液晶分子以提供第二延迟。第二延迟与第一延迟不同。

FIG.3

