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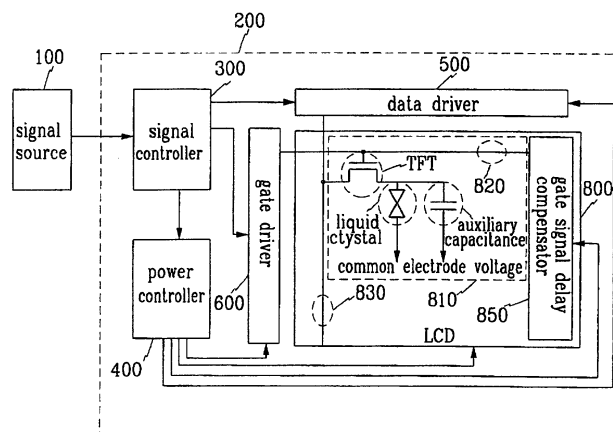
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(54) Gate signal delay compensating lcd and driving method thereof

(57) Disclosed is a gate signal delay compensating LCD that comprises an LCD panel including a plurality of gate lines, a plurality of data lines insulated from and crossing the gate lines, a plurality of TFT each of which having a gate electrode connected to the gate line and a source electrode connected to the data line, a pixel electrode connected to a drain electrode of the TFT and a common electrode facing the pixel electrode, liquid crystal filled between the pixel electrode and the common electrode, and a signal delay compensator connected to

ends of the gate lines to compensate for the gate signal delay; a gate driver for supplying a gate signal for turning on and off the TFT to the gate line so as to drive the LCD panel; a data driver for supplying a data voltage that represents an image signal to the data line so as to drive the LCD panel; and a signal controller connected to a signal source, the gate driver and the data driver, and processing the image signal provided by the signal source to enable the gate driver to supply a signal for turning on the TFT and the data driver to supply a data voltage to the pixel.

Fig. 1



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EUROPEAN SEARCH REPORT

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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
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			TECHNICAL FIELDS SEARCHED (IPC)
			G09G
The present search report has been drawn up for all claims			
Place of search The Hague		Date of completion of the search 14 March 2006	Examiner Gonzalez Ordóñez, O
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

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专利名称(译)	栅极信号延迟补偿液晶显示器及其驱动方法		
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摘要(译)

公开了一种栅极信号延迟补偿LCD，包括：LCD面板，包括多条栅极线，与栅极线绝缘并与栅极线交叉的多条数据线；多个TFT，每个TFT具有连接到栅极线的栅电极和连接到数据线的源电极，连接到TFT的漏电极的像素电极和面对像素电极的公共电极，填充在像素电极和公共电极之间的液晶，以及连接到端部的信号延迟补偿器栅极线用于补偿栅极信号延迟；栅极驱动器，用于提供栅极信号，用于导通和关断TFT至栅极线，以驱动LCD面板；数据驱动器，用于向数据线提供表示图像信号的数据电压，以驱动LCD面板；信号控制器，连接信号源，栅极驱动器和数据驱动器，处理信号源提供的图像信号，使栅极驱动器提供开启TFT的信号，数据驱动器提供数据像素的电压。

Fig. 1

