

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2021/0335957 A1 HAN et al.

Oct. 28, 2021 (43) **Pub. Date:**

(54) DISPLAY PANEL AND METHOD FOR FABRICATING SAME

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(21) Appl. No.: 16/615,474

(22) PCT Filed: Sep. 20, 2019

(86) PCT No.: PCT/CN2019/106924

§ 371 (c)(1),

(2) Date: Jan. 28, 2021

(30)Foreign Application Priority Data

Jul. 22, 2019 (CN) 201910658902.7

Publication Classification

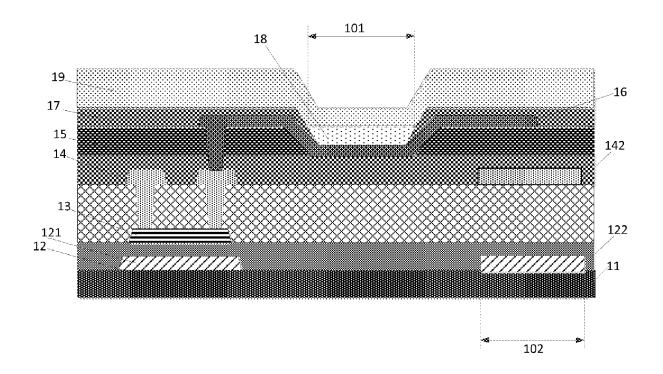
(51) Int. Cl. H01L 27/32 (2006.01)H01L 51/56 (2006.01)

U.S. Cl.

H01L 27/3265 (2013.01); H01L 27/3246 (2013.01); H01L 27/3258 (2013.01); H01L 27/1225 (2013.01); **H01L 51/56** (2013.01); H01L 2227/323 (2013.01); H01L 27/3262 (2013.01)

(57)ABSTRACT

The present disclosure provides a display panel and a method for fabricating the same. The display panel includes a light transmitting area for disposing an organic light emitting unit and a storage area for disposing a storage capacitor. A location of the light transmitting area corresponds to a location of the storage area. The storage capacitor includes a first electrode plate and a second electrode plate which are composed of a transparent material.



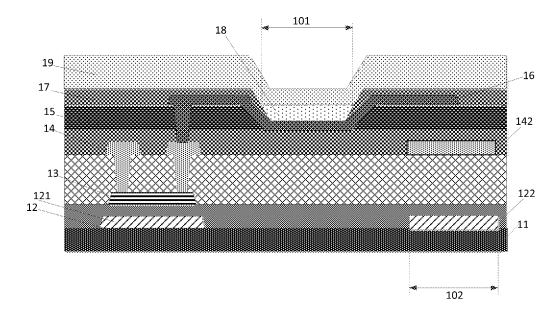


FIG. 1

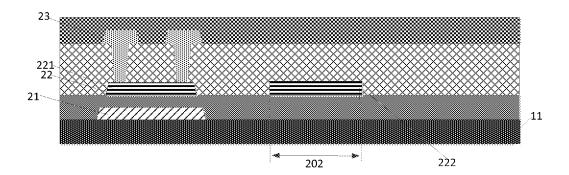


FIG. 2

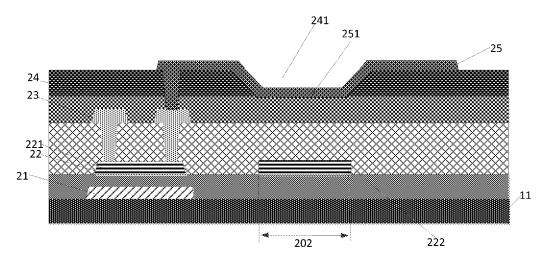


FIG. 3

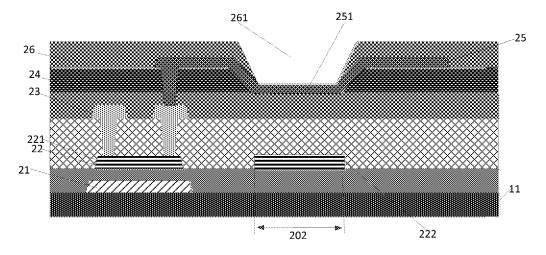


FIG. 4

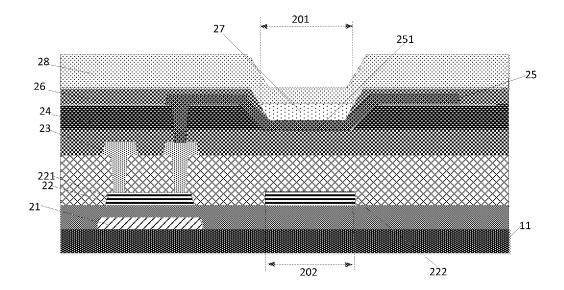


FIG. 5

DISPLAY PANEL AND METHOD FOR FABRICATING SAME

FIELD OF INVENTION

[0001] The present disclosure relates to a field of display technology, and particularly to a display panel and a method for fabricating the same.

BACKGROUND

[0002] A current display panel typically comprises a light transmitting area and a storage area. The light transmitting area is used for disposing an organic light emitting unit comprising an organic light emitting layer. The storage area is used for disposing a storage capacitor used to store charge. [0003] A current storage capacitor is made of a metal layer for making a gate and a metal layer for making a source/drain electrode. In order not to affect aperture ratio, it is necessary to avoid a light transmitting area. Therefore, an area occupied by pixels is increased, and a utilization rate of a display panel is reduced.

[0004] Therefore, it is necessary to provide a display panel and a method for fabricating the same to solve the problems in the prior art.

SUMMARY OF DISCLOSURE

[0005] An object of the present disclosure is to provide a display panel and a method for fabricating the same that can reduce an area occupied by pixels and improve a utilization rate of the display panel.

[0006] In order to solve the aforementioned technical problem, the present disclosure provides a display panel comprising:

[0007] a light transmitting area for disposing an organic light emitting unit; and

[0008] a storage area for disposing a storage capacitor;

[0009] wherein a location of the light transmitting area corresponds to a location of the storage area, and the storage capacitor comprises a first electrode plate and a second electrode plate which are composed of a transparent material

[0010] The present disclosure further provides a method for fabricating a display panel, comprising:

[0011] providing a base substrate;

[0012] forming a transparent film on the base substrate;

[0013] patterning the transparent film to form an active layer and an electrode portion;

[0014] metalizing the electrode portion to form a first electrode plate;

[0015] forming a flat layer above the active layer and the first electrode plate, wherein the flat layer is provided with an open area, and a location of the open area corresponds to a position of the first electrode plate;

[0016] forming an anode layer on the flat layer and in the open area, wherein the anode layer in the open area is used as a second electrode plate;

[0017] forming a pixel defining layer on the anode layer, wherein the pixel defining layer is provided with a pixel defining area, and a location of the pixel defining area corresponds to the location of the open area;

[0018] forming an organic light emitting layer in the pixel defining area; and

[0019] forming a cathode layer on the organic light emitting layer.

[0020] The present disclosure provides a display panel and a method for fabricating the same. The display panel comprises a light transmitting area for disposing an organic light emitting unit and a storage area for disposing a storage capacitor. A location of the light transmitting area corresponds to a location of the storage area. The storage capacitor comprises a first electrode plate and a second electrode plate which are composed of a transparent material. Therefore, aperture ratio is not reduced. Furthermore, because the location of the light transmitting area corresponds to the location of the storage area, an area occupied by pixels is reduced. Therefore, a utilization rate of the display panel is improved.

BRIEF DESCRIPTION OF DRAWINGS

[0021] FIG. 1 is a cross-sectional side view of a current display panel.

[0022] FIG. 2 is a cross-sectional side view of a structure formed by a first step and a second step of a method for fabricating a display panel according to the present disclosure.

[0023] FIG. 3 is a cross-sectional side view of a structure formed by a third step and a fourth step of a method for fabricating a display panel according to the present disclosure.

[0024] FIG. 4 is a cross-sectional side view of a structure formed by a fifth step of a method for fabricating a display panel according to the present disclosure.

[0025] FIG. 5 is a cross-sectional side view of a structure formed by a sixth step and a seventh step of a method for fabricating a display panel according to the present disclosure.

DETAILED DESCRIPTION

[0026] The following description of various embodiments of the present disclosure with reference to the accompanying drawings is used to illustrate specific embodiments that can be practiced. Directional terms mentioned in the present disclosure, such as "above", "below", "front", "rear", "left", "right", "inside", "outside", "side", are merely used to indicate the direction of the accompanying drawings. Therefore, the directional terms are used for illustrating and understanding the present disclosure rather than limiting the present disclosure. In the figures, elements with similar structure are indicated by the same reference numerals.

[0027] As shown in FIG. 1, a current display panel comprises a light transmitting area 101 and a storage area 102. A cross-sectional structure of the current display panel comprises a base substrate 11, a first metal layer 12, an active layer 13, a second metal layer 14, a flat layer 15, an anode layer 16, a pixel defining layer 17, an organic light emitting layer 18, and a cathode layer 19. The first metal layer 12 comprises a gate electrode 121 and a first electrode plate 122. The second metal layer 14 comprises a source electrode, a drain electrode, and a second electrode plate 142. The pixel defining layer 17 is provided with a pixel defining area. The organic light emitting layer 18 is disposed on the organic light emitting layer 19 is disposed on the organic light emitting layer 18.

[0028] Please refer to FIG. 2-5. FIG. 2 is a cross-sectional side view of a structure formed by a first step and a second step of a method for fabricating a display panel according to the present disclosure.

[0029] In an embodiment, a method for fabricating a display panel according to the present disclosure comprises following steps.

[0030] S101: providing a base substrate, forming a transparent film on the base substrate, and patterning the transparent film to form an active layer and an electrode portion.
[0031] As shown in FIG. 2, a base substrate 11 is provided. A transparent film 22 is formed on the base substrate 11. The transparent film 22 is patterned to form an active layer 221 and an electrode portion. The method may further comprise: forming a first metal layer on the base substrate 11, patterning the first metal layer to form a gate electrode 21. The active layer 221 is formed on the gate electrode 21.

[0032] The active layer is composed of a transparent metal oxide. Preferably, the active layer is composed of indium gallium zinc oxide.

[0033] S102: metalizing the electrode portion to form a first electrode plate.

[0034] As shown in FIG. 2, the electrode portion is metalized to form a first electrode plate 222.

[0035] The method may further comprise: forming a second metal layer 23 on the active layer 221 and patterning the second metal layer 23 to form a source electrode and a drain electrode.

[0036] S103: forming a flat layer above the active layer and the first electrode plate, wherein the flat layer is provided with an open area.

[0037] As shown in FIG. 3, a flat layer 24 is formed above the second metal layer 23. The flat layer 24 is provided with an open area 241. A location of the open area 241 corresponds to a position of the first electrode plate 222.

[0038] S104: forming an anode layer on the flat layer and in the open area, wherein the anode layer in the open area is used as a second electrode plate.

[0039] As shown in FIG. 3, an anode layer 25 is formed on the flat layer 24 and in the open area 241. The anode layer 25 in the open area 241 is used as a second electrode plate 251. The flat layer 24 is further provided with a via hole, and the anode layer 25 is connected to the drain electrode through the via hole.

[0040] S105: forming a pixel defining layer on the anode layer, wherein the pixel defining layer is provided with a pixel defining area.

[0041] As shown in FIG. 4, a pixel defining layer 26 is formed on the anode layer 25. The pixel defining layer 26 is provided with a pixel defining area 261. A location of the pixel defining area 261 corresponds to the location of the open area 241. The pixel defining layer 26 does not cover a bottom of the open area 241

[0042] S106: forming an organic light emitting layer in the pixel defining area.

[0043] Please refer to FIG. 4 and FIG. 5, an organic light emitting layer 27 is formed in the pixel defining area 261. [0044] S107: forming a cathode layer on the organic light emitting layer.

[0045] As shown in FIG. 5, a cathode layer 28 is formed on the organic light emitting layer 27.

[0046] As shown in FIG. 5, the present disclosure further provides a display panel comprising a light transmitting area 201 and a storage area 202. The light transmitting area 201 is used for disposing an organic light emitting unit. A cross-sectional structure of the organic light emitting unit comprises an anode electrode, an organic light emitting layer 27, and a cathode electrode.

[0047] The storage area 202 is used for disposing a storage capacitor. A location of the light transmitting area 201 corresponds to a location of the storage area 202. The storage capacitor comprises a first electrode plate 222 and a second electrode plate 251 which are composed of a transparent material.

[0048] In an embodiment, a cross-sectional structure of the display panel comprises a base substrate 11, a first metal layer, an active layer 221, a second metal layer 23, a flat layer 24, an anode layer 25, a pixel defining layer 26, an organic light emitting layer 27, and a cathode layer 28.

[0049] The first metal layer is disposed on the base substrate 11. The first metal layer comprises a gate electrode 21. The active layer 221 is disposed on the first metal layer. The first electrode plate 222 is in the same layer as the active layer 221. The first electrode plate 222 may be in another layer. In an embodiment, in order to simplify the process, an initial material of the first electrode plate 222 is the same as a material of the active layer 221, and the first electrode plate 222 is formed by metalizing the initial material of the first electrode plate 222. In an embodiment, the active layer 221 is composed of a transparent metal oxide. In an embodiment, the active layer 221 is composed of indium gallium zinc oxide. The first electrode plate 222 and the active layer 221 may be fabricated separately.

[0050] The second metal layer 23 is disposed on the active layer 221. The second metal layer 23 comprises a source electrode and a drain electrode.

[0051] Please refer to FIG. 3, the flat layer 24 is disposed on the second metal layer 23. The flat layer 24 is further provided with a via hole (not shown) and an open area 241. A location of the open area 241 corresponds to a location of the first electrode plate 222.

[0052] In an embodiment, the second electrode plate 251 is in the same layer as the anode layer 25. The second electrode plate 251 may be in another layer.

[0053] In order to simplify the process, the anode layer 25 is disposed on the flat layer 35 and in the open area 241. The second electrode plate 251 is the anode layer 25 in the open area 241. That is, the anode layer 25 in the open area 241 is used as the second electrode plate 251. The anode layer 25 is connected to the drain electrode through the via hole.

[0054] Please refer to FIG. 4, the pixel defining layer 26 is disposed on the anode layer 25. The pixel defining layer 26 is provided with a pixel defining area 261. A location of the pixel defining area 261 corresponds to the position of the open area 241.

[0055] The organic light emitting layer 27 is disposed in the pixel defining area 261. The cathode layer 28 is disposed on the organic light emitting layer 27. The display panel may further comprise an encapsulation layer disposed on the cathode layer 28.

[0056] It can be understood that a display panel according to the present disclosure can be produced by the above method, and can also be obtained by other methods.

[0057] Because that the location of the light transmitting area corresponds to the location of the storage area and that the first electrode plate and the second electrode plate are composed of a transparent material, an area occupied by pixels is reduced while aperture ratio is not reduced. Therefore, a utilization rate of the display panel is improved.

[0058] The present disclosure provides a display panel and a method for fabricating the same. The display panel comprises a light transmitting area for disposing an organic light

emitting unit and a storage area for disposing a storage capacitor. A location of the light transmitting area corresponds to a location of the storage area. The storage capacitor comprises a first electrode plate and a second electrode plate which are composed of a transparent material. Therefore, aperture ratio is not reduced. Furthermore, because the location of the light transmitting area corresponds to the location of the storage area, an area occupied by pixels is reduced. Therefore, a utilization rate of the display panel is improved.

[0059] In the above, the present application has been described in the above preferred embodiments, but the preferred embodiments are not intended to limit the scope of the present application, and those skilled in the art may make various modifications without departing from the scope of the present application. The scope of the present application is determined by claims.

What is claimed is:

- 1. A display panel, comprising:
- a light transmitting area for disposing an organic light emitting unit; and
- a storage area for disposing a storage capacitor;
- wherein a location of the light transmitting area corresponds to a location of the storage area, and the storage capacitor comprises a first electrode plate and a second electrode plate which are composed of a transparent material; and
- wherein a cross-sectional structure of the display panel comprises an active layer and an anode layer, the first electrode plate is in the same layer as the active layer, and the second electrode plate is in the same layer as the anode layer.
- 2. The display panel according to claim 1, wherein an initial material of the first electrode plate is the same as a material of the active layer, and the first electrode plate is formed by metalizing the initial material of the first electrode plate.
- 3. The display panel according to claim 2, wherein the active layer is composed of a transparent metal oxide.
- **4**. The display panel according to claim **3**, wherein the active layer is composed of indium gallium zinc oxide.
- 5. The display panel according to claim 1, wherein the cross-sectional structure of the display panel further comprises a flat layer provided with an open area, a location of the open area corresponds to a location of the first electrode plate, the anode layer is disposed on the flat layer and in the open area, and the second electrode plate is the anode layer in the open area.
- **6**. The display panel according to claim **5**, wherein the cross-sectional structure of the display panel further comprises:
 - a pixel defining layer disposed on the anode layer, wherein the pixel defining layer is provided with a pixel defining area, and a location of the pixel defining area corresponds to the position of the open area;
 - an organic light emitting layer disposed in the pixel defining area; and
 - a cathode layer disposed on the organic light-emitting layer.
- 7. The display panel according to claim 5, wherein the cross-sectional structure of the display panel further comprises a second metal layer comprising a source electrode and a drain electrode, the flat layer is further provided with

- a via hole, and the anode layer is connected to the drain electrode through the via hole.
- **8**. The display panel according to claim **1**, wherein the cross-sectional structure of the display panel further comprises a first metal layer comprising a gate electrode, the first metal layer is disposed below the active layer, and the first meta layer comprises a gate electrode.
 - 9. A display panel, comprising:
 - a light transmitting area for disposing an organic light emitting unit; and
 - a storage area for disposing a storage capacitor;
 - wherein a location of the light transmitting area corresponds to a location of the storage area, and the storage capacitor comprises a first electrode plate and a second electrode plate which are composed of a transparent material.
- 10. The display panel according to claim 9, wherein a cross-sectional structure of the display panel comprises an active layer, and the first electrode plate is in the same layer as the active layer.
- 11. The display panel according to claim 9, wherein an initial material of the first electrode plate is the same as a material of the active layer, and the first electrode plate is formed by metalizing the initial material of the first electrode plate.
- 12. The display panel according to claim 11, wherein the active layer is composed of a transparent metal oxide.
- 13. The display panel according to claim 12, wherein the active layer is composed of indium gallium zinc oxide.
- 14. The display panel according to claim 9, wherein a cross-sectional structure of the display panel comprises an anode layer, and the second electrode plate is in the same layer as the anode layer.
- 15. The display panel according to claim 14, wherein the cross-sectional structure of the display panel further comprises a flat layer provided with an open area, a location of the open area corresponds to a location of the first electrode plate, the anode layer is disposed on the flat layer and in the open area, and the second electrode plate is the anode layer in the open area.
- **16**. The display panel according to claim **15**, wherein the cross-sectional structure of the display panel further comprises:
 - a pixel defining layer disposed on the anode layer, wherein the pixel defining layer is provided with a pixel defining area, and a location of the pixel defining area corresponds to the position of the open area;
 - an organic light emitting layer disposed in the pixel defining area; and
 - a cathode layer disposed on the organic light-emitting layer.
- 17. The display panel according to claim 16, wherein the cross-sectional structure of the display panel further comprises a second metal layer comprising a source electrode and a drain electrode, the flat layer is further provided with a via hole, and the anode layer is connected to the drain electrode through the via hole.
 - **18**. A method for fabricating a display panel, comprising: forming a transparent film on a base substrate;
 - patterning the transparent film to form an active layer and an electrode portion;
 - metalizing the electrode portion to form a first electrode plate:

forming a flat layer above the active layer and the first electrode plate, wherein the flat layer is provided with an open area, and a location of the open area corresponds to a position of the first electrode plate;

forming an anode layer on the flat layer and in the open area, wherein the anode layer in the open area is used as a second electrode plate;

forming a pixel defining layer on the anode layer, wherein the pixel defining layer is provided with a pixel defining area, and a location of the pixel defining area corresponds to the location of the open area;

forming an organic light emitting layer in the pixel defining area; and

forming a cathode layer on the organic light emitting layer.

19. The method for fabricating a display panel according to claim 18, wherein the transparent film is composed of a transparent metal oxide.

20. The method for fabricating a display panel according to claim 18, wherein the transparent film is composed of indium gallium zinc oxide.

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