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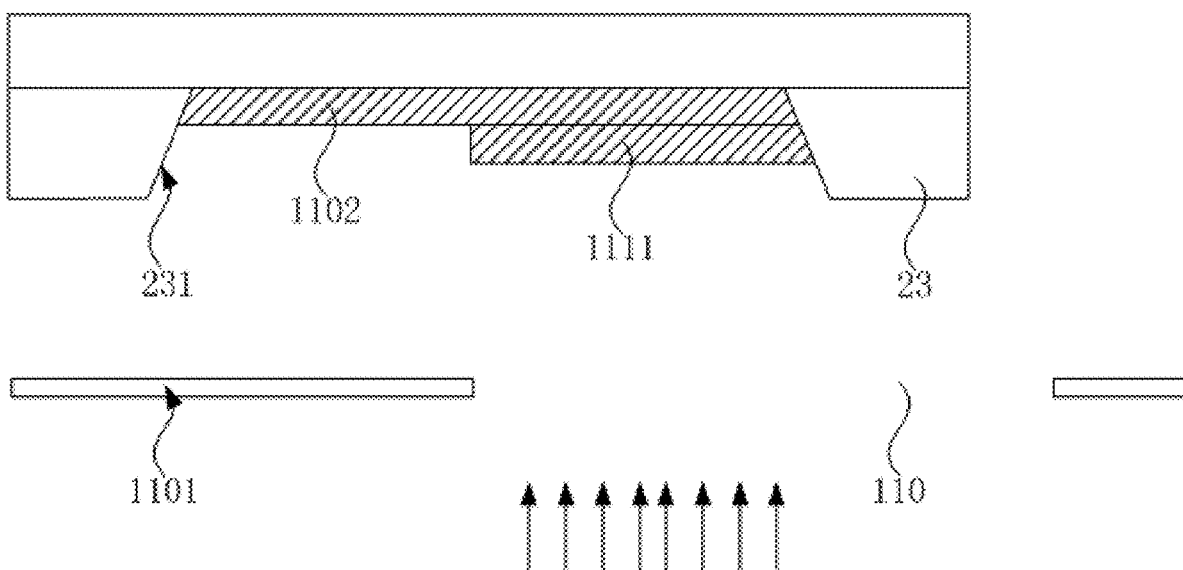
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(57)

**ABSTRACT**

The present disclosure provides a display panel. The light-emitting layers within a same sub-pixel have different thicknesses, and emit light that overlap with each other at the same time, resulting in that the peak of the overall microcavity characteristic curve has a wide range. Thus, the position of the peak of the overall microcavity characteristic curve produced by the sub-pixel can overlap with the position of the peak of the inherent brightness characteristic curve of the light-emitting layer within a wider range of angles, such that the electroluminescence spectrum changes less with change of viewing angles. Therefore, the color shift at different viewing angles is reduced, and there is no substantial difference for visual sensation for color when viewing the OLED display panel including such sub-pixels at different viewing angles.



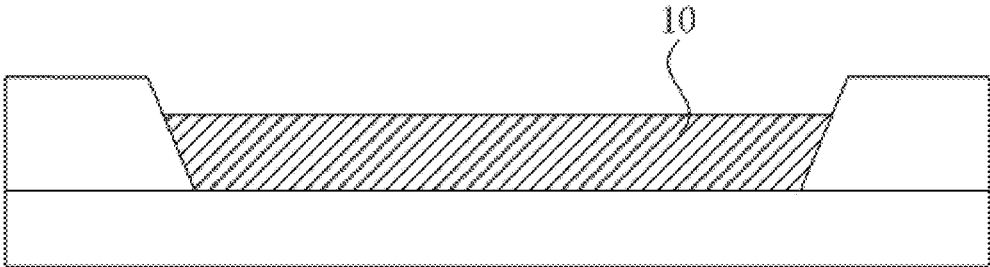


FIG. 1A

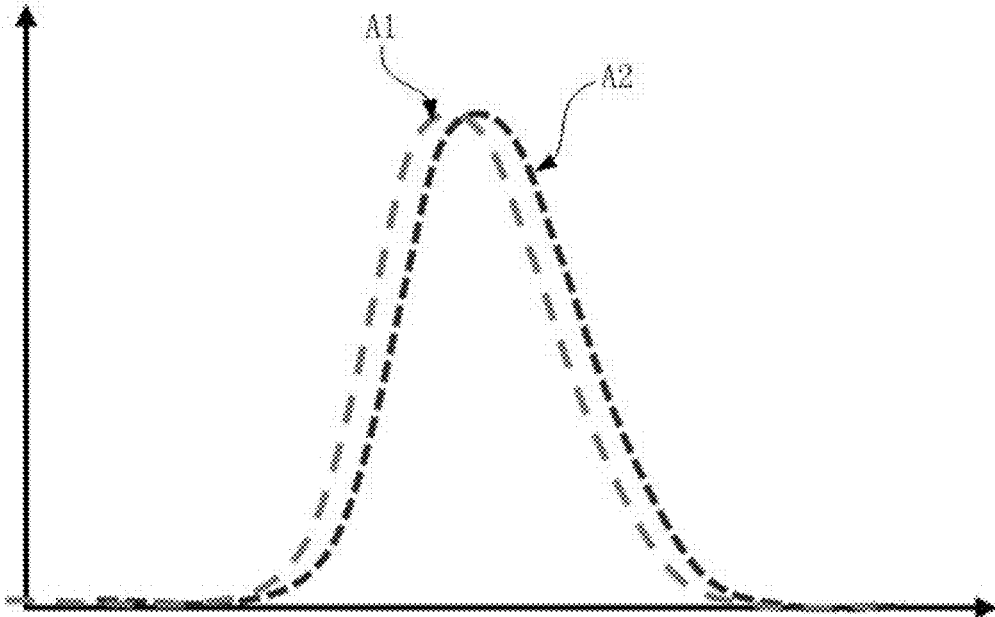


FIG. 1B

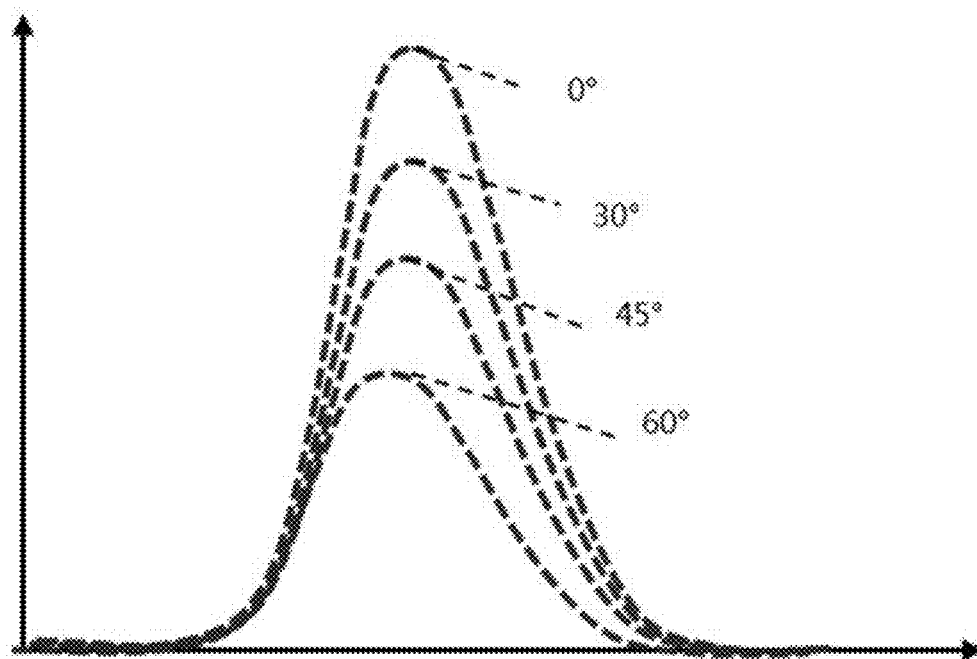


FIG. 1C

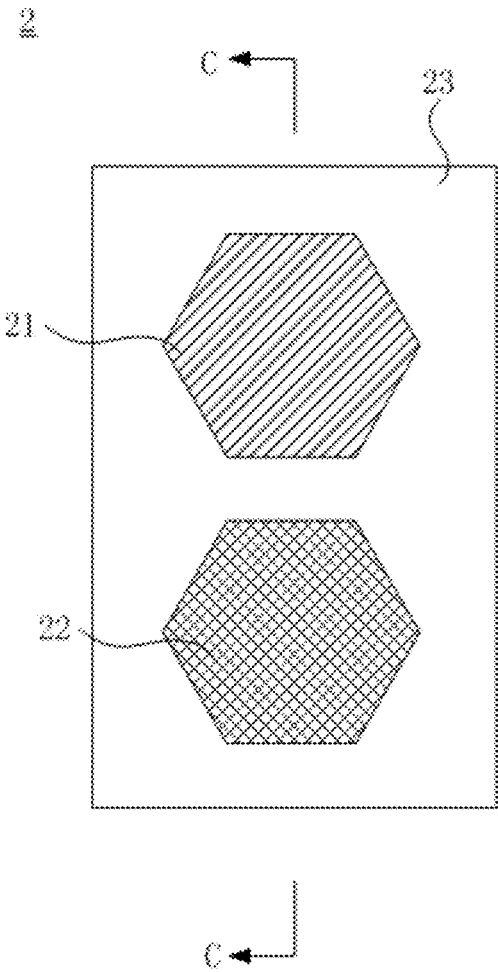


FIG. 2

2

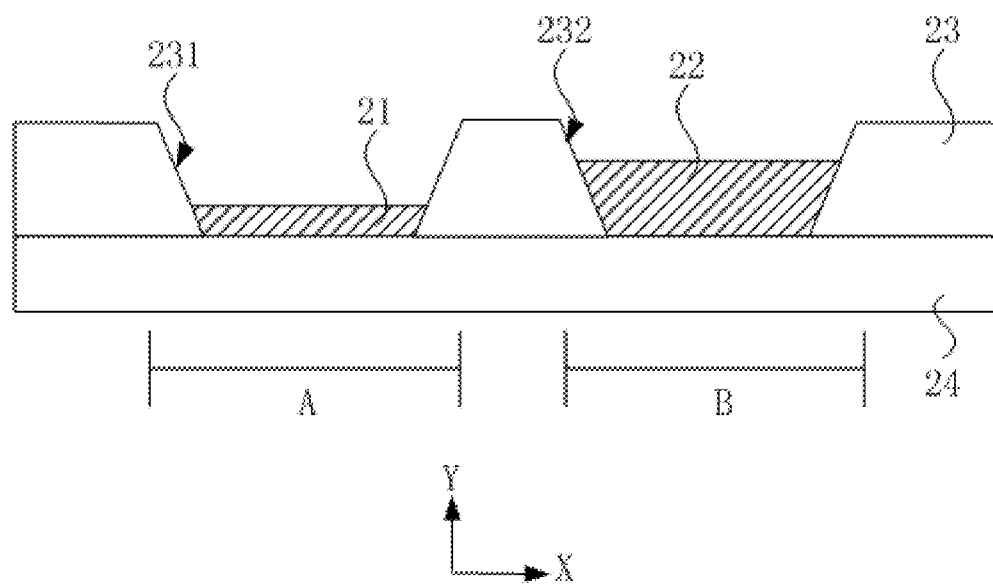


FIG. 3

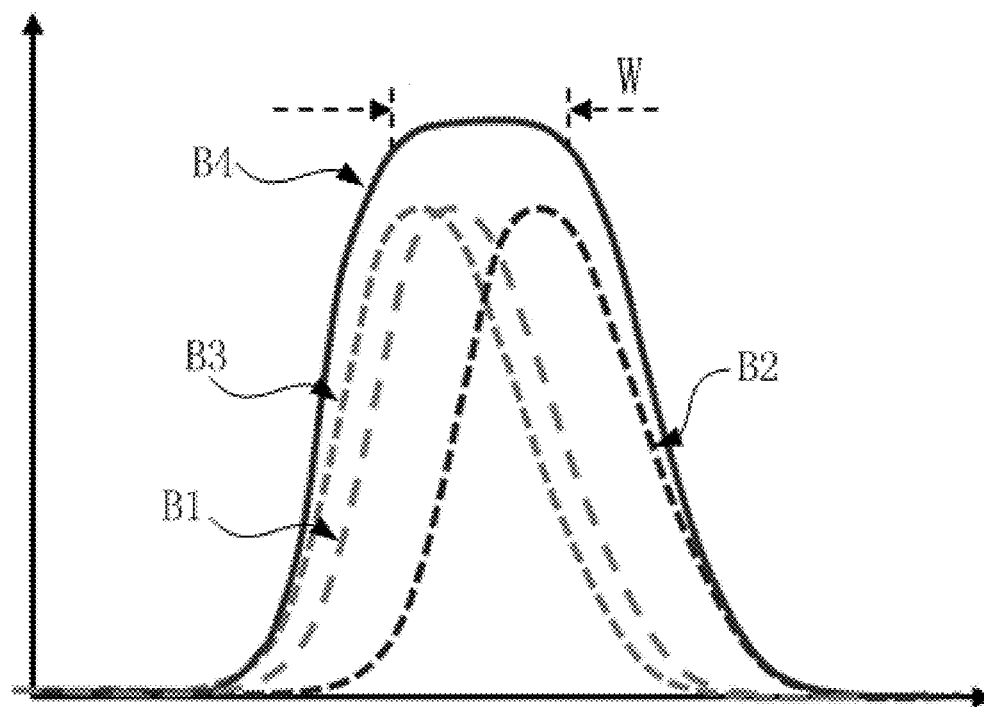


FIG. 4

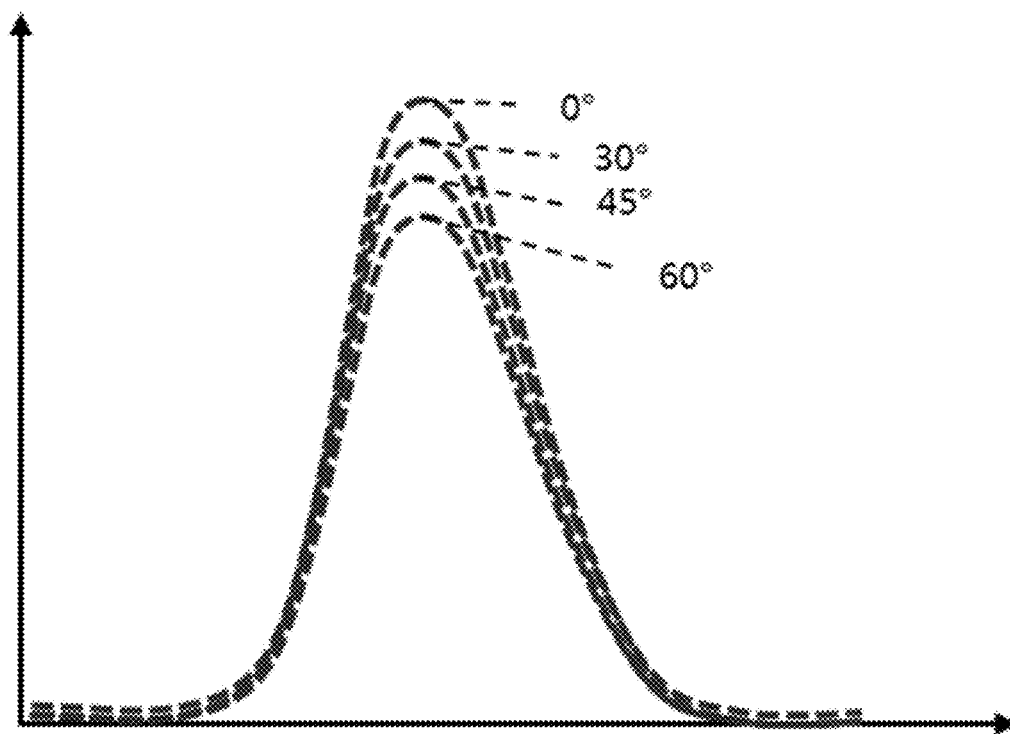


FIG. 5

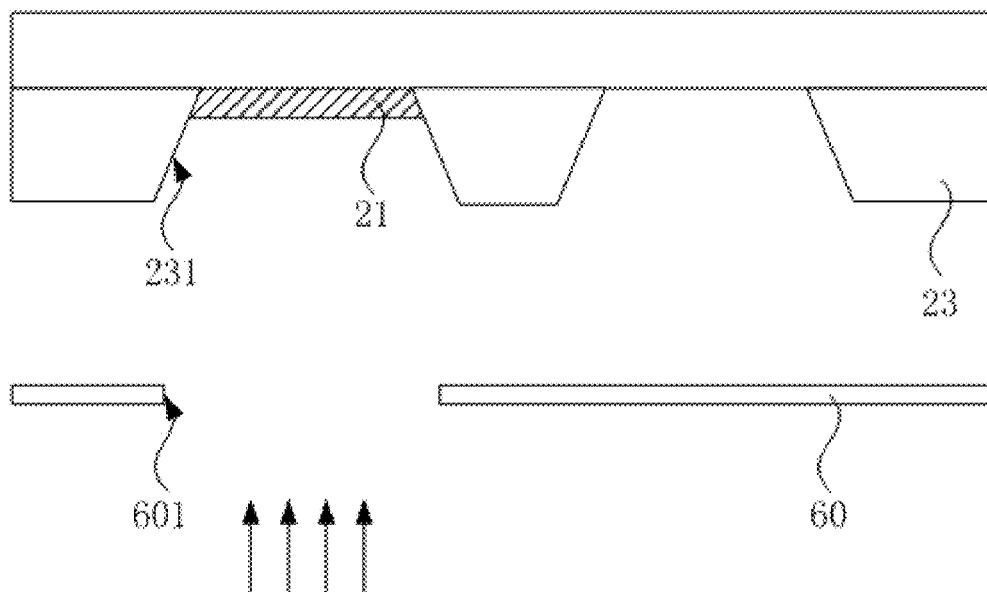


FIG. 6A

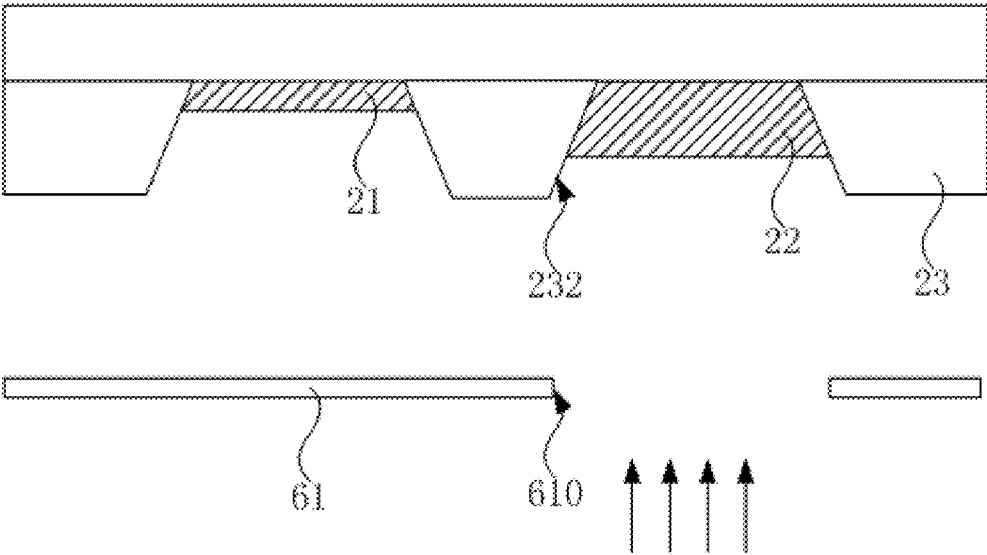


FIG. 6B

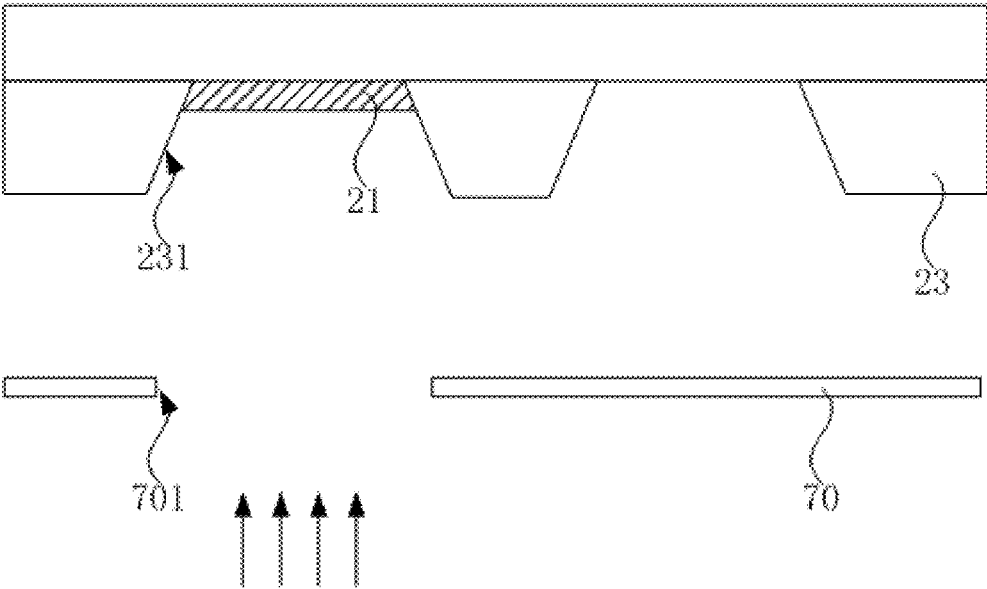


FIG. 7A

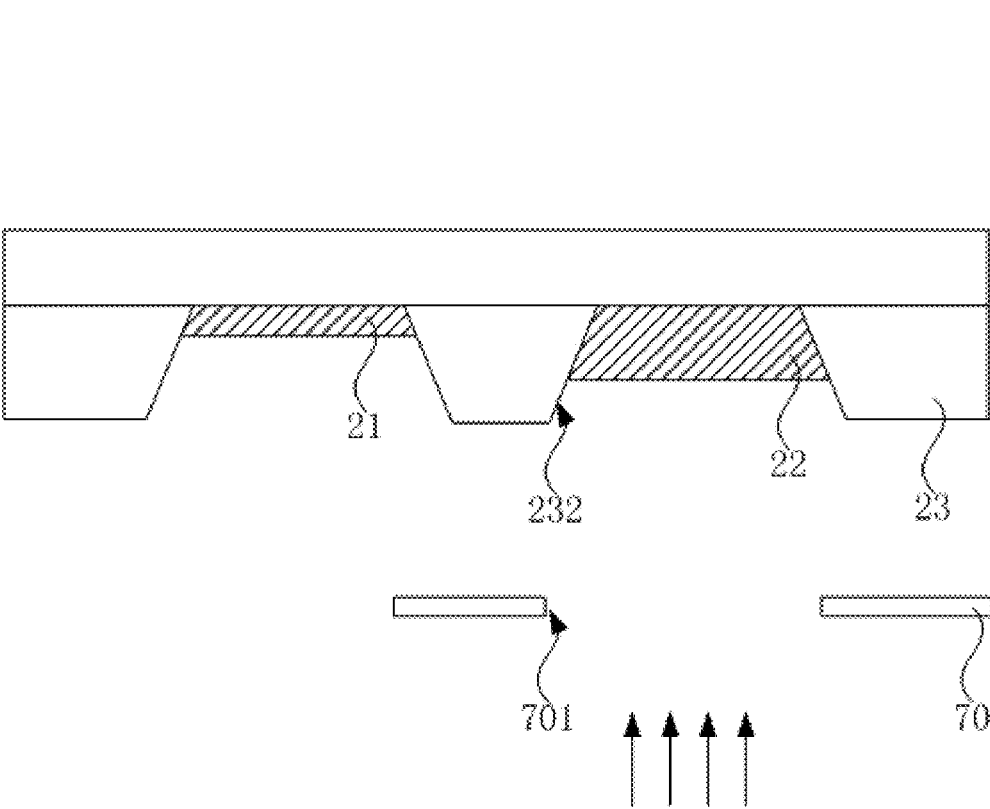


FIG. 7B



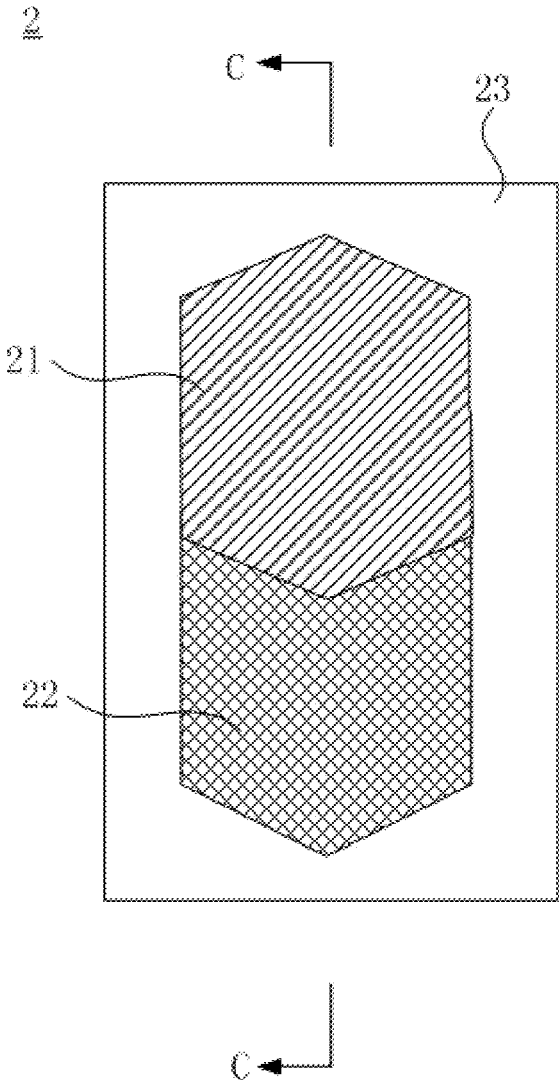


FIG. 8

2

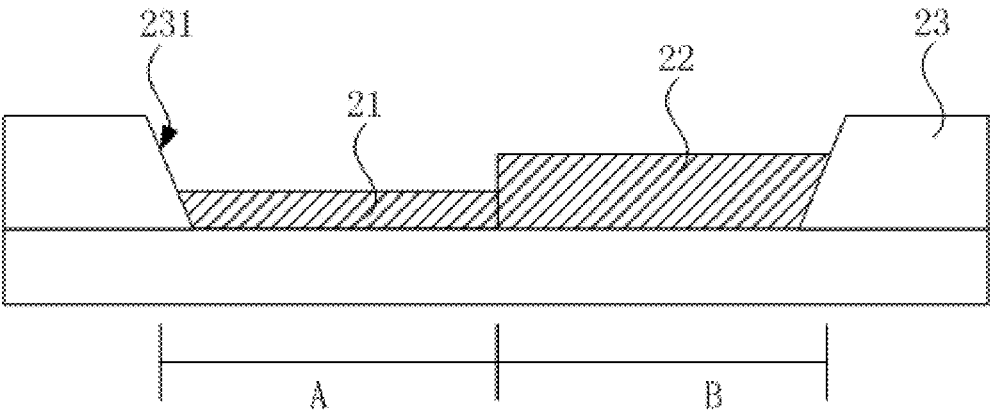


FIG. 9

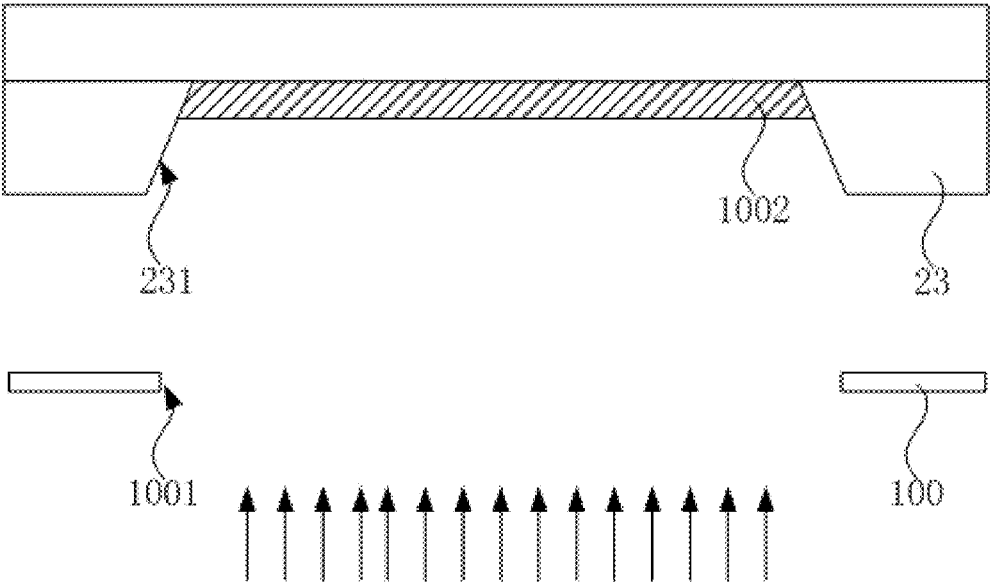


FIG. 10A

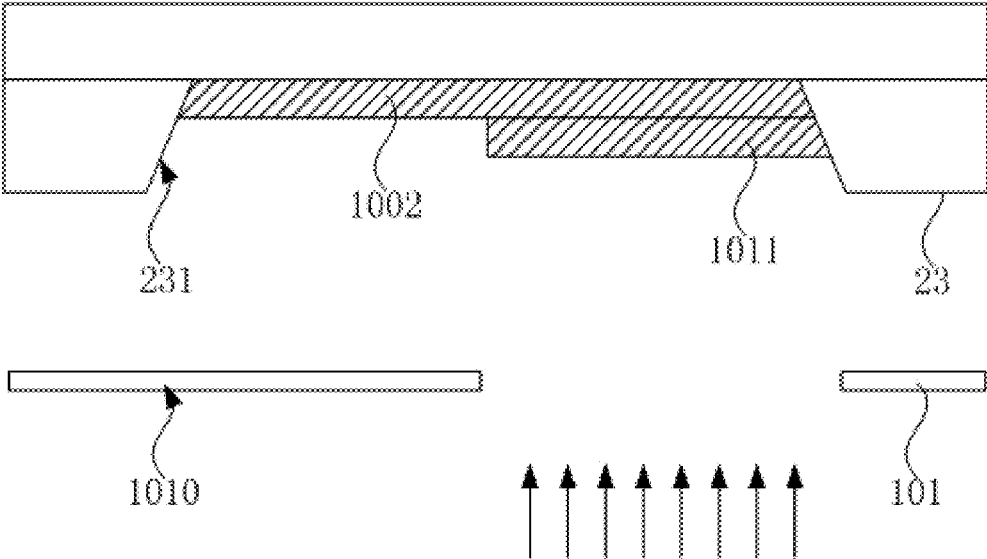


FIG. 10B

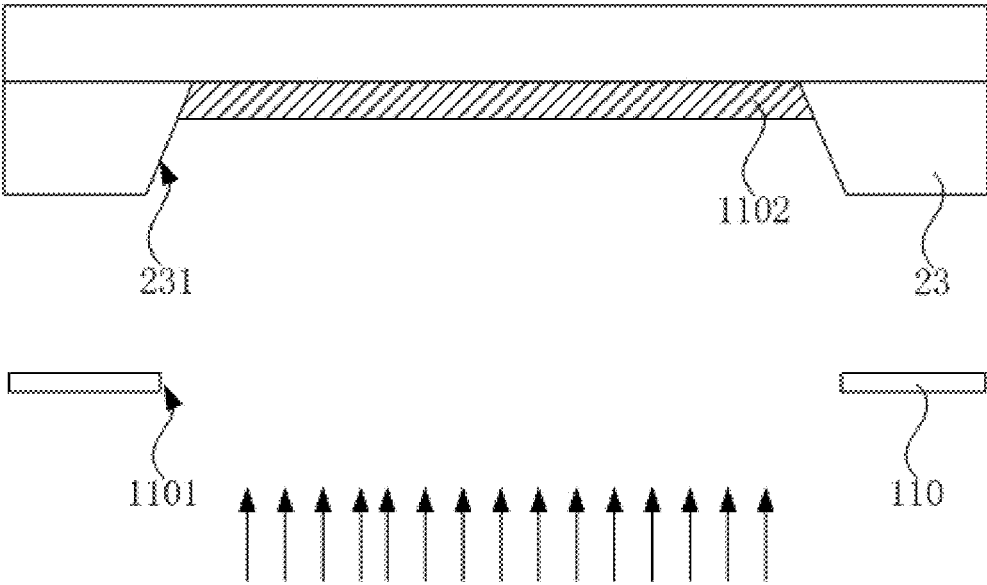


FIG. 11A

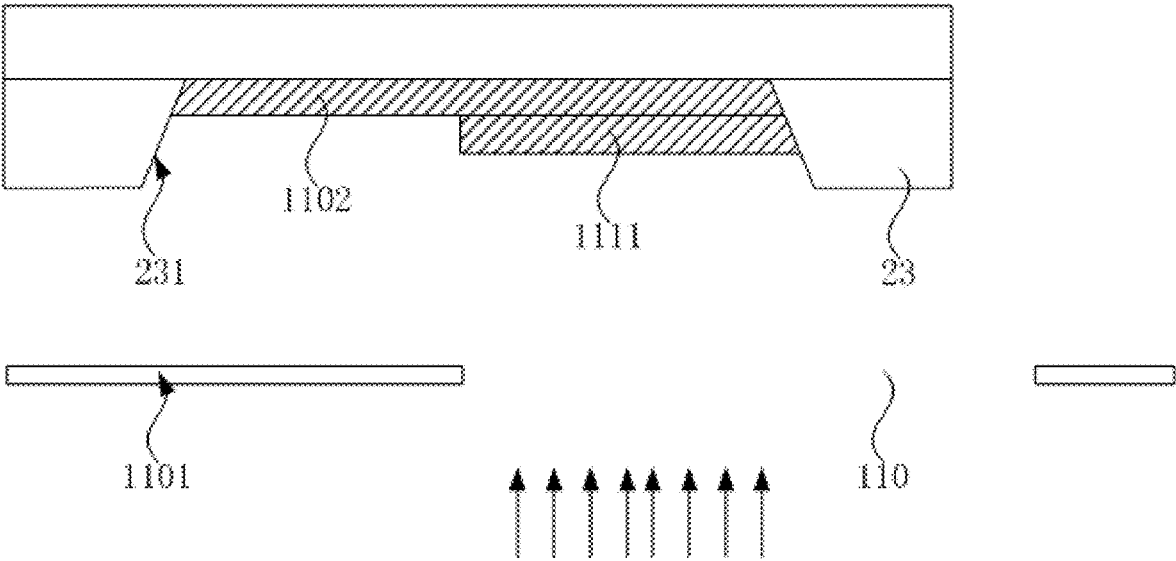


FIG. 11B

## DISPLAY PANEL AND METHOD FOR MANUFACTURING SAME

### FIELD OF INVENTION

[0001] The present disclosure relates to a field of display technology, and more particularly, to a display panel, a method for manufacturing the same, and an electronic device including the same.

### BACKGROUND

[0002] Because organic light emitting diode (OLED) display devices, compared to current mainstream thin film transistor liquid crystal displays (TFT-LCDs), have advantages, such as wide viewing angles, high brightness, high contrast, low power consumption, light weight, and thin thickness, OLED display devices attract user attention among various types of flat panel displays. OLED display devices are categorized to include passive matrix OLED (PM-OLED) and active matrix OLED (AM-OLED) based on a driving mode thereof. In comparison with PM-OLED, AM-OLED have advantages including large quantity of display information, low power consumption, long lifespan of devices, high contrast of displayed images.

[0003] FIG. 1A shows a cross-sectional view of a structure of a sub-pixel of an OLED display panel according to prior art. A light-emitting layer 10 of the OLED display panel has a uniform thickness. FIG. 1B shows an inherent brightness characteristic curve of a light-emitting layer of a sub-pixel and a microcavity characteristic curve of a light-emitting area according to a current OLED display panel, wherein the curve pointed at by the arrow A1 is the inherent brightness characteristic curve of the light-emitting layer of the sub-pixel, and the curve pointed at by the arrow A2 is the microcavity characteristic curve of the light-emitting area. FIG. 1C shows electroluminescence spectrum at different viewing angles according to the current OLED display panel. Please refer to FIGS. 1B and 1C. FIG. 1C shows electroluminescence spectrum at viewing angles of 0 degree, 30 degrees, 45 degrees, and 60 degrees. Obviously, the electroluminescence spectrum change a lot at different viewing angles. This results in color shift and reduction in brightness, which makes the color images composed of RGB have color shift because of visual difference, and thus users have different visual sensation for color when viewing the OLED display panel at different viewing angles.

### SUMMARY OF DISCLOSURE

#### Technical Problems

[0004] The objective of the present disclosure is to provide a display panel, a method for manufacturing the same, and an electronic device including the same, where the electroluminescence spectrum changes less with change of viewing angles, the color shift at different viewing angles is reduced, and there is no substantial difference for visual sensation for color when viewing the OLED display panel including such sub-pixels at different viewing angles.

#### Technical Solutions

[0005] To solve the aforementioned problems, the present disclosure provides a display panel including a plurality of sub-pixels, wherein at least one of the sub-pixels comprises at least one first light-emitting area and at least one second

light-emitting area, the first light-emitting area and the second light-emitting area are sequentially arranged in a direction perpendicular to a light-emitting direction of the sub-pixel, a first light-emitting layer is disposed in the first light-emitting area, a second light-emitting layer is disposed in the second light-emitting area, and a thickness of the second light-emitting layer is greater than a thickness of the first light-emitting layer.

[0006] In one embodiment, the sub-pixel further comprises a pixel defining layer, the pixel defining layer includes at least one first opening and at least one second opening, the first opening is positioned to correspond to the first light-emitting area, the first light-emitting layer is disposed in the first opening, the second opening is positioned to correspond to the second light-emitting area, and the second light-emitting layer is disposed in the second opening.

[0007] In one embodiment, the sub-pixel further comprises a pixel defining layer, the pixel defining layer includes at least one first opening, the first opening is positioned to correspond to the first light-emitting area and the second light-emitting area, and the first light-emitting layer and the second light-emitting layer are disposed in a same first opening.

[0008] In one embodiment, the sub-pixel further comprises one first light-emitting area and one second light-emitting area, and the first light-emitting area and the second light-emitting area are sequentially arranged in the direction perpendicular to the light-emitting direction of the sub-pixel.

[0009] The present disclosure further provides a method for manufacturing the above-said display panel, comprising steps of:

[0010] providing a first mask having at least one first deposition hole, positioning the first deposition hole to correspond to the first opening, and depositing the first light-emitting layer in the first opening; and

[0011] providing a second mask having at least one second deposition hole, positioning the second deposition hole to correspond to the second opening, and depositing the second light-emitting layer in the second opening such that thickness of the second light-emitting layer is greater than the thickness of the first light-emitting layer.

[0012] In one embodiment, the first mask and the second mask are same, and in the step of providing the second mask, the first mask is moved to position the first deposition hole to correspond to the second opening, and the second light-emitting layer is deposited in the second light-emitting area.

[0013] The present disclosure further provides a method for manufacturing the above-said display panel, comprising steps of:

[0014] providing a first mask having at least one first deposition hole, positioning the first deposition hole to correspond to the first opening, and depositing a bottom light-emitting layer in the first opening; and

[0015] providing a second mask having at least one second deposition hole, positioning the second deposition hole to correspond to a portion of the first opening, and depositing a top light-emitting layer in the first opening such that the bottom light-emitting layer overlaps with the top light-emitting layer to constitute the second light-emitting layer, and the bottom light-emitting layer constitutes the first light-emitting layer in a non-overlapping region.

[0016] In one embodiment, the first mask and the second mask are same, and in the step of providing the second mask, the first mask is moved to position the first deposition hole

to correspond to a portion of the first opening, and the top light-emitting layer is deposited in the first opening.

[0017] In addition, the present disclosure provides an electronic device, comprising the above-said display panel.

[0018] In one embodiment, the display panel includes a first type of sub-pixel, a second type of sub-pixel, and a third type of sub-pixel, wherein at least one type of the sub-pixels has a structure that is the same as that of the above-said sub-pixel.

#### Beneficial Effects

[0019] The present disclosure provides the following beneficial effects. Different light-emitting layers having different thickness produce different microcavity characteristic curve, and the peaks thereof have different positions. The light-emitting layers within the same sub-pixel have different thicknesses, and emit light that overlap with each other at the same time, resulting in that the peak of the overall microcavity characteristic curve has a wide range. Thus, the position of the peak of the overall microcavity characteristic curve produced by the sub-pixel can overlap with the position of the peak of the inherent brightness characteristic curve of the light-emitting layer within a wider range of angles, such that the electroluminescence spectrum changes less with change of viewing angles, the color shift at different viewing angles is reduced, and there is no substantial difference for visual sensation for color when viewing the OLED display panel including such sub-pixels at different viewing angles.

#### BRIEF DESCRIPTION OF DRAWINGS

[0020] FIG. 1A shows a cross-sectional view of a structure of a sub-pixel of an OLED display panel according to prior art.

[0021] FIG. 1B shows an inherent brightness characteristic curve of a light-emitting layer of a sub-pixel and a microcavity characteristic curve of a light-emitting area according to a current OLED display panel, wherein the curve pointed at by the arrow A1 is the inherent brightness characteristic curve of the light-emitting layer of the sub-pixel, and the curve pointed at by the arrow A2 is the microcavity characteristic curve of the light-emitting area.

[0022] FIG. 1C shows electroluminescence spectrum at different viewing angles according to the current OLED display panel.

[0023] FIG. 2 shows a top view of a sub-pixel of a display panel according to a first embodiment of the present disclosure.

[0024] FIG. 3 shows a cross-sectional view of FIG. 2 cut along line C-C.

[0025] FIG. 4 shows an inherent brightness characteristic curve of a light-emitting layer of a sub-pixel and a microcavity characteristic curve of a light-emitting area according to a display panel of the present disclosure, wherein the curve pointed at by the arrow B1 is the inherent brightness characteristic curve of the light-emitting layer of the sub-pixel, the curve pointed at by the arrow B2 is the microcavity characteristic curve of the first light-emitting area, the curve pointed at by the arrow B3 is the microcavity characteristic curve of the second light-emitting area, and the curve pointed at by the arrow B4 is the overall microcavity

characteristic curve of a combined light-emitting area consisting of the first light-emitting area and the second light-emitting area.

[0026] FIG. 5 shows electroluminescence spectrum of a sub-pixel at different viewing angles according to a display panel of the present disclosure.

[0027] FIGS. 6A and 6B show each stage in a process flow of a method for manufacturing a sub-pixel of a display panel according to a first embodiment of the present disclosure.

[0028] FIGS. 7A and 7B show each stage in a process flow of a method for manufacturing a sub-pixel of a display panel according to a second embodiment of the present disclosure.

[0029] FIG. 8 shows a top view of a sub-pixel of a display panel according to a second embodiment of the present disclosure.

[0030] FIG. 9 shows a cross-sectional view of FIG. 8 cut along line C-C.

[0031] FIGS. 10A and 10B show each stage in a process flow of a method for manufacturing a sub-pixel of a display panel according to a third embodiment of the present disclosure.

[0032] FIGS. 11A and 11B show each stage in a process flow of a method for manufacturing a sub-pixel of a display panel according to a fourth embodiment of the present disclosure.

#### DETAILED DESCRIPTION

[0033] A further description of the implementation of a display panel, a method for manufacturing the same, and an electronic device including the same of the present disclosure are provided below in detail, in conjunction with the drawings and the embodiments. Because the present invention is characterized in structure of the sub-pixels, the drawings merely show the structure of the sub-pixels. The structures of other portions of the display panel are commonly used structures.

[0034] Please refer to FIGS. 2 and 3. FIG. 2 shows a top view of a sub-pixel of a display panel according to a first embodiment of the present disclosure. FIG. 3 shows a cross-sectional view of FIG. 2 cut along line C-C. The display panel of the present disclosure includes a plurality of sub-pixels 2, wherein at least one of the sub-pixels 2 includes at least one first light-emitting area A and at least one second light-emitting area B. The sub-pixels include a red sub-pixel, a green sub-pixel, a blue sub-pixel, or a white sub-pixel. However, the sub-pixels are not limited thereto. In the present embodiment, the sub-pixels 2 include one first light-emitting area A and one second light-emitting area B. In other embodiments of the present disclosure, the sub-pixels 2 can include a plurality of first light-emitting areas A and a plurality of second light-emitting areas B, which are alternated with each other.

[0035] The first light-emitting area A and the second light-emitting area B are sequentially arranged in a direction perpendicular to a light-emitting direction of the sub-pixel 2. For example, in the present embodiment, the light-emitting direction of the sub-pixel 2 is the Y direction shown in FIG. 3, and the first light-emitting area A and the second light-emitting area B are sequentially arranged in the X direction perpendicular to the Y direction.

[0036] A first light-emitting layer 21 is disposed in the first light-emitting area A. A second light-emitting layer 22 is disposed in the second light-emitting area B. Both the first light-emitting layer 21 and the second light-emitting layer

**22** are an organic light emitting diode (OLED) layer, where a structure thereof is well-known by any person having ordinary skill in the art. Because the first light-emitting layer **21** and the second light-emitting layer **22** are disposed within a same sub-pixel, the first light-emitting layer **21** and the second light-emitting layer **22** emit a same color of light. For example, if the sub-pixel **2** is a red sub-pixel, both the first light-emitting layer **21** and the second light-emitting layer **22** emit a red light. And, if the sub-pixel **2** is a green sub-pixel, both the first light-emitting layer **21** and the second light-emitting layer **22** emit a green light.

[0037] A thickness of the second light-emitting layer **22** is greater than a thickness of the first light-emitting layer **21**. That is, the thickness of the light-emitting layer varies within a same sub-pixel. Because the thickness of the second light-emitting layer **22** is different from the thickness of the first light-emitting layer **21**, the microcavity characteristic curve produced by the second light-emitting layer **22** is different from the microcavity characteristic curve produced by the first light-emitting layer **21**. Specifically, a position of a peak of the microcavity characteristic curve produced by the second light-emitting layer **22** is different from a position of a peak of the microcavity characteristic curve produced by the first light-emitting layer **21**.

[0038] With reference to FIGS. 4 and 5, advantages provided by the present disclosure are detailed below. FIG. 4 shows an inherent brightness characteristic curve of a light-emitting layer of a sub-pixel and a microcavity characteristic curve of a light-emitting area according to a display panel of the present disclosure, wherein the curve pointed at by the arrow **B1** is the inherent brightness characteristic curve of the light-emitting layer of the sub-pixel, the curve pointed at by the arrow **B2** is the microcavity characteristic curve of the first light-emitting area, the curve pointed at by the arrow **B3** is the microcavity characteristic curve of the second light-emitting area, and the curve pointed at by the arrow **B4** is the overall microcavity characteristic curve of a combined light-emitting area consisting of the first light-emitting area and the second light-emitting area. FIG. 5 shows electroluminescence spectrum of a sub-pixel at different viewing angles according to a display panel of the present disclosure. Different light-emitting layers having different thickness produce different microcavity characteristic curve, and the peaks thereof have different positions. The light-emitting layers within the same sub-pixel have different thicknesses, and emit light that overlap with each other at the same time, resulting in that the peak of the overall microcavity characteristic curve has a wide range. Specifically, please see FIG. 4. The microcavity characteristic curve produced by the first light-emitting layer **21** overlap with the microcavity characteristic curve produced by the second light-emitting layer **22**. This makes the peak of the overall microcavity characteristic curve produced by the sub-pixel has a wider width **W**. Thus, the position of the peak of the overall microcavity characteristic curve produced by the sub-pixel can overlap with the position of the peak of the inherent brightness characteristic curve of the light-emitting layer within a wider range of angles, such that the electroluminescence spectrum changes less with change of viewing angles. Please refer to FIGS. 1C and 5. FIG. 5 shows electroluminescence spectrum of a sub-pixel of according to the present disclosure at viewing angles of 0 degree, 30 degrees, 45 degrees, and 60 degrees. Compared with FIG. 1C which shows the electroluminescence spectrum of a sub-pixel according to prior

art, FIG. 5 shows that the electroluminescence spectrum of a sub-pixel according to the present disclosure changes less with change of viewing angles. Therefore, the color shift at different viewing angles is reduced, and there is no substantial difference for visual sensation for color when viewing the OLED display panel including such sub-pixels at different viewing angles.

[0039] Further, please refer to FIG. 3. In the present embodiment, the sub-pixel **2** further includes a pixel defining layer **23**. The pixel defining layer **23** includes at least one first opening **231** and at least one second opening **232**. The first opening **231** is positioned to correspond to the first light-emitting area **A**, and the first light-emitting layer **21** is disposed in the first opening **231**. The second opening **232** is positioned to correspond to the second light-emitting area **B**, and the second light-emitting layer **22** is disposed in the second opening **232**. In other words, different light-emitting layers having different thicknesses are formed in different openings of the pixel defining layer **23**. Specifically, in the present embodiment, the pixel defining layer **23** includes one first opening **231** and one second opening **232**. The first opening **231** is positioned to correspond to the first light-emitting area **A**. The second opening **232** is positioned to correspond to the second light-emitting area **B**. The first light-emitting layer **21** and the second light-emitting layer **22** having different thicknesses are respectively formed in the first opening **231** and the second opening **232** of the pixel defining layer **23**. That is, the light-emitting area of a single one sub-pixel **2** is defined by two different openings of the pixel defining layer **23**, ie., the first opening **231** and the second opening **232**, where the first light-emitting layer **21** and the second light-emitting layer **22** are independent from each other, and they together constitute the light-emitting layer of the sub-pixel **2**.

[0040] Moreover, the sub-pixel **2** further includes an anode **24**. The anode **24** extends from the first light-emitting area **A** to the second light-emitting area **B**. The pixel defining layer **23** is disposed on the anode **24**. Both the first light-emitting layer **21** and the second light-emitting layer **22** are formed on the anode **24**, and share a same anode **24**. The structures and materials of the pixel defining layer **23** and the anode, and connections thereof to other components are well-known by any person having ordinary skill in the art, and thus description thereof are omitted.

[0041] In addition, the present disclosure provides a method for manufacturing the above-said sub-pixel. The method for forming the first light-emitting layer **21** and the second light-emitting layer **22** can include but is not limited to ink-jet printing (UP) technique, use of metal mask or other masks, or other methods that can form the light-emitting layer having varied thickness. Two embodiments are provided as examples of the present invention for forming the light-emitting layers. In the first embodiment, the first light-emitting layer **21** and the second light-emitting layer **22** are formed using different masks. In the second embodiment, the first light-emitting layer **21** and the second light-emitting layer **22** are formed using a same mask. Detailed description thereof is provided below.

[0042] FIGS. 6A and 6B show each stage in a process flow of a method for manufacturing a sub-pixel of a display panel according to a first embodiment of the present disclosure. In the present embodiment, the method includes the following steps.

[0043] In FIG. 6A, a first mask 60 having at least one first deposition hole 601 is provided. The first deposition hole 601 is a through-hole of the first mask 60 which allows deposition material to pass therethrough. Next, the first deposition hole 601 is positioned to correspond to the first opening 231 of the pixel defining layer 23. Then, the first light-emitting layer 21 is deposited in the first opening 231. The method used to deposit the first light-emitting layer 21 includes but is not limited to evaporation.

[0044] In FIG. 6B, after the first light-emitting layer 21 is formed, a second mask 61 having at least one second deposition hole 610 is provided. Thereafter, the second deposition hole 610 is positioned to correspond to the second opening 232, and the second light-emitting layer 22 is deposited in the second opening 232. The method used to deposit the second light-emitting layer 22 includes but is not limited to evaporation. The deposition leads to the result that thickness of the second light-emitting layer 22 is greater than the thickness of the first light-emitting layer 21, and therefore a structure of the above-said sub-pixel is formed.

[0045] In the present embodiment, the first mask 60 and the second mask 61 are different. The first light-emitting layer 21 and the second light-emitting layer 22 are formed using different masks.

[0046] FIGS. 7A and 7B show each stage in a process flow of a method for manufacturing a sub-pixel of a display panel according to a second embodiment of the present disclosure. In the present embodiment, the method includes the following steps.

[0047] In FIG. 7A, a first mask 70 having at least one first deposition hole 701 is provided. The first deposition hole 701 is a through-hole of the first mask 70 which allows deposition material to pass therethrough. Next, the first deposition hole 701 is positioned to correspond to the first opening 231 of the pixel defining layer 23. Then, the first light-emitting layer 21 is deposited in the first opening 231. The method used to deposit the first light-emitting layer 21 includes but is not limited to evaporation.

[0048] In FIG. 7B, after the first light-emitting layer 21 is formed, the first mask 70 is moved to position the first deposition hole 701 to correspond to the second opening 232, and the second light-emitting layer 22 is deposited in the second opening 232. The method used to deposit the second light-emitting layer 22 includes but is not limited to evaporation. The deposition leads to the result that thickness of the second light-emitting layer 22 is greater than the thickness of the first light-emitting layer 21, and therefore a structure of the above-said sub-pixel is formed.

[0049] In the present embodiment, the first mask 70 is moved, and is used to form the first light-emitting layer 21 and the second light-emitting layer 22. The first light-emitting layer 21 and the second light-emitting layer 22 are formed using a same mask.

[0050] Please refer to FIGS. 8 and 9. The present disclosure further provides a sub-pixel of a display panel according to a second embodiment of the present disclosure. FIG. 8 shows a top view of a sub-pixel of a display panel according to a second embodiment of the present disclosure. FIG. 9 shows a cross-sectional view of FIG. 8 cut along line C-C. The present embodiment differs from the first embodiment in that, in the present embodiment, the pixel defining layer 23 includes at least one first opening 231, the first opening 231 is positioned to correspond to the first light-emitting area A and the second light-emitting area B, and the

first light-emitting layer 21 and the second light-emitting layer 22 are disposed in a same first opening 231. That is, a light-emitting layer having varied thickness is formed in a same opening of the pixel defining layer 23. Specifically, in the present embodiment, the pixel defining layer 23 includes one first opening 231. The first opening 231 is positioned to correspond to the first light-emitting area A and the second light-emitting area B. The first light-emitting layer 21 and the second light-emitting layer 22 having different thicknesses are formed in the same first opening 231. That is, the first light-emitting area A and the second light-emitting area B of a single one sub-pixel 2 are defined by one first opening 231, such that the light-emitting layers in the same first opening have different thicknesses in different areas, and the light-emitting layers in the same first opening having different thicknesses together constitute the light-emitting layer of the sub-pixel 2.

[0051] In addition, the present disclosure provides a method for manufacturing the above-said sub-pixel. The method for forming the first light-emitting layer 21 and the second light-emitting layer 22 can include but is not limited to ink-jet printing (IF) technique, use of metal mask or other masks, or other methods that can form the light-emitting layer having varied thickness. Two embodiments are provided as examples of the present invention for forming the light-emitting layers. In the third embodiment, the first light-emitting layer 21 and the second light-emitting layer 22 are formed using different masks. In the fourth embodiment, the first light-emitting layer 21 and the second light-emitting layer 22 are formed using a same mask. Detailed description thereof is provided below.

[0052] FIGS. 10A and 10B show each stage in a process flow of a method for manufacturing a sub-pixel of a display panel according to a third embodiment of the present disclosure. In the present embodiment, the method includes the following steps.

[0053] In FIG. 10A, a first mask 100 having at least one first deposition hole 1001 is provided. The first deposition hole 1001 is a through-hole of the first mask 100 which allows deposition material to pass therethrough. Next, the first deposition hole 1001 is positioned to correspond to the first opening 231 of the pixel defining layer 23. Then, a bottom light-emitting layer 1002 is deposited in the first opening 231. The method used to deposit the bottom light-emitting layer 1002 includes but is not limited to evaporation.

[0054] In FIG. 10B, a second mask 101 having at least one second deposition hole 1010 is provided. The second deposition hole 1010 is a through-hole of the second mask 101 which allows deposition material to pass therethrough. Thereafter, the second deposition hole 1010 is positioned to correspond to a portion of the first opening 231, and a top light-emitting layer 1011 is deposited in the first opening 231. Specifically, in the present embodiment, a width of the second deposition hole 1010 is less than a width of the first opening 231. This makes that the top light-emitting layer 1011 is deposited in the first opening 231 at position corresponding to the second deposition hole 1010, and there is no the top light-emitting layer 1011 being deposited in the first opening 231 at position that does not correspond to the second deposition hole 1010. For the portion where the top light-emitting layer 1011 is deposited, the bottom light-emitting layer 1002 overlaps with the top light-emitting layer 1011 to constitute the second light-emitting layer 22.



In the non-overlapping region, the bottom light-emitting layer **1002** serves as the first light-emitting layer **21**.

**[0055]** In the present embodiment, the first light-emitting layer **21** and the second light-emitting layer **22** are disposed in the same first opening **231**. The first mask **100** and the second mask are different. That is, the first light-emitting layer **21** and the second light-emitting layer **22** are formed using different masks.

**[0056]** FIGS. **11A** and **11B** show each stage in a process flow of a method for manufacturing a sub-pixel of a display panel according to a fourth embodiment of the present disclosure. In the present embodiment, the method includes the following steps.

**[0057]** In FIG. **11A**, a first mask **110** having at least one first deposition hole **1101** is provided. The first deposition hole **1101** is a through-hole of the first mask **110** which allows deposition material to pass therethrough. Next, the first deposition hole **1101** is positioned to correspond to the first opening **231** of the pixel defining layer **23**. Then, a bottom light-emitting layer **1102** is deposited in the first opening **231**. The method used to deposit the bottom light-emitting layer **1102** includes but is not limited to evaporation.

**[0058]** In FIG. **11B**, after the bottom light-emitting layer **1102** is formed, the first mask **110** is moved to position the first deposition hole **1101** to correspond to a portion of the first opening **231**. Specifically, the first mask **110** is moved, such that a portion of the first opening **231** is shielded, and a top light-emitting layer **1111** is deposited in the non-shielded portion of the first opening **231**. The method used to deposit the top light-emitting layer **1111** includes but is not limited to evaporation. For the portion where the top light-emitting layer **1111** is deposited, the bottom light-emitting layer **1102** overlaps with the top light-emitting layer **1111** to constitute the second light-emitting layer **22**. In the non-overlapping region, the bottom light-emitting layer **1102** serves as the first light-emitting layer **21**.

**[0059]** In the present embodiment, the first light-emitting layer **21** and the second light-emitting layer **22** are disposed in the same first opening **231**. The first mask **110** is moved, and is used to form the first light-emitting layer **21** and the second light-emitting layer **22**. The first light-emitting layer **21** and the second light-emitting layer **22** are formed using a same mask.

**[0060]** Furthermore, the display panel includes a plurality of sub-pixels, wherein at least one sub-pixel has a structure that is the same as that of the above-said sub-pixel. In other words, the display panel includes a plurality of red sub-pixels, a plurality of green sub-pixels, a plurality of blue sub-pixels, and a plurality of white sub-pixels, wherein at least one sub-pixel has a structure that is the same as that of the above-said sub-pixel. Additionally, the display panel includes a first type of sub-pixel, a second type of sub-pixel, and a third type of sub-pixel, wherein at least one type of the sub-pixels has a structure that is the same as that of the above-said sub-pixel. In other words, in the display panel, all the sub-pixels of a same type have a structure that is the same as that of the above-said sub-pixel. For example, all the red sub-pixels have a structure that is the same as that of the sub-pixel of the present disclosure, or two or over two types of sub-pixels have a structure that is the same as that of the sub-pixel of the present disclosure.

**[0061]** In addition, the present disclosure provides an electronic device including the above-said display panel. It

is understood that the electronic device can include but is not limited to a mobile phone, a flat panel computer, a calculating device, a game device, a television, a display screen, a wearable device, and other appliances or household appliances having display function.

**[0062]** While the present disclosure has been described with the aforementioned preferred embodiments, it is preferable that the above embodiments should not be construed as limiting of the present disclosure. Anyone having ordinary skill in the art can make a variety of modifications and variations without departing from the spirit and scope of the present disclosure as defined by the following claims.

#### INDUSTRIAL APPLICABILITY

**[0063]** The inventions provided by the present disclosure can be made and used in industry, and thus possess industrial applicability.

**1.** A display panel including a plurality of sub-pixels, wherein at least one of the sub-pixels comprises at least one first light-emitting area and at least one second light-emitting area, the first light-emitting area and the second light-emitting area are sequentially arranged in a direction perpendicular to a light-emitting direction of the sub-pixel, a first light-emitting layer is disposed in the first light-emitting area, a second light-emitting layer is disposed in the second light-emitting area, and a thickness of the second light-emitting layer is greater than a thickness of the first light-emitting layer.

**2.** The display panel according to claim **1**, wherein the sub-pixel further comprises a pixel defining layer, the pixel defining layer includes at least one first opening and at least one second opening, the first opening is positioned to correspond to the first light-emitting area, the first light-emitting layer is disposed in the first opening, the second opening is positioned to correspond to the second light-emitting area, and the second light-emitting layer is disposed in the second opening.

**3.** The display panel according to claim **1**, wherein the sub-pixel further comprises a pixel defining layer, the pixel defining layer includes at least one first opening, the first opening is positioned to correspond to the first light-emitting area and the second light-emitting area, and the first light-emitting layer and the second light-emitting layer are disposed in a same first opening.

**4.** The display panel according to claim **1**, wherein the sub-pixel further comprises one first light-emitting area and one second light-emitting area, and the first light-emitting area and the second light-emitting area are sequentially arranged in the direction perpendicular to the light-emitting direction of the sub-pixel.

**5.** A method for manufacturing the display panel of claim **2**, comprising steps of:

providing a first mask having at least one first deposition hole, positioning the first deposition hole to correspond to the first opening, and depositing the first light-emitting layer in the first opening; and

providing a second mask having at least one second deposition hole, positioning the second deposition hole to correspond to the second opening, and depositing the second light-emitting layer in the second opening such that thickness of the second light-emitting layer is greater than the thickness of the first light-emitting layer.

6. The method for manufacturing the display panel according to claim 5, wherein

the first mask and the second mask are same, and in the step of providing the second mask, the first mask is moved to position the first deposition hole to correspond to the second opening, and the second light-emitting layer is deposited in the second light-emitting area.

7. A method for manufacturing the display panel of claim 3, comprising steps of:

providing a first mask having at least one first deposition hole, positioning the first deposition hole to correspond to the first opening, and depositing a bottom light-emitting layer in the first opening; and

providing a second mask having at least one second deposition hole, positioning the second deposition hole to correspond to a portion of the first opening, and depositing a top light-emitting layer in the first opening such that the bottom light-emitting layer overlaps with the top light-emitting layer to constitute the second light-emitting layer, and the bottom light-emitting layer constitutes the first light-emitting layer in a non-overlapping region.

8. The method for manufacturing the display panel according to claim 7, wherein

the first mask and the second mask are same, and in the step of providing the second mask, the first mask is moved to position the first deposition hole to correspond to a portion of the first opening, and the top light-emitting layer is deposited in the first opening.

9-12. (canceled)

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